# INTRODUCTION TO MICROCONTROLLER

#### Introduction

8051 is one of the first and most popular microcontrollers also known as MCS-51. Intel introduced it in the year 1981. Initially, it came out as an N-type metal-oxide-semiconductor (NMOS) based microcontroller, but later versions were based on complementary metal-oxide-semiconductor (CMOS) technology. These microcontrollers were named 80C51, where C in the name tells that it is based on CMOS technology. It is an 8-bit microcontroller which means the data bus is 8-bit. Therefore, it can process 8 bits at a time. It is used in a wide variety of embedded systems like robotics, remote controls, the automotive industry, telecom applications, power tools, etc.

#### FEATURES OF 8051 MICROCONTROLLER

An 8051 microcontroller comes bundled with the following features –

- 4KB bytes on-chip program memory (ROM)
- 128 bytes on-chip data memory (RAM)
- Four register banks
- 128 user defined software flags
- 8-bit bidirectional data bus
- 16-bit unidirectional address bus
- 32 general purpose registers each of 8-bit
- 16 bit Timers (usually 2, but may have more or less)
- Three internal and two external Interrupts
- Four 8-bit ports,(short model have two 8-bit ports)
- 16-bit program counter and data pointer

### APPLICATIONS OF MICROCONTROLLER

- 1. Most personal computer keyboards are implemented with a microcontroller. It replaces scanning, debounce matrix decoding and serial transmission circuits.
- 2. Generally in low cost products, such as toys, electric drills, microwave ovens, VCRs microcontrollers are used.
- 3. Microcontrollers are used as machine tools, chemical processors and in medical instruments.
- 4. It also controls mechanism of electronic systems, music system, home security system, etc.

In this chapter we will study a brief overview of 8051 microcontrollers.

## **ARCHITECTURE 8051**

The 8051 is a second generation 8-bit microcontroller. The first Intel's 8-bit microcontroller was the 8048. The 8051 provide a more powerful architecture, a more powerful instruction set, a full serial port.

Main Features of 8051 Microcontroller

1) An 8 bit ALU

- 2)  $4K \times 8 \text{ ROM (OR EPROM)}$
- 3) 128 × 8 RAM
- 4) Dual 16 bit timer event counter
- 5) 32 I/O lines
- 6) Addresses of 64 Kbytes of program memory
- 7) Addresses of 64 Kbytes of data memory
- 8) Powerful 111 instruction set
- 9) Full featured serial port
- 10) Up to 12 MHz. clock
- 11) Two external interrupts

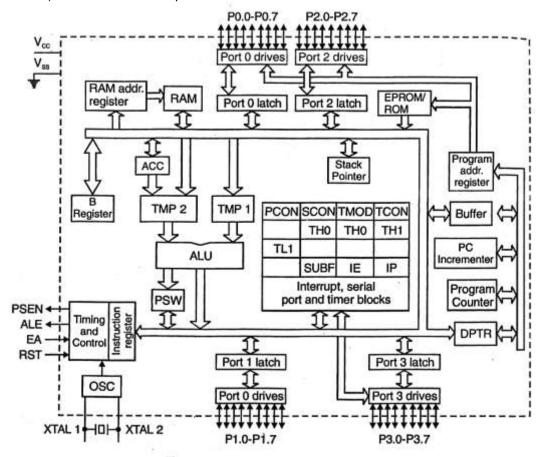


Fig. An architectural block diagram of 8051

#### Architecture of 8051

There are 32 pins needed by the four 8-bit bi-directional I/O ports. Eight additional pins provide power and allow to connect to a clock crystal and also provide timing and control signals.

The standard functions, which make up a microprocessor, are in the center of the diagram. It includes the ALU, accumulator, stack pointer, a block of registers and a general purpose registers. All of these devices are connected to 8051 internal 8-bit data bus.

Each I/O port is also connected to the 8 bit internal data bus through a series of register. These registers hold data during I/O transfers and control I/O ports. It is also having ROM and RAM.

## 8051 Memory Register Map

Generally 8051 addresses two memory spaces. It uses one memory space for storing programs and the other for storing variable data. The program memory space is a read only memory space. You can read program instructions from this space but the processor cannot write data or read data from these memory locations. The 8051 internal ROM is in program memory space. All instructions fetches are from program memory space.

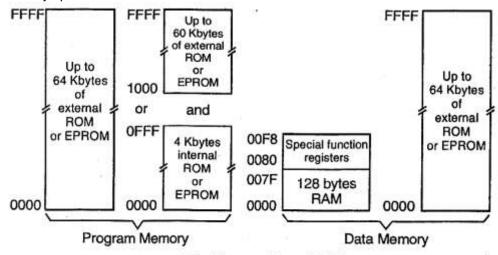


Fig. Memory Map of 8051

The data memory space is read-write memory space. The processor can read data from this memory space and can write data to this memory space. But it cannot execute program instructions from this memory space. The 8051 internal RAM is in this memory space.

The 128 bytes of internal RAM provide general read-write data storage. Part of this memory space is often referred to as general purpose registers.

The 8051 also has 22 special function registers which are not part of 128 bytes of internal RAM. They occupy memory space from 80H to F8H.

If more program memory is needed the internal 4 Kbytes memory can be expanded by an additional 60 Kbytes. So 8051 has now a full 64 Kbytes program memory space. If 8051 pin is connected to ground, it does not use the internal 4 K ROM. If user needs more RAM, external data memory can be added. It is of 64 Kbytes. Generally 8051 operates with separate program memory and data memory space but in some applications it is desirable to have these work as common memory. In that case 8051 has 64 Kbytes of total external memory.

In this configuration 8051 can input a block of data through serial communication port, load data into memory and then execute that data as a program. This is called downloaded program.

# **COMPARISON WITH MICRPROCESSOR**

	Microprocessor 8085	Microcontroller 8051				
a)	It is an 8-bit μp.	It is a 8-bit microcontroller.				
b)	Address bus is 16-bit, hence	Address bus is 16-bit, hence can				
	can access 64 KB memory.	access 64 KB memory				
c)	It provides seven 8-bit registers	It provides 34 8-bit registers-A,				
	−A, B, H	Band 32 general purpose				
		registers.				
d)	8 – bit of data bus but ports are	It has four ports P0-P3 for I/O				
	not available.					
e)	Flag register is 8-bit and	Flag register is 8-bit and contains				
	contains Five flags	Nine flags.				
f)	Peripheral chips are required	Peripheral chips are not required				

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# Select the correct alternative and rewrite the following.

	(i) 8085 (iii) 8051	ontroller chip. (ii) 80286	(iii) 8051	(iv) Pentium
2. 2.	8051 has RAI (i) 128 bytes (i) 128 bytes	M. (ii) 64K bytes	(iii) 1K bytes	(iv) None of these
3. 3.	The 8051 microcontr (i) 99 (ii) 111			
	8051 has clock upto (i) 12 MHz (i) 12 MHz	frequency. (ii) 4 MHz	(iii) 9 MHz	(iv) 6 MHz
5. 5.	The 8051 is a (i) First (ii) Second			(iv) Fourth
6. 6.	The 8051 microcontr (i) 8 (i) 8			(iv) 64
7. 7.	is not a ch (i) 4 kbyte of interna (iii) 4 parallel bi–dire (i) 4 kbyte of interna	al RAM ctional I/O port	(ii) 4 kbyte of interr	nal ROM

8.		IC consists	of internal RAM.			
8.	(i) (iii)	8080 8051	(ii) 8085	(iii)	8051	(iv) 8086
	805 (i) (iii)	1	r IC have r (ii)   2		per of 8 bit I/O po 4	
10.	805 (i)	51 micro-controll 99	er has instruction se (ii) 111			(iv) 110
11.	Inte	12 MHz	upto frequer (ii) 4 MHz	ncy. (iii)	9 MHz	(iv) 6MHz
12.	Am (i)	12 MHz ong following 8048 8051	is the lates (ii) 8051	t 8– (iii)	bit single chip mi 8096	crocontroller. (iv) 8044
13.	In 8 (i)	8051 size of inter	nal ROM is (ii) 2KB	(iii)	 8KB	(iv) 16 KB
	(i) (ii) (iii) (iv)	Found in the Da Used to store va 4 kBytes of ROM All of the	OM is  Ita Memory Space.  Ariable program data  If in the Program Me  If in the Program Me	emo		
	Wh (i)		ng is not a part of an	805 (ii)	51 Single–chip M	t
15.	٠,	Dual Serial Port		(14)	Tour o' bit parai	ioi i/O porto
	has (i) (iii)	e additional featu additional Internal RAM or 16 bit ALU Both Internal RA	nly	(ii)	er over 8085 Mic Internal ROM or Both Internal RA	
17.	Inte		emory of 8052 Micro (ii) 8 k byte	cont (iii)	roller is 256 k byte	(iv) 64 k byte
	In c (i) (ii)	3	rocontroller Chip, the	ere a (iii)		xternal interrupts. (iv) 5

	(i)	051, 22 special 08H to F8H 80H to F8H							
	. ,								
		ro–controller 805 1 2					al Interrupts. 3	(iv)	4
21.	(i)	is a charact	erist	ics feature o	of 80	51 N (ii)	/licro-controller.	orts	
		Four 8 bit I/O Po 4kB RAM Four External Ir				(iv)	Four External li	nterr	upts
22.	Mic	ro-controller 805	52 ha	as	exte	rnal	interrupts.		
		2					4	(iv)	5
23.	Mic	ro-controller 805	50 ha	as	byte	s of	RAM		
	(i)	64	(ii)	128	2,10	(iii)	256	(iv)	32
23.	(iii)	256							
		rnal Data memo 128 bytes						 (iv)	4 k bytes
24.	(i)	128 bytes							
	(i)	is not a 8052 Pentium				(iii)	Pentium	(iv)	8051
26.	805 (i)	51 Bit 8	Micr (ii)	o-Controlle	r.	(iii)	16	(iv)	32
26.	(i)	8							
27.	(i)	is a Micr 8086	o–C (ii)	ontroller. 8051		(iii)	8088	(iv)	80286
27.	(ii)	8051	` ,			` '		, ,	
28.	Inte	l 8051 Microcon	trolle	er has	RA	M.			
	(i)	128 × 8 128 × 8					64 × 8	(iv)	8K × 8
29. 29.	(i)	instruction set of 111 111	of int (ii)		ro–co	ontro (iii)			instruction. 100
	(i)	e 8081 Micro–coi 101 111		ler has instru 110	uctio	n se (iii)			tions. 111