Register - 16 Bit - Sequential (clocked input/output) - Store data only when Load selected mLoad - select to store input min[16] - Data out[16] - Data out[0] Bit Bit Bit Bit Bit **6** Bit Bit 0 **6** Bit 0 Bit 0 Bit Bit Bit 0 Bit Bit Bit out[15] Bit