

DIGITAL LOGIC DESIGN

ECE2002

Tutorial -3

Dr. Anirban Bhowmick

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1. Design a Mod 5 asynchronous counter using JK flipflop.
 2. Design a Mod 7 synchronous counter using T flipflop
 3. Design a Sequence detector with D flipflop which can detect '1100'.
 4. Draw the state diagram of a sequence detector which can detect a sequence '10101' (overlapping).