

IMOB_TAG_hv1.PrjPcb

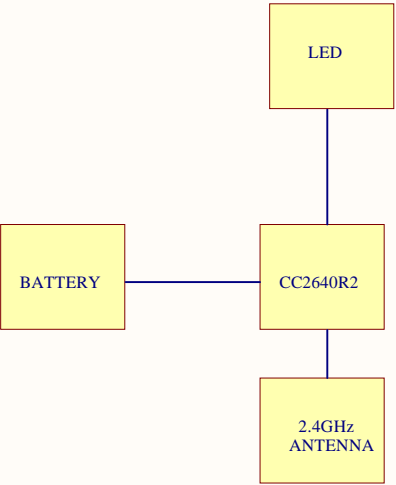
STATUS: TESTING 06.09.2018

STATUS TYPES:IN WORK,TESTING,RELEASED

Page	Index	Page	Index
1	COVER PAGE	11
2	BLOCK DIAGRAM	12
3	MCU	13
4	RF	14
5	CONNECTORS	15
6	16
7	17
8	18
9	19
10	20

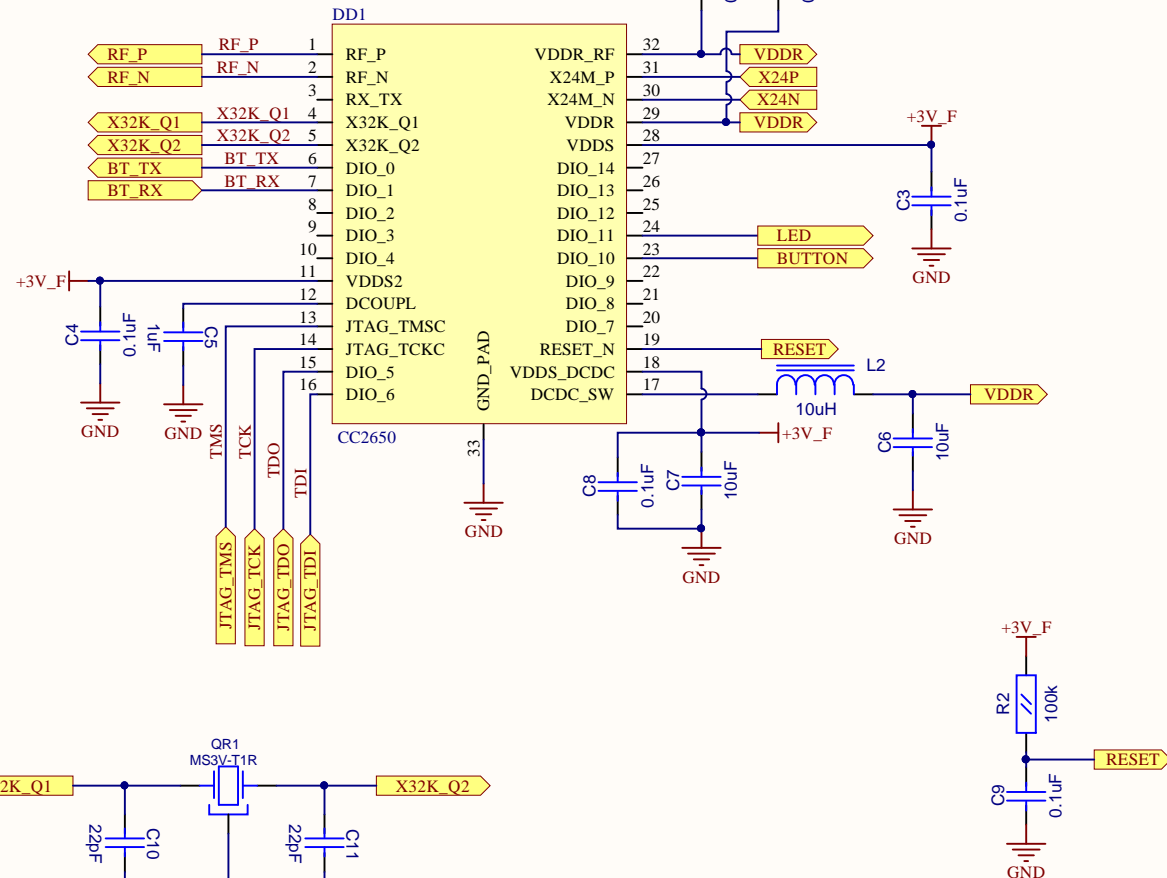
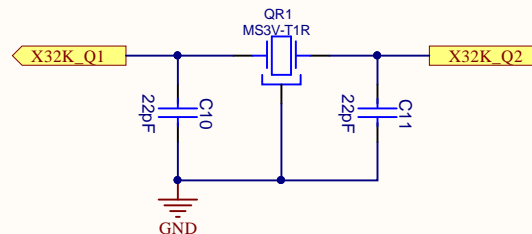
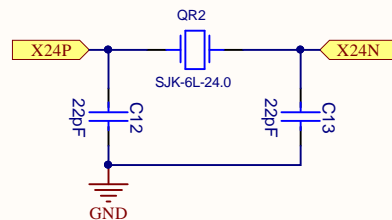
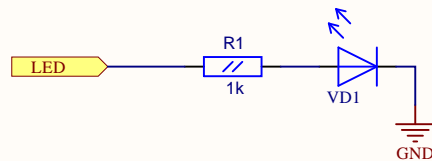
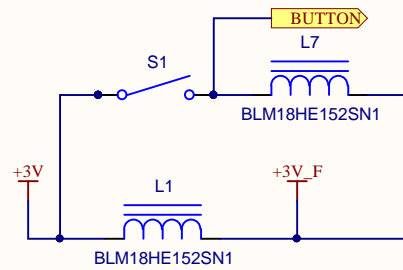
Title			
Size	Number		Revision
A4			
Date:	06.09.2018	Sheet	of
File:	E:\Projects Files\...\[1]-COVER_PAGE_Sd.dwg		
		Drawn By:	

[2]-BLOCK_DIAGRAM.SchDoc



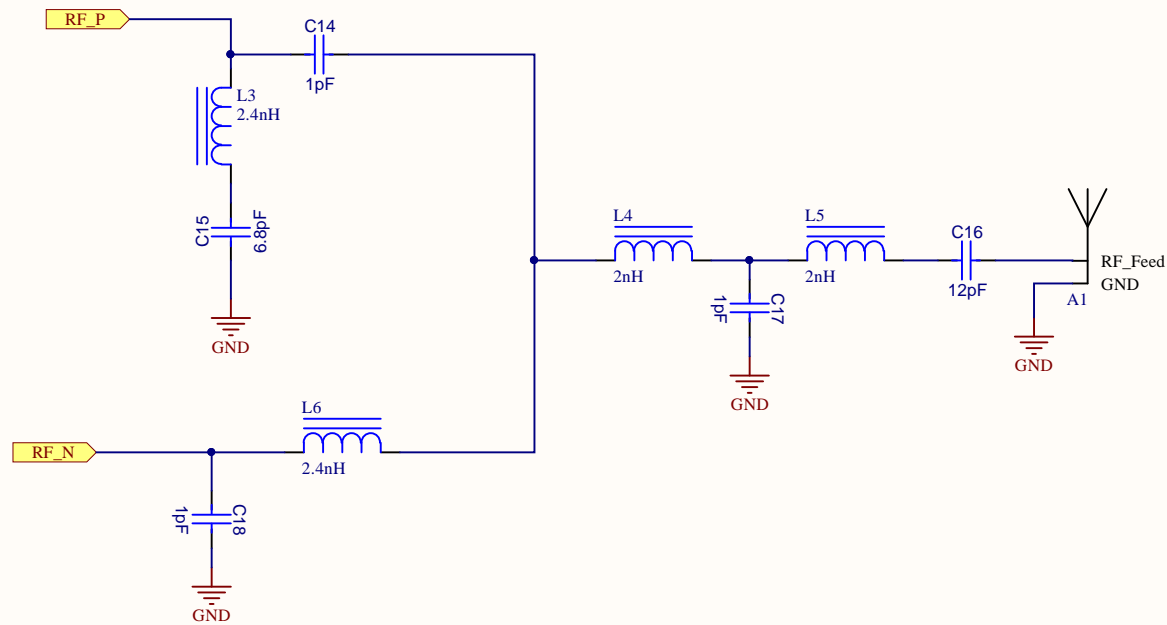
Title			
Size	Number		Revision
A4			
Date:	06.09.2018	Sheet	of
File:	E:\Projects Files\...\[2]-BLOCK_DIAGRAM.SchDoc		

[3]-MCU.SchDoc



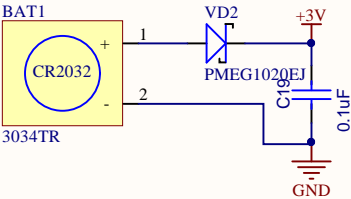
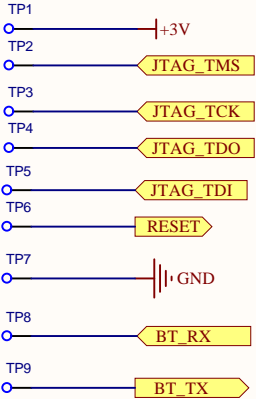
Title		
Size	Number	Revision
A4		
Date:	06.09.2018	Sheet of
File:	E:\Projects_Files\...\[3]-MCU.SchDoc	Drawn By:

[4]-RF.SchDoc



Title			
Size	Number		Revision
A4			
Date:	06.09.2018	Sheet	of
File:	E:\Projects_Files\...\[4]-RF.SchDoc	Drawn By:	

[5]-CONNECTORS.SchDoc



Title			
Size	Number		Revision
A4			
Date:	06.09.2018	Sheet	of
File:	E:\Projects Files\...\[5]-CONNECTORS.SchDoc		
Drawn By:			