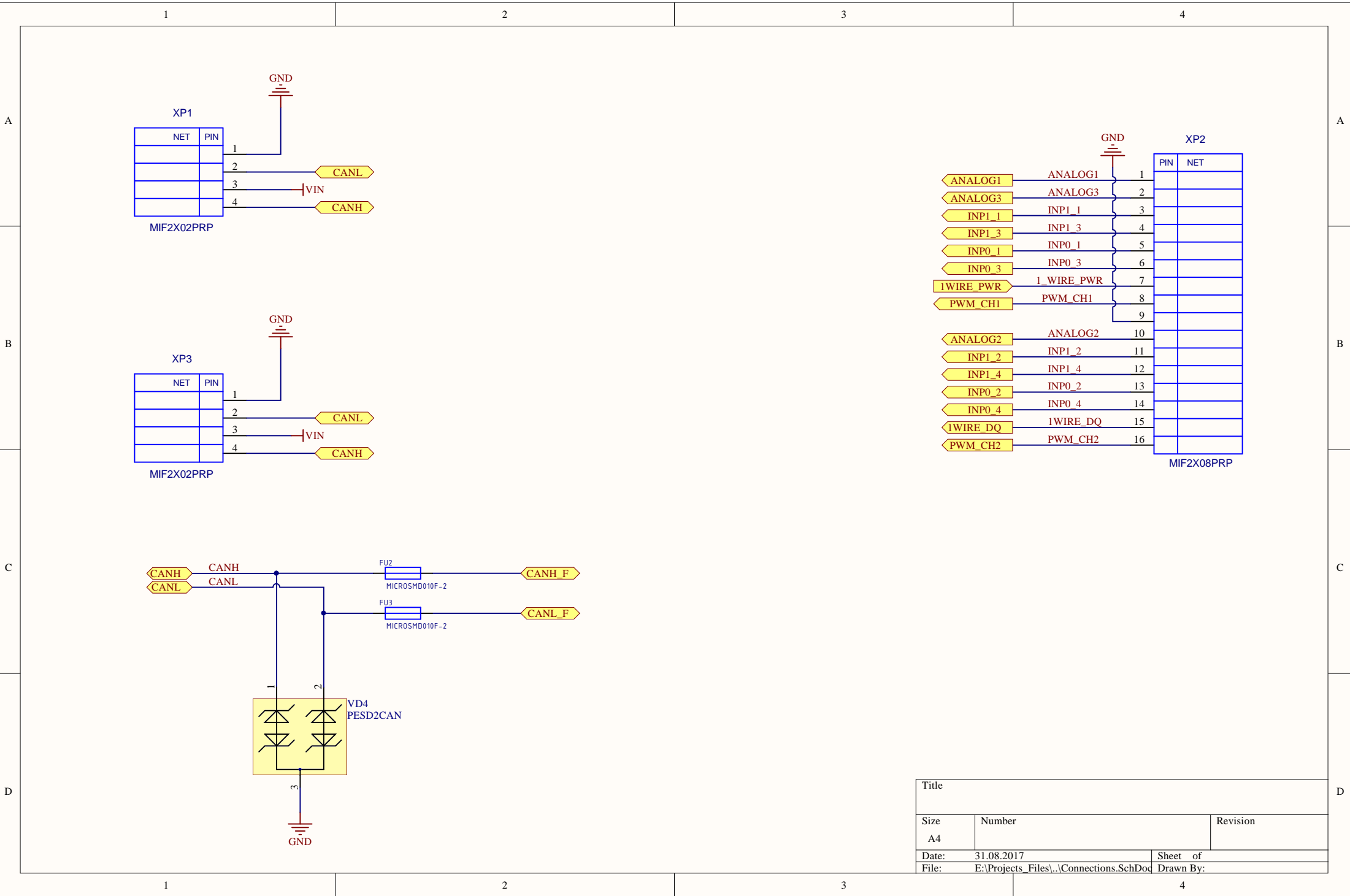
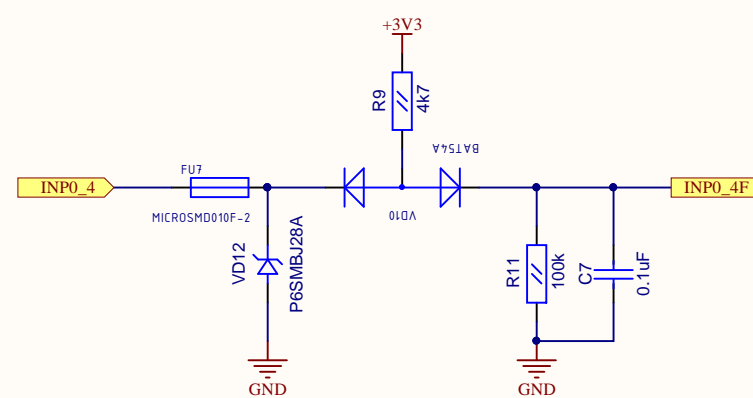
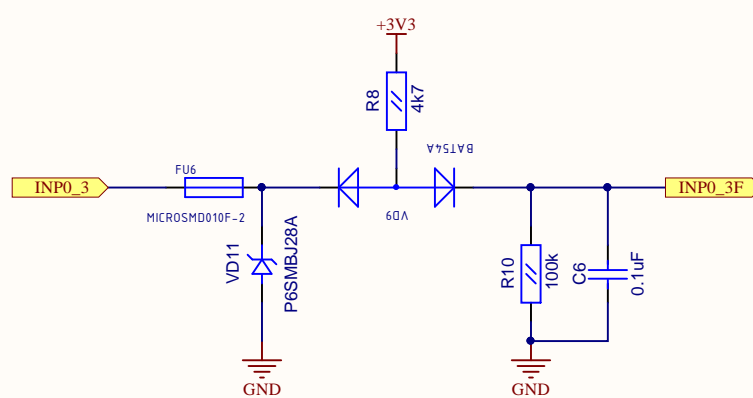
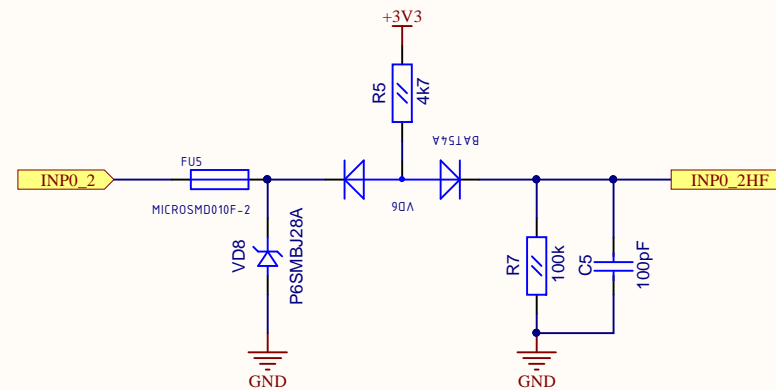
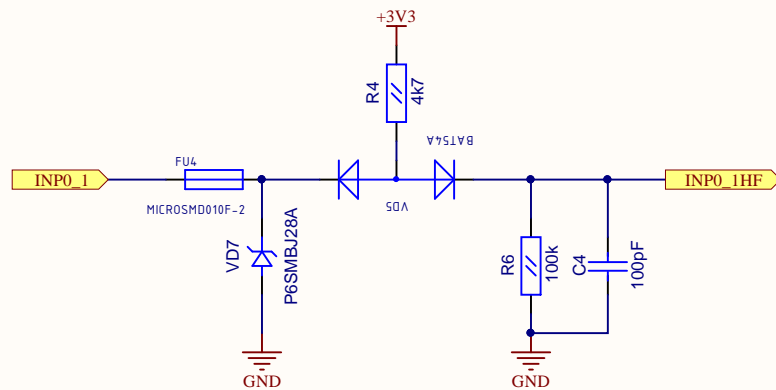


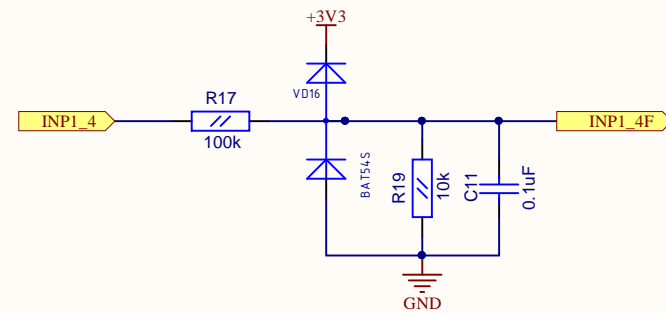
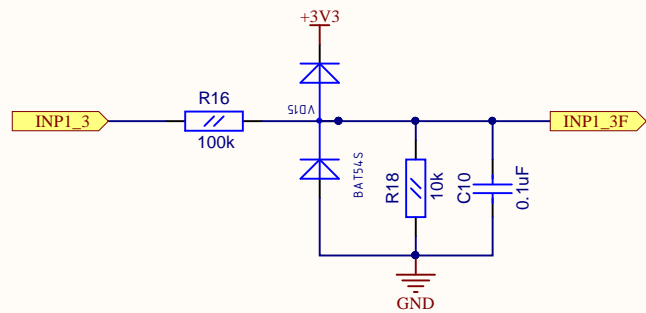
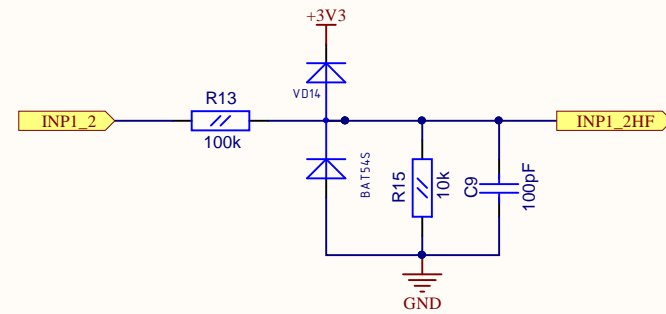
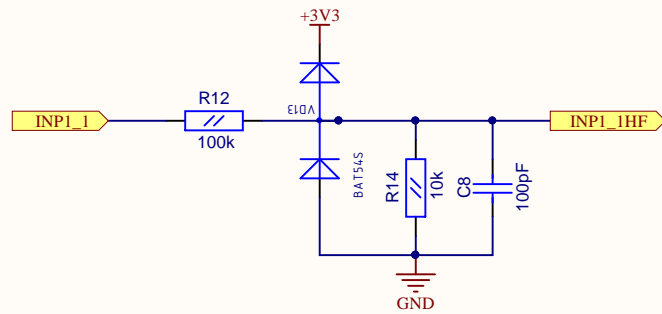
Title		
Size	Number	Revision
A4		
Date:	31.08.2017	Sheet of
File:	E:\Projects_Files\...\1Wire.SchDoc	Drawn By:



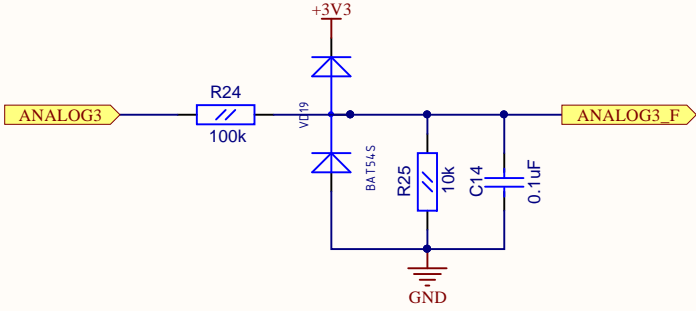
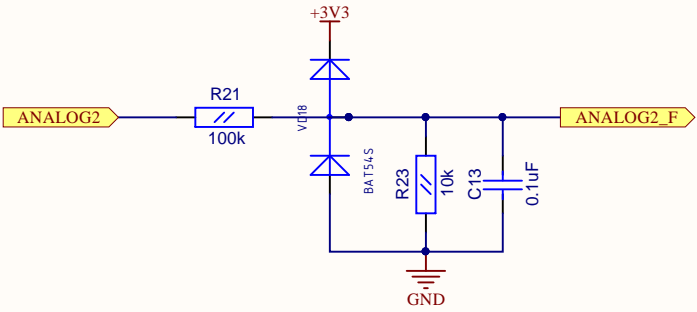
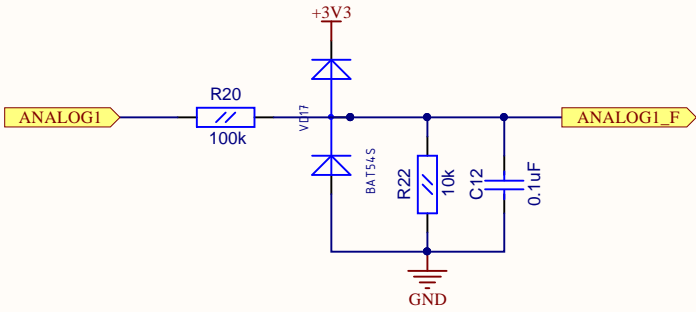
Title		
Size	Number	Revision
A4		
Date:	31.08.2017	Sheet of
File:	E:\Projects_Files\...\Connections.SchDoc Drawn By:	



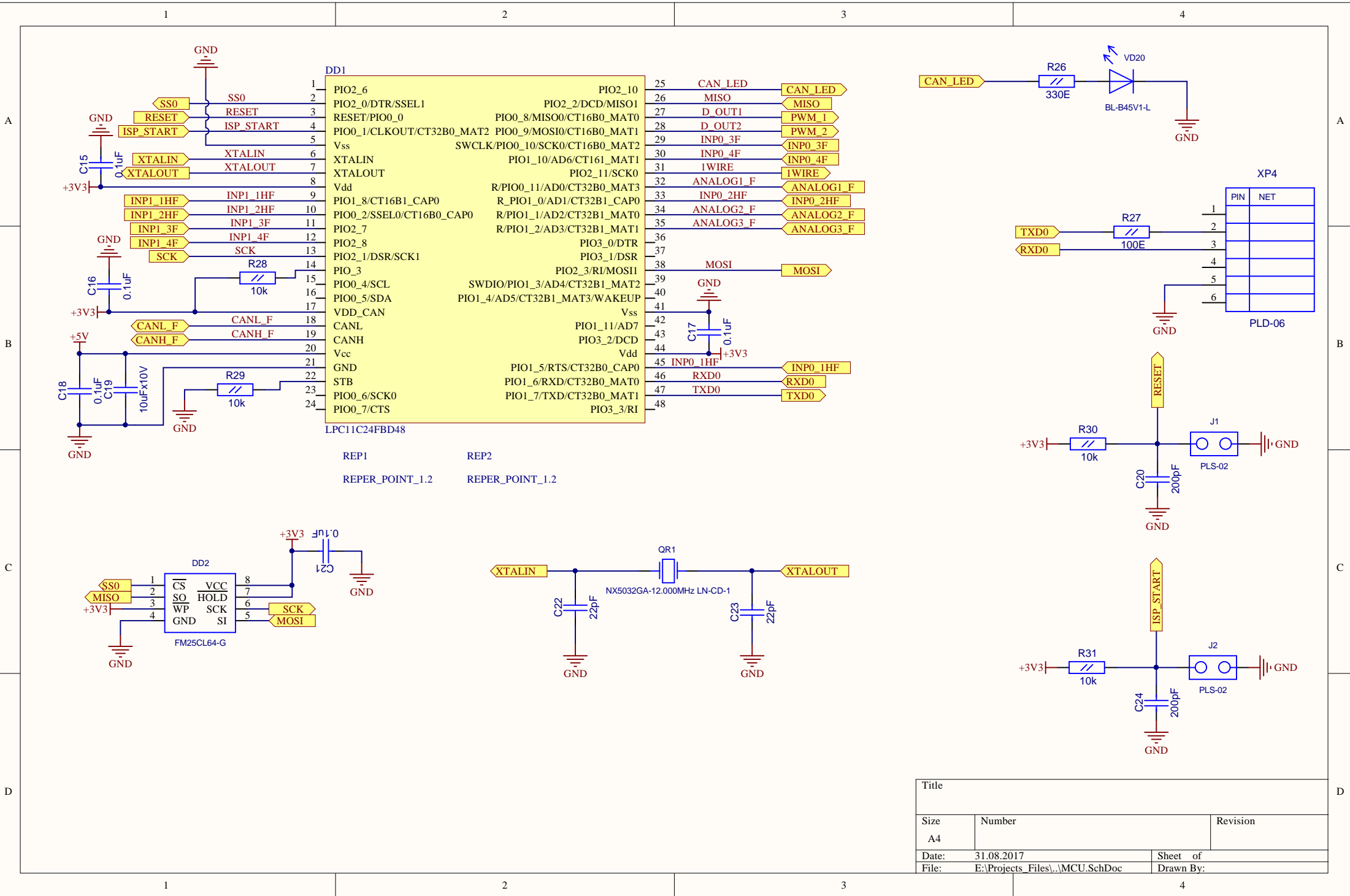
Title		
Size A4	Number	Revision
Date:	31.08.2017	Sheet of
File:	E:\Projects Files\...\Input_0.SchDoc	Drawn By:



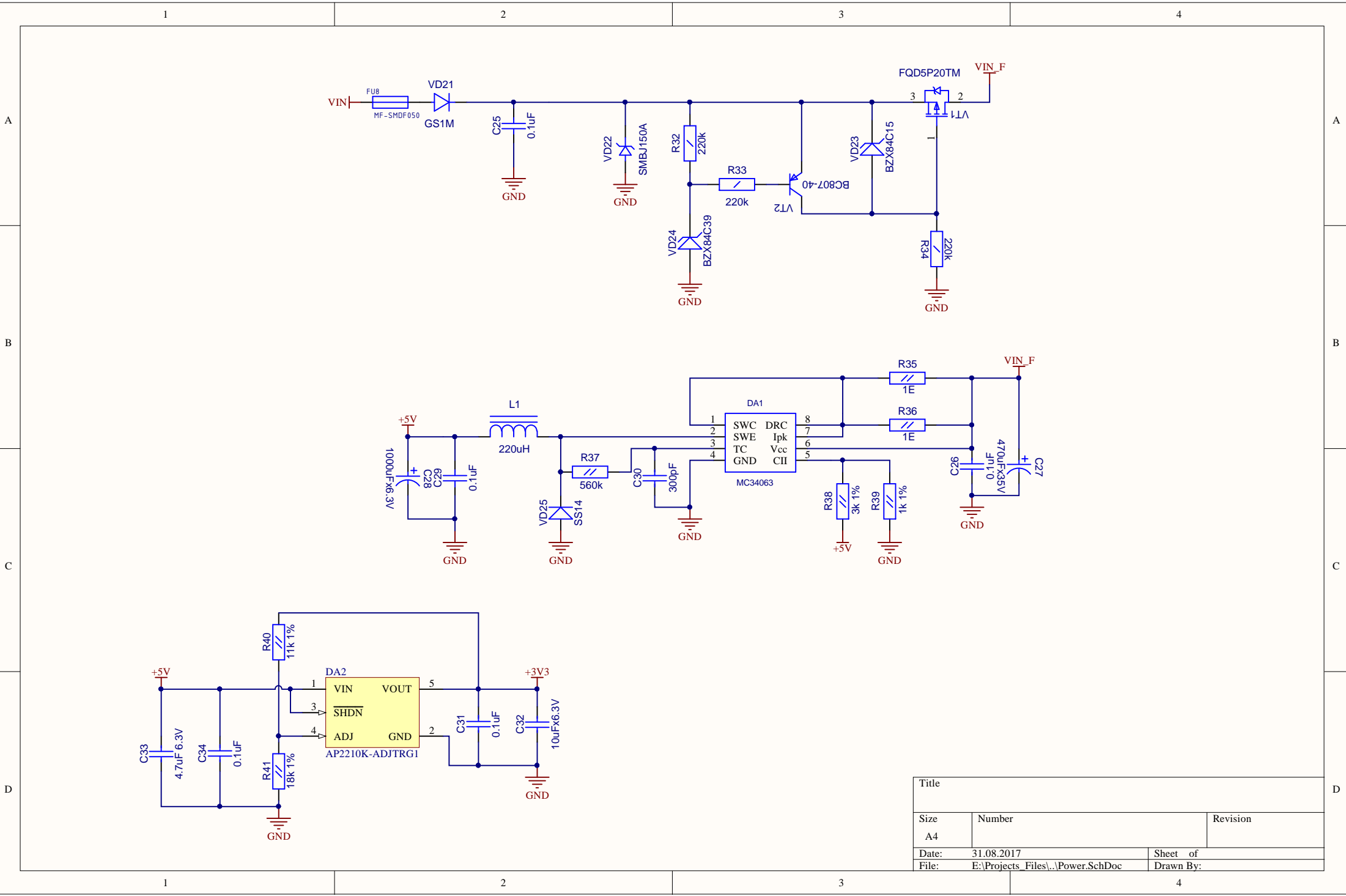
Title		
Size	Number	Revision
A4		
Date:	31.08.2017	Sheet of
File:	E:\Projects Files\...\Input_1.SchDoc	Drawn By:



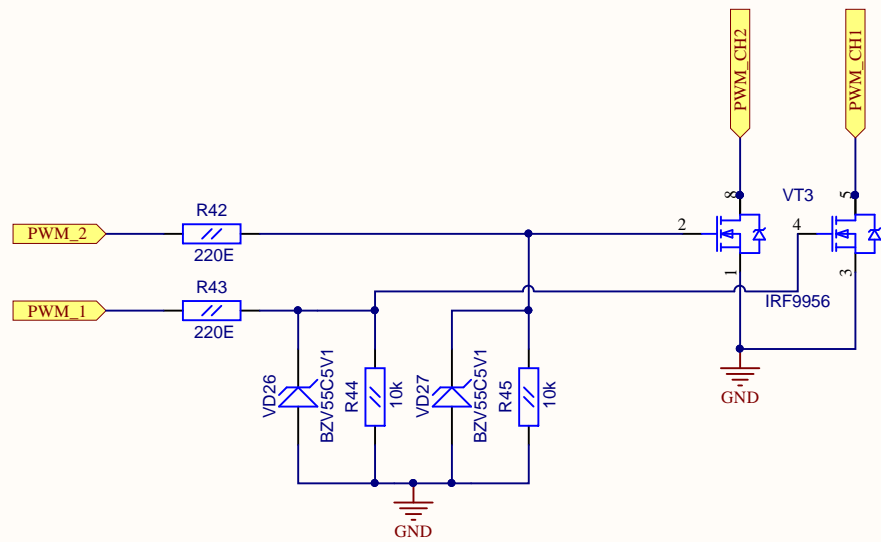
Title		
Size	Number	Revision
A4		
Date:	31.08.2017	Sheet of
File:	E:\Projects_Files\...\Input_analog.SchDoc Drawn By:	



Title		
Size	Number	Revision
A4		
Date:	31.08.2017	Sheet of
File:	E:\Projects_Files\...\MCU.SchDoc	Drawn By:



Title		
Size	Number	Revision
A4		
Date:	31.08.2017	Sheet of
File:	E:\Projects Files\...\Power.SchDoc	Drawn By:



Title			
Size	Number		Revision
A4			
Date:	31.08.2017	Sheet	of
File:	E:\Projects Files\...\PWM.SchDoc	Drawn By:	