

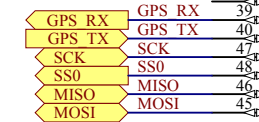
1

2

3

4

DD1A



P0[0]/RD1/TXD3/SDA1
P0[1]/TD1/RXD3/SCL1
P0[2]/TXD0/AD0[7]
P0[3]/RXD0/AD0[6]

P0[6]/I2SRX_SDA/SSEL1/MAT2[0]
P0[7]/I2STX_CLK/SCK1/MAT2[1]
P0[8]/I2STX_WS/MISO1/MAT2[2]
P0[9]/I2STX_SDA/MOSI1/MAT2[3]
P0[10]/TXD2/SDA2/MAT3[0]
P0[11]/RXD2/SCL2/MAT3[1]
P0[15]/TXD1/SCK0/SCK
P0[16]/RXD1/SSEL0/SSEL
P0[17]/CTS1/MISO0/MISO
P0[18]/DCD1/MOSI0/MOSI

P0[22]/RTS1/TD1

P0[25]/AD0[2]/I2SRX_SDA/TXD3
P0[26]/AD0[3]/AOUT/RXD3

P0[29]/USB_D+
P0[30]/USB_D-

P1[0]/ENET_TXD0
P1[1]/ENET_TXD1
P1[4]/ENET_TX_EN
P1[8]/ENET_CR_S
P1[9]/ENET_RXD0
P1[10]/ENET_RXD1
P1[14]/ENET_RX_ER
P1[15]/ENET_REF_CLK

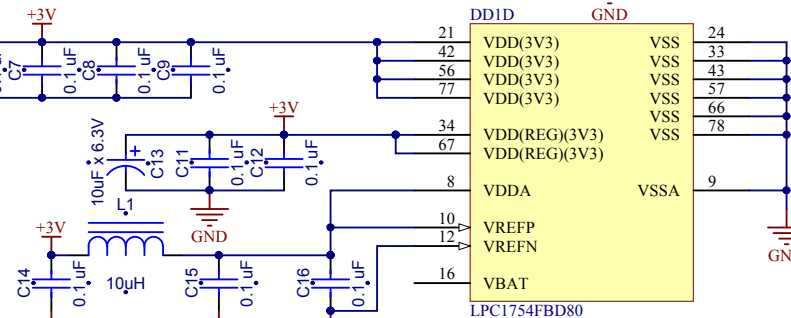
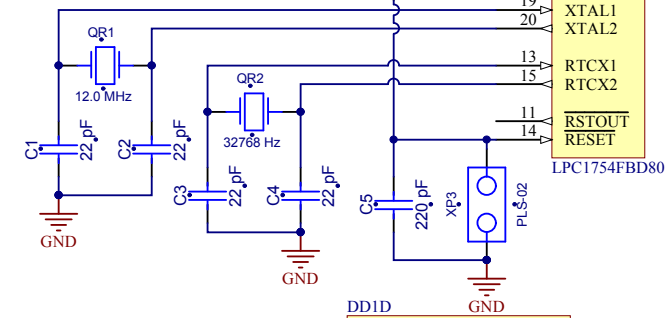
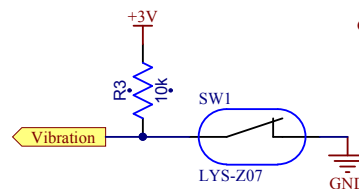
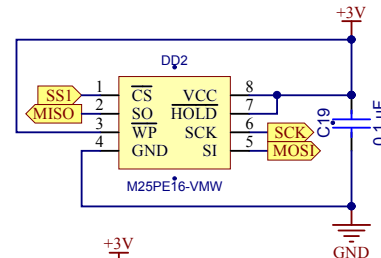
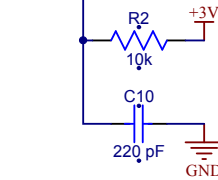
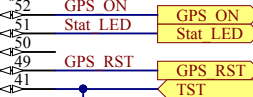
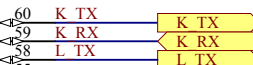
P1[18]/USB_UP_LED/PWM1[1]/CAP1[0]
P1[19]/MCOA0/USB_PPWR/CAP1[1]
P1[20]/MCI0/PWM1[2]/SCK0

P1[22]/MCOB0/USB_PPWR/MAT1[0]
P1[23]/MCI1/PWM1[4]/MISO0
P1[24]/MCI2/PWM1[5]/MOSI0
P1[25]/MCOA1/MAT1[1]
P1[26]/MCOB1/PWM1[6]/CAP0[0]

P1[28]/MCOA2/PCAP1[0]/MAT0[0]
P1[29]/MCOB2/PCAP1[1]/MAT0[1]
P1[30]/VBUS/AD0[4]
P1[31]/SCK1/AD0[5]

LPC1754FBD80

P2[0]/PWM1[1]/TXD1
P2[1]/PWM1[2]/RXD1
P2[2]/PWM1[3]/CTS1/TRACEDATA[3]
P2[3]/PWM1[4]/DCD1/TRACEDATA[2]
P2[4]/PWM1[5]/DSR1/TRACEDATA[1]
P2[5]/PWM1[6]/DTR1/TRACEDATA[0]
P2[6]/PCAP1[0]/R11/TRACECLK
P2[7]/RD2/RTS1
P2[8]/TD2/TXD2
P2[9]/USB_CONNECT/RXD2
P2[10]/EINT0/NMI

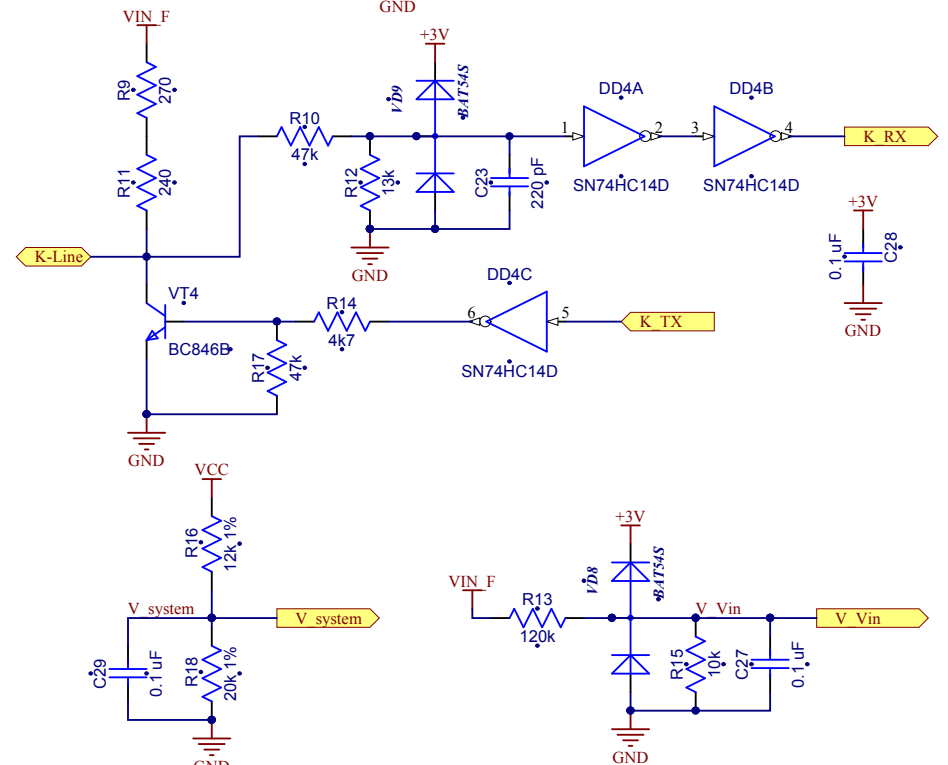
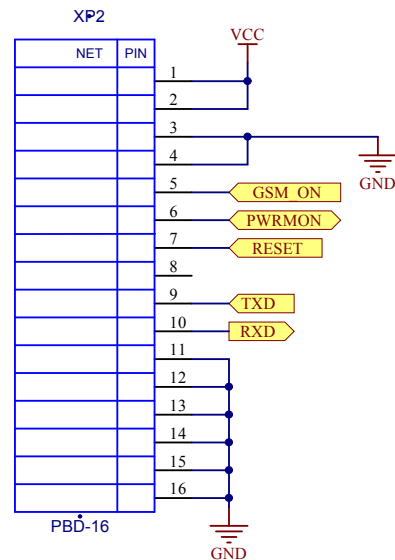
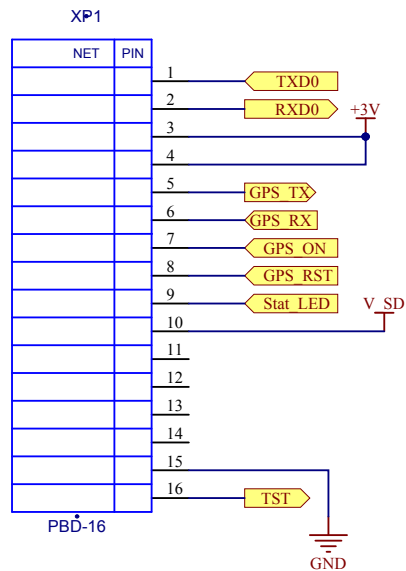
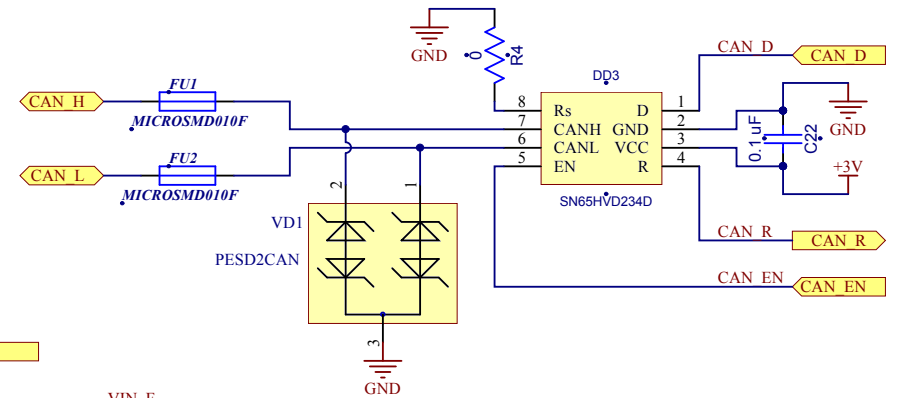
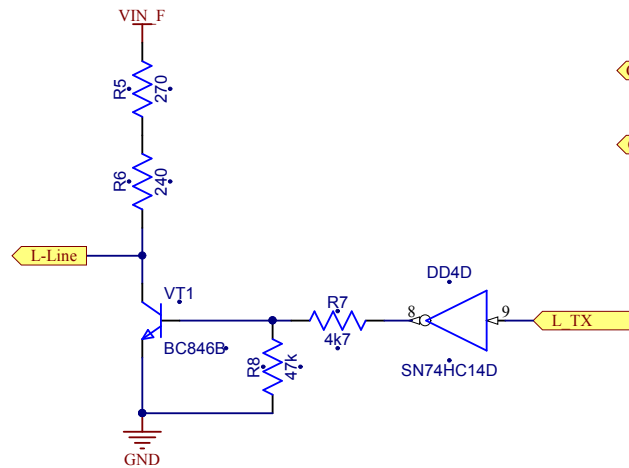
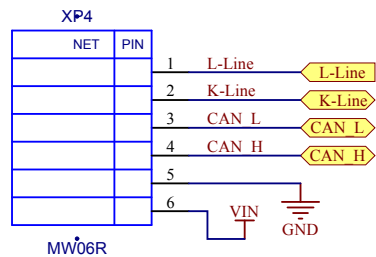


1

2

3

4



Title		
Size	Number	Revision
A4		
Date:	24.03.2014	Sheet of
File:	D:\projects\...IO.SchDoc	Drawn By:

1

2

3

4

