

VM03_hv4.PrjPcb

STATUS: TESTING

14.01.2020

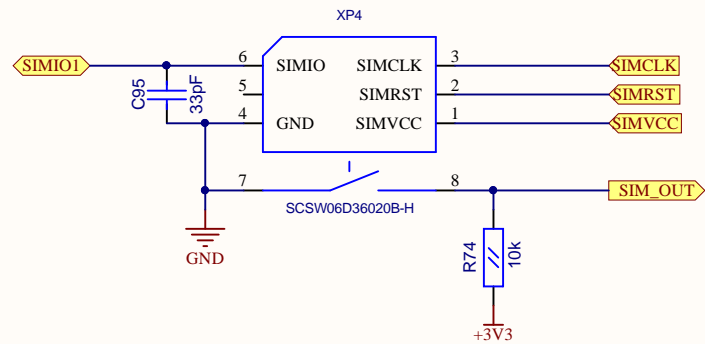
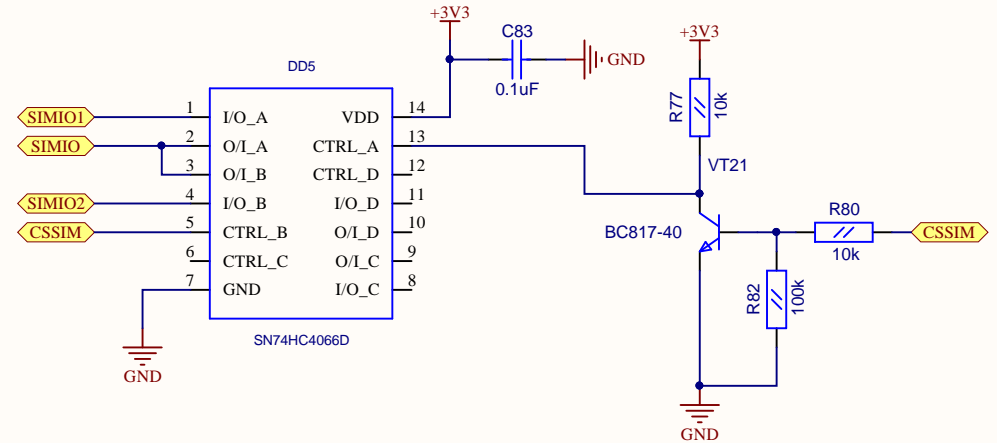
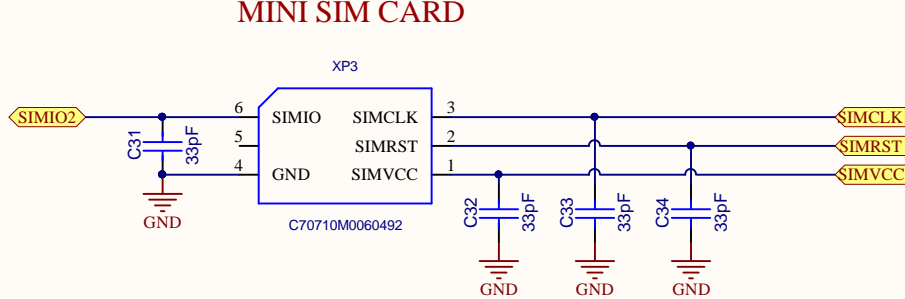
STATUS TYPES:IN WORK,TESTING,RELEASED

Page	Index	Page	Index
1	COVER PAGE	11	MAIN_POWER
2	BLOCK DIAGRAM	12	CAMERA_POWER
3	MCU_MAIN	13	BACKUP_POWER
4	MCU_POWER	14	CURRENT_METER
5	MCU_CONNECTORS	15	CLOCK
6	H3_MAIN	16	ETH_SWITCH
7	H3_POWER	17	ETH_CONNECTOR
8	MODEM_MAIN	18	WIFI_MODULE
9	MODEM_POWER	19	CAMERA_PWR_CONNECTOR
10	MODEM_CONNECTORS	20	CONNECT_CONNECTORS

Title		
Size	Number	Revision
A4		
Date:	14.01.2020	Sheet of
File:	F:\Projects_Files\...\[1]-COVER_PAGE.dwg	
	Drawn By:	

[10]-MODEM_CONNECTORS.SchDoc

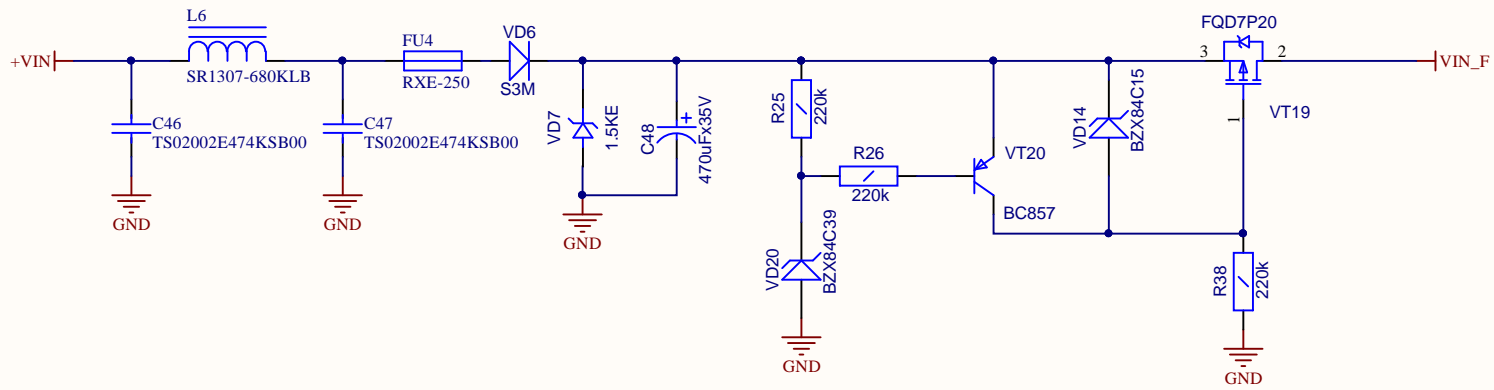
MINI SIM CARD



Title		
Size	Number	Revision
A4		
Date:	14.01.2020	Sheet of
File:	F:\Projects_Files\.[10]-MODEM_CONNECTORS.SchDoc	

[11]-MAIN_POWER.SchDoc

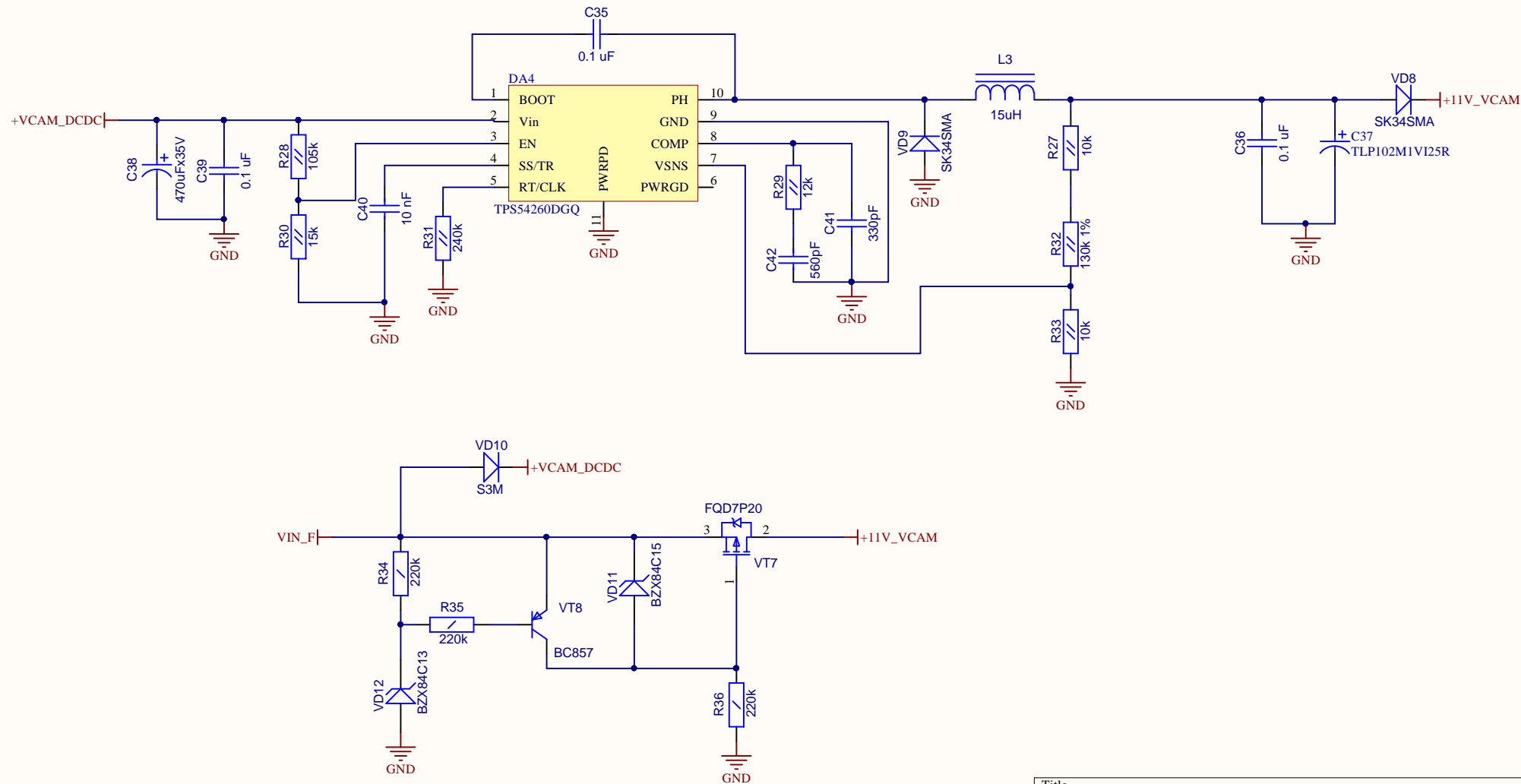
PROTECTION CURCUIT



Title			
Size	Number		Revision
A4			
Date:	14.01.2020	Sheet	of
File:	F:\Projects Files\...\[11]-MAIN_POWER.SchDoc		

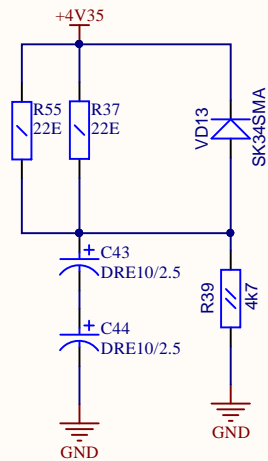
[12]-CAMERA_POWER.SchDoc

CAMERA POWER



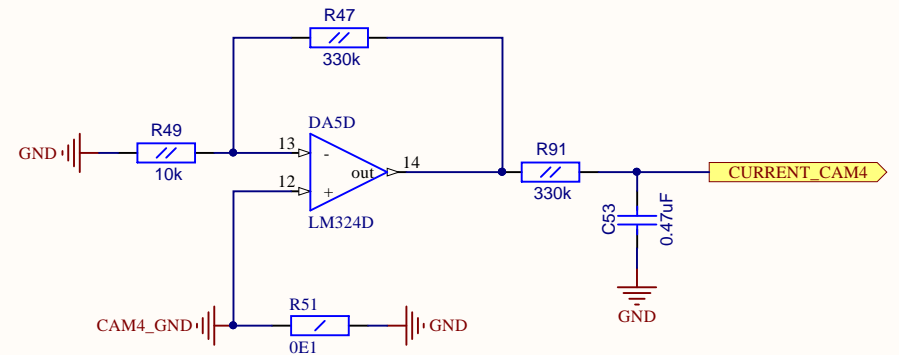
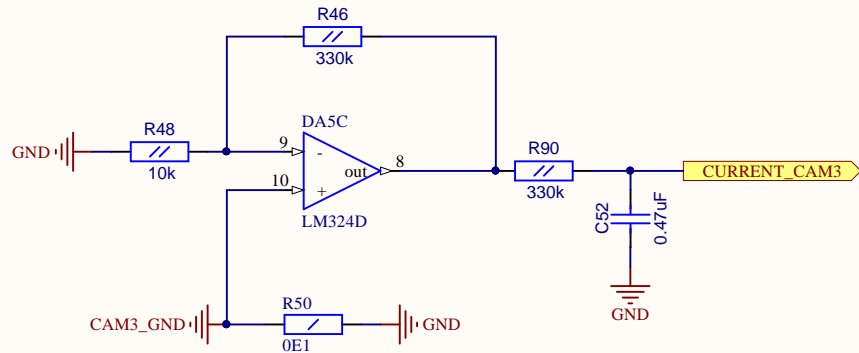
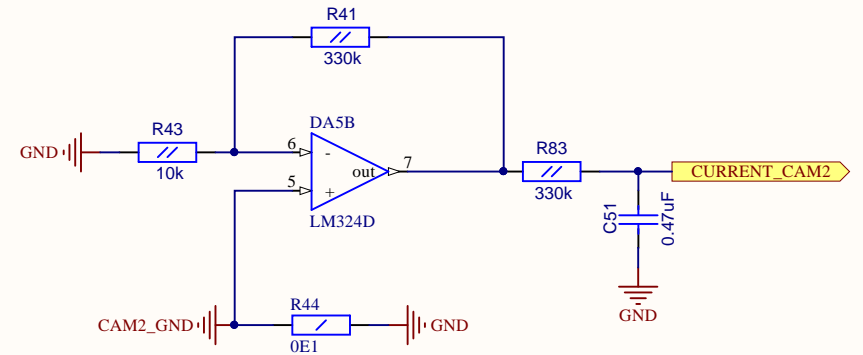
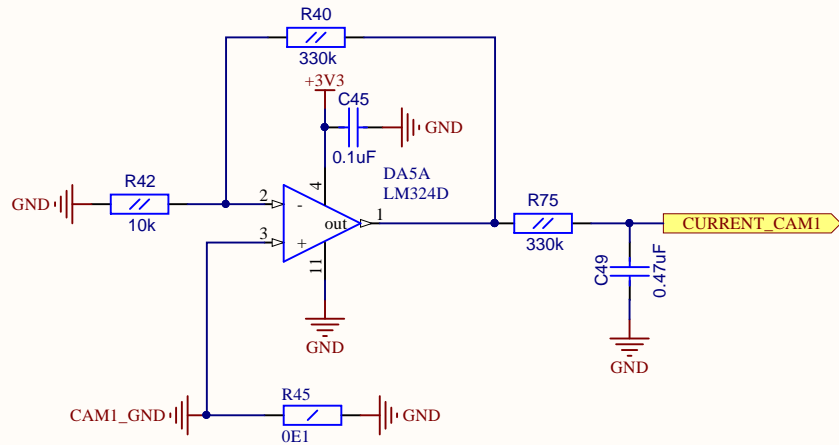
Title		
Size	Number	Revision
A4		
Date:	14.01.2020	Sheet of
File:	F:\Projects_Files\...\[12]-CAMERA_POWER.SchDoc	

[13]-BACKUP_POWER.SchDoc



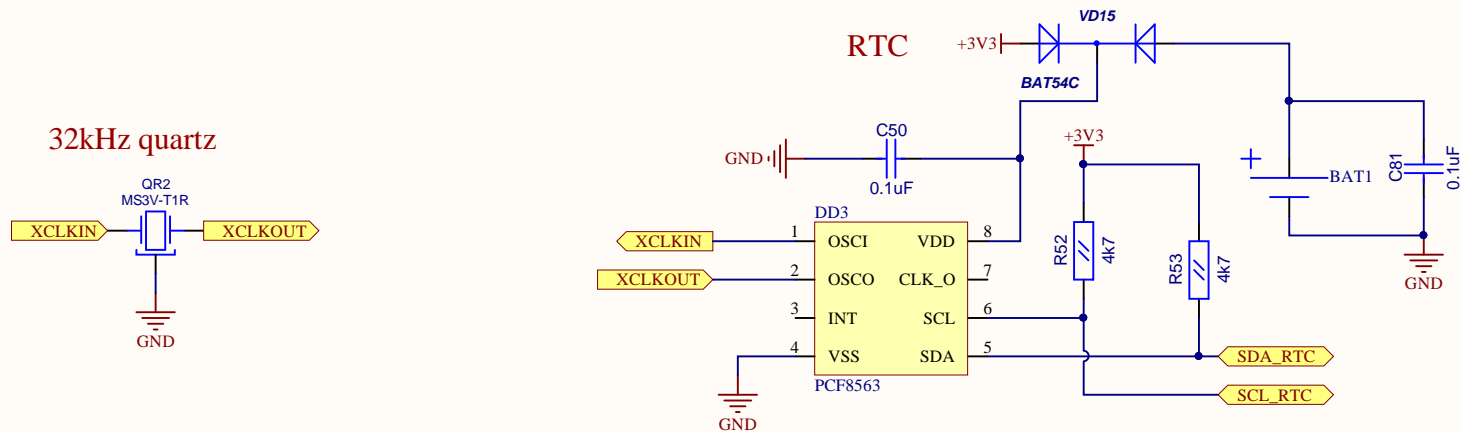
Title		
Size	Number	Revision
A4		
Date:	14.01.2020	Sheet of
File:	F:\Projects_Files\...\[13]-BACKUP_POWER.SchDoc	

[14]-CURRENT_METER.SchDoc



Title		
Size	Number	Revision
A4		
Date:	14.01.2020	Sheet of
File:	F:\Projects_Files\.[14]-CURRENT_METER.SchDoc	

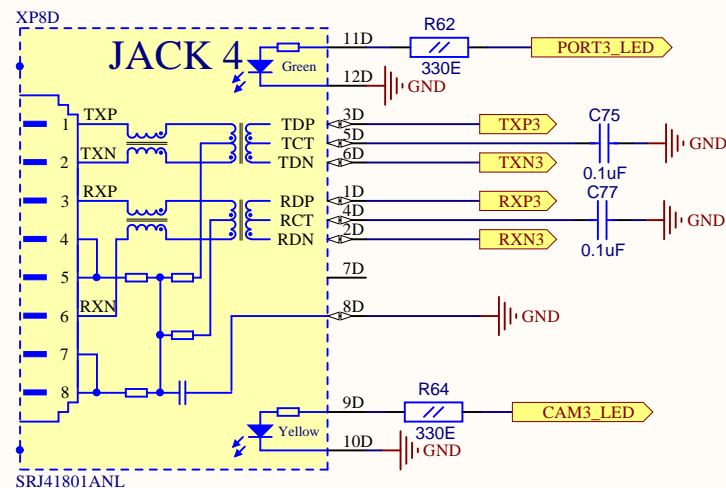
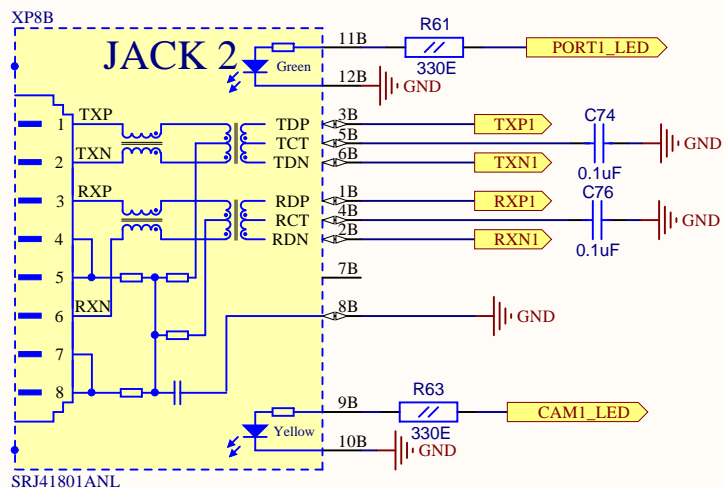
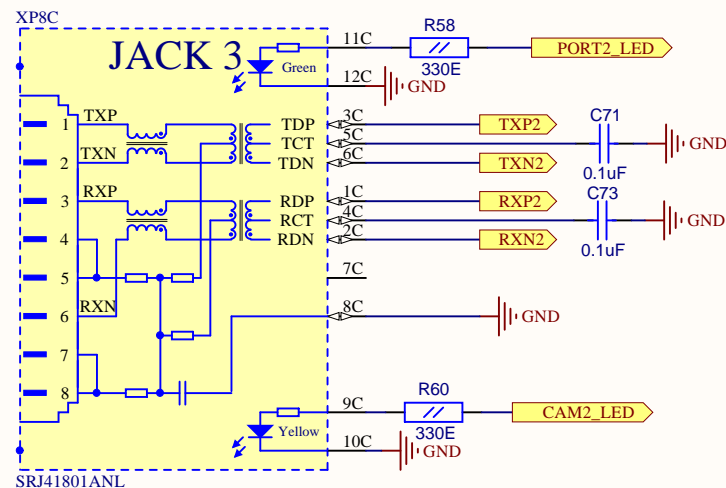
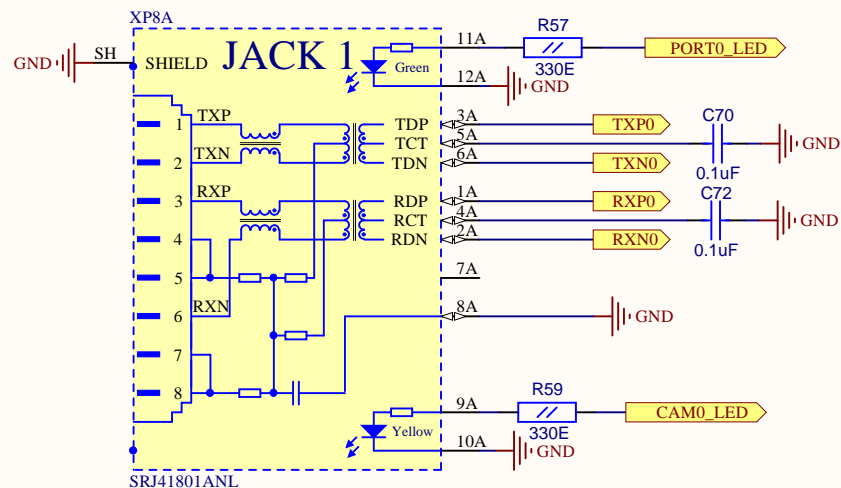
[15]-CLOCK.SchDoc



Title		
Size	Number	Revision
A4		
Date:	14.01.2020	Sheet of
File:	F:\Projects_Files\...\[15]-CLOCK.SchDoc Drawn By:	

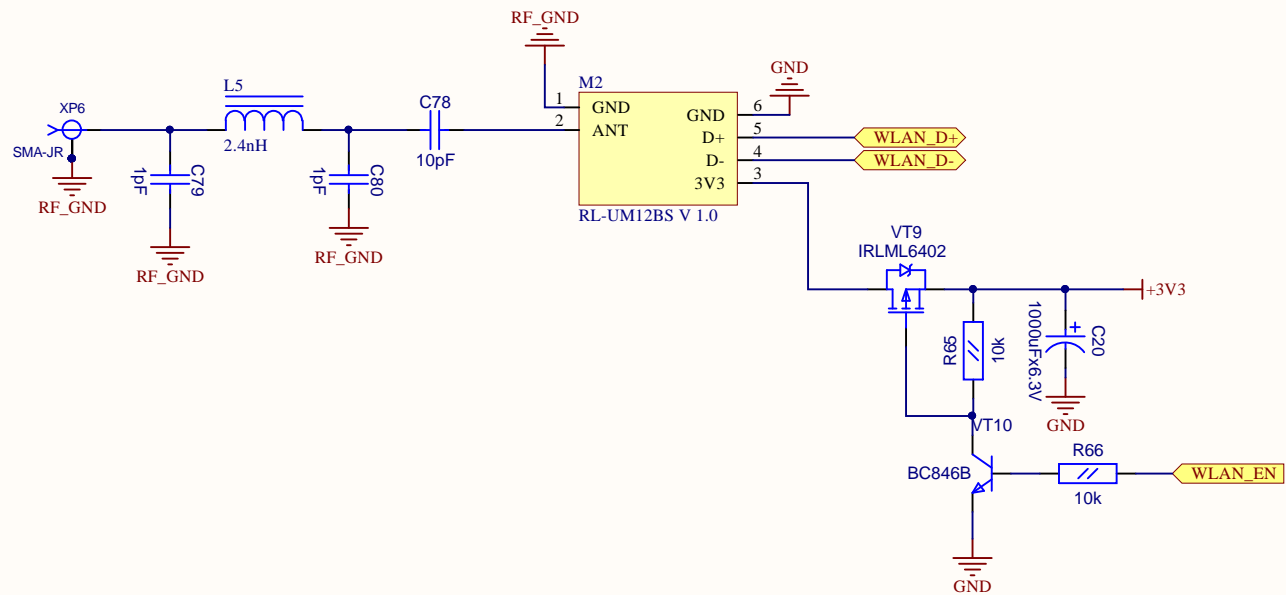
Title		
Size A4	Number	Revision
Date:	14.01.2020	Sheet of
File:	F:\Projects_Files\...\[16]-ETH_SWITCH_SCHROEN By:	

[17]-ETH_CONNECTOR.SchDoc



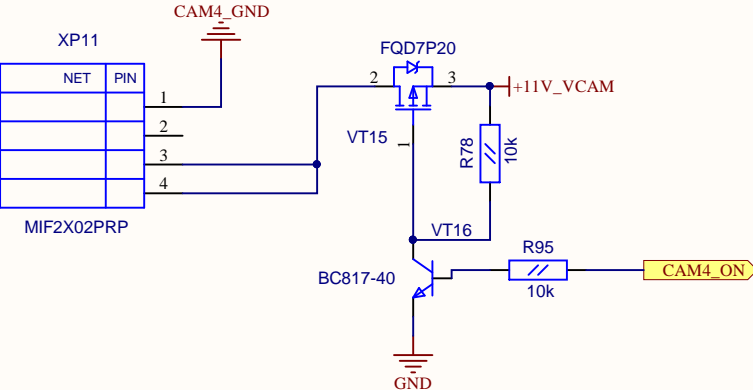
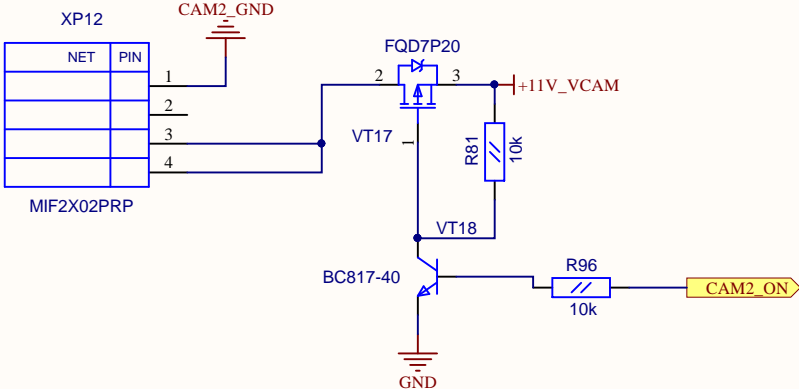
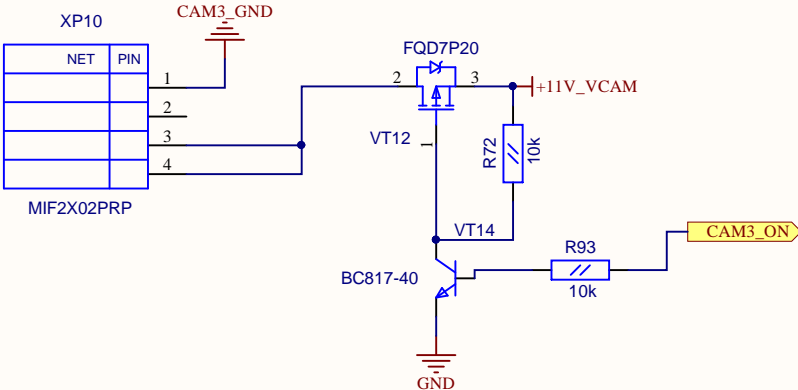
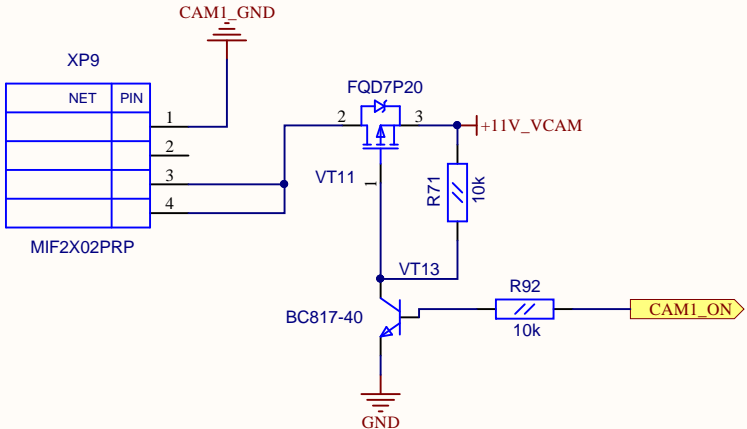
Title		
Size	Number	Revision
A4		
Date:	14.01.2020	Sheet of
File:	F:\Projects Files\.[17]-ETH_CONNECTOR.SchDoc	

[18]-WIFI_MODULE.SchDoc



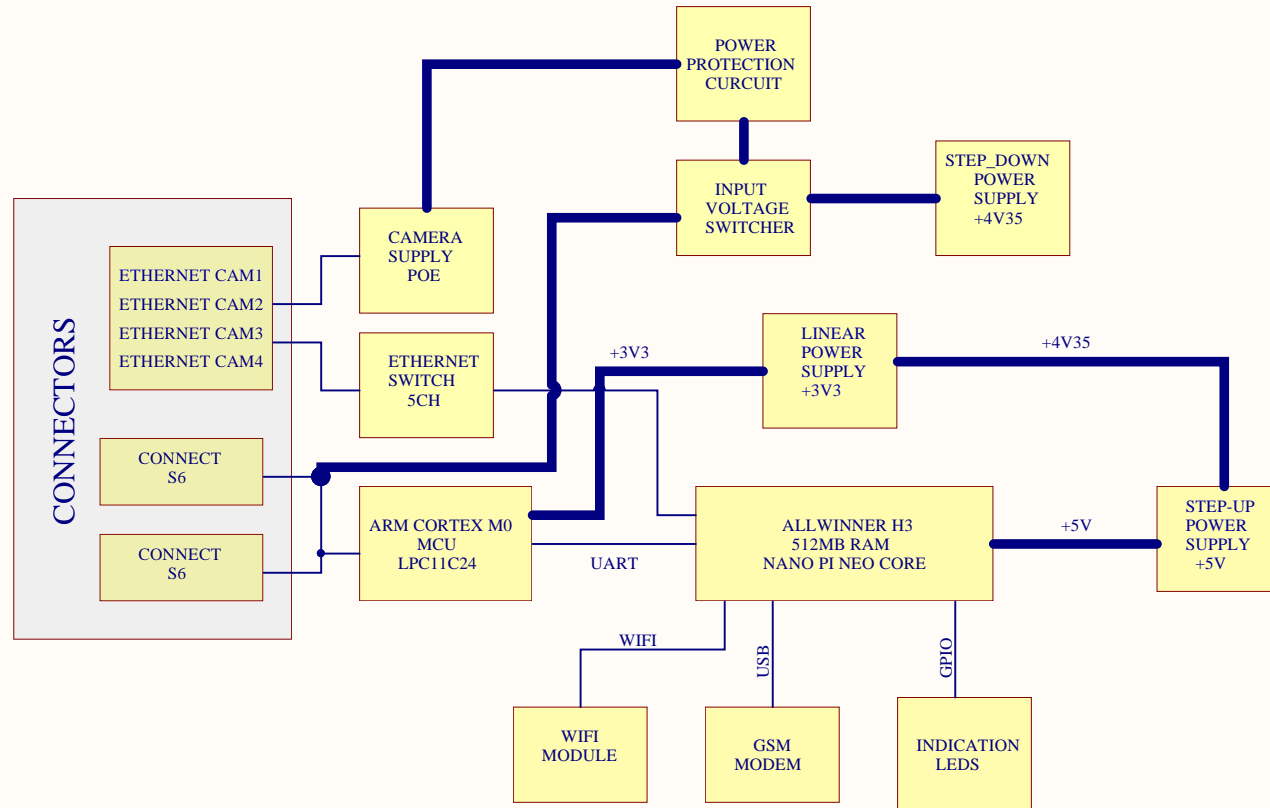
Title		
Size	Number	Revision
A4		
Date:	14.01.2020	Sheet of
File:	F:\Projects Files\...\[18]-WIFI_MODULE.SchDoc	

[19]-CAMERA_PWR_CONNECTORS.SchDoc



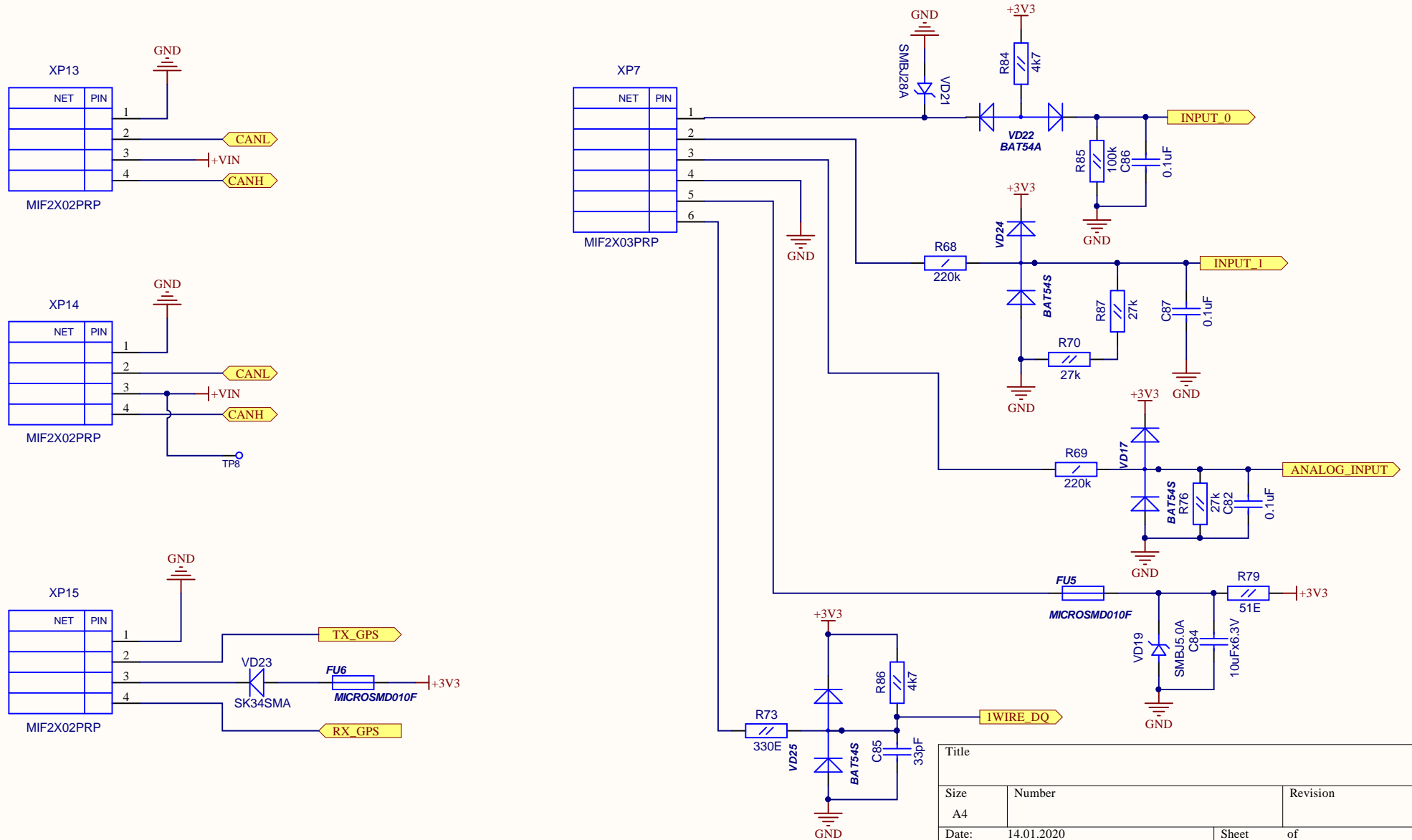
Title		
Size	Number	Revision
A4		
Date:	14.01.2020	Sheet of
File:	F:\Projects Files\.[19]-CAMERA_PWR_CONNECTORS.SchDoc	

[2]-BLOCK_DIAGRAM.SchDoc



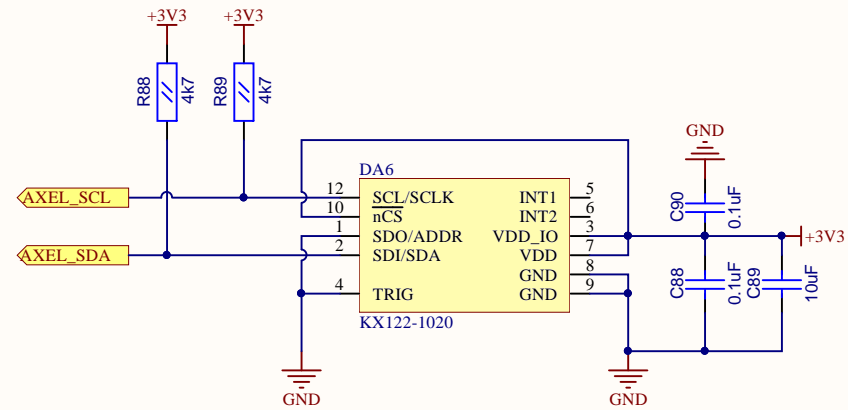
Title		
Size	Number	Revision
A4		
Date:	14.01.2020	Sheet of
File:	F:\Projects Files\...\[2]-BLOCK_DIAGRAM.SchDoc	

[20]-CONNECT_CONNECTORS.SchDoc



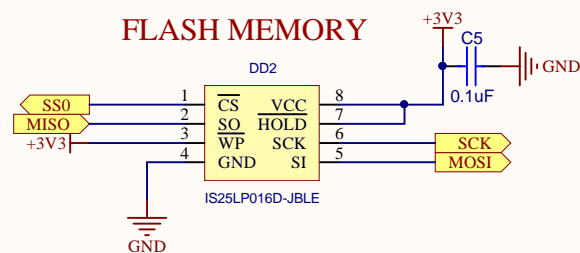
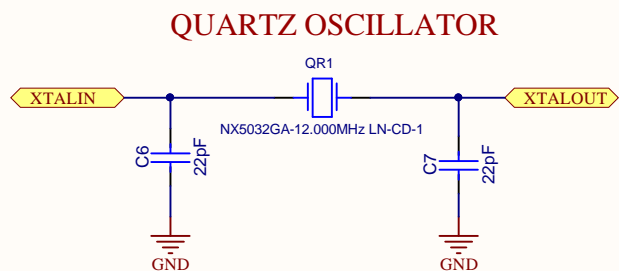
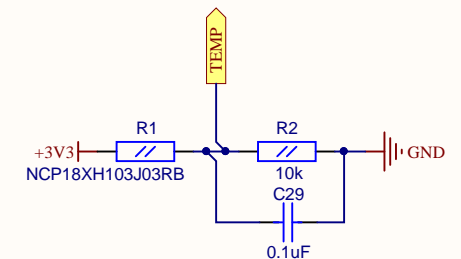
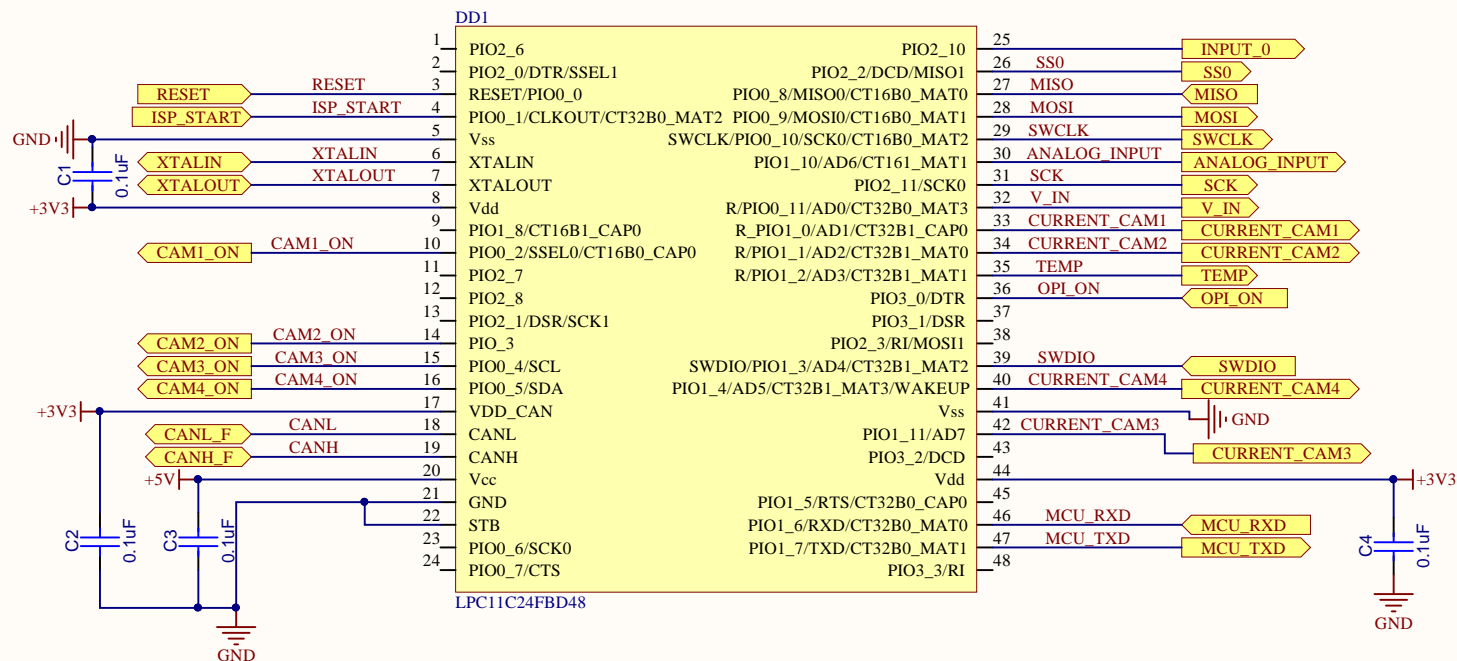
Title		
Size	Number	Revision
A4		
Date:	14.01.2020	Sheet of
File:	F:\Projects Files\.[20]-CONNECT_CONNECTORS.SchDoc	

[21]-AXEL.SchDoc



Title		
Size A4	Number	Revision
Date: 14.01.2020	Sheet of	
File: F:\Projects_Files\...\[21]-AXEL.SchDoc	Drawn By:	

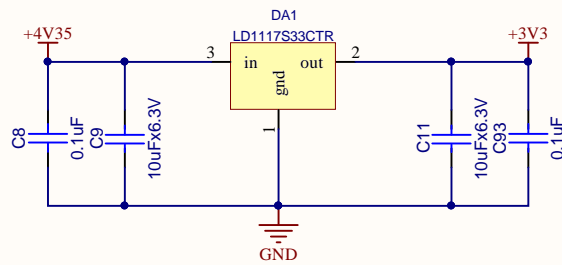
[3]-MCU_MAIN.SchDoc



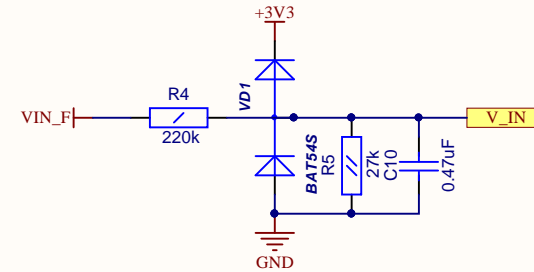
Title		
Size A4	Number	Revision
Date:	14.01.2020	Sheet of
File:	F:\Projects_Files\.[3]-MCU_MAIN.SchDoc	
Drawn By:		

[4]-MCU_POWER.SchDoc

+3V3 LINEAR POWER SUPPLY



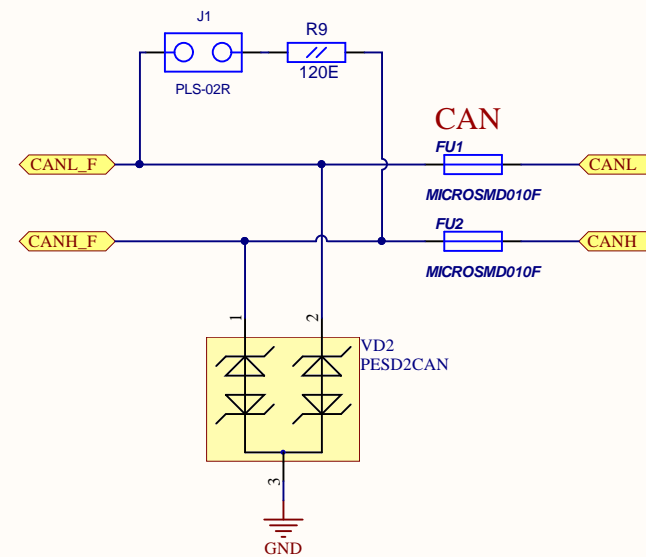
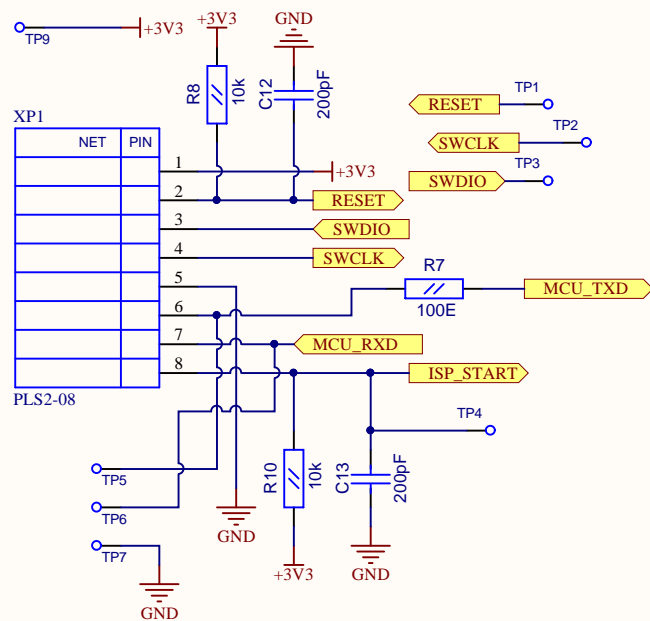
INPUT VOLTAGE MEASURE



Title		
Size A4	Number	Revision
Date: 14.01.2020	Sheet	of
File: F:\Projects_Files\...\[4]-MCU_POWER.SchDoc		

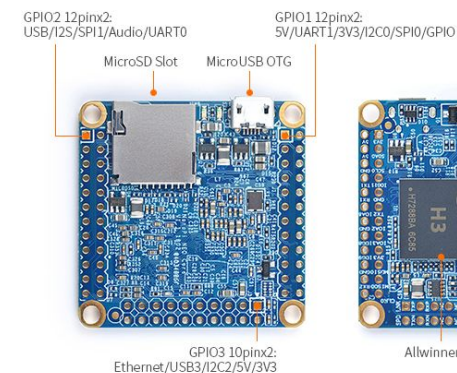
Download By:

[5]-MCU_CONNECTORS.SchDoc



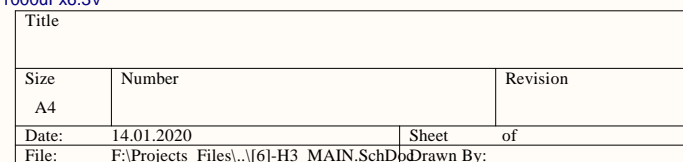
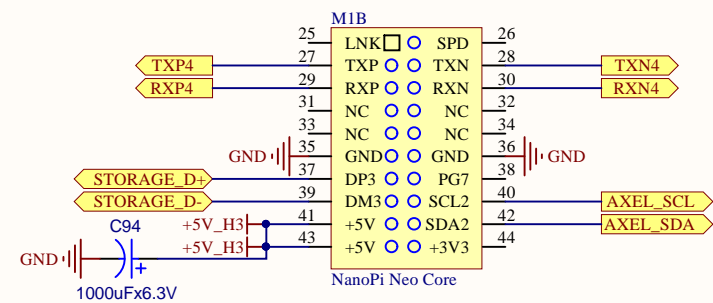
Title		
Size	Number	Revision
A4		
Date:	14.01.2020	Sheet of
File:	F:\Projects Files\...[5]-MCU_CONNECTORS.SchDoc	

- **Layout & Interface**



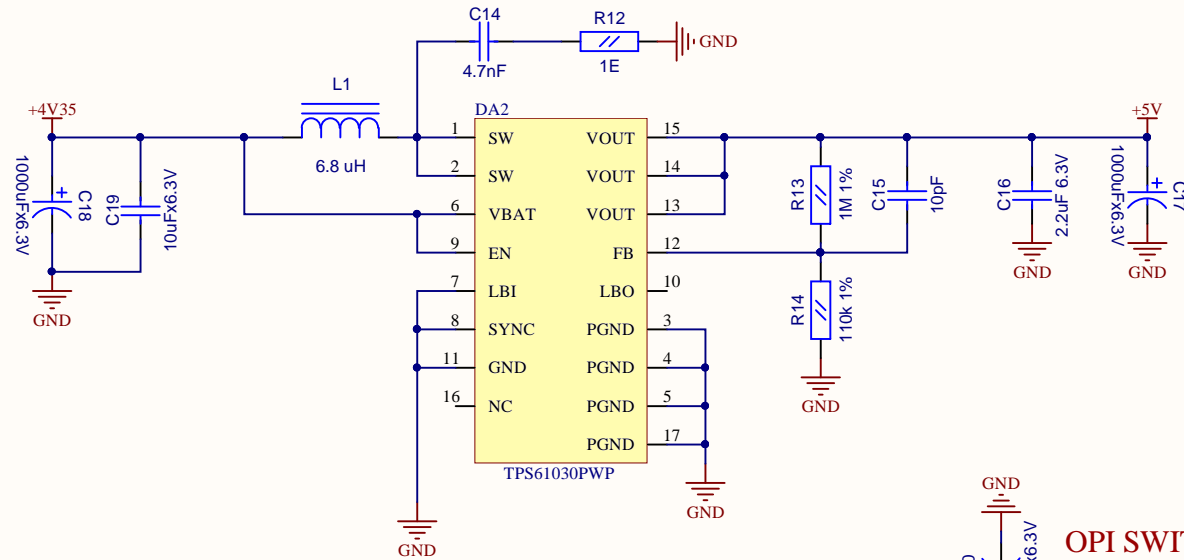
The diagram shows the NanoPi NEO Core board with various pin headers. The connections are as follows:

- Top Left Header:**
 - OUT: 5V
 - DP1: USB1
 - DM1: USB2
 - DP2: USB2
 - DM2: IR
 - BL1: SPDIF
 - PA17: LR
 - LCK: LR
 - BCK: LR
 - DOU: LR
 - FIN: LR
 - GND: GND
- Top Right Header:**
 - 5V: 5V
 - I2C0: I2C0
 - IN/OUT: IN/OUT
 - GND: GND
 - PG6: PG6
 - PG7: PG7
 - PA6: PA6
 - PG8: PG8
 - PG9: PG9
 - GND: GND
 - PA1: PA1
 - PC3: PC3
- Bottom Left Header:**
 - LINEIN: LINEIN
 - LINEOUT: LINEOUT
 - UART0: UART0
 - 5V: 5V
 - GND: GND
- Bottom Right Header:**
 - TX: TX
 - RX: RX
 - OUT: OUT
 - GND: GND
- Bottom Header:**
 - LNK: LNK
 - TXP: TXP
 - NC: NC
 - NC: NC
 - NC: NC
 - GND: GND
 - DP3: DP3
 - DM3: DM3
 - OUT: OUT
 - 5V: 5V
 - I2C2: I2C2
 - NET: NET
 - NET: NET
 - SPD: SPD
 - TXN: TXN
 - R2N1: R2N1
 - NC: NC
 - NC: NC
 - PG7: PG7
 - SCL2: SCL2
 - SDA2: SDA2
 - DVA: DVA
 - GND: GND

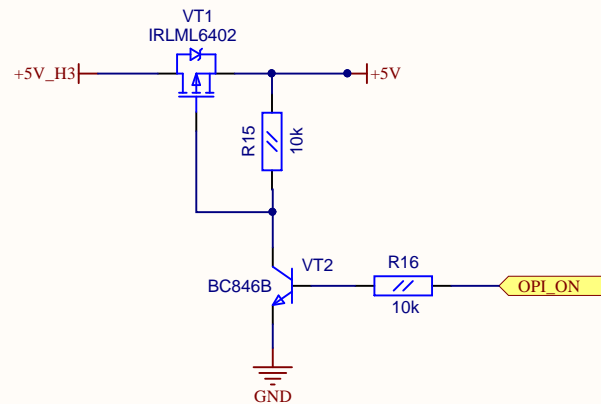


[7]-H3_POWER.SchDoc

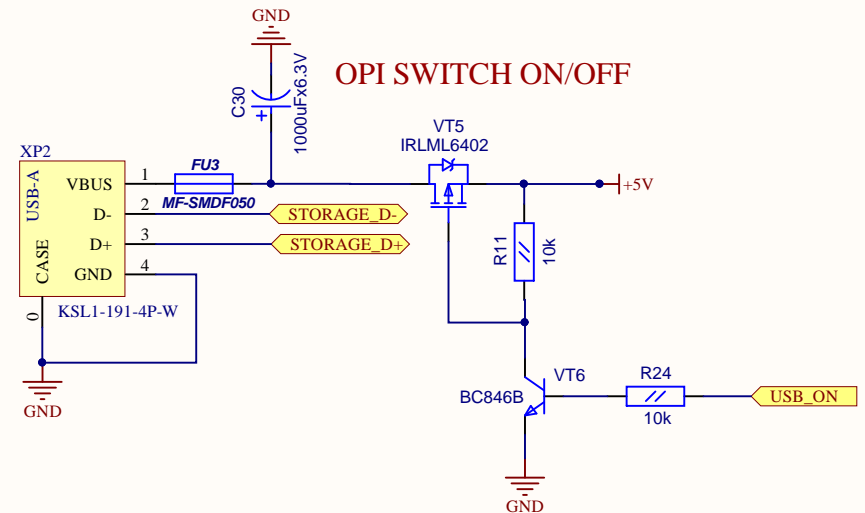
STEP-UP CONVERTER 4V3 to 5V



OPI SWITCH ON/OFF

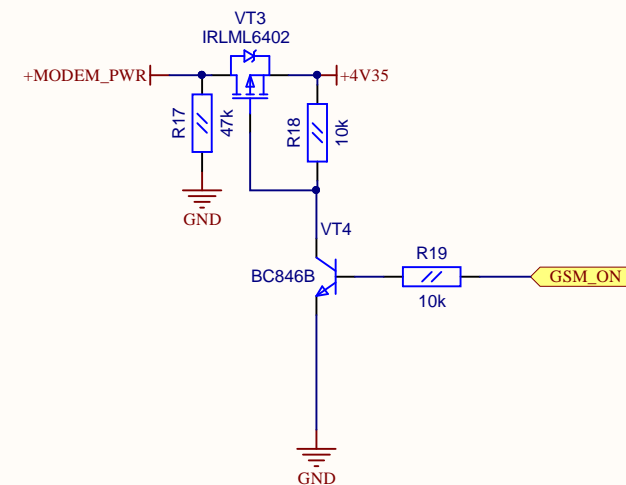
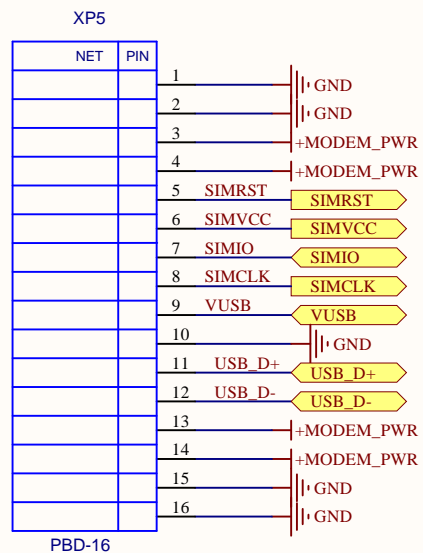


OPI SWITCH ON/OFF



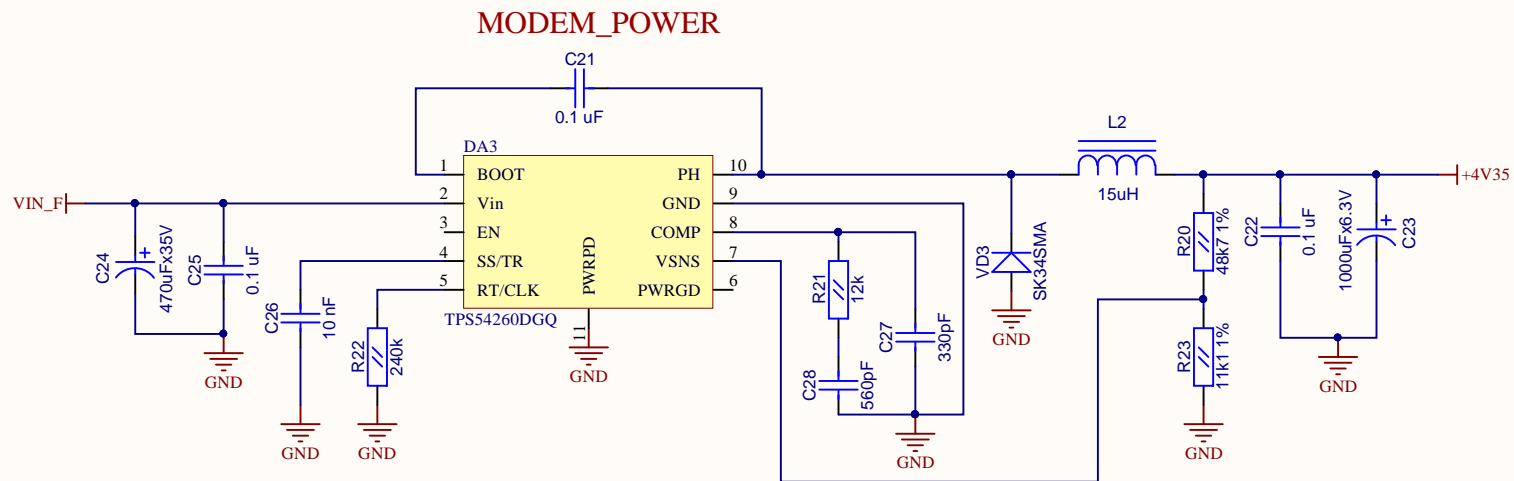
Title		
Size	Number	Revision
A4		
Date:	14.01.2020	Sheet of
File:	F:\Projects Files\...\[7]-H3_POWER.SchDoc	

[8]-MODEM_MAIN.SchDoc



Title		
Size	Number	Revision
A4		
Date:	14.01.2020	Sheet of
File:	F:\Projects Files\...\[8]-MODEM_MAIN.SchDoc By:	

[9]-MODEM_POWER.SchDoc



Title		
Size	Number	Revision
A4		
Date:	14.01.2020	Sheet of
File:	F:\Projects Files\..[9]-MODEM_POWER.SchDoc	