

Parallel Breadth First Search

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Abstract—This is an $n - 1$ level abstract

I. INTRODUCTION

A. Subsection Heading Here

Subsection text here.

1) Subsubsection Heading Here: Subsubsection text here.

II. RELATED WORK

A. Terms

- T_s is the running time for the most efficient serial algorithm.
- $T_1 \equiv \text{WORK}$ is the running time for a parallel algorithm running on one processing elements.
- T_p is the running time for a parallel algorithm running on p processing elements.
- $T_\infty \equiv \text{SPAN}$ is the critical path for a parallel algorithm or the time it takes given infinite processing elements.
- $W_p \leq pT_p$ is the total amount of work for p processing elements. Idle processing elements do not count towards W_p .

B. Level-Synchronous BFS Algorithms

One approach for BFS is to find all nodes at distance $0 \leq d \leq |V|$ from the source before any nodes at distance $d' > d$.

The general approach for Level-Synchronous BFS can be seen in ??.

[1] uses data structures called penants and bags

Definition 2.1: soup

III. PPS(RENAME)

IV. BFS(RENAME)

BFS algo

```
parallel for u gets 0 .. n - 1
parallel for i = 0 to p-1
if i = 0
var{offset} = 0
else
var{offset} =  $D[\frac{iN}{p}]$ 
var{offset}
```

V. ANALYSIS

VI. CONCLUSION

VII. FUTURE WORK

TODO "WRITE" these instead of what we have now.

Add labels and links for each of these

- Optimize for false sharing
 - From parallel Prefix Sum :- pretty easy, just set appropriate GRAIN-Size and align properly.
 - For Dist array.
 - can use radix sort for large levels where $\text{num degrees} = \Omega(n^c)$
 - Each proc can create an unsorted list of all cache lines its degrees touch, then each proc takes $\frac{\text{cachelines}}{p}$ cache lines
 - For find sublist
- Optimize for cache-misses
- Make processor-oblivious
- Make cache-oblivious? Is it already?
- Optimize T_p, T_1, W_p for PRAM model

REFERENCES

- [1] C. E. Leiserson and T. B. Schardl, "A work-efficient parallel breadth-first search algorithm (or how to cope with the nondeterminism of reducers)," in *Proceedings of the 22nd ACM symposium on Parallelism in algorithms and architectures*, ser. SPAA '10. New York, NY, USA: ACM, 2010, pp. 303–314. [Online]. Available: <http://doi.acm.org/10.1145/1810479.1810534>

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SERIAL-BFS(  $V, \Gamma, s$  )
1)  for each vertex  $u \in V$ 
2)     $\text{DIST}[u] \leftarrow \infty$ 
3)   $\text{DIST}[s] \leftarrow 0$ 
4)  while  $Q \neq \emptyset$  do
5)     $u \leftarrow \text{DEQUEUE}( Q )$ 
6)    for each vertex  $v \in \Gamma(u)$  do
7)      if  $\text{DIST}[v] = \infty$  then
8)         $\text{DIST}[v] \leftarrow \text{DIST}[u] + 1$ 
9)         $\text{ENQUEUE}( Q, v )$ 

```

Fig. 1. General approach for Level-Synchronous BFS

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PARALLEL-PREFIX-SUM(  $V, \text{GRAIN-SIZE}$  )
 $V[0 : n - 1]$  is a sequence of  $n$  integers. This function replaces  $V[i]$  with  $\sum_{0 \leq j \leq i} V[j]$ 
1)  if  $|V| > 1$  then
2)     $\text{PARALLEL-PREFIX-SUM-UP}( V, \text{GRAIN-SIZE}, 0, n )$ 
3)     $\text{PARALLEL-PREFIX-SUM-DOWN}( V, \text{GRAIN-SIZE}, 0, n, \text{false}, 0 )$ 

```

Fig. 2.

```

PARALLEL-PREFIX-SUM-UP(  $V, \text{GRAIN-SIZE}, \text{start}, \text{limit}$  )
1)   $\text{size} \leftarrow \text{limit} - \text{start}$ 
2)  if  $\text{size} \leq \text{GRAIN-SIZE}$  then
3)    return  $\text{SERIAL-PREFIX-SUM}( V, \text{start}, \text{limit} )$ 
4)  else
5)     $\text{mid} \leftarrow \lfloor \frac{\text{start} + \text{limit}}{2} \rfloor$ 
6)     $x \leftarrow \text{spawn } \text{PARALLEL-PREFIX-SUM-UP}( V, \text{GRAIN-SIZE}, \text{start}, \text{mid} )$ 
7)     $y \leftarrow \text{PARALLEL-PREFIX-SUM-UP}( V, \text{GRAIN-SIZE}, \text{mid}, \text{limit} )$ 
8)    sync
9)     $V[\text{limit} - 1] \leftarrow x + y$ 
10) return  $x + y$ 

```

Fig. 3.

```

PARALLEL-PREFIX-SUM-DOWN(  $V, \text{start}, \text{limit}, \text{rightmost\_excluded}, \text{partial\_sum}$  )
1)   $\text{size} \leftarrow \text{limit} - \text{start}$ 
2)  if  $\text{size} \leq \text{GRAIN-SIZE}$  then
3)     $\text{SERIAL-PREFIX-SUM-DOWN}( V, \text{start}, \text{limit}, \text{partial\_sum}, \text{rightmost\_excluded} )$ 
4)    return
5)  else
6)     $\text{mid} \leftarrow \lfloor \frac{\text{start} + \text{limit}}{2} \rfloor$ 
7)     $\text{sum\_left} \leftarrow V[\text{mid} - 1]$ 
8)     $\text{spawn } \text{PARALLEL-PREFIX-SUM-DOWN}( V, \text{GRAIN-SIZE}, \text{start}, \text{mid}, \text{false}, \text{partial\_sum} )$ 
9)    if  $\neg \text{rightmost\_excluded}$  then
10)      $V[\text{limit} - 1] \leftarrow V[\text{limit} - 1] + \text{partial\_sum}$ 
11)    if  $\text{limit} - \text{mid} > 1$ 
12)      $\text{PARALLEL-PREFIX-SUM-DOWN}( V, \text{GRAIN-SIZE}, \text{mid}, \text{limit}, \text{true}, \text{partial\_sum} + \text{sum\_left} )$ 

```

Fig. 4.

```

SERIAL-PREFIX-SUM(  $V, \text{start}, \text{limit}$  )
1)   $V[\text{start}] \leftarrow V[\text{start}] + \text{partial\_sum}$ 
2)  for  $i \leftarrow \text{start} + 1$  to  $\text{limit} - 1$  do
3)     $V[i] \leftarrow V[i] + V[i - 1]$ 
4)  return  $V[\text{limit} - 1]$ 

```

Fig. 5.

```

SERIAL-PREFIX-SUM-DOWN(  $V, \text{start}, \text{limit}, \text{rightmost\_excluded}, \text{partial\_sum}$  )
1)  for  $i \leftarrow \text{start}$  to  $\text{limit} - 2$  do
2)     $V[\text{start}] \leftarrow V[\text{start}] + \text{partial\_sum}$ 
3)  if  $\neg \text{rightmost\_excluded}$  then
4)     $V[\text{limit} - 1] \leftarrow V[\text{limit} - 1] + \text{partial\_sum}$ 

```

Fig. 6.

PARALLEL-BFS($V, \Gamma, \gamma, s, p_{max}$)

$V[0 : n - 1]$ are the n nodes in the graph. $\Gamma[u]$ is the sequence of adjacent nodes to node u . $\gamma[u] = |\Gamma[u]|$. s is the source vertex from which distance is calculated. p_{max} is the maximum number of processors to use. Returns $DIST[0 : n - 1]$ which represents the distance from s to each vertex.

```

1)  parallel for  $u \leftarrow 0$  to  $n - 1$  do
2)       $DIST[u] \leftarrow \infty$ 
3)       $OWNER[u] \leftarrow \infty$ 
4)   $DIST[s] \leftarrow 0$ 
5)   $OWNER[s] \leftarrow 0$ 
6)  if  $\gamma[s] = 0$  then
7)      return  $DIST$ 
8)   $INPUT \leftarrow \text{array}[0 : 0]$ 
9)   $INPUT[0] \leftarrow s$ 
10)  $LEVEL \leftarrow 0$ 
11)  $p \leftarrow 1$ 
12) while  $|INPUT| \neq 0$  do
13)      $LEVEL \leftarrow LEVEL + 1$ 
14)      $N \leftarrow |INPUT|$ 
15)      $WORK \leftarrow \text{array}[0 : N - 1]$ 
16)     parallel for  $u \leftarrow 0$  to  $N - 1$  do
17)          $WORK[u] \leftarrow \gamma[INPUT[u]]$ 
18)     PARALLEL-PREFIX-SUM(  $WORK, \lfloor \frac{N}{p} \rfloor$  )
19)      $W \leftarrow WORK[N - 1]$ 
20)      $p \leftarrow \text{MIN}(p_{max}, W)$ 
21)      $SUBLIST \leftarrow \text{FIND-SUBLIST}(WORK, W, p)$ 
22)      $Q \leftarrow \text{LEVEL-TO-QUEUES}(INPUT, WORK, SUBLIST, DIST, \Gamma, \gamma, W, p, LEVEL)$ 
23)      $SIZES \leftarrow \text{array}[0 : p - 1]$ 
24)     parallel for  $i \leftarrow 0$  to  $p - 1$  do
25)          $Q\text{-NEW} \leftarrow \text{queue}$ 
26)         for  $v$  in  $Q[i]$  do
27)             if  $OWNER[v] = i$  then
28>                  $Q\text{-NEW.ENQUEUE}(v)$ 
29)          $Q[i] \leftarrow Q\text{-NEW}$ 
30)          $SIZES[i] \leftarrow |Q[i]|$ 
31)     PARALLEL-PREFIX-SUM(  $SIZES, 1$  )
32)      $INPUT \leftarrow \text{array}[0 : SIZES[p - 1]]$ 
33)     parallel for  $i \leftarrow 0$  to  $p - 1$  do
34)         if  $i = 0$  then
35>              $OFFSET \leftarrow 0$ 
36)         else
37>              $OFFSET \leftarrow SIZES[i - 1]$ 
38)         for  $j \leftarrow OFFSET$  to  $OFFSET + |Q[i]|$  do
39>              $INPUT[OFFSET] \leftarrow Q[i].\text{DEQUEUE}()$ 

```

}

Fig. 7.

FIND-SUBLIST($WORK, W, p$)

```

1)   $N \leftarrow |WORK|$ 
2)   $p_n \leftarrow \text{MIN}(p, N)$ 
3)   $SUBLIST \leftarrow \text{array}[0 : p - 1]$ 
4)   $RANGESSTART \leftarrow \text{array}[0 : p_n - 1]$ 
5)   $RANGESEND \leftarrow \text{array}[0 : p_n - 1]$ 
6)  parallel for  $i \leftarrow 0$  to  $p_n - 1$  do
7)      if  $i = 0$  then
8>           $FIRSTDEGREE \leftarrow 0$ 
9)      else
10>          $FIRSTDEGREE \leftarrow WORK[\lfloor \frac{iN}{p_n} \rfloor - 1]$ 
11>          $FIRSTDEGREE\text{NEXT} \leftarrow WORK[\lfloor \frac{(i+1)N}{p_n} \rfloor - 1]$ 
12>          $RANGESSTART[i] \leftarrow \lfloor \frac{p \cdot FIRSTDEGREE}{W} \rfloor$ 
13>          $RANGESEND[i] \leftarrow \lfloor \frac{p \cdot FIRSTDEGREE\text{NEXT}}{W} \rfloor - 1$ 
14)  parallel for  $i \leftarrow 0$  to  $p_n - 1$  do
15>      if  $RANGESSTART[i] \leq RANGESEND[i]$  then
16>          parallel for  $j \leftarrow RANGESSTART[i]$  to  $RANGESEND[i]$  do
17>               $SUBLIST[j] \leftarrow i$ 
18)  return  $SUBLIST$ 

```

{Use cores $RANGESSTART[i]$ to $RANGESEND[i]$ }

Fig. 8.

```

LEVEL-TO-QUEUES( INPUT, WORK, SUBLIST, DIST,  $\Gamma$ ,  $\gamma$ ,  $W$ ,  $p$ , LEVEL )
1)   $N \leftarrow |\text{WORK}|$ 
2)   $p_n \leftarrow \text{MIN}( p, N )$ 
3)   $Q \leftarrow \text{array}[0 : p - 1]$ 
4)  parallel for  $i \leftarrow 0$  to  $p - 1$  do
5)     $Q[i].\text{CLEAR}()$ 
6)     $\text{FIRSTDEGREE} \leftarrow \left\lfloor \frac{iW}{p} \right\rfloor$ 
7)     $\text{WORKITEMS} \leftarrow \left\lfloor \frac{(i+1)W}{p} \right\rfloor - \text{FIRSTDEGREE}$ 
8)     $\text{VERTEX} \leftarrow \text{BINARY-SEARCH-FOR-INDEX}( \text{WORK}, \left\lfloor \frac{N \cdot \text{SUBLIST}[i]}{p_n} \right\rfloor, \left\lfloor \frac{N \cdot (\text{SUBLIST}[i]+1)}{p_n} \right\rfloor, \text{FIRSTDEGREE} + 1 )$ 
9)     $\text{DEGREE} \leftarrow \text{FIRSTDEGREE} - \text{WORK}[\text{VERTEX} - 1]$ 
10)  while  $\text{WORKITEMS} > 0$  do
11)     $u \leftarrow \text{INPUT}[\text{VERTEX}]$ 
12)     $\text{LIMIT} \leftarrow \text{MIN}( \text{WORKITEMS} + \text{DEGREE}, \gamma[u] )$ 
13)    for  $j \leftarrow \text{DEGREE}$  to  $\text{LIMIT}$  do
14)       $v \leftarrow \Gamma[u][j]$ 
15)      if  $\text{DIST}[v] = \infty$  then
16)         $\text{DIST}[v] \leftarrow \text{LEVEL}$ 
17)         $\text{OWNER}[v] \leftarrow i$ 
18)        if  $\gamma[v] > 0$  then
19)           $Q[i].\text{ENQUEUE}( v )$ 
20)         $\text{WORKITEMS} \leftarrow \text{WORKITEMS} - \text{DEGREE}$ 
21)         $\text{DEGREE} \leftarrow 0$ 
22)         $\text{VERTEX} \leftarrow \text{VERTEX} + 1$ 
23)  return  $Q$ 

```

{Benign race condition. All threads write the same value.}
 {Benign race condition. One thread's value will win.}

Fig. 9.