			Martala in NO Onia	4				13. Mrz. 20
	Sen	niconductor	Models in NG-Spice					1
This table is dist	wile and in the character	a sheet is will be week						
nerchantability	for fitness for a pa	e that it will be usert irticular purpose.	il, but without any warranty; without even the implie	ed warrant	y oi			
`opyright @ 200	M Peter Kaiser: w	www.easy-asic de or	peter@easy-asic.de					
Jopyright © 200	74 Feter Kaiser, w	ww. easy-asic.de or	pelel @ easy-asic.ue					+
Model	Level	Name	Documentation		Gnucap	Hspice	Pspice	WinSpice
BJT	1	BJT	Default Spice 3F5	[1]	Griucap	пърісе	rspice	WillSpice
	2	BJT2	Delaut Opice of o	נין				
	4	VBIC						
	-							
JFET	1	JFET	Default Spice 3F5	[1]				
	2	JFET2						
MESFET	1	MES	Default Spice 3F5	[1]				
	2	MESA						
	3	MESA						
	4	MESA						1
	5	HFET1						-
	6	HFET2						-
MOS	1	MOS1	Default Spice 3F5 MOS 1	[1]	1	1	1	1
	2	MOS2	Default Spice 3F5 MOS 2	[1]	2	2	2	2
	3	MOS3	Default Spice 3F5 MOS 3	[1]	3	3	3	3
	4	BSIM1	Default Spice 3F5 BSIM 1	[1]	4	13	4	
	5	BSIM2	Default Spice 3F5 BSIM 2	[1]	5	39	-	
	6	MOS6	Default Spice 3F5 MOS 6	[1]	6	00		
	7	MOS7	?	[1]				
	8	BSIM3	BSIM 3V3.2.4	[2]	8	53 (49)	7	8
	9	MOS9	?	[-]		00 (10)	-	
	10	B3SOI		[2]		57		
	14	BSIM4		[2]	10	54		
	15	BSIM5						
	16	BSIM6						
	29	B3SOIPD						
	30	B3SOIFD						
	31	B3SOIDD						
	44	EKV		[3]				
	49	BSIM3v1S						
	50	BSIM3v1		[2]				
		BSIM3v1A						
	52	BSIM3v0		[2]				
	60	SOI						
	61	SOI2						
	62	SOI3						
	64	HiSIM1						
								-
locumontati	on available at	<u> </u>						
1] Spice use			 keley.edu/pub/Spice3/					
ij opice use	i illalludl.		keley.edu/pub/Spice3/ k.ac.uk/teaching/CDHW/Electronics2/use	rauido/				+
		nup.//newton.ex	k.ac.uk/teaching/CDHVV/Electronics2/USe	guide/				-
2] BSIM hon	ne page.	http://www.dovi	ce.eecs.berkeley.edu/~bsim3/					+
ווטוו ואווסם ני	ne paye.	nup.//www-devi	ce.eecs.berkeley.euu/~bsiifis/					+
	e page:		 pfl.ch/ekv/index.html					1