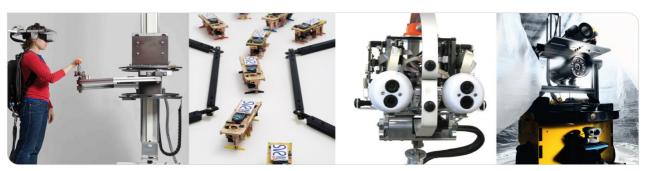
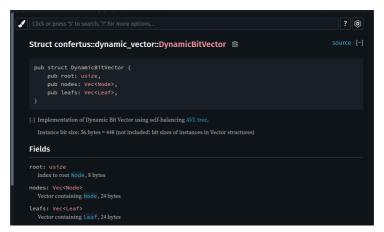


confertus: a dynamic bitvector implementation in rust

Felix Karg | 25. Juli 2022



Automatically Generated documentation



Documentation can be found at https://fkarg.me/confertus

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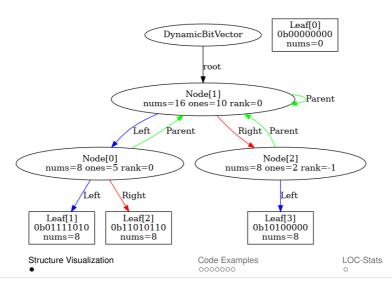
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Structure Visualization with dotviz



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DynamicBitVector

```
/// Implementation of Dynamic Bit Vector based on
                                   /// a self-balancing [AVL tree]. ...
                                   #[derive(Debug, PartialEq, Clone, Default)]
+----+
                                   pub struct DynamicBitVector {
|DynamicBitVector |
                                      /// index to root [`Node`]
+----+
                                      pub root: usize, // 8 bytes
Iroot: usize
                                      // positively indexed, usize
Inodes: Vec<Node>
                                      /// Vector containing [`Node`]
|leafs: Vec<Leaf> |
                                      pub nodes: Vec<Node>, // 24 bytes
+----+
                                      // negatively indexed, isize
                                      /// Vector containing [`Leaf`]
                                      pub leafs: Vec<Leaf>, // 24 bytes
                               12
                               13
```

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Node

```
pub struct Node {
                                            /// index of parent Node
                                            pub parent: Option<usize>, // 8 bytes + 1bit
Node
                                            /// left side subtree child index
                                            pub left: Option<isize>, // 8 bytes + 1bit
                                            /// right side subtree child index
|parent: Option<usize>|
                                            pub right: Option<isize>, // 8 bytes + 1bit
|left: Option<isize>
                                            /// number of filled bits on the left subtree
|right: Option<isize> |
                                            pub nums: usize, // 8 bytes
|nums: usize
                                            /// number of ones on the left subtree
ones: usize
                                            pub ones: usize, // 8 bytes
|rank: i8
                                            /// difference of height between left and
                                    12
                                            /// right subtree. Valid values are -1, 0, 1
                                    13
                                            pub rank: i8, // 1 byte
                                    14
                                    15
```



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Leaf

```
/// Leaf element of [`DynamicBitVector`]
                              /// ...
                                #[derive(PartialEq, Clone, Default)]
                                pub struct Leaf {
|Leaf
                                   /// reference to parent [`Node`]
+----+
                                    pub parent: usize, // 8 bytes
|parent: usize|
                                    /// container for actual bit values
|value: u128
                                    pub value: u128, // 16 bytes
Inums: u8
                                    /// number of bits used in `value`-container
+----+
                                    pub nums: u8, // 1 byte
                                    // realistically below u128::BITS,
                                    // so u8::MAX = 255 is sufficient
                            12
                            13
```

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Implementation of rank and select for u64

```
use core::arch::x86_64::{_pdep_u64, _popcnt64, _tzcnt_u64};
   #[inline]
   #[cfg(all(target_arch = "x86_64", target_feature = "bmi1", target_feature = "bmi2"))]
    unsafe fn select_internal(&self. bit: bool. n: usize) -> usize {
        _{\text{u}} _tzcnt_u64(_pdep_u64(1 << n. if bit { *self } else { !self })) as usize
   #[inline]
   \#[cfg(target\_arch = "x86_64")]
    unsafe fn rank_internal(&self. bit: bool. index: usize) -> usize {
11
        _popcnt64({if bit {*self} else {!self}}.overflowing_shl(u64::BITS - index as u32).0 as i64)
12
            as usize
13
14
```

(I also have respective non-architecture-specific fallbacks for both)

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Index for DynamicBitVector

I have **two** implementations for indexing!

```
fn index(&DynamicBitVector self, index: usize) -> Node { ... }
fn index(&DynamicBitVector self, index: isize) -> Leaf { ... }
```

This way, I can do type-safe indexing:

- self[+3 as usize] -> Node
- self[+3 as isize] -> Leaf
- self[-3 as isize] -> Leaf



Unified Traversal: Apply

```
pub fn apply<T>(&mut self, mut f: FnMut(...) -> T, index: usize) -> T {
         self.apply_node(self.root, f, index)
    fn apply_node<T>(&mut self, node: usize, mut f: FnMut(...) -> T, index: usize) -> T {
         if self[nodel.nums <= index {</pre>
             let right_id = self[node].right.unwrap();
             if right_id >= 0 {
                 self.apply_node(right_id as usize, f, index - self[node].nums)
             } else {
                 f(self, right_id, index - self[node].nums)
11
        } else {
12
             let left_id = self[node].left.unwrap();
13
             if left id >= 0 {
                 self.applv_node(left_id as usize, f, index)
15
             } else {
16
                 f(self, left_id, index)
18
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```

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Beispielimplementation: flip

```
impl DynamicBitVector {
        . . .
        #[inline]
        pub fn flip(&mut self, index: usize) {
            let leaf = self.apply(Self::flip_leaf, index);
            self.update_left_values(self[leaf].parent, leaf);
        }
        #[inline]
        fn flip_leaf(&mut self, leaf: isize, index: usize) -> isize {
11
            self[leaf].flip(index);
12
            leaf
14
15
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```

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Language	Files	Lines	Code	Comments	Blanks
Rust	18	3510	2740	389	381
- Markdown	15	434	Θ	389	45
(Total)		3944	2740	778	426

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Time invested



Does not include time needed to create this presentation

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Unfortunately, I didn't fully get it to run until the end.

```
$ RUSTFLAGS="-C target-cpu=native" cargo run --release bv input/example_bv_10k.txt out.txt
   Compiling confertus v0.1.0 (/home/pars/Coding/confertus)
    Finished release [optimized] target(s) in 4.11s
     Running `target/release/confertus bv input/example_bv_10k.txt out.txt`
index 7 out of bounds for 5
Frror: "Leaf.insert: Index out of bounds `index > self.nums`"
```

Turns out, it doesn't uphold all invariances regarding nums and ones, causing them to drift which eventually causes frustrating errors like this.

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What are your Questions?

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Abstraction: Static Bit Vector

```
/// Functions associated with static bit vectors. ...
    pub trait StaticBitVec {
        . . .
        /// Return number of on-bits in container or on left half for tree-based elements
        fn ones(&self) -> usize;
        /// Access bit value at position `index`
        fn access(&self, index: usize) -> bool;
10
        /// Returns number of `bit`-values up to `index` in container
11
        fn rank(&self, bit: bool, index: usize) -> usize;
12
13
        /// Return index of `n`-th `bit`-value in container
14
        fn select(&self, bit: bool, n: usize) -> usize;
15
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  •0
```



Abstraction: Dynamic Bit Vector

```
/// Functions associated with dynamic bit vectors.
   pub trait DynBitVec: StaticBitVec {
       /// Insert `bit` at position `index` in underlying container
       fn insert(&mut self, index: usize, bit: bool) -> Result<(), &'static str>;
       /// Remove bit value at position `index`
       fn delete(&mut self, index: usize) -> Result<(), &'static str>;
       /// Flip bit at position `index`, updates `ones` and `num` values accordingly
       fn flip(&mut self, index: usize);
10
11
       /// Return used capacity of underlying container
       fn nums(&self) -> usize;
13
14
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```

