

# Chunsheng Mei

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## EDUCATION

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- **Master of Science, Circuit and System** Northwestern Polytechnical University  
*GPA: 88.88/100; Rank: 2/25* Sept. 2015 – Apr. 2018(Expected)
- **Bachelor of Science, Electronic Engineering and Information** Northwestern Polytechnical University  
*GPA 87/100; Rank: 4/10* Sept. 2011 – June 2015

## MASTER THESIS

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Title: Person Re-identification Using Convolutional Neural Network

Supervisor: Prof. Wei Zhou and Dr. Guanwen Zhang

Develop CNN-based algorithms to solve person re-identification problem and design hardware architecture to accelerate the processing speed.

## BACHELOR THESIS

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Title: Hybrid Computation Model for HEVC Intra-prediction

Supervisor: Prof. Wei Zhou

Designed heterogeneous computation model with GPU and CPU for HEVC intra prediction mode decision.

## EXPERIENCE

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- **Research Assistant**  
*RIIT&TNList, Tsinghua University, Beijing, China* Mar. 2016 – June 2017  
Supervisor: Prof. Zhengyu Liu
  - Developed an hardware accelerator on FPGA for convolutional operation in VGG16 model.
  - Conducted research on parameter compression algorithms for CNN.
- **Research Assistant**  
*School of Electronics and Information, Northwestern Polytechnical University, Xi'an, China* Oct. 2014 – June 2015  
Supervisor: Prof. Wei Zhou
  - Developed a low complexity rate estimation method for RDO mode decision based on an approximation of bit rate through linear model.
- **Robot Club member/leader**  
*School of Electronics and Information, Northwestern Polytechnical University, Xi'an, China* Oct. 2014 – June 2012
  - Developed object detection and tracking algorithms for moving platform on robots

## AWARDS AND DISTINCTIONS

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- First Class Students Award at NWPU (Top 9%), 2016
- Third Prize Award, China Robot Contest cum RoboCup open, 2014
- First Prize Award, National Robot Championships, 2014
- First Class Students Award at NWPU (Top 10%), 2012-2014

## CONFERENCE

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- A 200MHZ 202.4GFLOPS@10.8W VGG16 ACCELERATOR IN XILINX VX690T  
**Chunsheng Mei**, Zhengyu Liu, Yue Niu, Xiangyang Ji, Wei Zhou, Dongsheng Wang  
The fifth IEEE Global Conference on Signal and Information Processing (GlobalSIP), Montreal, Canada  
on November 14-16, 2017
- SENSITIVITY-BASED ACCELERATION AND COMPRESSION ALGORITHM FOR CONVOLUTION  
NEURAL NETWORK  
Yue Niu, Zhengyu Liu, **Chunsheng Mei**, Xiangyang Ji, Wei Zhou, Dongsheng Wang  
The fifth IEEE Global Conference on Signal and Information Processing (GlobalSIP), Montreal, Canada  
on November 14-16, 2017

## REFERENCES

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- Prof. **Wei Zhou** (thesis supervisor)  
Associate Professor, School of Electronics and Information, Northwestern Polytechnical University.  
Email:zhouwei@nwpu.edu.cn
- Dr. **Guanwen Zhang** (thesis supervisor)  
Assistant Professor, School of Electronics and Information, Northwestern Polytechnical University.  
Email:guanwen.zh@nwpu.edu.cn
- Prof. **Zhengyu Liu** (research supervisor)  
Associate Professor, RIIT&TNList, Tsinghua University.  
Email:liuzhenyu73@tsinghua.edu.cn

## LANGUAGES

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|--------------------|--------|
| • Mandarin Chinese | native |
| • English          | fluent |

## SKILLS

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|----------------|------------------------------|
| • Basic        | Assembly                     |
| • Intermediate | Matlab, Python               |
| • Expert       | C/C++, CUDA C++, verilog HDL |