Chunsheng Mei

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EDUCATION

Master of Science, Circuit and System Northwestern Polytechnical University (NPU)
GPA: 88.88/100; Rank: 2/25 *Sept. 2015 – Apr. 2018(Expected)*

Bachelor of Science, Electronics and Information Engineering NPU
GPA 87/100; Rank: 4/10 *Sept. 2011 – June 2015*

MASTER THESIS

- Title: Person Re-identification Using Convolutional Neural Network
- Supervisor: Prof. Wei Zhou and Dr. Guanwen Zhang
- Develop CNN-based algorithms to solve person re-identification problem.
  Design hardware architecture to accelerate the processing speed.

BACHELOR THESIS

- Title: Hybrid Computation Model for HEVC Intra-prediction
- Supervisor: Prof. Wei Zhou
- Designed heterogeneous computation model with GPU and CPU for HEVC intra-prediction mode decision.

EXPERIENCE

- **Research Assistant**
  RIIT&TNList, Tsinghua University, Beijing, China *Mar. 2016 – June 2017*
  Supervisor: Prof. Zhengyu Liu
  ·Developed an hardware accelerator on FPGA for VGG16 model.
  ·Conducted research on parameter compression algorithms for CNN.

- **Research Assistant**
  School of Electronics and Information, NPU, Xi'an, China *Oct. 2015 – Mar. 2016*
  Supervisor: Prof. Wei Zhou
  ·Developed a low complexity rate estimation method for RDO mode decision.

- **Robot Club member/leader**
  School of Computer Science and Engineering, NPU, Xi'an, China *June 2012 – Oct. 2014*
  ·Developed object detection and tracking algorithms for moving platform on robots
  ·Managing the visual team

AWARDS AND DISTINCTIONS

- First Class Students Award at NWPU (Top 9%), 2016
- Third Prize Award, China Robot Contest cum RoboCup open, 2014
- First Prize Award, National Robot Championships, 2014
- First Class Students Award at NWPU (Top 10%), 2012-2014

Conference

- A 200MHZ 202.4GFLOPS@10.8W VGG16 ACCELERATOR IN XILINX VX690T
  Chunsheng Mei, Zhengyu Liu, Yue Niu, Xiangyang Ji, Wei Zhou, Dongsheng Wang
  The fifth IEEE Global Conference on Signal and Information Processing (GlobalSIP), Montreal, Canada
  on November 14-16, 2017

- SENSITIVITY-BASED ACCELERATION AND COMPRESSION ALGORITHM FOR CONVOLUTION
  NEURAL NETWORK
  Yue Niu, Zhengyu Liu, **Chunsheng Mei**, Xiangyang Ji, Wei Zhou, Dongsheng Wang
  The fifth IEEE Global Conference on Signal and Information Processing (GlobalSIP), Montreal, Canada
  on November 14-16, 2017

References

- Prof. **Wei Zhou** (thesis supervisor)
  Associate Professor, School of Electronics and Information, NPU.
  Email:zhouwei@nwpu.edu.cn

- Dr. **Guanwen Zhang** (thesis supervisor)
  Assistant Professor, School of Electronics and Information, NPU.
  Email:guanwen.zh@nwpu.edu.cn

- Prof. **Zhengyu Liu** (research supervisor)
  Associate Professor, RIIT&TNList, Tsinghua University.
  Email:liuzhenyu73@tsinghua.edu.cn

Skills

- Algorithm design and implementation
- Programming Language: C/C++, CUDA C++, verilog HDL, Matlab, python