

Pin Grid

Package:	TQFP48	Pin No:	8	9	10	11	12	21	22	25	26	27	33	34	35	36	37	45	46	47	48	1	2	7	15	16	20	38	39	17	24	28	29	40	41	3	4	44	30	23	6						
Module:		Function:		Direction:																																											
All					0	1	2	3	4	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	0	1	2	3	4	5	6	7	8	9	10	11	12	13	1	8	10	13				
ADC1	ANx	input																																													
	ADCTRG31	input																																													
Clock	CLKO	output																																													
	REFI	input																																													
	REFO	output																																													
ICD	PGCx	input																																													
	PGDx	input																																													
Pins	GPIO	input																																													
	GPIO	output																																													

ADC Basic

▼ Basic Settings

Custom Name	ADC1
Resolution	12
Conversion Time (us)	Refer PLIB for the Calculations. 0.37
Data Output Format	<input checked="" type="radio"/> Integer <input type="radio"/> Fractional
Common Interrupt	<input type="checkbox"/>
Pin Help	Pin Selection should only be done using the table. Channel Enable checkbox selects the corresponding analog channel and auto locks the analog pin in the Pins Grid View.

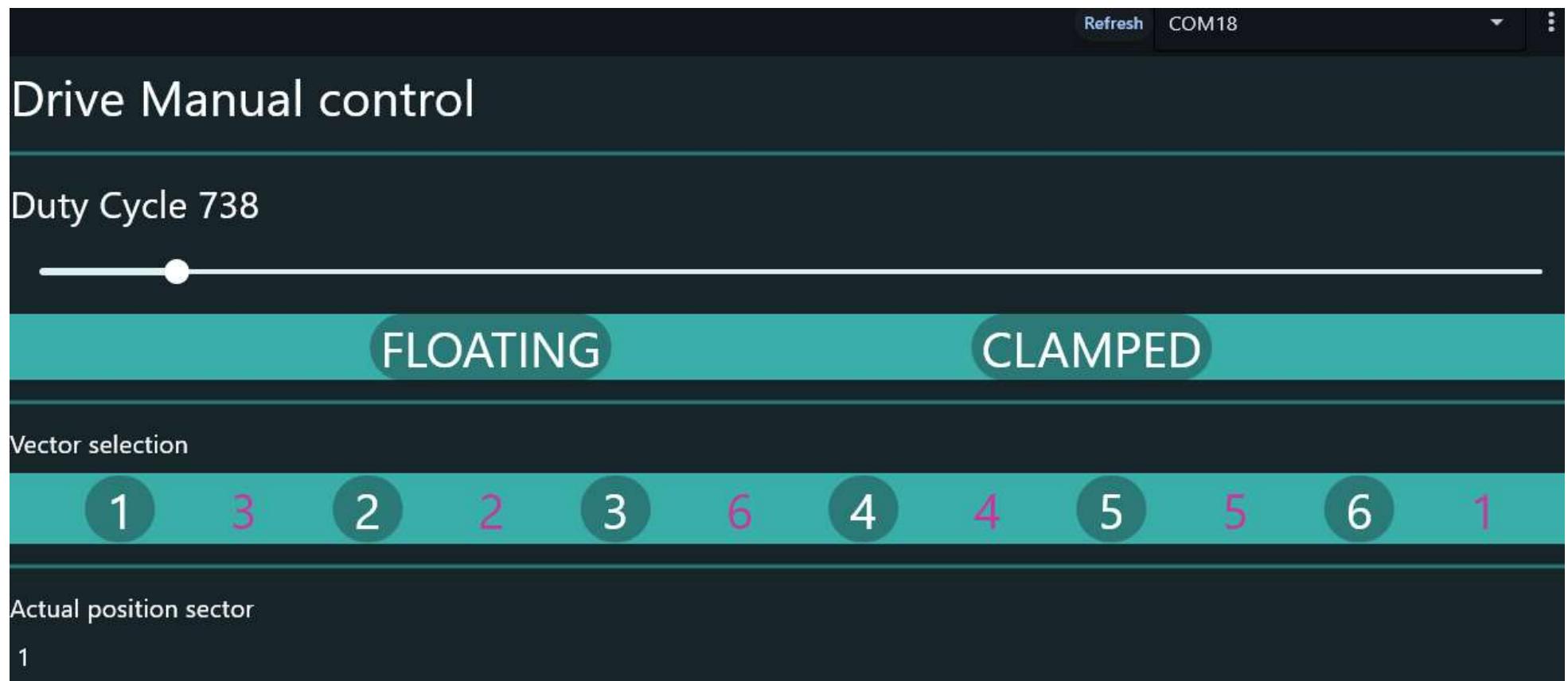
▼ Shared Core Settings

Requested Sampling Time (us)	0.04 ≤ 1 ≤ 20.5
Calculated Sampling Time (us)	1

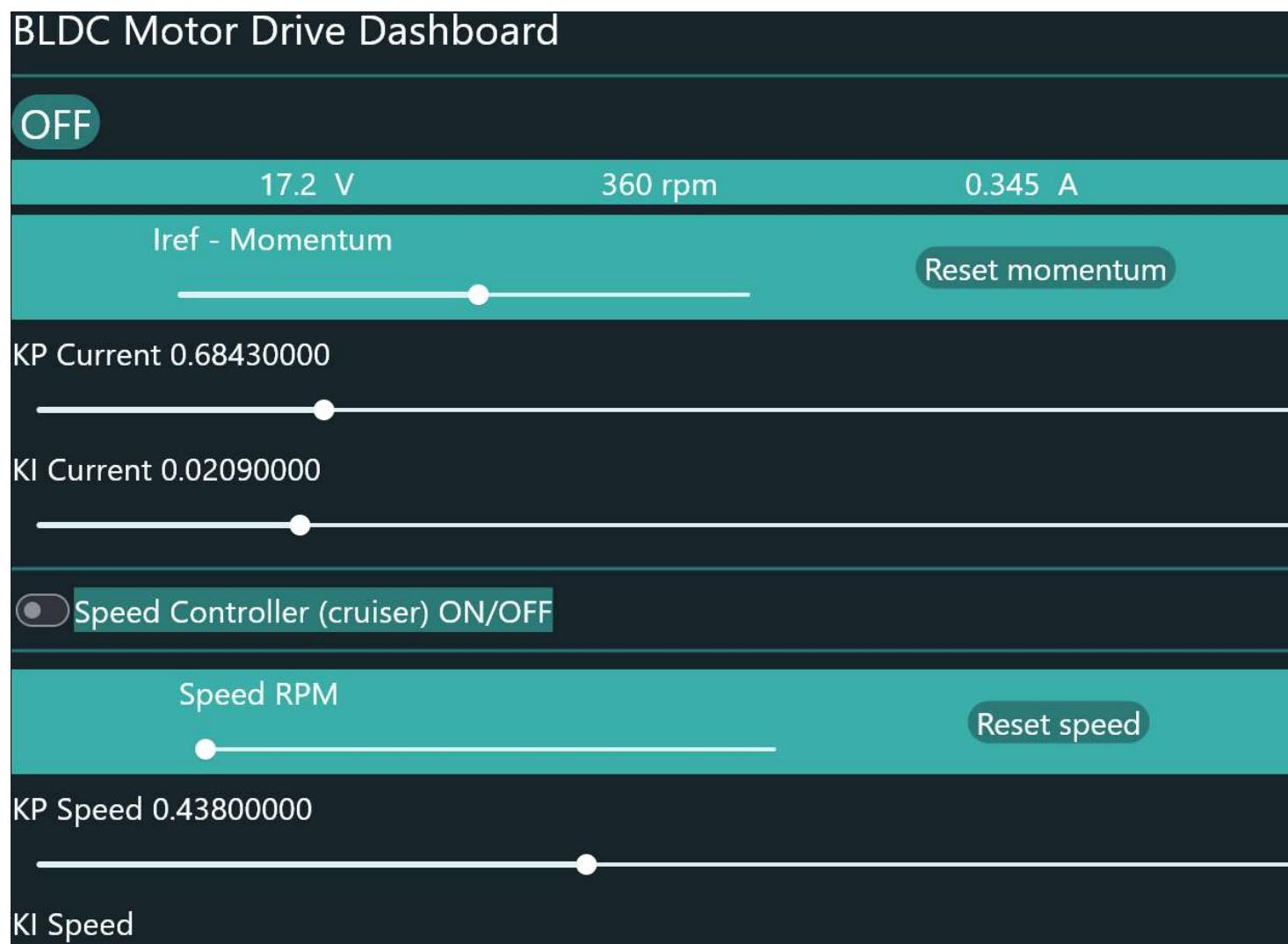
ADC Shared Core Settings

Shared Core Configuration						
Channel	Pins	Enable	Custom Name	Trigger Source	Interrupts :	
AN0	RA0	<input type="checkbox"/>		None		
AN1	RB2	<input checked="" type="checkbox"/>	_TEMPERA	PWM Generator1 ADC Trigger2		
AN2	RB7	<input type="checkbox"/>		None		
AN3	RA3	<input checked="" type="checkbox"/>	_I3	PWM Generator1 ADC Trigger1		
AN4	RA4	<input checked="" type="checkbox"/>	_I2	PWM Generator1 ADC Trigger1		<input checked="" type="checkbox"/>
AN5	RB0	<input type="checkbox"/>		None		
AN6	RB1	<input checked="" type="checkbox"/>	_MOMEN1	PWM Generator1 ADC Trigger2		
AN7	RB2	<input type="checkbox"/>		None		
AN8	RB3	<input type="checkbox"/>		None		
AN9	RA2	<input type="checkbox"/>		None		
AN10	RB8	<input type="checkbox"/>		None		
AN11	RB9	<input checked="" type="checkbox"/>	_I2_Power1	PWM Generator1 ADC Trigger1		<input checked="" type="checkbox"/>
AN12	RC0	<input checked="" type="checkbox"/>	_VLINK	PWM Generator1 ADC Trigger2		

```
21  
22 // #define FLETUINO_APPLICATION_DEMO  
23 // #define FLETUINO_PI_CONTROLLER_SETTINGS  
24 #define FLETUINO_MANUAL_CONTROL  
25  
26
```



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22 // #define FLETUINO_APPLICATION_DEMO  
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```



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25
```

Drive Demonstration

Emergency OFF

0

0

0

36.9 V

0 rpm

0.000 A

Momentum-GAS



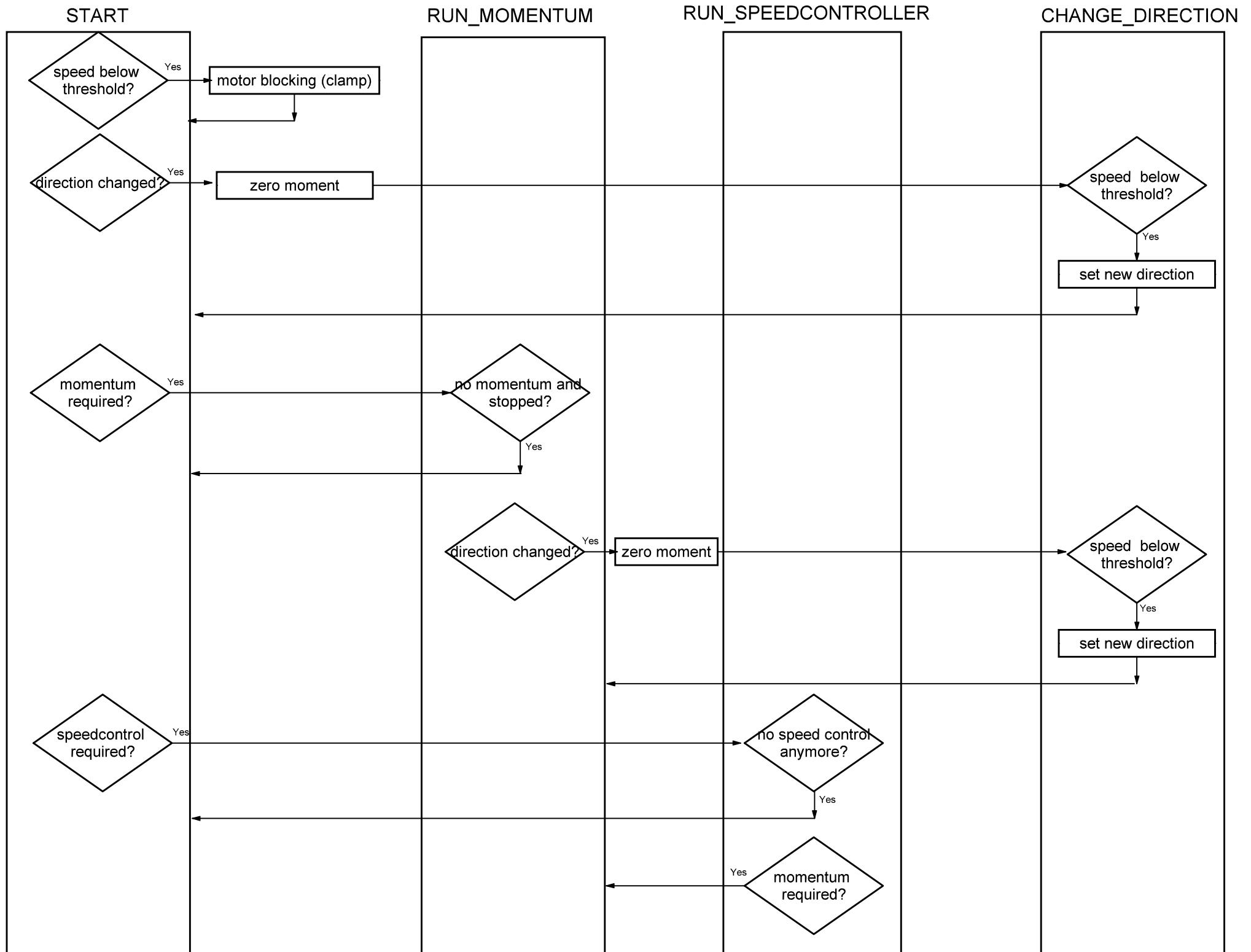
SpeedControl OFF/ON



Forward/Reverse (F/R)

Speed RPM





Shared Core Configuration

Channel	Pins	Enable	Custom Name	Trigger Source	Interrupts
AN0	RA0	<input type="checkbox"/>		None	<input type="checkbox"/>
AN1	RB2	<input checked="" type="checkbox"/>	_TEMPERA	PWM Generator1 ADC Trigger2	<input type="checkbox"/>
AN2	RB7	<input type="checkbox"/>		None	<input type="checkbox"/>
AN3	RA3	<input checked="" type="checkbox"/>	_I3	PWM Generator1 ADC Trigger1	<input type="checkbox"/>
AN4	RA4	<input checked="" type="checkbox"/>	_I2	PWM Generator1 ADC Trigger1	<input checked="" type="checkbox"/>
AN5	RB0	<input type="checkbox"/>		None	<input type="checkbox"/>
AN6	RB1	<input checked="" type="checkbox"/>	_MOMENT	PWM Generator1 ADC Trigger2	<input type="checkbox"/>
AN7	RB2	<input type="checkbox"/>		None	<input type="checkbox"/>
AN8	RB3	<input type="checkbox"/>		None	<input type="checkbox"/>
AN9	RA2	<input type="checkbox"/>		None	<input type="checkbox"/>
AN10	RB8	<input type="checkbox"/>		None	<input type="checkbox"/>
AN11	RB9	<input checked="" type="checkbox"/>	_I2_Power	PWM Generator1 ADC Trigger1	<input checked="" type="checkbox"/>
AN12	RC0	<input checked="" type="checkbox"/>	_VLINK	PWM Generator1 ADC Trigger2	<input type="checkbox"/>

Interrupt ausgelöst, obwohl nicht aktiviert!

Interrupt wird nicht ausgelöst! Wie erwartet!!

adc-current-measurement-and-control.c
void ADC_Callback(enum ADC_CHANNEL channel, uint16_t adcVal)

