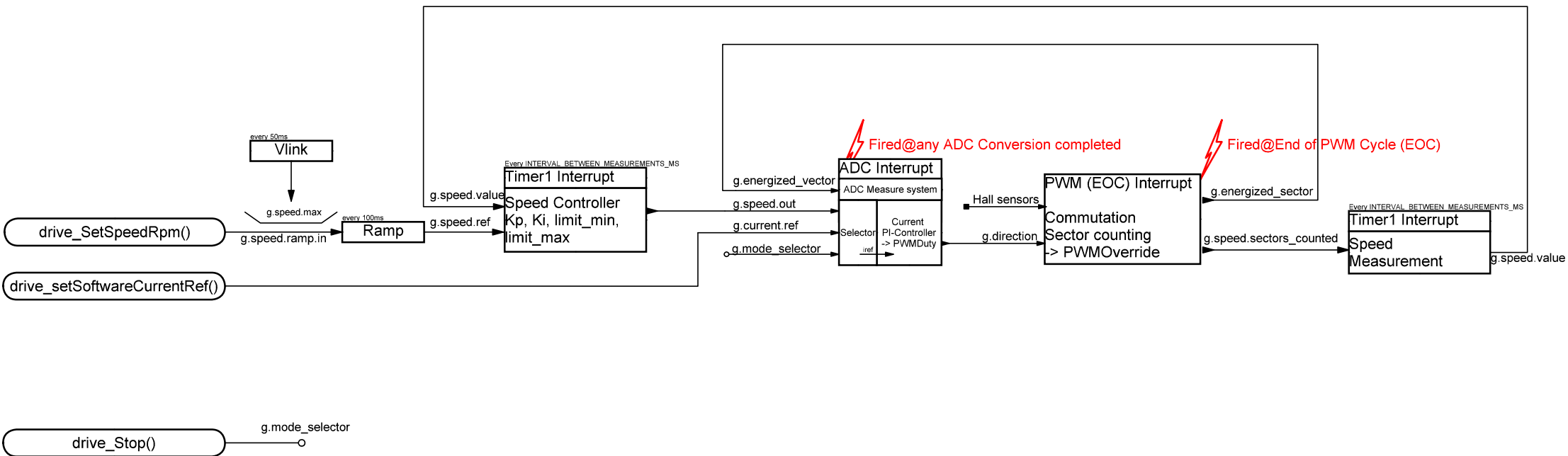


⚡ Fired@Timer1 period compare match - every 250µs



defined in global.h

```
#define MODE_SELECTOR_ZERO_MOTOR_BLOCKED 0  
#define MODE_SELECTOR_ZERO_MOTOR_FLOATING 1  
#define MODE_SELECTOR_SPEEDCONTROLLER 2  
#define MODE_SELECTOR_MOMENTUM 3  
#define MODE_SELECTOR_IREF 4
```

Package:	TQFP48	Pin No:	8	9	10	11	12	21	22	25	26	27	33	34	35	36	37	45	46	47	48	1	2	7	15	16	20	38	39	17	24	28	29	40	41	3	4	44	30	23	6		
Module	Function	Direction	PORTA					PORTB																	PORTC															PORTD			
All			0	1	2	3	4	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	0	1	2	3	4	5	6	7	8	9	10	11	12	13	1	8	10	13		
ADC1	ANx	input	🔒		🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒			🔒	🔒	🔒							🔒	🔒	🔒	🔒																
	ADCTRG31	input						🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒		
Clock	CLKO	output						🔒																																			
	REF1	input						🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒		
	REFO	output						🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒		
ICD	PGCx	input										🔒			🔒																												
	PGDx	input									🔒			🔒			🔒																										
Pins	GPIO	input	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒			
	GPIO	output	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒			

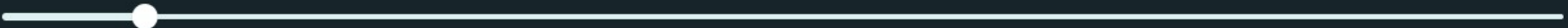
Channel	Pins	Enable	Custom Name	Trigger Source	Interrupts S
AN0	RA0	<input checked="" type="checkbox"/>	_I1	PWM Generator1 ADC Trigger1	<input checked="" type="checkbox"/>
AN1	RB2	<input checked="" type="checkbox"/>	_TEMPERA	None	<input type="checkbox"/>
AN2	RB7	<input type="checkbox"/>	Channel_A	None	<input type="checkbox"/>
AN3	RA3	<input checked="" type="checkbox"/>	_I3	PWM Generator1 ADC Trigger1	<input checked="" type="checkbox"/>
AN4	RA4	<input checked="" type="checkbox"/>	_I2	PWM Generator1 ADC Trigger1	<input checked="" type="checkbox"/>
AN5	RB0	<input type="checkbox"/>	Channel_A	CLC2 Output	<input type="checkbox"/>
AN6	RB1	<input checked="" type="checkbox"/>	_MOMEN1	None	<input type="checkbox"/>
AN7	RB2	<input type="checkbox"/>		None	<input type="checkbox"/>
AN8	RB3	<input type="checkbox"/>	Channel_A	None	<input type="checkbox"/>
AN9	RA2	<input checked="" type="checkbox"/>	_TEMPERA	None	<input type="checkbox"/>
AN10	RB8	<input type="checkbox"/>	Channel_A	None	<input type="checkbox"/>
AN11	RB9	<input type="checkbox"/>		None	<input type="checkbox"/>
AN12	RC0	<input checked="" type="checkbox"/>	_VLINK	Common Software Trigger	<input type="checkbox"/>
AN13	RC1	<input checked="" type="checkbox"/>	_UM3	None	<input type="checkbox"/>
AN14	RC2	<input checked="" type="checkbox"/>	_UM2	None	<input type="checkbox"/>
AN15	RC3	<input checked="" type="checkbox"/>	_UM1	None	<input type="checkbox"/>
AN16	Internal-T	<input type="checkbox"/>	Channel_A	None	<input type="checkbox"/>
AN17	Internal-B	<input type="checkbox"/>	Channel_A	None	<input type="checkbox"/>

```
18
19 // #define DEBUG // only one option (DEBUG or FLETUINO) is allowed
20 // #define DEBUG_SERIAL_COMMAND_HANDLING
21 #define FLETUINO // DEBUG or FLETUINO or nothing
22 // #define FLETUINO_APPLICATION_DEMO
23 // #define FLETUINO_PI_CONTROLLER_SETTINGS
24 #define FLETUINO_MANUAL_CONTROL
25
```

Refresh COM18

Drive Manual control

Duty Cycle 738



FLOATING CLAMPED

Vector selection

1	3	2	2	3	6	4	4	5	5	6	1
---	---	---	---	---	---	---	---	---	---	---	---

Actual position sector

1

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