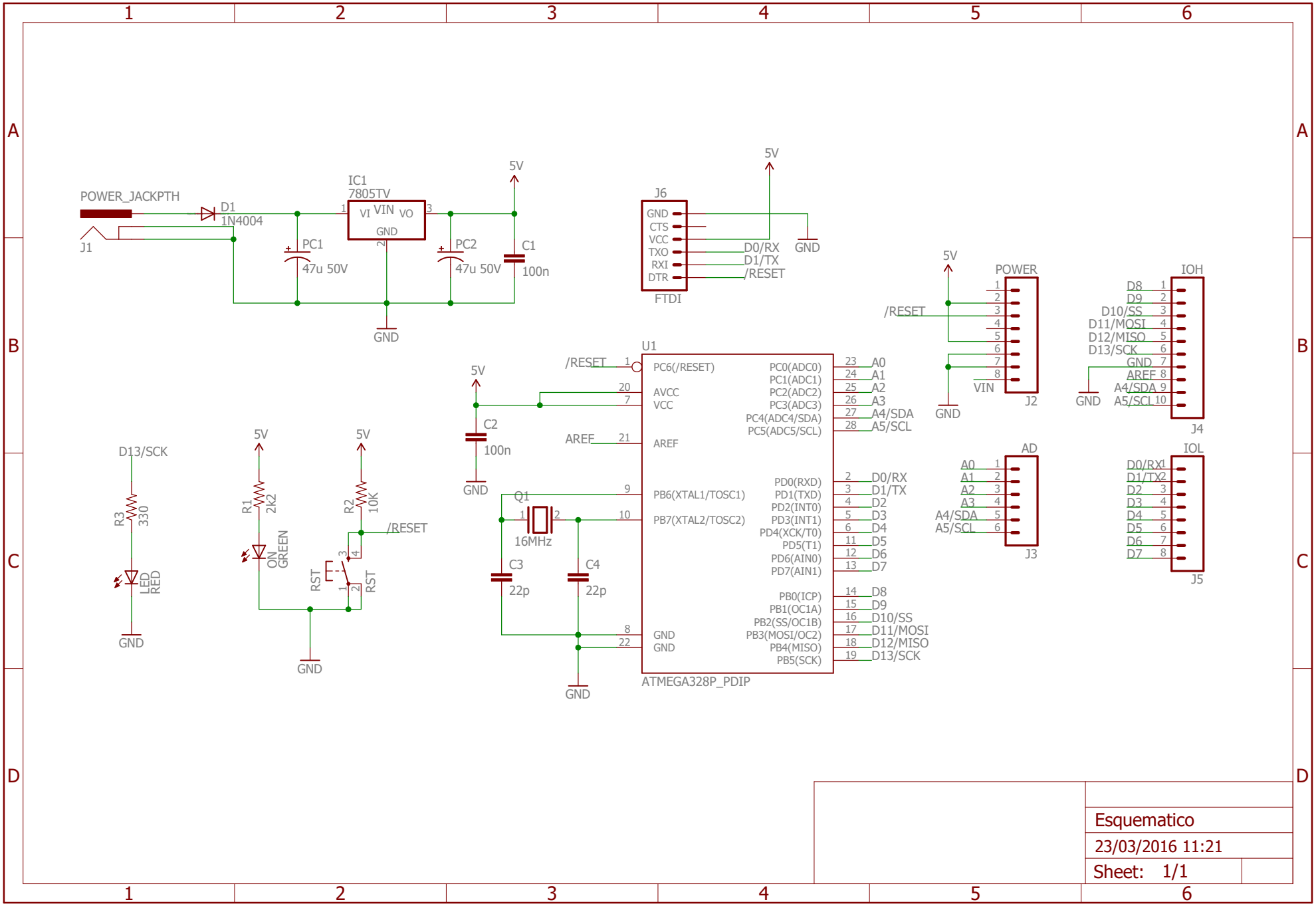
POWER_JACKPTH
J1
D1
1N4004
IC1
7805TV
VI VIN VO
GND
PC1
47u 50V
PC2
47u 50V
C1
100n
5V
GND
J6
GND
CTS
VCC
TXO
RXI
DTR
FTDI
D0/RX
D1/TX
/RESET
U1
PC6(/RESET)
AVCC
VCC
AREF
PB6(XTAL1/TOSC1)
PB7(XTAL2/TOSC2)
GND
GND
PC0(ADC0)
PC1(ADC1)
PC2(ADC2)
PC3(ADC3)
PC4(ADC4/SDA)
PC5(ADC5/SCL)
PD0(RXD)
PD1(TXD)
PD2(INT0)
PD3(INT1)
PD4(XCK/T0)
PD5(T1)
PD6(AIN0)
PD7(AIN1)
PB0(ICP)
PB1(OC1A)
PB2(SS/OC1B)
PB3(MOSI/OC2)
PB4(MISO)
PB5(SCK)
ATMEGA328P_PDIP
A0
A1
A2
A3
A4/SDA
A5/SCL
D0/RX
D1/TX
D2
D3
D4
D5
D6
D7
D8
D9
D10/SS
D11/MOSI
D12/MISO
D13/SCK
C2
100n
Q1
16MHz
C3
22p
C4
22p
POWER
J2
VIN
IOH
J4
AD
J3
IOL
J5
D13/SCK
R3
330
LED
RED
R1
2k2
ON
GREEN
R2
10K
RST
Esquematico
23/03/2016 11:21
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