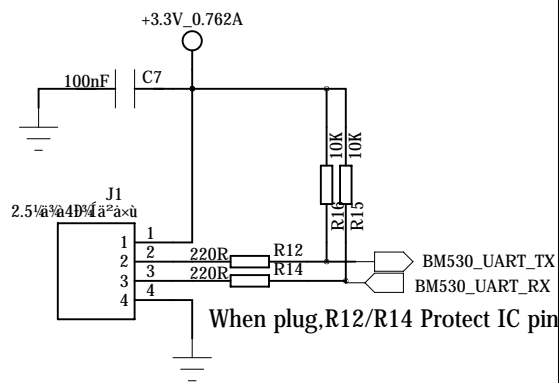
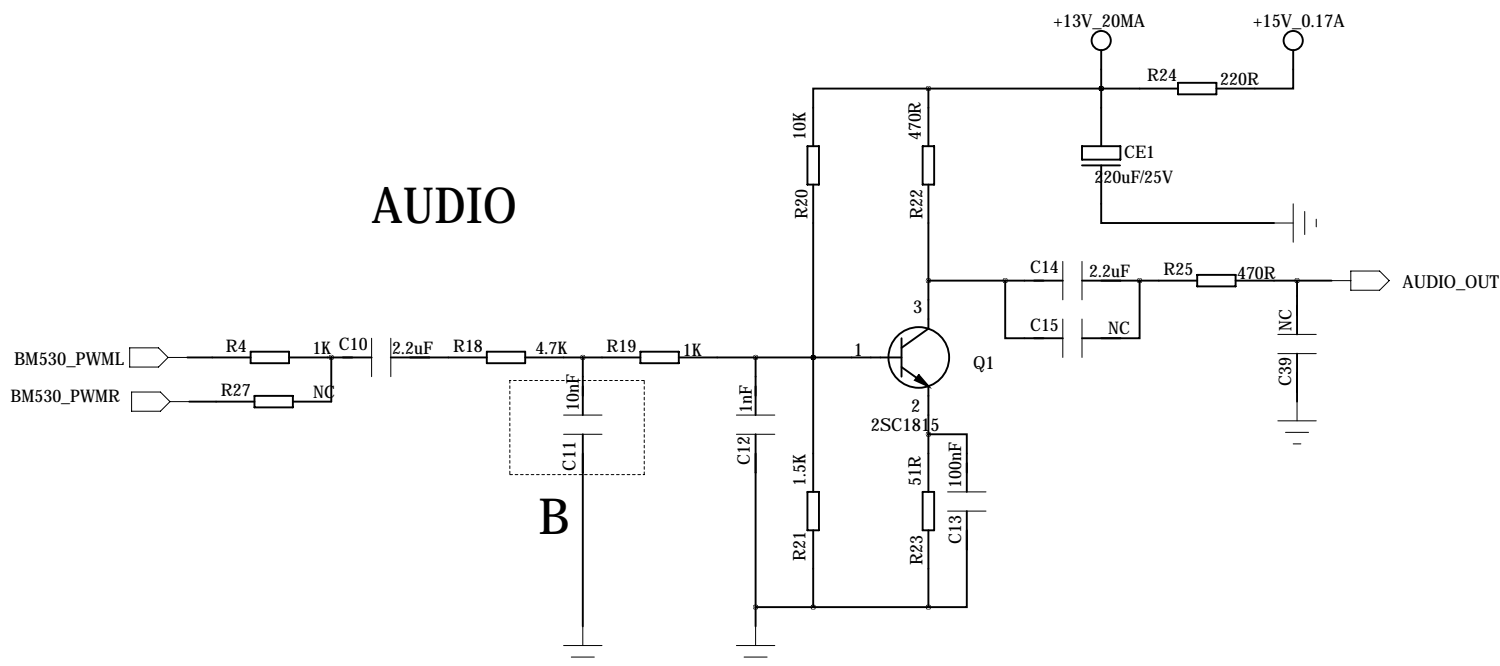


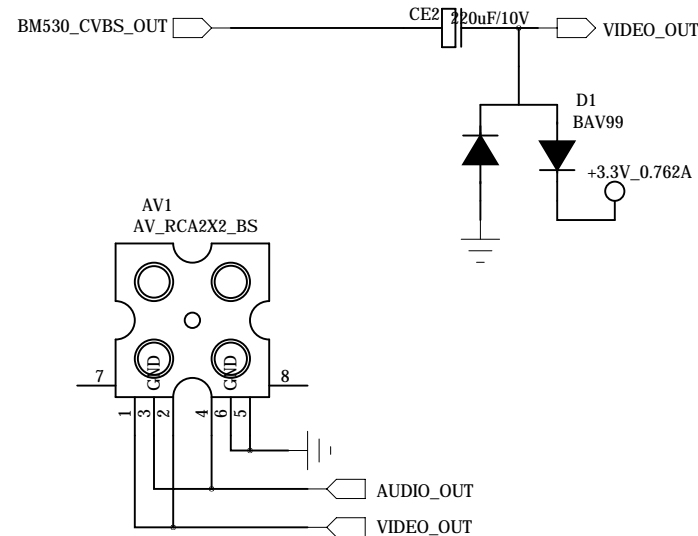
UART



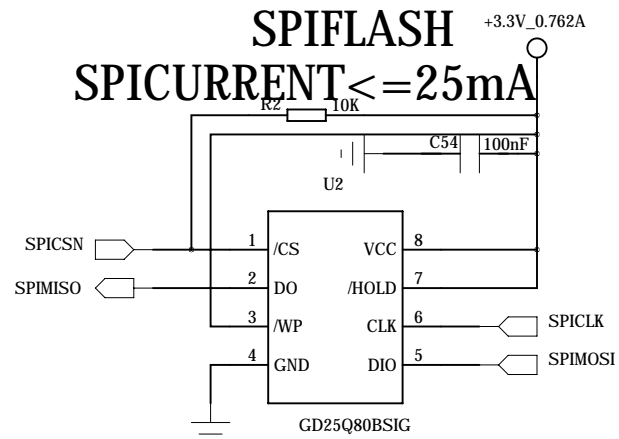
AUDIO



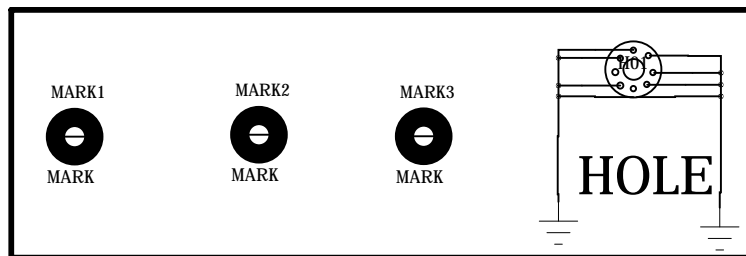
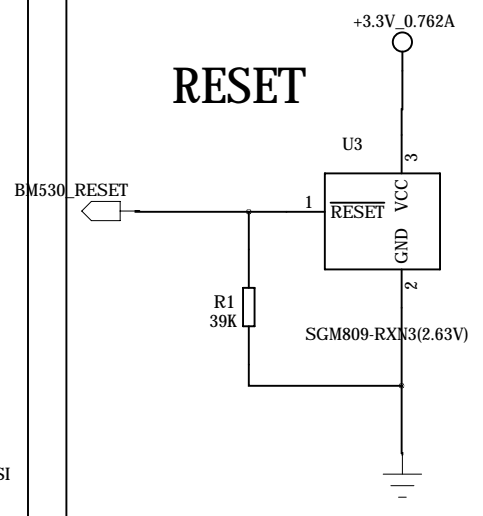
VIDEO



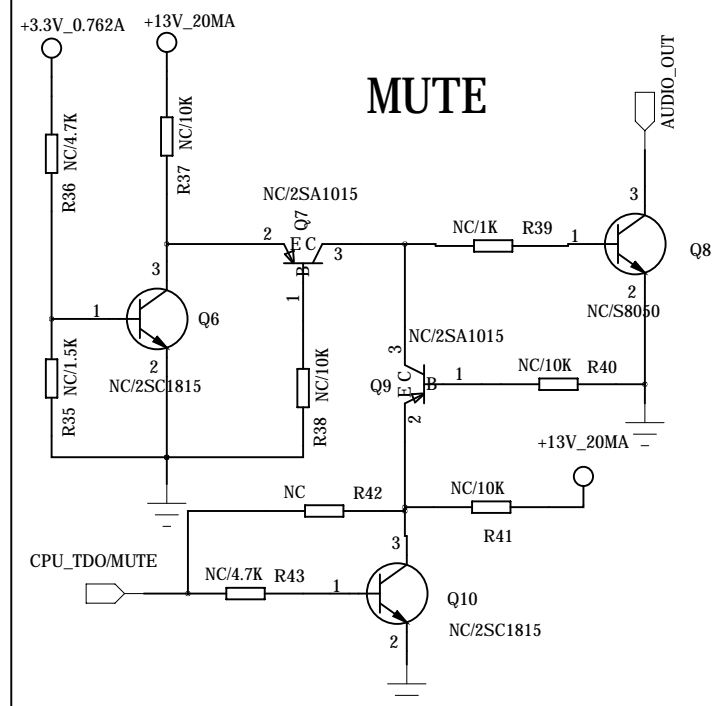
SPIFLASH



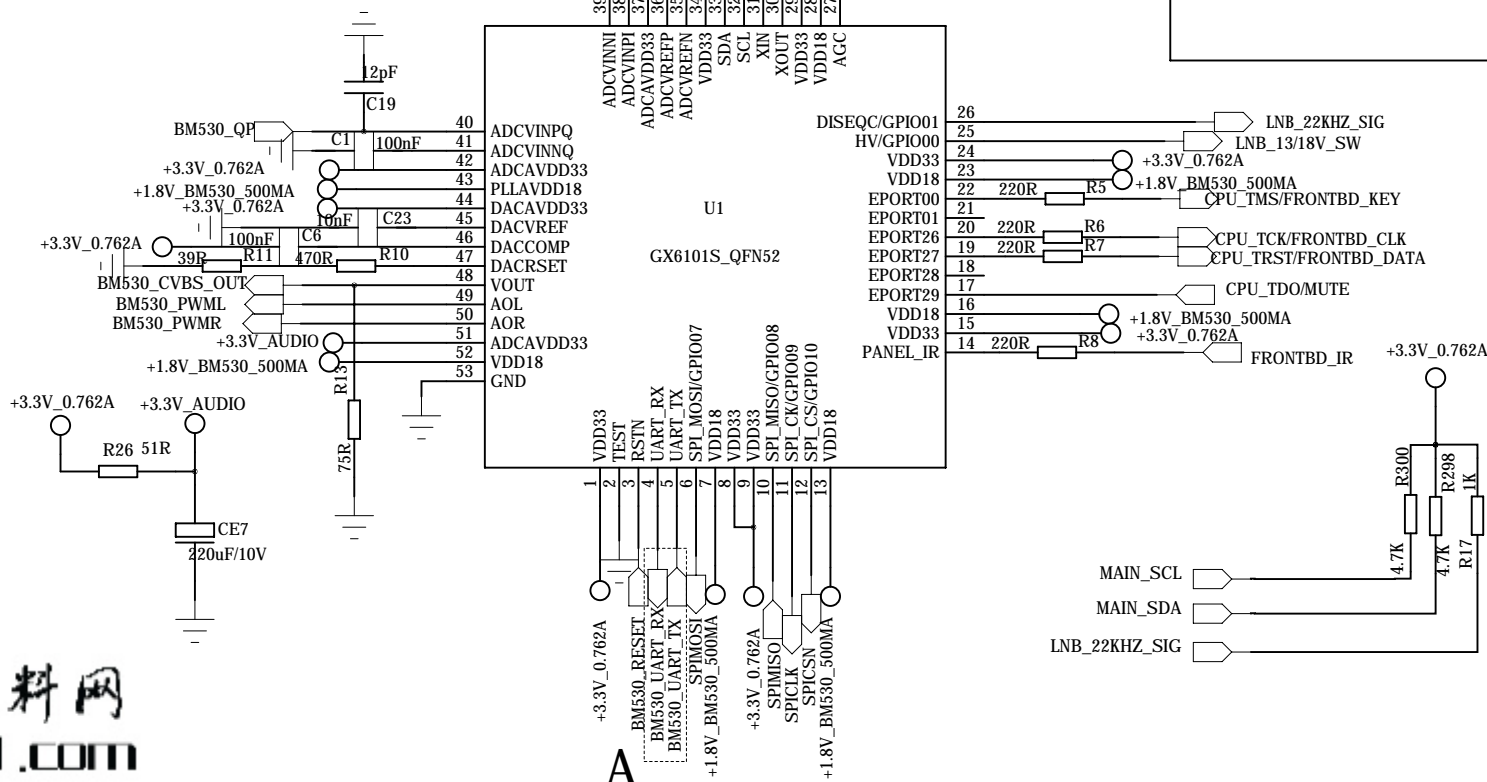
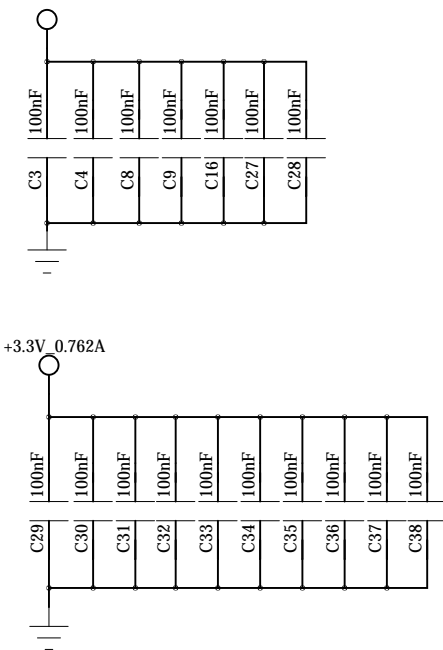
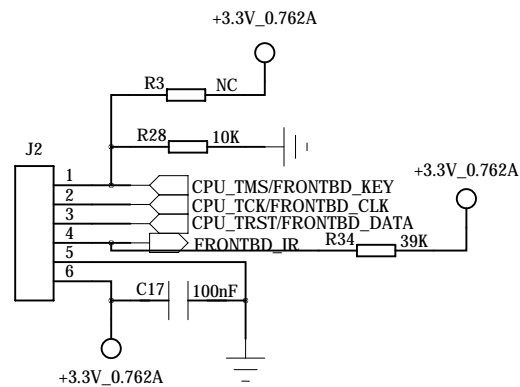
RESET



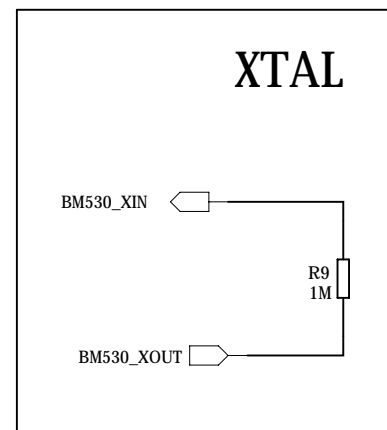
MUTE



KEY BOARD



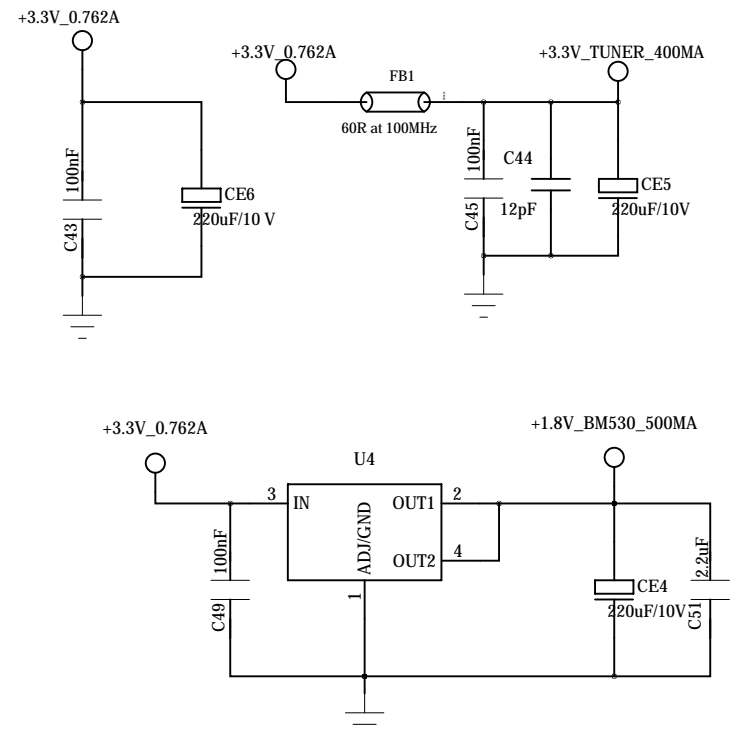
XTAL



3.3V IMax=0.762A

20V I_{Max}=0.21A(Load=100R)

With 68R load (LNB current approximately 200mA) not less than 19.5V



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The schematic diagram illustrates the electrical connections for the BM530 module. Key components and connections include:

- RF Section:** Features a 90 OHM input, a 75 OHM trace, and various capacitors (C115, C116, C117, C41, C53, C114, C18, C26) and inductors (L3, L2) for impedance matching and filtering.
- Power Section:** Includes power inputs for LNB_POWER_150MA and +3.3V_TUNER_400MA, with decoupling capacitors (C77, C74, C76, C75) and a voltage regulator (U9).
- Control Section:** Shows I2C connections for MAIN_SDA and MAIN_SCL, and a crystal oscillator (X2, 27MHz) with matching capacitors (C124, C136).
- RF AGC Section:** Includes a variable capacitor (C114) and a 3.3nH inductor (L2) for automatic gain control.
- Notice:** A note specifies that the 75 Ohm RF trace is 30mil wide and the spacing is 25mil between the 75 Ohm RF trace and GND.

Version_Update_Record:

GX6101S_AV2028_DEMO_V1.1_20150525 zhaodf

- A.µ÷»»RX,TXIøÂ°.°ÍĐ³⁄Æ¬.â×°ÃüÃûÒ»ÖÂ¡£
- B.Ôö¼⁄ÔÔôÆµ¹Û¼⁄ÅRCÂË²¨£¬51R+220uF¡£
- C.ü,ÄR4,Ôö¼⁄ÔÔôÆµ·Å´ó±¶ËËý£»ü,ÄR23£¬¼⁄öÉÛÔª¼⁄µÄÖÖÀàËËý¡£

GX6101S_AV2028_DEMO_V1.2_20151010 zhaodf

- Ô-À¡Í¼⁄£°
- A.C68,C69,ÄÎª22pF,Ìá,ß1050MHzÁéÃô¶Ë£
- B.C11,ÄÎª10nF,ÄÉÆËÓÆµÐ±ÌõÎÆ¡£
- C.CPU_TMS/FrontBD_OP,ÄÎªCPU_TMS/FrontBD_KEY£¬±ãÓÚË¼¼⁄Í¬ÊÊ¶±ð¡£

PCB Layout£°

- A.µçÔ´×ùJ4Ë¿Ó¡·´Ïð£¬±£³Ö°ÍGX6101DÒ»ÖÂ¡£
- B.C70,C71¿¿¼⁄üAV2028£¬Ìá,ß¿¹,ÉÈÅÄÜÁ¡¡£