|  |  |  |
| --- | --- | --- |
| IOCFG0 MRFI\_SETTING\_IOCFG0 | 6 |  |
| MCSM1 MRFI\_SETTING\_MCSM1 | 0x3c |  |
| MCSM0 MRFI\_SETTING\_MCSM0 | 0x18 | 0x10 | (0x18 & (0x4 | 0x8)) |
| PKTLEN MRFI\_SETTING\_PKTLEN |  | MRFI\_MAX\_FRAME\_SIZE – MRFI\_LENGTH\_FIELD\_SIZE |
| PKTCTRL0 MRFI\_SETTING\_PKTCTRL0 | 0x05 | 0x05 | (0x05 & 0x40) |
| FSCTRL1 SMARTRF\_SETTING\_FSCTRL1 | 0x07 |  |
| FSCTRL0 SMARTRF\_SETTING\_FSCTRL0 | 0x00 |  |
| FREQ2 SMARTRF\_SETTING\_FREQ2 | 0x5d |  |
| FREQ1 SMARTRF\_SETTING\_FREQ1 | 0x44 |  |
| FREQ0 SMARTRF\_SETTING\_FREQ0 | 0xec |  |
| MDMCFG4 SMARTRF\_SETTING\_MDMCFG4 | 0x2d |  |
| MDMCFG3 SMARTRF\_SETTING\_MDMCFG3 | 0x3b |  |
| MDMCFG2 SMARTRF\_SETTING\_MDMCFG2 | 0x73 |  |
| MDMCFG1 SMARTRF\_SETTING\_MDMCFG1 | 0x23 | FEC off, min preamble bytes = 4, CHANSPC\_E = 3 |
| MDMCFG0 SMARTRF\_SETTING\_MDMCFG0 | 0x3b | CHANSPC\_M = 59 |
| DEVIATN SMARTRF\_SETTING\_DEVIATN | 0x01 |  |
| FOCCFG SMARTRF\_SETTING\_FOCCFG | 0x1d |  |
| BSCFG SMARTRF\_SETTING\_BSCFG | 0x1c |  |
| AGCCTRL2 SMARTRF\_SETTING\_AGCCTRL2 | 0xc7 |  |
| AGCCTRL1 SMARTRF\_SETTING\_AGCCTRL1 | 0x00 |  |
| AGCCTRL0 SMARTRF\_SETTING\_AGCCTRL0 | 0xb0 |  |
| FREND1 SMARTRF\_SETTING\_FREND1 | 0xb6 |  |
| FREND0 SMARTRF\_SETTING\_FREND0 | 0x10 |  |
| FSCAL3 SMARTRF\_SETTING\_FSCAL3 | 0xea |  |
| FSCAL2 SMARTRF\_SETTING\_FSCAL2 | 0x0a |  |
| FSCAL1 SMARTRF\_SETTING\_FSCAL1 | 0x00 |  |
| FSCAL0 SMARTRF\_SETTING\_FSCAL0 | 0x11 |  |
| TEST2 SMARTRF\_SETTING\_TEST2 | 0x88 |  |
| TEST1 SMARTRF\_SETTING\_TEST1 | 0x31 |  |
| TEST0 SMARTRF\_SETTING\_TEST0 | 0x0b |  |

|  |  |
| --- | --- |
| mrfiLogicalChanTable | |
| 3 | SMARTRF\_SETTING\_CHANNR |
| 103 |  |
| 202 |  |
| 212 |  |

|  |  |
| --- | --- |
| mrfiPowerTable | |
| 0x46 |  |
| 0x97 |  |
| 0xfe |  |

MRFI\_MAX\_PAYLOAD\_SIZE 20