# CC2500 Configuration Register Defaults

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| MRFI\_SETTING\_ | IOCFG0 | 6 |  |
| MCSM1 | 0x3c |  |
| MCSM0 | 0x18 | 0x10 | (0x18 & (0x4 | 0x8)) |
| PKTLEN |  | MRFI\_MAX\_FRAME\_SIZE – MRFI\_LENGTH\_FIELD\_SIZE |
| PKTCTRL0 | 0x05 | 0x05 | (0x05 & 0x40) |
| SMARTRF\_ SETTING\_ | FSCTRL1 | 0x07 |  |
| FSCTRL0 | 0x00 |  |
| FREQ2 | 0x5d |  |
| FREQ1 | 0x44 |  |
| FREQ0 | 0xec |  |
| MDMCFG4 | 0x2d |  |
| MDMCFG3 | 0x3b |  |
| MDMCFG2 | 0x73 |  |
| MDMCFG1 | 0x23 | FEC off, min preamble bytes = 4, CHANSPC\_E = 3 |
| MDMCFG0 | 0x3b | CHANSPC\_M = 59 |
| DEVIATN | 0x01 |  |
| FOCCFG | 0x1d |  |
| BSCFG | 0x1c |  |
| AGCCTRL2 | 0xc7 |  |
| AGCCTRL1 | 0x00 |  |
| AGCCTRL0 | 0xb0 |  |
| FREND1 | 0xb6 |  |
| FREND0 | 0x10 |  |
| FSCAL3 | 0xea |  |
| FSCAL2 | 0x0a |  |
| FSCAL1 | 0x00 |  |
| FSCAL0 | 0x11 |  |
| TEST2 | 0x88 |  |
| TEST1 | 0x31 |  |
| TEST0 | 0x0b |  |

|  |  |
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| mrfiLogicalChanTable | |
| 3 | SMARTRF\_SETTING\_CHANNR |
| 103 |  |
| 202 |  |
| 212 |  |

|  |  |
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| mrfiPowerTable | |
| 0x46 |  |
| 0x97 |  |
| 0xfe |  |

MRFI\_MAX\_PAYLOAD\_SIZE 20