

# ADSNARK: Nearly Practical and Privacy-Preserving Proofs on Authenticated Data

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**Abstract.** We study the problem of privacy-preserving proofs on authenticated data, where a party receives data from a trusted source and is requested to prove computations over the data to third parties in a correct and private way, i.e., the third party learns no information on the data but is still assured that the claimed proof is valid. Our work particularly focuses on the challenging requirement that the third party should be able to verify the validity with respect to the specific data authenticated by the source — even without having access to that source. This problem is motivated by various scenarios emerging from several application areas such as wearable computing, smart metering, or general business-to-business interactions. Furthermore, these applications also demand any meaningful solution to satisfy additional properties related to usability and scalability.

In this paper, we formalize the above three-party model, discuss concrete application scenarios, and then we design, build, and evaluate ADSNARK, a nearly practical system for proving arbitrary computations over authenticated data in a privacy-preserving manner. ADSNARK improves significantly over state-of-the-art solutions for this model. For instance, compared to corresponding solutions based on Pinocchio (Oakland’13), ADSNARK achieves up to 25× improvement in proof-computation time and a 20× reduction in prover storage space.

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# 1 Introduction

With the emergence of modern IT services, many aspects of the operation of our society have come to critically depend on the ability to share information between multiple parties, subject to complex information flow restrictions. The advance of information and communication technology has often lead to the deployment of systems that offer the desired functionality, but do not offer a technical solution to enforcing the secure information flow restrictions. Instead, parties must simply trust each other, often without reasonable grounds.

The last few years have seen exciting developments in cryptography, where (quasi-)practical solutions to some of these problems were proposed, prototyped, and sometimes deployed (as we will see later in this section). In this paper, we make further progress in this direction by proposing and efficiently instantiating a new cryptographic primitive called AD-SNARK, which targets an important class of applications that is out of reach of current technology. Such applications involve a potentially large set of secret data and three parties with the following trust relationships:

- The *data owner* wishes to keep her data secret, but is forced by circumstances to reveal partial information on this data to a service provider. Typically, this is an aggregated result computed by some public function  $f$  on the secret data.
- The *service provider* does not trust the data owner to correctly compute the partial information on the data, but wants to be convinced of its validity.
- The data owner has access to a *trusted source*, who can be given local access to the data, and who is trusted by the service provider to vouch for the quality and legitimacy of the data.

For concreteness, let us look at a few applications that fall into this model, and where the public function that must be applied to the data has varying degrees of complexity.

**Health Risk Assessment.** A wearable biosensor [Vit14, BBC14] collects fine-grained health information of an individual; the individual should give this information to a health insurance company that wants to assess her health risk in order to evaluate a corresponding premium. Privacy determines that the fine-grained health data collected by the sensor remains secret as it may reveal more about the individual’s lifestyle and habits than she wishes to reveal. The computation of the premium due to the insurance company (or an aggregate, less privacy-invasive, information of the collected data) should therefore be carried out by the client. However, the client must convince the insurance company that this computation is correct *and* performed on *legitimate* data produced by the biosensor (we call this property *integrity*). In this setting, the biosensor can play the role of the trusted source, provided that it is equipped to cryptographically authenticate the individual measurements that it produces. Then the AD-SNARK primitive can be used to provide the required assurance to the health insurance company.

**Smart Metering.** The service provider of some commodity installs a trusted device in the facilities of the client. This trusted device periodically measures consumptions and produces a list of readings, which are delivered to the client; the client should give these readings to the service provider for billing purposes. For privacy, the client may not want to disclose these measurements as they may reveal more about the client’s habits than she wishes to reveal (see, e.g., [AF10]). For integrity, the supplier wants to evaluate a correct bill and prevent customers from cheating. As before, the customer keeps all the readings provided by the local meter, which must be able to authenticate the data and operate as a trusted source. Then, the customer computes the amount due to the provider, and uses AD-SNARK to prove that the result is correct.

**Financial Audits.** Organizations are often subject to financial audits. Auditors will typically look at specific parts of the accounting data and assert that the results of relevant computations are accurate. However the accounting data should be treated as sensitive information due to its business-critical nature, and minimizing the amount of information disclosed to auditors is desirable. In this scenario, the auditor plays the role of the service provider, and the organization the role of the data owner. The natural entity to play the role of the trusted source is the person (or third party) who is legally responsible for certifying the accounts of the organization, e.g., the official bookkeeper. This entity would authenticate the accounting data, so that the organization could internally compute the audit data in a way that is verifiable by the auditors with respect to both correctness and legitimacy. As intended, using AD-SNARK in this context will transfer the responsibility of any wrongdoing to the official bookkeeper.

In Section 7 we present three more example applications: pay-as-you-drive insurance, loyalty cards, and health statistics. We believe that, with the rise of small computing devices and an increased awareness with respect to privacy protection, many more applications will come to fall into this three-party scenario.

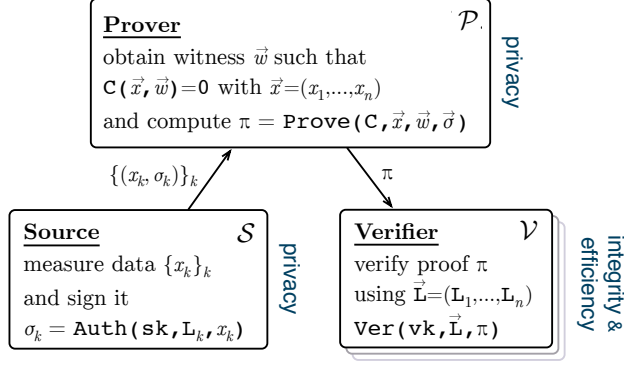
Although the trust model in all of the previous applications is the same, the complexity of the associated computations varies significantly. Solutions have been proposed for smart-metering, pay-as-you-drive insurance, and loyalty cards, e.g., in [RD11, FKDL13], and [FL14], respectively (and also for other applications of similarly low complexity). However, currently no *generic* solution is able to scale in a satisfactory way to deal with computations of arbitrary size such as those required for scenarios like the ones of financial audits or health statistics. Furthermore, although some scenarios admit to a close relation between the trusted source and the service provider that could lead to secret information being shared between the two (in the style of symmetric cryptography), other scenarios require verification for multiple parties, i.e., a form of public verifiability that is even more challenging. The AD-SNARK primitive and the efficient instantiation that we propose in this paper provides a practical solution for the moderately complex computations, even with public verifiability. Furthermore, the proposed AD-SNARK construction is as practical as the existing state of the art solutions for computations of arbitrary size on non-authenticated data.<sup>5</sup>

**Formal Model.** We now illustrate more formally the three party model we have introduced above (see Figure 1). We consider a scenario in which a prover  $\mathcal{P}$  (the data owner) is requested to prove certain computations  $C(D)$  on input data  $D$  to third parties  $\mathcal{V}$  (one or more service providers), which we call the verifiers. Since the two parties  $\mathcal{P}$  and  $\mathcal{V}$  may not trust each other, we are interested in the simultaneous achievement of two main security properties: (1) *integrity*, in the sense that  $\mathcal{V}$  should be convinced about the correctness of  $C(D)$ . In particular, in order to verify that this statement holds for some specific input  $D$ , the data is assumed to be generated and authenticated by some *trusted source*  $\mathcal{S}$ ; and (2) *privacy*, in the sense that  $\mathcal{V}$  should not learn any information about  $D$  beyond what is trivially revealed by  $C(D)$ .

In addition to the security requirements above, any meaningful solution has to meet the following properties that have been identified as key for practical scalability in previous work: (3) *efficiency*, meaning that  $\mathcal{V}$ 's verification cost should be much cheaper than the cost of computing  $C(D)$ ; and (4) *data independence*, in the sense that the data source  $\mathcal{S}$  should be independent of  $\mathcal{P}$ , i.e.,  $\mathcal{S}$  should be able to provide  $D$  without knowing in advance what computations will be executed on  $D$ .

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<sup>5</sup> Hence the designation “nearly practical” in the title of the paper.



**Figure 1.** Three-party scenario in which a source  $\mathcal{S}$  authenticates data  $x_k$ , and a prover  $\mathcal{P}$  proves to a verifier  $\mathcal{V}$  the satisfiability of a circuit  $C$  based on  $x_k$ . The source and the prover are interested in data privacy, whereas the verifier is interested in integrity and efficiency.

(e.g., the billing function may change over time). In particular, also  $D$ 's size should not be fixed in advance, i.e.,  $\mathcal{S}$  can continuously provide data to  $\mathcal{P}$ , even after some proofs have been generated.

**Related Work.** The simultaneous achievement of integrity and privacy is a fundamental goal that has a long research history starting with the seminal work on zero-knowledge proofs [GMR89]. In the last years, the efficiency of zero-knowledge proofs has improved a lot, and nowadays we are on the verge of having nearly practical schemes for general-purpose computations [PGHR13, BSCG<sup>+</sup>13, BSCTV14]. Proofs on authenticated data are an important class of proofs that have been considered earlier especially in very specialized contexts such as credentials and electronic cash [Cha85, Dam88, LRSW99, MEK<sup>+</sup>10]. In the more general case of proving arbitrary computations over authenticated data, there is however little prior work, especially if one is concerned about achieving practical efficiency. While we review this related work later in Section 6, at this point we mention that the recent work ZQL [FKDL13] aimed to address this problem by considering a three party setting such as the one we presented above. ZQL provides an expression language for (privacy-preserving) processing of data that can be originated (i.e., authenticated) by trusted data sources, and proposes a cryptographic scheme that achieves integrity, privacy, and data independence. However, the current ZQL language has some intrinsic limitations that limit its applicability to arbitrary computations while achieving efficiency (i.e., if the verifier should perform less work than that required to generate the proof). In summary, while we do have efficient zero-knowledge proof systems for arbitrary computations, in the case of proofs on authenticated data the situation is not satisfactory.

## 1.1 Detailed Contributions

Inspired by the goals of ZQL, we formalize a cryptographic primitive for privacy-preserving proofs on authenticated data, and we propose a new realization that achieves the desired efficiency goal for arbitrary computations. We then build a system called ADSNARK and evaluate its performance in comparison with solutions based on the state of the art. More in detail, our contributions are the following.

We fully formalize a model for the above problem by defining a new cryptographic primitive that we call *Succinct Non-Interactive Arguments of Knowledge on Authenticated Data* (or AD-SNARK,

for short). Succinct Non-Interactive Arguments, first introduced by Micali under the name of “CS proofs” [Mic94], are proof systems that provide *succinct verification*, i.e., the verifier is able to check a long poly-time computation in much less time than that required to run the computation, given the witness. Our new notion of AD-SNARKs extends SNARKs to explicitly capture proofs of  $\mathcal{NP}$  relations  $R(x, w)$  in which the statement  $x$  (or a part of it) is *authenticated*. More precisely, the main difference between SNARKs and AD-SNARKs is that in the former, the verifier always knows the statement, whereas in the latter, the authenticated statements are not disclosed to the verifier, yet the verifier can be assured about the existence of  $w$  such that  $R(x, w)$  holds for the specific  $x$  authenticated by some trusted source. Moreover, to model privacy (and looking ahead to our applications) we define the zero-knowledge property to hold not only for the witnesses of the relation, but also for the authenticated statements. In particular, our zero-knowledge definition holds also against adversaries who generate the authentication keys.

Turning our attention to realizations, we show that AD-SNARKs can be constructed in a generic fashion by embedding digital signatures into SNARKs. However, motivated by the fact that this “generic construction” is not efficient in practice, our second contribution is a *direct and more efficient realization* of AD-SNARKs, that from now on we refer to as ADSNARK. Compared to instantiating the generic construction with state-of-the-art SNARK schemes, ADSNARK performs way better on the prover side, and achieves a level of efficiency that makes it a plausible candidate for real-world deployment. In what follows we give more details on this efficiency aspect: We first discuss the efficiency of the generic construction with state-of-the-art instantiations, and then we describe our solution.

ON THE (IN)EFFICIENCY OF THE GENERIC CONSTRUCTION. The idea of the generic (not very practical) construction of AD-SNARK for an  $\mathcal{NP}$  relation  $R(x, w)$  is to let the prover  $\mathcal{P}$  prove an extended  $\mathcal{NP}$  relation  $R'$  which contains the set of tuples  $(x', w')$  with  $x' = (|x|, \text{pk})$ ,  $w' = (w, x, \sigma)$ , and  $\sigma = (\sigma_1, \dots, \sigma_{|x|})$ , such that there is a valid signature  $\sigma_i$  for every statement value  $x_i$  at position  $i$  under public key  $\text{pk}$ . The problem with this generic construction is that, in practice, a proof for such extended relation  $R'$  is much more expensive than a proof for  $R$ . The issue is that  $R'$  needs to “embed” the verification algorithm of a signature scheme. If we consider very efficient SNARKs, such as the recent optimization of Pinocchio [PGHR13] proposed in [BSCTV14], then embedding the verification algorithm means encoding the verification algorithm of the signature with an arithmetic circuit over a specific finite field  $\mathbb{F}_p$  (where  $p$  is a large prime, the order of some bilinear groups), and then creating a Quadratic Arithmetic Program [GGPR13], a QAP for short, out of this circuit. Without going into the details of QAPs (we will review them later in Section 2), we note that the efficiency of the prover in these systems depends on the size of the QAP, which in turn depends on the number of multiplication gates in the relation satisfiability circuit.

Our main observation is that the circuit resulting from expressing the verification algorithm of a digital signature scheme is very likely to be quite inefficient (from a QAP perspective), especially for the prover. Such inefficiency stems from the fact that the circuit would contain a huge number of multiplication gates. In Section 3.3 we discuss why this is the case for various examples of signatures in both the random oracle and the standard model, and based on different algebraic problems. Our conclusions indicate that a QAP encoding a signature verification circuit is likely to have significantly more than one thousand multiplications for *every* signature that must be checked. If, for instance, we consider smart-metering, in which the prover wants to certify about 1 000 (signed) meter readings (amounting to approximately 1 month of electricity measurements), the costs can become prohibitive!

	AD-PGHR	ADSNARK	Improvement
Key Generator	299 s	16 s	18.7×
Prover	491 s	20 s	24.5×
Verifier	0.062 s	(PK) 0.61 s (SK) 0.035 s	0.1× 1.8×
Proving key size	319 MB	16 MB	19.9×
Verification key size	31 KB	31 KB	same
Proof size	0.3 KB	(PK) 126 KB (SK) 0.4 KB	0.002× 0.75×

**Figure 2.** Comparison between ADSNARK and the generic solution (AD-PGHR) based on the [BSCTV14] SNARK considering an arithmetic circuit with 50K multiplication gates and 1000 authenticated inputs. Results obtained by running `libsark` for AD-PGHR and our implementation (based on `libsark`) of ADSNARK, both at a 128-bits security level.

**OUR SOLUTION.** In contrast, we propose ADSNARK, a new, *direct*, AD-SNARK scheme that achieves the same efficiency as state-of-the-art SNARKs, e.g., [BSCTV14], yet it additionally allows for proofs on authenticated statements. Our scheme builds upon an optimized version of Pinocchio proposed and implemented in [BSCTV14], and our key technical contribution is a technique (illustrated in Section 1.2) for embedding the authentication verification mechanism directly in the proof system, without having to resort to extended relations that would incur the efficiency loss discussed earlier. As a result, the performance of our scheme is almost the same as that of running [BSCTV14], but with the additional benefit of obtaining proofs about authenticated values.

When comparing our direct construction with an instantiation of the generic scheme with [BSCTV14], ADSNARK introduces a dramatic improvement (cf. Figure 2 above) in the generation of setup keys (for the relation) and proofs, which is currently the main bottleneck of state-of-the-art SNARKs (e.g., [PGHR13, BSCG<sup>+</sup>13, BSCTV14]). Namely, while these schemes perform excellently in terms of verification time and proof size, the performances get much worse when it comes to generating keys and proofs, especially for relations that have “unfriendly” arithmetic circuit representations, such as signature verification algorithms, as discussed earlier. This is where our technique for avoiding the explicit encoding of signature verification in the circuits allows us to use much smaller QAPs, thus saving at least one thousand multiplication gates *per* authenticated input. This improvement is clearly evident in our experimental results that show that the prover can obtain up to a 25× speed-up (20 s vs. 8 mins) and a 20× reduction in storage (16 MB vs. 320 MB). As we discuss later, on the verifier side ADSNARK allows for two different verification modes: one using the secret authentication key and one completely public. Although in the secret-key case, ADSNARK essentially achieves the *same* verification efficiency and proof size of the generic solution, our scheme pays more for public verification. However, in contrast to what happens on the prover side of the generic solution, the public verification of ADSNARK still achieves timing (0.61 s) and proof size (126 KB) that can be definitely considered practical.

## 1.2 An Intuitive Description of Our Techniques

The key idea for the construction of our AD-SNARK scheme is to build upon SNARKs based on QAPs, and in particular on the PGHR scheme in [BSCTV14]. At a high level, our technique consists of extending PGHR by embedding a linearly-homomorphic MAC that enforces the prover to run the PGHR’s Prove algorithm on correctly authenticated statements.

More precisely, the PGHR verifier, given a statement  $x = (x_1, \dots, x_n)$ , has to compute the linear combination  $a_{in} = \sum_{k=1}^n x_k \cdot a_k(X)$  (where the  $a_k(X)$  are the QAP polynomials). However, recall that in AD-SNARKs the verifier does not know the statement  $x$ , and thus is not able to compute  $a_{in}$ . Our key idea to solve this issue is to shift the computation of the linear combination  $a_{in}$  from the verifier to the prover. Then, to enforce a cheating prover to provide the correct  $a_{in}$ , we ask the prover to additionally show that  $a_{in}$  was indeed obtained by using authenticated values  $x_k$ . To this end, we employ another proof system, namely efficient linearly-homomorphic MACs [CF13, BFR13], that are particularly suitable for linear computations over authenticated data. Specifically, we designed a novel homomorphic MAC (which is implicitly embedded in our AD-SNARK construction) that fits the above setting.

This technique, however, does not completely solve the problem: a further complication arises from the fact that in order to achieve zero-knowledge, the value  $a_{in}$  computed by the prover must be randomized (by adding a random multiple of the QAP target polynomial  $z(X)$ ). Unfortunately, homomorphic MACs are known to authenticate only deterministic computations. We solve this issue using the following ideas. First, we provide a novel technique to publicly re-randomize our homomorphic MACs: roughly speaking, by publicly revealing a MAC of  $z(X)$ . Second, we enforce the prover to use the same random coefficient for  $z(X)$  in both  $a_{in}$  and its MAC. Intuitively, this is achieved by asking the prover to provide the linear combination  $a_{in}$  in two distinct subspaces. A final observation is that by using a MAC we only get secret-key verification. Although this may not be an issue in several applications, we also show how to further generalize these techniques to obtain public verification.

### 1.3 Organization

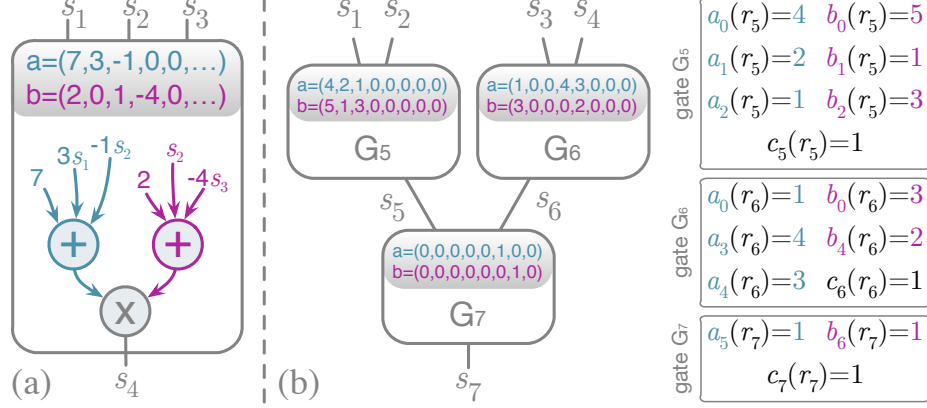
The paper is organized as follows. In Section 2, we recall common definitions and background information on QAPs. Section 3 presents our definition of AD-SNARKs, the generic construction, and a discussion on the efficiency of encoding signature verification with arithmetic circuits. We describe our ADSNARK scheme in Section 4 together with a theoretical evaluation and comparison to the generic solution. In Section 5, we present our implementation and discuss the experimental results. Section 6 discusses further related work, Section 7 provides the description of more application scenarios, and finally Section 8 concludes the paper. The appendix includes additional background and the discussion of two extensions of AD-SNARKs: handling multiple data sources, and achieving (amortized) constant-time verification.

## 2 Background

In this section, we review the notation and some basic definitions that we will use in our work.

**Notation.** We will denote with  $\lambda \in \mathbb{N}$  a security parameter. We say that a function  $\epsilon$  is *negligible* if it vanishes faster than the inverse of any polynomial. If not explicitly specified otherwise, negligible functions are negligible with respect to  $\lambda$ . If  $S$  is a set,  $x \leftarrow_{\mathcal{R}} S$  denotes the process of selecting  $x$  uniformly at random in  $S$ . If  $\mathcal{A}$  is a probabilistic algorithm,  $x \leftarrow_{\mathcal{R}} \mathcal{A}(\cdot)$  denotes the process of running  $\mathcal{A}$  on some appropriate input and assigning its output to  $x$ . Moreover, for a positive integer  $n$ , we denote by  $[n]$  the set  $\{1, \dots, n\}$ . We denote by  $\mathbb{F}$  a finite field and  $\mathbb{F}_n$  is the field of size  $n$ . When  $n$  is a prime number, then elements of  $\mathbb{F}_n$  are represented as integers modulo  $n$ . Elements of  $\mathbb{F}$  are typically denoted by greek letters.  $\mathbb{F}[X]$  denotes the field of polynomials in one variable  $X$  and coefficients in  $\mathbb{F}$ , while  $\mathbb{F}^{\leq d}[X]$  is the subring of polynomials in  $\mathbb{F}[X]$  of degree at most  $d$ .





**Figure 3.** Part (a): A bilinear gate representing the arithmetic function  $(7 + 3s_1 - 1s_2) \cdot (2 + s_2 - 4s_3)$  specified by coefficients  $a$  and  $b$ .

Part (b): A QAP for an arithmetic circuit with 4 input wires, 1 output wire, 3 bilinear gates. The circuit encodes the function  $f(s_1, s_2, s_3, s_4) = (4 + 2s_1 + s_2) \cdot (5 + s_1 + 3s_2) \cdot (1 + 4s_3 + 3s_4) \cdot (3 + 2s_4)$ . The non-zero equations for the QAP polynomials are shown on the right.

**Algebraic Tools.** Let  $\mathcal{G}(1^\lambda)$  be an algorithm that, upon input of the security parameter  $1^\lambda$ , outputs the description of (asymmetric) bilinear groups  $\text{bgpp} = (p, \mathbb{G}_1, \mathbb{G}_2, \mathbb{G}_T, e, \mathcal{P}_1, \mathcal{P}_2)$  where  $\mathbb{G}_1, \mathbb{G}_2$ , and  $\mathbb{G}_T$  are groups of the same prime order  $p > 2^\lambda$ ;  $\mathcal{P}_1 \in \mathbb{G}_1$  and  $\mathcal{P}_2 \in \mathbb{G}_2$  are the respective generators; and  $e : \mathbb{G}_1 \times \mathbb{G}_2 \rightarrow \mathbb{G}_T$  is an efficiently computable bilinear map. We call such an algorithm  $\mathcal{G}$  a *bilinear group generator*. Note that  $\mathbb{G}_1$  and  $\mathbb{G}_2$  are additive groups, whereas  $\mathbb{G}_T$  is a multiplicative group. In this work we rely on specific computational assumptions in such bilinear groups: the  $q$ -DHE [CKS09], the  $q$ -BDHE [BBG05], and the  $q$ -PKE [Gro10] assumptions.

**Arithmetic Circuits and Quadratic Arithmetic Programs.** An *arithmetic circuit*  $C$  over a finite field  $\mathbb{F}$  consists of addition and multiplication *gates* and of a set of *wires* between the gates. The wires carry values over  $\mathbb{F}$ . As in previous work [BSCTV14], here we consider only arithmetic circuits with *bilinear gates*: a gate with inputs  $\vec{x} = (x_1, \dots, x_k)$  is *bilinear* if its output can be written as inner product  $\langle \vec{a}, (1, x_1, \dots, x_k) \rangle \cdot \langle \vec{b}, (1, x_1, \dots, x_k) \rangle$  for some  $\vec{a}, \vec{b} \in \mathbb{F}^{k+1}$ . Note that this definition includes addition, multiplication, and constant gates (cf. Fig. 3(a) for an example).

Associated to any arithmetic circuit, we define a satisfaction problem as follows.

**Definition 1 (Arith. Circuit Satisfaction [BSCTV14]).** The circuit satisfaction problem of a circuit  $C : \mathbb{F}^n \times \mathbb{F}^h \rightarrow \mathbb{F}^l$  with bilinear gates is defined by the relation  $\mathcal{R}_C = \{(\vec{x}, \vec{w}) \in \mathbb{F}^n \times \mathbb{F}^h : C(\vec{x}, \vec{w}) = 0^l\}$  and its language is  $\mathcal{L}_C = \{\vec{x} \in \mathbb{F}^n : \exists \vec{w} \in \mathbb{F}^h, C(\vec{x}, \vec{w}) = 0^l\}$ .

The state-of-the-art SNARK schemes that we build on in this paper directly operate on a different model to represent computations called *quadratic arithmetic programs* (QAPs).

**Definition 2 (QAP [GGPR13]).** A quadratic arithmetic program  $Q$  of size  $m$  and degree  $d$  over  $\mathbb{F}$  consists of three vectors of  $m + 1$  polynomials  $\vec{a}, \vec{b}, \vec{c} \in \mathbb{F}^{\leq d-1}[X]$  of degree at most  $d - 1$ , and a target polynomial  $z(X) \in \mathbb{F}[X]$  of degree exactly  $d$ .

Associated to any QAP, there is a satisfaction problem defined as follows.

**Definition 3 (QAP Satisfaction).** *The satisfaction problem of a QAP  $Q = (\vec{a}, \vec{b}, \vec{c}, z)$  of size  $m$  and degree  $d$  is the relation  $\mathcal{R}_Q$  of pairs  $(\vec{x}, \vec{s})$  such that:*

- (1)  $\vec{x} \in \mathbb{F}^n, \vec{s} \in \mathbb{F}^m$  for some  $n \leq m$ ;
- (2)  $x_i = s_i$  for  $i \in [n]$ , i.e.,  $\vec{s}$  extends  $\vec{x}$ ;
- (3)  $z(X)$  divides the polynomial  $p(X)$  defined as

$$p(X) = \left(a_0(X) + \sum_{i=1}^m s_i a_i(X)\right) \cdot \left(b_0(X) + \sum_{i=1}^m s_i b_i(X)\right) - \left(c_0(X) + \sum_{i=1}^m s_i c_i(X)\right)$$

The following result implies that one can use any QAP-based SNARK scheme as an efficient SNARK scheme taking computations more conveniently represented as arithmetic circuits.

**Lemma 1 (Constructing QAPs [GGPR13, BSCTV14]).** *There exist two polynomial time algorithms QAPInst and QAPwit such that, for any circuit  $C : \mathbb{F}^n \times \mathbb{F}^h \rightarrow \mathbb{F}^l$  with  $u$  wires and  $v$  (bilinear) gates,  $Q_C = (\vec{a}, \vec{b}, \vec{c}, z) = \text{QAPInst}(C)$  is a QAP of size  $m$  and degree  $d$  over  $\mathbb{F}$  satisfying the following properties:*

Efficiency:  $m = u$ , and  $d = v + l + 1$ .

Completeness: For any  $(\vec{x}, \vec{w}) \in \mathcal{R}_C$ , if it holds that  $\vec{s} = \text{QAPwit}(C, \vec{x}, \vec{w})$  then  $(\vec{x}, \vec{s}) \in \mathcal{R}_{Q_C}$ .

Proof of Knowledge: For any  $(\vec{x}, \vec{s}) \in \mathcal{R}_{Q_C}$ , it holds  $(\vec{x}, \vec{w}) \in \mathcal{R}_C$  where  $\vec{w}$  is a prefix of  $\vec{s}$ .

Non-Degeneracy: the polynomials  $a_0(X), \dots, a_n(X)$  are all nonzero and distinct.

The very basic intuition for building a QAP according to Lemma 1 is to encode the input-output correctness for each bilinear gate in the polynomials  $\vec{a}, \vec{b}, \vec{c}, z$  (see Fig. 3(b) for a simple example). Slightly more in detail, for a gate  $g$  this is done by first selecting an arbitrary value  $r_g \in \mathbb{F}$  (a “root”) and then, for every left wire  $i$  going to gate  $g$ , one imposes  $a_i(r_g) = c$ , where  $c$  is the coefficient which multiplies the value of wire  $i$  in  $g$ ’s left input (note that  $c = 0$  if wire  $i$  is not a left input). A similar process is done for polynomials  $b_i$  and  $c_i$  w.r.t. right input and output wires respectively.<sup>6</sup> Once this procedure has been iterated for every bilinear gate  $g$  (selecting distinct roots  $r_g$ ), one will have essentially obtained three tables of size  $u \cdot v$  with entries  $a_i(r_j)$ ,  $b_i(r_j)$ , and  $c_i(r_j)$ , respectively, where  $i = 0$  to  $u$  are all the wires (where the 0 wire represents constants) and  $j = 1$  to  $v$  are all the bilinear gates. The final QAP polynomials  $\vec{a}, \vec{b}, \vec{c}$  are built by extending each row  $i$  of the table into a polynomial  $a_i(X)$  (resp.  $b_i(X), c_i(X)$ ) of degree  $v - 1$  via interpolation in  $\mathbb{F}$ . The target polynomial  $z(X)$  is the degree- $v$  polynomial defined over the roots  $r_g$  of the  $v$  bilinear gates:  $z(X) := \prod_{g=1}^v (X - r_g)$ .<sup>7</sup> To see why the satisfiability of the QAP implies the satisfiability of the circuit, the key observation is that the third condition of Definition 3, i.e.,  $z(X) \mid p(X)$ , means that  $\langle (1, \vec{s}), \vec{a}(r_g) \rangle \cdot \langle (1, \vec{s}), \vec{b}(r_g) \rangle = \langle (1, \vec{s}), \vec{c}(r_g) \rangle$  for all roots  $r_g$  of the target polynomial  $z(X)$ . In other words, given the specific construction of the polynomials, the input-output correctness of every bilinear gate  $g$  of the circuit is satisfied.

<sup>6</sup> The case of  $c_i$  is slightly different as coefficients are only 0 or 1.

<sup>7</sup> More precisely, in construction of Lemma 1 one needs to add one “artificial” bilinear gate for every output wire, plus an additional constraint to guarantee non-degeneracy: from which the final degree is  $d = v + l + 1$ .

### 3 Zero-Knowledge SNARKs over Authenticated Data

In this section, we define the notion of SNARKs [Mic94, BCCT12] on authenticated data (AD-SNARKs, for short). Let  $C : \mathbb{F}^n \times \mathbb{F}^h \rightarrow \mathbb{F}^l$  be an arithmetic circuit, and let  $\mathcal{R}_C = \{(\vec{x}, \vec{w})\} \subseteq \mathbb{F}^n \times \mathbb{F}^h$  be the corresponding circuit satisfaction relation, where  $\vec{x} \in \mathbb{F}^n$  is called the *statement*, and  $\vec{w} \in \mathbb{F}^h$  is the *witness*.

Proof systems for the circuit satisfaction of  $C$  typically consider the problem in which a prover  $\mathcal{P}$  tries to convince a verifier  $\mathcal{V}$  about the existence of a witness  $\vec{w}$  such that  $(\vec{x}, \vec{w}) \in \mathcal{R}_C$ . In this scenario, the statement  $\vec{x}$  is supposed to be public, i.e., it is known to both the prover and the verifier. For example,  $\mathcal{V}$  could be convinced by  $\mathcal{P}$  that 3 colors are sufficient to color a public graph  $\vec{x}$  such that no two adjacent vertices are assigned the same color. The coloring serves as witness  $\vec{w}$ .

In this work, we consider a variation of the above problem in setting in which (1) the statement  $\vec{x}$  (or part of it) is provided to the prover by a trusted source  $\mathcal{S}$ , and (2) the portion of  $\vec{x}$  provided by  $\mathcal{S}$  is not known to  $\mathcal{V}$  (see Figure 1 for illustration). Yet,  $\mathcal{V}$  wants to be convinced by  $\mathcal{P}$  that  $(\vec{x}, \vec{w}) \in \mathcal{R}_C$  holds for the specific  $\vec{x}$  provided by  $\mathcal{S}$ , and not for some other  $\vec{x}'$  of  $\mathcal{P}$ 's choice (which can still be in the language  $\mathcal{L}_C$ ). For example,  $\mathcal{S}$  might have provided a graph  $\vec{x}$  – not known to  $\mathcal{V}$  – for which  $\mathcal{P}$  proves to  $\mathcal{V}$  that  $\vec{x}$  is 3-colorable. A proof for any other graph  $\vec{x}'$  is meaningless.

To formalize the idea that  $\mathcal{V}$  checks that some values unknown to  $\mathcal{V}$  have been authenticated by  $\mathcal{S}$ , we adopt the concept of *labeling* used for homomorphic authenticators [GW13, BFR13]. Namely, we assume that the source  $\mathcal{S}$  authenticates a set of values  $X_{auth} = \{x_i, \dots, x_\ell\}$  against a set of (public) labels  $\mathbf{L} = \{L_i, \dots, L_\ell\}$  by using a secret authentication key (e.g., a signing key).  $\mathcal{S}$  then sends the authenticated  $X_{auth}$  to  $\mathcal{P}$ . Later,  $\mathcal{P}$ 's goal is to prove to  $\mathcal{V}$  that  $(\vec{x}, \vec{w}) \in \mathcal{R}_C$  for a statement  $\vec{x}$  in which some positions have been correctly authenticated by  $\mathcal{S}$ , i.e.,  $x_i \in X_{auth}$  for some  $i \in [n]$ .

For such a proof system, we define the usual properties of *completeness* and *soundness*, and in addition, to model privacy, we define a *zero-knowledge* property. Moreover, since we are interested in efficient and scalable protocols, we define *succinctness* to model that the size of the proofs (and implicitly the verifier's running time) should be independent of the witness' size  $h = |\vec{w}|$ . Finally, we consider AD-SNARKs that can have either public or secret verifiability, the difference being in whether the adversary knows or not the verification key for the authentication tags produced by the data source  $\mathcal{S}$ .

#### 3.1 SNARKs over Authenticated Data

First, we provide the formal definition for SNARKs over authenticated data. The definition of SNARKs of knowledge (i.e., SNARKs) over authenticated data is provided later.

**Definition 4 (AD-SNARG).** *A scheme for Succinct Non-interactive Arguments over Authenticated Data (AD-SNARG, for short) for arithmetic circuit satisfiability consists of a tuple of algorithms (Setup, AuthKG, Auth, AuthVer, Gen, Prove, Ver) satisfying authentication correctness, completeness, succinctness, and adaptive soundness (as defined below):*

**Setup( $1^\lambda$ ):** *On input the security parameter  $\lambda$ , output some common public parameters  $\mathbf{pp}$ . The parameters also define the finite field  $\mathbb{F}$  over which the circuits will be defined.*

**AuthKG( $\mathbf{pp}$ ):** *given the public parameters  $\mathbf{pp}$ , the key generation algorithm outputs a secret authentication key  $\mathbf{sk}$ , a verification key  $\mathbf{vk}$ , and public authentication parameters  $\mathbf{pap}$ .*

$\text{Auth}(\text{sk}, \mathbf{L}, x)$ : the authentication algorithm takes as input the secret authentication key  $\text{sk}$ , a label  $\mathbf{L} \in \mathcal{L}$ , and a value  $x \in \mathbb{F}$ , and it outputs an authentication tag  $\sigma$ .

$\text{AuthVer}(\text{vk}, \sigma, \mathbf{L}, x)$ : the authentication verification algorithm takes as input a verification key  $\text{vk}$ , a tag  $\sigma$ , a label  $\mathbf{L} \in \mathcal{L}$ , and a value  $x \in \mathbb{F}$ . It outputs  $\perp$  (reject) or  $\top$  (accept).

$\text{Gen}(\text{pap}, C)$ : given the public authentication parameters  $\text{pap}$  and an arithmetic circuit  $C : \mathbb{F}^n \times \mathbb{F}^h \rightarrow \mathbb{F}^l$ , the algorithm outputs an evaluation key  $\text{EK}_C$  and a verification key  $\text{VK}_C$ .  $\text{Gen}$  can hence be seen as a circuit encoding algorithm.

$\text{Prove}(\text{EK}_C, \vec{x}, \vec{w}, \vec{\sigma})$ : on input an evaluation key  $\text{EK}_C$ , a statement  $\vec{x} \in \mathbb{F}^n$ , a witness  $\vec{w} \in \mathbb{F}^h$ , and authentication tags for the statement  $\vec{\sigma} = (\sigma_1, \dots, \sigma_n)$ , the proof algorithm outputs a proof of membership  $\pi$  for  $(\vec{x}, \vec{w}) \in \mathcal{R}_C$ . We stress that  $\vec{\sigma}$  does not need to contain authentication tags for all positions: in case a value at position  $i$  is not authenticated, the empty tag  $\sigma_i = \star$  is used instead.

$\text{Ver}(\text{vk}, \text{VK}_C, \vec{\mathbf{L}}, \{x_i\}_{\mathbf{L}_i=\star}, \pi)$ : given the verification key  $\text{vk}$ , a circuit verification key  $\text{VK}_C$ , labels  $\vec{\mathbf{L}} = (\mathbf{L}_1, \dots, \mathbf{L}_n)$  for the statement, unauthenticated statement components  $x_i$ , and a proof  $\pi$ , the verification algorithm outputs  $\perp$  (reject) or  $\top$  (accept).

**AUTHENTICATION CORRECTNESS.** Intuitively, an AD-SNARG scheme has authentication correctness if any tag  $\sigma$  generated by  $\text{Auth}(\text{sk}, \mathbf{L}, x)$  authenticates  $x$  with respect to  $\mathbf{L}$ . More formally, we say that a AD-SNARG scheme satisfies authentication correctness if for any value  $x \in \mathbb{F}$ , all keys  $(\text{sk}, \text{vk}, \text{pap}) \leftarrow_{\mathcal{R}} \text{AuthKG}(1^\lambda)$ , any label  $\mathbf{L} \in \mathcal{L}$ , and any authentication tag  $\sigma \leftarrow_{\mathcal{R}} \text{Auth}(\text{sk}, \mathbf{L}, x)$ , we have that  $\text{AuthVer}(\text{vk}, \sigma, \mathbf{L}, x) = \top$  with probability 1.

**COMPLETENESS.** This property aims at capturing that if the  $\text{Prove}$  algorithm produces  $\pi$  when run on  $(\vec{x}, \vec{w}, \vec{\sigma})$  for some  $(\vec{x}, \vec{w}) \in \mathcal{R}_C$ , then verification  $\text{Ver}(\text{vk}, \text{VK}_C, \mathbf{L}, \{x_i\}_{\mathbf{L}_i=\star}, \pi)$  must output  $\top$  with probability 1 whenever  $\text{AuthVer}(\text{vk}, \sigma_i, \mathbf{L}_i, x_i) = \top$ . More formally, let us fix  $(\text{sk}, \text{vk}, \text{pap}) \leftarrow_{\mathcal{R}} \text{AuthKG}(\text{pp})$ , and a circuit  $C : \mathbb{F}^n \times \mathbb{F}^h \rightarrow \mathbb{F}^l$  with keys  $(\text{EK}_C, \text{VK}_C) \leftarrow_{\mathcal{R}} \text{Gen}(\text{pap}, C)$ . Let  $(\vec{x}, \vec{w}) \in \mathcal{R}_C$  be given. Let  $\vec{\mathbf{L}} = (\mathbf{L}_1, \dots, \mathbf{L}_n) \in (\mathcal{L} \cup \{\star\})^n$  be a vector of labels, and let  $\vec{\sigma} = (\sigma_1, \dots, \sigma_n)$  be tags for the statement such that  $\{\text{AuthVer}(\text{vk}, \sigma_i, \mathbf{L}_i, x_i) = \top\}_{\mathbf{L}_i \neq \star}$ . Then if  $\pi \leftarrow_{\mathcal{R}} \text{Prove}(\text{EK}_C, \vec{x}, \vec{w}, \vec{\sigma})$ , we have that  $\text{Ver}(\text{vk}, \text{VK}_C, \vec{\mathbf{L}}, \{x_i\}_{\mathbf{L}_i=\star}, \pi) = \top$  with probability 1.

**SUCCINCTNESS.** Given a circuit  $C : \mathbb{F}^n \times \mathbb{F}^h \rightarrow \mathbb{F}^l$ , the length of the proof  $\pi$  is bounded by  $|\pi| = \text{poly}(\lambda) \text{polylog}(n, h)$ .

**ADAPTIVE SOUNDNESS.** Intuitively, the soundness property captures that no malicious party can produce proofs that verify correctly for a statement which is not in the language. We formalize our definition via an experiment, called  $\text{Exp}_{\mathcal{A}}^{\text{AD-Soundness}}$ , which is described in Figure 4. The experiment is parametrized by an adversary  $\mathcal{A}$  who is given access to three oracles (aka procedures) **Gen**, **Auth**, and **Ver** that can be (concurrently) run.

The three procedures **Gen**, **Auth**, and **Ver** essentially give to the adversary oracle access to the algorithms  $\text{Gen}$ ,  $\text{Auth}$ , and  $\text{Ver}$ , respectively, with some additional bookkeeping information. In particular, it is worth noting that **Ver** returns the output of  $\text{Ver}$ , and additionally, checks whether a proof accepted by  $\text{Ver}$  (i.e.,  $v = \top$ ) proves a false statement according to  $\mathcal{R}_C$ . In this case, **Ver** sets  $\text{GameOutput} \leftarrow 1$ .

More formally, let  $\mathcal{C}$  be a class of circuits. Then for any  $\lambda \in \mathbb{N}$ , we define the advantage of an adversary  $\mathcal{A}$  in the experiment  $\text{Exp}_{\mathcal{A}}^{\text{AD-Soundness}}(\mathcal{C}, 1^\lambda)$  against AD-Soundness for  $\mathcal{C}$  as

$$\text{Adv}_{\mathcal{A}}^{\text{AD-Soundness}}(\mathcal{C}, \lambda) = \Pr[\text{Exp}_{\mathcal{A}}^{\text{AD-Soundness}}(\mathcal{C}, 1^\lambda) = 1].$$

<pre> <b>Exp</b><sub><math>\mathcal{A}, E</math></sub><sup>AD-Soundness</sup>(<math>1^\lambda</math>): pp <math>\leftarrow_{\mathcal{R}}</math> Setup(<math>1^\lambda</math>) (sk, vk, pap) <math>\leftarrow_{\mathcal{R}}</math> AuthKG(pp) GameOutput <math>\leftarrow</math> 0 <math>S \leftarrow \emptyset</math>, <math>T \leftarrow \{(\star, \star)\}</math> <math>\mathcal{A}^{\text{Gen, Auth, Ver}}(\text{pp}, \text{pap})</math> Return GameOutput  <b>procedure</b> Gen(<math>C</math>) (EK<sub>C</sub>, VK<sub>C</sub>) <math>\leftarrow_{\mathcal{R}}</math> Gen(pap, <math>C</math>) <math>S \leftarrow S \cup \{(C, \text{EK}_C, \text{VK}_C)\}</math> Return (EK<sub>C</sub>, VK<sub>C</sub>)  <b>procedure</b> Auth(<math>L, x</math>) if (<math>L, \cdot, \cdot</math>) <math>\in T</math> Return <math>\perp</math> <math>\sigma \leftarrow_{\mathcal{R}}</math> Auth(sk, <math>L, x</math>) <math>T \leftarrow T \cup \{(L, x, \sigma)\}</math> Return <math>\sigma</math> </pre>	<pre> <b>procedure</b> Ver(<math>C, \vec{L}, \{x_i\}_{L_i=\star}, \pi</math>) if (<math>C, \cdot, \cdot</math>) <math>\notin S</math> then Return <math>\perp</math> fetch VK<sub>C</sub> with (<math>C, \cdot, \text{VK}_C</math>) <math>\in S</math> <math>v \leftarrow \text{Ver}(\text{vk}, \text{VK}_C, \vec{L}, \{x_i\}_{L_i=\star}, \pi)</math> if <math>v = \top</math> then   if <math>\exists L_i \in \vec{L} : (L_i, \cdot, \cdot) \notin T</math> then     GameOutput <math>\leftarrow</math> 1 // Type 1   else     fetch <math>\vec{x} = (x_1, \dots, x_n)</math>     with <math>\{(L_1, x_1, \cdot), \dots, (L_n, x_n, \cdot)\} \subseteq T</math>     for all <math>L_i \neq \star</math>     if <math>\vec{x} \notin \mathcal{L}_C</math> then       GameOutput <math>\leftarrow</math> 1 // Type 2 Return <math>v</math> </pre>
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Figure 4. Game AD-Soundness.

An AD-SNARK with respect to a class of circuits  $\mathcal{C}$  is *adaptive computationally sound* if for any PPT  $\mathcal{A}$ , it holds that  $\text{Adv}_{\mathcal{A}}^{\text{AD-Soundness}}(\mathcal{C}, \lambda)$  is negligible in  $\lambda$ .

Our soundness definition is inspired by the security definition for homomorphic MACs [GW13, CF13, BFR13]. The catch here is that there are essentially two ways to create a “cheating proof”, and thus to break the soundness of an AD-SNARG. The first way, Type 1, is to produce an accepting proof without having ever queried an authentication tag for a label  $L_i$ . This basically captures that, in order to create a valid proof, one needs to have all authenticated parts of the statement, each with a valid authentication tag. The second way to break the security, Type 2, is the more “classical” one, i.e., generating a proof that accepts for a tuple  $(\vec{x}, \vec{w})$  which is not the correct one, i.e.,  $\vec{x} \notin \mathcal{L}_C$ .

Second, we note that the above game definition captures the setting in which the verification key vk is kept secret. The definition for the publicly verifiable setting is obtained by providing vk to the adversary.

**AD-SNARKs.** An AD-SNARG of knowledge (AD-SNARK) is an AD-SNARG where adaptive soundness is strengthened as follows.

**Definition 5 (AD-SNARK).** A tuple of algorithms (Setup, AuthKG, Auth, AuthVer, Gen, Prove, Ver) is an AD-SNARK if it is an AD-SNARG where adaptive soundness is replaced by the stronger property of adaptive proof of knowledge (as defined below).

**ADAPTIVE PROOF OF KNOWLEDGE.** Consider a variation of the adaptive soundness experiment that is parametrized by an additional algorithm  $E$  called the *extractor*. Both  $\mathcal{A}$  and  $E$  run on exactly the same input and random tape, including some auxiliary input  $z$ .  $E$  is an algorithm that, for every verification query of  $\mathcal{A}$  that is accepted by the Ver algorithm, outputs a witness  $\vec{w}$ . One should think of such  $E$  as  $\mathcal{A}$  itself, and the extraction capability intuitively means that if  $\mathcal{A}$  is able to produce an accepting proof, then  $\mathcal{A}$  must know the corresponding witness, and thus such witness can be extracted from  $\mathcal{A}$ ’s memory. A detailed description of the experiment procedures is presented in Figure 5.

<p><b>Exp<sub>A,E</sub><sup>AD-PoK</sup>(1<sup>λ</sup>):</b>  pp <math>\leftarrow_{\mathcal{R}}</math> Setup(1<sup>λ</sup>)  (sk, vk, pap) <math>\leftarrow_{\mathcal{R}}</math> AuthKG(pp)  GameOutput <math>\leftarrow</math> 0  S <math>\leftarrow \emptyset</math>, T <math>\leftarrow \{(\star, \star)\}</math>  <math>\mathcal{A}^{\text{Gen,Auth,Ver}}(\text{pp}, \text{pap}, z)</math>  Return GameOutput</p> <p><b>procedure Gen(C)</b>  (EK<sub>C</sub>, VK<sub>C</sub>) <math>\leftarrow_{\mathcal{R}}</math> Gen(pap, C)  S <math>\leftarrow S \cup \{(C, \text{EK}_C, \text{VK}_C)\}</math>  Return (EK<sub>C</sub>, VK<sub>C</sub>)</p> <p><b>procedure Auth(L, x)</b>  if (L, ·, ·) <math>\in T</math> Return <math>\perp</math>  σ <math>\leftarrow_{\mathcal{R}}</math> Auth(sk, L, x)  T <math>\leftarrow T \cup \{(L, x, \sigma)\}</math>  Return σ</p>	<p><b>procedure Ver(C, <math>\vec{L}</math>, {x<sub>i</sub>}<sub>L<sub>i</sub>=*</sub>, π)</b>  if (C, ·, ·) <math>\notin S</math> then Return <math>\perp</math>  fetch VK<sub>C</sub> with (C, ·, VK<sub>C</sub>) <math>\in S</math>  v <math>\leftarrow</math> Ver(vk, VK<sub>C</sub>, <math>\vec{L}</math>, {x<sub>i</sub>}<sub>L<sub>i</sub>=*</sub>, π)  if v = <math>\top</math> then  if <math>\exists L_i \in \vec{L} : (L_i, \cdot, \cdot) \notin T</math> then  GameOutput <math>\leftarrow</math> 1 // Type 1  else  fetch <math>\vec{x} = (x_1, \dots, x_n)</math>  with <math>\{(L_1, x_1, \cdot), \dots, (L_n, x_n, \cdot)\} \subseteq T</math>  for all L<sub>i</sub> <math>\neq \star</math>  <math>\vec{w} \leftarrow E(\text{pp}, \text{pap}, z, T, S, \text{Coins}[A])</math>  if (<math>\vec{x}, \vec{w}</math>) <math>\notin \mathcal{R}_C</math> then  GameOutput <math>\leftarrow</math> 1 // Type 2  Return v</p>
--	--

**Figure 5.** Experiment for the adaptive proof of knowledge definition.

Then we say that a scheme ADSNARK satisfies *adaptive proof of knowledge* for  $\mathcal{C}$  if for any sufficiently large  $\lambda \in \mathbb{N}$ , and for every PPT adversary  $\mathcal{A}$ , there exists a PPT extractor  $E$  such that for every polynomial-size auxiliary input  $z \in \{0, 1\}^{\text{poly}(\lambda)}$  the probability  $\Pr[\mathbf{Exp}_{\mathcal{A},E}^{\text{AD-PoK}}(\mathcal{C}, \lambda, z) = 1]$  is negligible.

**Zero-Knowledge AD-SNARKs.** Finally we extend the AD-SNARK definition with the zero-knowledge property. Loosely speaking, a *zero-knowledge* AD-SNARK is an AD-SNARK in which the Prove algorithm generates proofs  $\pi$  that reveal no information: neither about the *witness*, nor about the authenticated statements. In other words, the proofs do not reveal anything beyond what is known by the verifiers when checking a proof. A formal definition follows.

**Definition 6 (Zero-Knowledge AD-SNARKs).** A zero-knowledge AD-SNARK is an AD-SNARK that satisfies the following additional property “ZERO-KNOWLEDGE”. Let  $C \in \mathcal{C}$  be an arithmetic circuit. Then there exists a simulator  $\text{Sim} = (\text{Sim}_1, \text{Sim}_2)$ , such that for all PPT distinguishers  $\mathcal{D}$ , the following difference is negligible

$$|\Pr[\mathbf{Exp}_{\text{Real}}^{\mathcal{D},C}(1^\lambda) = 1] - \Pr[\mathbf{Exp}_{\text{Sim}}^{\mathcal{D},C}(1^\lambda) = 1]|$$

where the experiments Real and Sim are defined as follows:

<p><b>Exp<sub>D,C</sub><sup>Real</sup>(1<sup>λ</sup>) :</b>  pp <math>\leftarrow_{\mathcal{R}}</math> Setup(1<sup>λ</sup>)  (sk, vk, pap) <math>\leftarrow_{\mathcal{R}}</math> <math>\mathcal{D}(1^\lambda, \text{pp})</math>  (EK<sub>C</sub>, VK<sub>C</sub>) <math>\leftarrow_{\mathcal{R}}</math> Gen(pap, C)  <math>(\vec{x}, \vec{L}, \vec{\sigma}, \vec{w}) \leftarrow \mathcal{D}(\text{EK}_C, \text{VK}_C)</math>  π <math>\leftarrow_{\mathcal{R}}</math> Prove(EK<sub>C</sub>, <math>\vec{x}, \vec{w}, \vec{\sigma}</math>)  if (<math>\vec{x}, \vec{w}</math>) <math>\notin \mathcal{R}_C \vee</math>  <math>\exists i \in [n]</math>,  AuthVer(vk, σ<sub>i</sub>, L<sub>i</sub>, x<sub>i</sub>) = <math>\perp</math>  then Return 0  else Return <math>\mathcal{D}(\pi)</math></p>	<p><b>Exp<sub>D,C</sub><sup>Sim</sup>(1<sup>λ</sup>) :</b>  pp <math>\leftarrow_{\mathcal{R}}</math> Setup(1<sup>λ</sup>)  (sk, vk, pap) <math>\leftarrow_{\mathcal{R}}</math> <math>\mathcal{D}(1^\lambda, \text{pp})</math>  (EK<sub>C</sub>, VK<sub>C</sub>, td)  <math>\leftarrow_{\mathcal{R}}</math> Sim<sub>1</sub>(sk, vk, pp, pap, C)  <math>(\vec{x}, \vec{L}, \vec{\sigma}, \vec{w}) \leftarrow \mathcal{D}(\text{EK}_C, \text{VK}_C)</math>  π <math>\leftarrow_{\mathcal{R}}</math> Sim<sub>2</sub>(td, L, {x<sub>i</sub>}<sub>L<sub>i</sub>=*</sub>)  if (<math>\vec{x}, \vec{w}</math>) <math>\notin \mathcal{R}_C \vee</math>  <math>\exists i \in [n]</math>,  AuthVer(vk, σ<sub>i</sub>, L<sub>i</sub>, x<sub>i</sub>) = <math>\perp</math>  then Return 0  else Return <math>\mathcal{D}(\pi)</math></p>
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Note that the distinguisher  $\mathcal{D}$  in the above game has a shared state that is persistent over all invocations of  $\mathcal{D}$  during an experiment.

We stress that the above zero-knowledge notion aims at capturing, in the strongest possible sense, that the verifier cannot learn any useful information on the inputs, *even if it knows (or chooses) the secret authentication key*. Indeed, as one can see, our definition allows the distinguisher to choose the authentication key pair as well as the authentication tags.

Interestingly, we note that the notion of AD-SNARKs immediately implies a corresponding notion of *verifiable computation on authenticated data* (similar to [BFR13]). In [BCCT12], it is discussed how to construct a verifiable computation scheme from SNARKs for  $\mathcal{NP}$  with adaptive soundness. This is simply based on the fact that the correctness of a computation can be described with an  $\mathcal{NP}$  statement. It is not hard to see that, in a very similar way, one can construct verifiable computation on authenticated data from AD-SNARKs.

### 3.2 A Generic Construction of Zero-Knowledge AD-SNARKs

We show how to construct a zero-knowledge AD-SNARK scheme from SNARKs and digital signatures. A similar construction was informally sketched in [BCCT12][Appendix 10.1.2 of the full version]. Here we make it more formal with the main purpose of offering a comparison with our direct AD-SNARK construction proposed in the next section.

The high-level idea of the generic construction is to embed digital signatures into SNARKs. Let therefore  $\Pi' = (\text{Gen}', \text{Prove}', \text{Ver}')$  be a SNARK scheme, and  $\Sigma = (\Sigma.\text{KG}, \Sigma.\text{Sign}, \Sigma.\text{Ver})$  be a signature scheme.

We will use the signature scheme to sign pairs consisting of a label  $L$  and an actual message  $m$ . Although labels and messages can be arbitrary binary strings, for ease of description we assume that labels can take a special value  $\star$ . Also, we modify the signature scheme in such a way that  $\Sigma.\text{Sign}(\text{sk}, \star|m) = \star$  and  $\Sigma.\text{Ver}(\text{vk}, \star|m', \star) = 1$ . Basically, we let everyone (trivially) generate a valid signature on a message with label  $\star$ .

We define an AD-SNARK  $\Pi = (\text{Setup}, \text{AuthKG}, \text{Auth}, \text{AuthVer}, \text{Prove}, \text{Ver})$  as follows.

**Setup**( $1^\lambda$ ): Output  $\text{pp} = 1^\lambda$ .

**AuthKG**( $\text{pp}$ ): run  $(\text{sk}', \text{vk}') \leftarrow_{\mathcal{R}} \Sigma.\text{KG}(1^\lambda)$  to generate the key pair of the signature scheme and return  $\text{sk} = \text{sk}'$  and  $\text{vk} = \text{pap} = \text{vk}'$ .

**Auth**( $\text{sk}, L, x$ ): compute a signature on the concatenation of the label  $L$  and the value  $x$ , i.e.,  $\sigma' \leftarrow \Sigma.\text{Sign}(\text{sk}', L|x)$ . Finally, output  $\sigma = (\sigma', L)$ .

**AuthVer**( $\text{vk}, \sigma, L, x$ ): let  $\sigma = (\sigma', L')$ . Output the result of the signature verification algorithm  $\text{Ver}'(\text{vk}', L|x, \sigma')$ .

**Gen**( $\text{pap}, C$ ): for the given circuit  $C : \mathbb{F}^n \times \mathbb{F}^h \rightarrow \mathbb{F}^l$  we define  $C'$  as the circuit that outputs  $0^l$  on all the pairs  $(\vec{x}, \vec{w})$  such that  $C(\vec{x}, \vec{w}) = 0^l$  and each  $x_i$  is correctly signed with respect to a set of labels and a public key. More formally, define  $C' : \mathbb{F}^{n'} \times \mathbb{F}^{h'} \rightarrow \mathbb{F}^l$  as the circuit that takes as inputs pairs  $(\vec{x}', \vec{w}')$  with  $\vec{x}' = (y_1, L_1, \dots, y_n, L_n, \text{vk})$  and  $\vec{w}' = (\vec{w}, z_1, \sigma_1, \dots, z_n, \sigma_n)$  such that, by setting  $x_i = y_i$  if  $L_i = \star$  and  $x_i = z_i$  otherwise, for all  $i \in [n]$ , it holds: (i)  $((x_1, \dots, x_n), \vec{w}) \in \mathcal{R}_C$ , and (ii)  $\Sigma.\text{Ver}(\text{vk}, L_i|x_i, \sigma_i) = 1$ .

Finally, run  $\text{Gen}'(1^\lambda, C')$  to generate  $(\text{EK}'_{C'}, \text{VK}'_{C'})$  and output  $\text{EK}_C = \text{EK}'_{C'}, \text{VK}_C = \text{VK}'_{C'}$ .

**Prove**( $\text{EK}_C, \vec{x}, \vec{w}, \vec{\sigma}$ ): Let  $\text{EK}_C$  be the evaluation key as defined above,  $(\vec{x}, \vec{w}) \in \mathbb{F}^n \times \mathbb{F}^h$  be a statement-witness pair, and  $\vec{\sigma} = (\sigma_1, \dots, \sigma_n)$  be a tuple of authentication tags for  $\vec{x} =$

$(x_1, \dots, x_n)$ . If all the tags verify correctly, define  $\vec{x}' = (y_1, L_1, \dots, y_n, L_n, \mathbf{vk})$ ,  $\vec{w}' = (\vec{w}, z_1, \sigma'_1, \dots, z_n, \sigma'_n)$  so that for all  $i \in [n]$ :  $z_i = x_i$ ,  $y_i = x_i$  if  $\sigma_i = \star$  and  $y_i = 0$  otherwise. Next, run  $\pi \leftarrow_{\mathcal{R}} \text{Prove}(\text{EK}'_{C'}, \vec{x}', \vec{w}')$  to generate a proof for  $(\vec{x}', \vec{w}') \in \mathcal{R}_{C'}$  and return  $\pi$ .

$\text{Ver}(\mathbf{vk}, \text{VK}_C, \vec{L}, \{x_i\}_{L_i=\star}, \pi)$ : given the verification key  $\mathbf{vk}$ , a circuit verification key  $\text{VK}_C$ , statement labels  $\vec{L} = (L_1, \dots, L_n)$ , unauthenticated statement components  $x_i$ , and a proof  $\pi$ , the verification algorithm defines  $\vec{x}' = (y_1, L_1, \dots, y_n, L_n, \mathbf{vk})$  with  $y_i = x_i$  if  $L_i = \star$  and  $y_i = 0$  otherwise. Finally, it returns the output of  $\text{Ver}'(\text{VK}'_{C'}, \vec{x}', \pi)$ .

Note that the input size of  $C'$  is a circuit larger than  $C$  as follows:  $n' = n + n \cdot |L_i| + |\mathbf{vk}|$  and  $h' = h + n + n \cdot |\sigma|$ , where  $|\mathbf{vk}|$ ,  $|L_i|$ , and  $|\sigma|$  represent the size, in terms of field elements, of the public key, a label, and a signature, respectively. In terms of gates and wires,  $C'$  is at least as large as  $C$  plus the circuit size of  $\Sigma.\text{Ver}$  for *every* signature verification, that is up to  $n$  of such circuits.

**Theorem 1.** *If  $\Pi'$  is a zero-knowledge SNARK and  $\Sigma$  is a secure digital signature, then the scheme described above is a zero-knowledge AD-SNARK.*

*Proof (Sketch).* We provide a proof sketch to show that the generic construction satisfies all the properties. First, it is easy to see that if the SNARK is succinct, then the AD-SNARK proofs are succinct as well. Moreover, authentication correctness and completeness immediately follow from the correctness of the signature scheme and the completeness of the SNARK respectively.

Second, to see that adaptive proof of knowledge holds, note that for every adversary producing an accepting proof for statement  $\vec{x}'$  there is an extractor that returns a corresponding witness  $\vec{w}'$  (since  $\Pi'$  is an argument of knowledge) such that  $(\vec{x}', \vec{w}') \in \mathcal{R}_{C'}$  with all but negligible probability. Such witness  $\vec{w}'$ , by definition, will contain a statement-witness pair  $\vec{x}, \vec{w}$  for  $\mathcal{R}_C$  and a collection of signatures. Moreover,  $(\vec{x}', \vec{w}') \in \mathcal{R}_{C'}$  implies that  $(\vec{x}, \vec{w}) \in \mathcal{R}_C$  and all signatures are valid. Then, if for such a proof there is a message-label pair  $L_i|x_i$  which was not queried to the **Auth** oracle, then  $L_i|x_i$  and the corresponding signature  $\sigma_i$  can be used as a forgery to break the unforgeability of the signature scheme. Otherwise, if no forgery occurs, all signatures are valid for the same statement values queried to **Auth** (and thus stored in  $T$ ). This means that in the check of **Ver**, it also holds  $(\vec{x}, \vec{w}) \in \mathcal{R}_C$ , i.e., **GameOutput** remains 0.

Third, the zero-knowledge of the AD-SNARK follows from the one of the SNARK in a straightforward way.

### 3.3 Signature Verification Overhead

We now discuss why the circuit  $C'$  resulting from explicitly encoding the the verification algorithm of a digital signature scheme, as described in the generic construction, is bound to render the construction very inefficient. We consider various examples of signatures in both the random oracle and the standard model, and based on different algebraic problems.

If one considers signature schemes in the random oracle model (which include virtually all the schemes used in practice), any such scheme uses a collision-resistant hash function (e.g., SHA-1) which is thus part of the verification algorithm computation. Unfortunately, as shown also in [PGHR13], a QAP (just) for a SHA-1 computation is terribly inefficient due to the high number of multiplication gates (roughly 24 000, for inputs of 416 bits). On the other hand, if we focus on standard model signature schemes, it does not get any better: These schemes involve specific algebraic computations, and encoding these computations into an arithmetic circuit over a field  $\mathbb{F}_p$  is



costly. For instance, signatures based on pairings [BB04, Wat05] require pairing computations that amount to, roughly, 10 000 multiplications. RSA-based standard-model signatures (e.g., Cramer-Shoup [CS99]) require exponentiations over rings of large order (e.g., 3 000 bits), and simulating such computations over  $\mathbb{F}_p$  ends up with thousands of multiplication gates as well. Lattice-based signatures (in the standard model), e.g., [Boy10], can be cheaper in terms of the number of multiplications. However, such multiplications typically work over  $\mathbb{Z}_q$  for a  $q$  much smaller than our  $p$ . An option would be to implement mod- $q$ -reductions in  $\mathbb{F}_p$  circuits, which is costly. Another option would be to let these schemes work over  $\mathbb{Z}_p$ , but then one has to work with higher dimensional lattices (or polynomial rings) for security reasons, again incurring a large number of multiplications.

This state of affairs suggests that a QAP encoding a signature verification circuit is likely to require at least (and this is a very optimistic estimate) one thousand multiplications for *every* signature that must be checked.

## 4 Our Construction of Zero-Knowledge AD-SNARKs

In this section we describe our construction of an AD-SNARK scheme for the satisfiability of arbitrary arithmetic circuits. The scheme can be used with either secret or public verifiability. The main difference between the two verification modes is that the size of the proof in the secretly verifiable case is a fixed constant, whereas in the publicly verifiable case, the proof grows linearly with the number of authenticated statement values. Although we loose constant-size proofs for public verifiability, we stress that: (i) proofs are linear only in the number  $N \leq n$  of authenticated values and their size does not depend on the complexity of the circuit, and (ii) the verification algorithm runs linearly in  $N$  in any case (even in the generic construction). Furthermore, when considering concrete implementations and applications, although the proof size of ADSNARK with public verifiability is not constant, it still scales very well, e.g., the size of an ADSNARK proof for a monthly electricity bill is under 170 KB vs. a constant-size proof of 0.3 KB when using the generic scheme with [BSCTV14]. In contrast, when considering the prover’s performance, ADSNARK remains in the realm of practicality – 18 seconds for a monthly bill – whereas for the generic scheme the timing goes up to 10 minutes.

For verifiers that know the secret authentication key (e.g., as in a smart metering/insurance application where companies install a symmetric key in the devices), ADSNARK proofs have constant size, and – crucially – the knowledge of such a secret key by the verifier does *not* compromise privacy.

Our scheme is proven secure under two computational assumptions in bilinear groups, the  $q$ -Diffie-Hellman Exponent assumption ( $q$ -DHE) [CKS09] and the  $q$ -Power Knowledge of Exponent assumption ( $q$ -PKE) [Gro10]. We note that the latter one is a non-falsifiable assumption. As discussed in Section 6, this kind of assumption is likely to be inherent for SNARKs for  $\mathcal{NP}$ . For privacy, we show that the scheme offers statistical zero-knowledge. We stress that this property holds even against adversaries who know (and even generate) the authentication keys.

A detailed description of our scheme follows.

**Setup( $1^\lambda$ ):** On input the security parameter  $1^\lambda$ , run  $\mathbf{pp} = (p, \mathbb{G}_1, \mathbb{G}_2, \mathbb{G}_T, e, \mathcal{P}_1, \mathcal{P}_2) \leftarrow_{\mathcal{R}} \mathcal{G}(1^\lambda)$  to generate a bilinear group description, where  $\mathbb{G}_1$ ,  $\mathbb{G}_2$ , and  $\mathbb{G}_T$  are groups of the same prime order  $p > 2^\lambda$ ,  $\mathcal{P}_1 \in \mathbb{G}_1$  and  $\mathcal{P}_2 \in \mathbb{G}_2$  are the respective generators, and  $e : \mathbb{G}_1 \times \mathbb{G}_2 \rightarrow \mathbb{G}_T$  is an efficiently computable bilinear map. We let the finite field  $\mathbb{F}$  be the set of integers modulo  $p$ .

**AuthKG(pp)**: Create a key pair  $(\text{sk}', \text{vk}') \leftarrow_{\mathcal{R}} \Sigma.\text{KG}(1^\lambda)$  for a regular signature scheme. Run  $(S, \text{prfpp}) \leftarrow_{\mathcal{R}} \text{F.KG}(1^\lambda)$  to obtain the seed  $S$  and the public parameters  $\text{prfpp}$  of a pseudorandom function  $\text{F}_S : \{0, 1\}^* \rightarrow \mathbb{F}$ . Choose a random value  $\kappa \leftarrow_{\mathcal{R}} \mathbb{F}$  and compute  $K_1 = \kappa \mathcal{P}_1 \in \mathbb{G}_1$ ,  $K_2 = \kappa \mathcal{P}_2 \in \mathbb{G}_2$ . Return the secret key  $\text{sk} = (\text{sk}', S, \kappa)$ , the public verification key  $\text{vk} = (\text{vk}', K_2)$ , and the public authentication parameters  $\text{pap} = (\text{pp}, \text{prfpp}, K_1)$ .

**Auth(sk, L, x)**: To authenticate a value  $x \in \mathbb{F}$  with label  $L$ , generate  $\phi \leftarrow \text{F}_S(L)$  using the PRF, compute  $\mu = \phi + \kappa \cdot x \in \mathbb{F}$  and  $\Phi = \phi \mathcal{P}_2 \in \mathbb{G}_2$ . Then compute a signature  $\sigma' \leftarrow_{\mathcal{R}} \Sigma.\text{Sign}(\text{sk}', \Phi|L)$ , and output the tag  $\sigma = (\mu, \Phi, \sigma')$ .

**AuthVer(vk, σ, L, x)**: Let  $\text{vk} = (\text{vk}', K_2)$  be the verification key. To verify that  $\sigma = (\mu, \Phi, \sigma')$  is a valid authentication tag for a value  $x \in \mathbb{F}$  with respect to label  $L$ , output  $\top$  if  $\mu \mathcal{P}_2 = \Phi + x K_2$  in  $\mathbb{G}_2$ , and if  $\Sigma.\text{Ver}(\text{vk}', \Phi|L, \sigma') = 1$ . Output  $\perp$  otherwise. In the secret key setting (i.e., if  $\text{vk}$  is replaced by  $\text{sk}$ ), the tag  $\sigma$  can be verified by checking whether  $\mu = \text{F}_S(L) + \kappa \cdot x$ .

**Gen(pap, C)**: Let  $C : \mathbb{F}^n \times \mathbb{F}^h \rightarrow \mathbb{F}^l$  be an arithmetic circuit. To generate the keys, proceed as follows.

1. Compute  $Q_C = (\vec{a}, \vec{b}, \vec{c}, z) = \text{QAPInst}(C)$  to build a QAP of size  $m$  and degree  $d$  for  $C$ . Recall that  $\vec{a}, \vec{b}, \vec{c}$  are vectors of  $m+1$  polynomials in  $\mathbb{F}^{\leq d-1}[X]$ , while the target polynomial  $z \in \mathbb{F}[X]$  has degree  $d$ . Extend  $\vec{a}, \vec{b}, \vec{c}$  with 3 more polynomials each, by setting:

$$\begin{aligned} a_{m+1}(X) &= b_{m+2}(X) = c_{m+3}(X) = z(X), \\ a_{m+2}(X) &= a_{m+3}(X) = b_{m+1}(X) = b_{m+3}(X) = c_{m+1}(X) = c_{m+2}(X) = 0. \end{aligned}$$

Let  $I_x, I_{\text{mid}}$  be the following partitions of  $\{1, \dots, m+3\}$ :  $I_x = \{1, \dots, n\}$ ,  $I_{\text{mid}} = \{n+1, \dots, m+3\}$ . In other words, we partition all the circuit wires into the  $n$  statement wires  $I_x$ , and the remaining “internal” wires  $I_{\text{mid}}$  (which include the  $h$  witness wires).

2. Pick  $\rho_a, \rho_b, \tau, \alpha_a, \alpha_b, \alpha_c, \beta, \gamma \leftarrow_{\mathcal{R}} \mathbb{F}$  uniformly at random, set  $\rho_c = \rho_a \cdot \rho_b$ , and compute the following values:

$$\begin{aligned} Z &= z(\tau) \rho_c \mathcal{P}_2, & K_a &= z(\tau) \rho_a K_1, \\ \forall k \in \{0, \dots, m+3\} : & A_k &= a_k(\tau) \rho_a \mathcal{P}_1, & A'_k &= \alpha_a a_k(\tau) \rho_a \mathcal{P}_1, \\ & B_k &= b_k(\tau) \rho_b \mathcal{P}_2, & B'_k &= \alpha_b b_k(\tau) \rho_b \mathcal{P}_1, \\ & C_k &= c_k(\tau) \rho_c \mathcal{P}_1, & C'_k &= \alpha_c c_k(\tau) \rho_c \mathcal{P}_1, \\ & E_k &= \beta(a_k(\tau) \rho_a + b_k(\tau) \rho_b + c_k(\tau) \rho_c) \mathcal{P}_1. \end{aligned}$$

3. Output the *evaluation key*  $\text{EK}_C$  and the *verification key*  $\text{VK}_C$  defined as follows:

$$\begin{aligned} \text{EK}_C &= \left( Q_C, \vec{A}, \vec{A}', \vec{B}, \vec{B}', \vec{C}, \vec{C}', \vec{E}, \{\tau^i \mathcal{P}_1\}_{i \in \{0, \dots, d\}}, K_a \right) \\ \text{VK}_C &= \left( \mathcal{P}_1, \mathcal{P}_2, \alpha_a \mathcal{P}_2, \alpha_b \mathcal{P}_1, \alpha_c \mathcal{P}_2, \gamma \mathcal{P}_2, \beta \gamma \mathcal{P}_1, \beta \gamma \mathcal{P}_2, Z, \{A_k\}_{k=0}^n \right) \end{aligned}$$

**Prove(EK<sub>C</sub>,  $\vec{x}, \vec{w}, \vec{\sigma}$ )**: Let  $\text{EK}_C$  be an evaluation key defined as above,  $(\vec{x}, \vec{w}) \in \mathbb{F}^n \times \mathbb{F}^h$  be a statement-witness pair, and  $\sigma = (\sigma_1, \dots, \sigma_n)$  be a tuple of authentication tags for  $x$  such that, for any  $i \in [n]$ , either  $\sigma_i = (\mu_i, \Phi_i, \sigma'_i)$  or  $\sigma_i = \star$ . We define  $I_\sigma = \{i \in I_x : \sigma_i \neq \star\} \subseteq I_x$  as the set of indices for which there is an authenticated statement value, and let  $I_\star = I_x \setminus I_\sigma$  be its complement. To produce a proof for the satisfiability of  $C(\vec{x}, \vec{w}) = 0^l$  proceed as follows.

1. Compute  $\vec{s} = \text{QAPwit}(C, \vec{x}, \vec{w}) \in \mathbb{F}^m$  (and recall that  $s_i = x_i$  for all  $i \in [n]$ ).
2. Randomly sample  $\delta_a^\sigma, \delta_a^{\text{mid}}, \delta_b, \delta_c \leftarrow_{\mathcal{R}} \mathbb{F}$ , and set  $\delta_a = \delta_a^\sigma + \delta_a^{\text{mid}}$ . Also, define the vector  $\vec{u} = (1, \vec{s}, \delta_a, \delta_b, \delta_c) \in \mathbb{F}^{m+4}$ .
3. Solve the QAP  $Q_C$  by computing the coefficients  $(h_0, \dots, h_d) \in \mathbb{F}^{d+1}$  of the polynomial  $h \in \mathbb{F}[X]$  such that  $h(X)z(X) = a(X)b(X) - c(X)$ , where  $a, b, c \in \mathbb{F}[X]$  are

$$\begin{aligned} a(X) &= a_0(X) + \sum_{k \in [m]} s_k \cdot a_k(X) + \delta_a \cdot z(x) \\ b(X) &= b_0(X) + \sum_{k \in [m]} s_k \cdot b_k(X) + \delta_b \cdot z(x) \\ c(X) &= c_0(X) + \sum_{k \in [m]} s_k \cdot c_k(X) + \delta_c \cdot z(x) \end{aligned}$$

Then compute  $H = h(\tau) \mathcal{P}_1$  using the values  $\tau^i \mathcal{P}_1$  contained in the evaluation key  $\text{EK}_C$ . Note that we have  $a(X) = \langle \vec{u}, \vec{a} \rangle$ ,  $b(X) = \langle \vec{u}, \vec{b} \rangle$  and  $c(X) = \langle \vec{u}, \vec{c} \rangle$ .

4. Compute the following values:

$$\begin{aligned} \pi_b &= \langle \vec{u}, \vec{B} \rangle, & \pi'_b &= \langle \vec{u}, \vec{B}' \rangle, & \pi_c &= \langle \vec{u}, \vec{C} \rangle, & \pi'_c &= \langle \vec{u}, \vec{C}' \rangle, \\ \pi_\sigma &= \langle \vec{u}, \vec{A} \rangle_{I_\sigma} + \delta_a^\sigma A_{m+1}, & \pi'_\sigma &= \langle \vec{u}, \vec{A}' \rangle_{I_\sigma} + \delta_a^\sigma A'_{m+1} \\ \pi_{\text{mid}} &= \langle \vec{u}, \vec{A} \rangle_{I_{\text{mid}}} - \delta_a^\sigma A_{m+1}, & \pi'_{\text{mid}} &= \langle \vec{u}, \vec{A}' \rangle_{I_\sigma} - \delta_a^\sigma A'_{m+1} \\ \pi_E &= \langle \vec{u}, \vec{E} \rangle. \end{aligned}$$

5. Authenticate the value  $\pi_\sigma$  by computing

$$\pi_\mu = \langle \vec{\mu}, \vec{A} \rangle_{I_\sigma} + \delta_a^\sigma K_a$$

6. Construct and return proof  $\pi$  as the tuple  $(\pi_\mu, \pi_\sigma, \pi'_\sigma, \pi_{\text{mid}}, \pi'_{\text{mid}}, \pi_b, \pi'_b, \pi_c, \pi'_c, \pi_E, H)$ . To make the proof publicly verifiable, include also  $\{\Phi_k, \sigma'_k\}_{k \in I_\sigma}$ .

$\text{Ver}(\text{vk}, \text{VK}_C, \mathbf{L}, \{x_i\}_{\mathbf{L}_i=\star}, \pi)$ : Let  $\text{VK}_C$  be the verification key for the circuit  $C$ ,  $\vec{\mathbf{L}} = (\mathbf{L}_1, \dots, \mathbf{L}_n)$  be a vector of labels, and let  $\pi$  be a proof as defined above. In a similar way as in  $\text{Prove}$ , we define  $I_\sigma = \{i \in I_x : \mathbf{L}_i \neq \star\} \subseteq I_x$  and  $I_\star = I_x \setminus I_\sigma$ . The verification algorithm computes  $A_\star = A_0 + \langle \vec{x}, \vec{A} \rangle_{I_\star}$  and proceeds as follows:

- (A.1<sup>secret</sup>) If verification is done using the secret key  $\text{sk} = (S, \kappa)$ , check the authenticity of  $\pi_\sigma$  against the labels  $\vec{\mathbf{L}}$  by checking whether the following equation holds in  $\mathbb{G}_1$ :<sup>8</sup>

$$\pi_\mu = \langle F_S(\vec{\mathbf{L}}), \vec{A} \rangle_{I_\sigma} + \kappa \pi_\sigma$$

- (A.1<sup>public</sup>) If the verification is performed using the public verification key  $\text{vk} = (\text{vk}', K_2)$ : first, check the validity of all  $\Phi_k$  by verifying that  $\Sigma.\text{Ver}(\text{vk}', \Phi_k | \mathbf{L}_k, \sigma'_k) = 1$  for all  $k \in I_\sigma$ ; second, check the authenticity of  $\pi_\sigma$  by verifying that the following equation is satisfied over  $\mathbb{G}_T$ :

$$e(\pi_\mu, \mathcal{P}_2) = \prod_{k \in I_\sigma} e(A_k, \Phi_k) \cdot e(\pi_\sigma, K_2)$$

<sup>8</sup> The expansion of  $\langle F_S(\vec{\mathbf{L}}), \vec{A} \rangle_I$  is defined as the component-wise application of  $F$ , i.e.,  $\sum_{i \in I} F_S(\mathbf{L}_i) \cdot A_i$ .

(A.2) Check the validity of knowledge commitments for the authenticated values:

$$e(\pi'_\sigma, \mathcal{P}_2) = e(\pi_\sigma, \alpha_a \mathcal{P}_2)$$

(P.1) Check the satisfiability of the QAP:

$$e(A_\star + \pi_\sigma + \pi_{mid}, \pi_b) = e(H, Z) \cdot e(\pi_c, \mathcal{P}_2)$$

(P.2) Check the validity of knowledge commitments:

$$e(\pi'_{mid}, \mathcal{P}_2) = e(\pi_{mid}, \alpha_a \mathcal{P}_2) \wedge e(\pi'_b, \mathcal{P}_2) = e(\alpha_b \mathcal{P}_1, \pi_b) \wedge e(\pi'_c, \mathcal{P}_2) = e(\pi_c, \alpha_c \mathcal{P}_2)$$

(P.3) Check that all the QAP linear combinations use the same coefficients:

$$\begin{aligned} & e(\pi_E, \gamma \mathcal{P}_2) = \\ & e(A_\star + \pi_\sigma + \pi_{mid} + \pi_c, \beta \gamma \mathcal{P}_2) \cdot e(\beta \gamma \mathcal{P}_1, \pi_b) \end{aligned}$$

If all the checks above are satisfied, then return  $\top$ ; otherwise return  $\perp$ .

**ReRand**( $\text{EK}_C, \mathbf{L}, \{x_i\}_{\mathbf{L}_i=\star}, \pi$ ): The scheme also allows for perfect re-randomization of an existing proof, say  $\pi$  given by tuple  $(\pi_\mu, \pi_\sigma, \pi'_\sigma, \pi_{mid}, \pi'_{mid}, \pi_b, \pi'_b, \pi_c, \pi'_c, \pi_E, H)$ . If  $\pi$  verifies for a set of labels  $\mathbf{L}$  and a set of non-authenticated values  $\{x_i\}_{\mathbf{L}_i=\star}$ , then  $\pi$  can be re-randomized as follows. First, choose random values  $\tilde{\delta}_a^\sigma, \tilde{\delta}_a^{mid}, \tilde{\delta}_b, \tilde{\delta}_c \leftarrow_{\mathcal{R}} \mathbb{F}$ , and set  $\tilde{\delta}_a = \tilde{\delta}_a^\sigma + \tilde{\delta}_a^{mid}$ . Second, compute

$$\begin{aligned} \tilde{\pi}_b &= \pi_b + \tilde{\delta}_b B_{m+2}, & \tilde{\pi}'_b &= \pi'_b + \tilde{\delta}_b B'_{m+2}, \\ \tilde{\pi}_c &= \pi_c + \tilde{\delta}_c C_{m+3}, & \tilde{\pi}'_c &= \pi'_c + \tilde{\delta}_c C'_{m+3}, \\ \tilde{\pi}_\sigma &= \pi_\sigma + \tilde{\delta}_a^\sigma A_{m+1}, & \tilde{\pi}'_\sigma &= \pi'_\sigma + \tilde{\delta}_a^\sigma A'_{m+1}, \\ \tilde{\pi}_{mid} &= \pi_{mid} + \tilde{\delta}_a^{mid} A_{m+1}, \\ \tilde{\pi}'_{mid} &= \pi'_{mid} + \tilde{\delta}_a^{mid} A'_{m+1}, \\ \tilde{\pi}_E &= \pi_E + \tilde{\delta}_a E_{m+1} + \tilde{\delta}_b E_{m+2} + \tilde{\delta}_c E_{m+3}, \\ \tilde{\pi}_\mu &= \pi_\mu + \tilde{\delta}_a^\sigma K_a, \\ \tilde{H} &= H + \tilde{\delta}_a \pi_b + \tilde{\delta}_b \pi_a + \tilde{\delta}_a \tilde{\delta}_b z(\tau) \mathcal{P}_1 - \tilde{\delta}_c \mathcal{P}_1. \end{aligned}$$

where  $z(\tau) \mathcal{P}_1$  can be included in  $\text{EK}_C$ . Finally, output the re-randomised proof  $\tilde{\pi}$  as  $(\tilde{\pi}_\mu, \tilde{\pi}_\sigma, \tilde{\pi}'_\sigma, \tilde{\pi}_{mid}, \tilde{\pi}'_{mid}, \tilde{\pi}_b, \tilde{\pi}'_b, \tilde{\pi}_c, \tilde{\pi}'_c, \tilde{\pi}_E, \tilde{H})$ .

It is not hard to check that  $\tilde{\pi}$  is identically distributed as a fresh proof  $\pi$  generated by **Prove**.

The following theorem shows that the scheme **ADSNARK** described above is a zero-knowledge AD-SNARK as in Definition 5.

**Theorem 2.** *If  $F$  is a pseudorandom function, and the  $q$ -PKE [Gro10] and the  $q$ -DHE [CKS09] assumptions hold, then **ADSNARK** is a secretly-verifiable zero-knowledge AD-SNARK. Furthermore, if additionally  $\Sigma$  is a secure signature scheme, then **ADSNARK** is a publicly-verifiable zero-knowledge AD-SNARK.*

We prove theorem by showing separately that the properties of completeness, adaptive proof of knowledge and zero-knowledge are all satisfied. This is done in Sections 4.1, 4.2 and 4.3 respectively.

**Performance and Comparison.** Before proving Theorem 2, we pause to discuss the performance of our scheme ADSNARK in comparison with the SNARK of Parno et al. [PGHR13] that we call PGHR (more precisely, we consider its optimization proposed by Ben-Sasson et al. [BSCTV14] that for convenience we recall in Appendix C).

First, we note that the **Gen** algorithm is virtually the same in both schemes except that in ADSNARK we have one more exponentiation<sup>9</sup> in  $\mathbb{G}_1$  to generate  $K_a = z(\tau) \rho_a K_1$ . Also, from a bandwidth point of view, the evaluation key of  $EK_C$  of ADSNARK contains only one more  $\mathbb{G}_1$  element,  $K_a$ , compared to the evaluation key of PGHR. The verification key instead is the same in both schemes.

Second, let us focus on the differences in the **Prove** algorithm. ADSNARK’s **Prove** has to compute three more  $\mathbb{G}_1$  elements:  $\pi_\sigma$ ,  $\pi'_\sigma$ , and  $\pi_\mu$ . Generating these elements amounts to performing three multi-exponentiations that involve  $N = |I_\sigma|$  terms each. When looking at the proof size, ADSNARK’s proof contains such three additional elements in the group  $\mathbb{G}_1$ , plus the signatures  $\{\sigma_k\}_{k \in I_\sigma}$  in the publicly verifiable setting.

Third, we analyze the differences between ADSNARK and PGHR in the **Ver** algorithm. The equations (P.1), (P.2), and (P.3) are identical in both schemes and thus require the same computational effort. In PGHR one computes  $A_x = A_0 + \sum_{k=1}^n x_k A_k \in \mathbb{G}_1$ , whereas in ADSNARK we compute a similar value  $A_\star = A_0 + \sum_{k \in I_\star} x_k A_k \in \mathbb{G}_1$  which involves fewer terms: precisely  $|I_\star| = n - N$ . Then, ADSNARK has to perform some additional computation for verifying equations (A.1) and (A.2). (A.2) costs only two pairings – a constant overhead. The first equation instead requires different computations according to whether we are in the secretly verifiable case  $((A.1)^{secret})$  or in the publicly verifiable case  $((A.1)^{public})$ .  $(A.1)^{secret}$  requires one multi-exponentiation with  $N = |I_\sigma|$  terms (plus the cost of running the PRF which is unnoticeable compared to the multi-exponentiation). Hence, considering the cost of computing  $A_x$  in PGHR and the total cost of computing  $A_\star$  and  $(A.1)^{secret}$  in ADSNARK, these are essentially the same. In other words, ADSNARK’s secretly verifiable case is slightly slower than PGHR for the cost of computing two pairings in (A.2).

In the publicly verifiable case, equation  $(A.1)^{public}$  requires to check a total of  $N$  signatures,  $\{\sigma_k\}_{k \in I_\sigma}$ , and then to compute  $e(\pi_\mu, \mathcal{P}_2)e(\pi_\sigma, -K_2)$  and  $\prod_{k \in I_\sigma} e(A_k, \Phi_k)$ . In general, note that the verification of such  $N$  signatures can be done by using batching techniques, and the “multi pairings” can also be computed efficiently. In particular, as we show in our instantiation, this cost is close to the cost of computing  $A_x$  in PGHR. In other words, ADSNARK’s publicly verifiable case is slightly slower than PGHR for the cost of computing the pairings in (A.1) and (A.2) and for checking the signatures.

In Section 5, we give concrete comparisons resulting from our experiments, which are consistent with the analysis above. Indeed, we show based on concrete timings that ADSNARK performs almost as PGHR used *without* authenticated data. These results conclude that our technique added an important property to the SNARK at almost no cost.

However, for the sake of fairness, we should also consider a comparison of the two protocols when they are used to provide equivalent guarantees, i.e., when proving statements on authenticated data. To this end, we now compare ADSNARK against the best possible instantiation of the generic construction of Section 3.2, which we take to be PGHR working with the “extended” circuit  $C'$ .

<sup>9</sup> We use the term “exponentiation” only for ‘historical’ reasons, as  $\mathbb{G}_1$  is actually an additive group.

We call this scheme AD-PGHR. In our analysis, we assume that the verification of every signature requires an arithmetic circuit with  $c$  multiplication gates, and also assume (very optimistically) that this is the only additional cost for the design of  $C'$ . This means that: if  $C$  yields a QAP of size  $m$  and degree  $d$ , then  $C'$  yields a QAP of, at least, size  $m' = m + cN$  and degree  $d' = d + cN$ .

In AD-PGHR, the performance of **Ver** remains the same as the one of **Ver** in PGHR discussed above. On the other hand, the **Prove** algorithm of AD-PGHR heavily depends on the QAP size  $m'$  and degree  $d'$ . Precisely, **Prove** performs multi-exponentiations with  $m'$  and  $d'$  terms, and a polynomial division operation whose cost is  $O(d' \log^2 d')$ .

In conclusion, if we fix a circuit  $C$  and a number  $N$  of authenticated values, and we compare ADSNARK for circuit  $C$  against AD-PGHR for the same  $C$  (i.e., PGHR with the extended circuit  $C'$ ), then we obtain:

For secret verification, both schemes perform almost the same, the only difference being that we need to perform two more pairings; for public verification, ADSNARK has an additional cost of one multi-pairing computation with  $N$  terms plus the signature verification. For proof generation, AD-PGHR has to perform additional operations that involve a factor at least linear in  $c \cdot N$ . We recall from the discussion in Section 3.3 that such  $c$  is likely to be larger than 1000. Therefore, one can see that while our solution charges a little more to the verifier (and only in the public verification case), the costs of our scheme on the prover side can be much cheaper, at least by a factor  $c \cdot N$ . We confirm the above asymptotic comparison in Section 5 by showing the experimental results obtained by running our implementation.

## 4.1 Completeness

**Theorem 3.** *The scheme ADSNARK satisfies authentication correctness and completeness.*

*Proof.* It is straightforward to see that the scheme has authentication correctness by the correctness of the regular signature scheme and by construction. To show the completeness, we prove the satisfaction of all verification equations in the order they appear in the verification procedure.

(A.1<sup>secret</sup>)

$$\begin{aligned}
\pi_\mu &\stackrel{\text{Prove}}{=} \langle \vec{\mu}, \vec{A} \rangle_{I_\sigma} + \delta_a^\sigma K_a \\
&\stackrel{\text{Gen}}{=} \langle \vec{\mu}, \vec{A} \rangle_{I_\sigma} + \delta_a^\sigma z(\tau) \rho_a K_1 \\
&\stackrel{\text{Auth}}{=} \langle F_S(\vec{L}) + \kappa \cdot \vec{x}, \vec{A} \rangle_{I_\sigma} + \delta_a^\sigma z(\tau) \rho_a K_1 \\
&\stackrel{\text{AuthKG}}{=} \langle F_S(\vec{L}), \vec{A} \rangle_{I_\sigma} + \kappa \langle \vec{x}, \vec{A} \rangle_{I_\sigma} + \delta_a^\sigma z(\tau) \rho_a \kappa \mathcal{P}_1 \\
&\stackrel{\text{Gen}}{=} \langle F_S(\vec{L}), \vec{A} \rangle_{I_\sigma} + \kappa \langle \vec{x}, \vec{A} \rangle_{I_\sigma} + \delta_a^\sigma A_{m+1} \\
&\stackrel{\text{Prove}}{=} \langle F_S(\vec{L}), \vec{A} \rangle_{I_\sigma} + \kappa \cdot \pi_\sigma
\end{aligned}$$

(A.1<sup>pub</sup>)

$$\begin{aligned}
e(\pi_\mu, \mathcal{P}_2) &\stackrel{\text{Prove}}{=} e(\langle \vec{\mu}, \vec{A} \rangle_{I_\sigma} + \delta_a^\sigma K_a, \mathcal{P}_2) \\
&\stackrel{\text{Gen}}{=} e(\langle \vec{\mu}, \vec{A} \rangle_{I_\sigma} + \delta_a^\sigma z(\tau) \rho_a K_1, \mathcal{P}_2) \\
&\stackrel{\text{Auth}}{=} e(\langle F_S(\vec{L}) + \kappa \cdot \vec{x}, \vec{A} \rangle_{I_\sigma} + \delta_a^\sigma z(\tau) \rho_a K_1, \mathcal{P}_2) \\
&\stackrel{\text{AuthKG}}{=} e(\langle F_S(\vec{L}), \vec{A} \rangle_{I_\sigma}, \mathcal{P}_2) \cdot e(\kappa \langle \vec{x}, \vec{A} \rangle_{I_\sigma} + \delta_a^\sigma z(\tau) \rho_a \kappa \mathcal{P}_1, \mathcal{P}_2) \\
&\stackrel{\text{Gen}}{=} e\left(\sum_{k \in I_\sigma} (F_S(L_k) \rho_a a_k(\tau)) \mathcal{P}_1, \mathcal{P}_2\right) \cdot e(\langle \vec{x}, \vec{A} \rangle_{I_\sigma} + \delta_a^\sigma z(\tau) \rho_a \mathcal{P}_1, \mathcal{P}_2)^\kappa \\
&\stackrel{\text{AuthKG,Prove}}{=} e\left(\sum_{k \in I_\sigma} \rho_a a_k(\tau) \mathcal{P}_1, F_S(L_k) \mathcal{P}_2\right) \cdot e(\langle \vec{x}, \vec{A} \rangle_{I_\sigma} + \delta_a^\sigma A_{m+1}, K_2) \\
&\stackrel{\text{Gen,Auth}}{=} \prod_{k \in I_\sigma} e(A_k, \Phi_k) \cdot e(\langle \vec{x}, \vec{A} \rangle_{I_\sigma} + \delta_a^\sigma A_{m+1}, K_2) \\
&\stackrel{\text{Prove}}{=} \prod_{k \in I_\sigma} e(A_k, \Phi_k) \cdot e(\pi_\sigma, K_2)
\end{aligned}$$

(A.2)

$$\begin{aligned}
e(\pi'_\sigma, \mathcal{P}_2) &\stackrel{\text{Prove}}{=} e(\langle \vec{u}, \vec{A}' \rangle_{I_\sigma} + \delta_a^\sigma A'_{m+1}, \mathcal{P}_2) \\
&\stackrel{\text{Gen}}{=} e(\alpha_a \langle \vec{u}, \vec{A} \rangle_{I_\sigma} + \delta_a^\sigma \alpha_a A_{m+1}, \mathcal{P}_2) \\
&= e(\langle \vec{u}, \vec{A} \rangle_{I_\sigma} + \delta_a^\sigma A_{m+1}, \alpha_a \mathcal{P}_2) \\
&\stackrel{\text{Prove}}{=} e(\pi_\sigma, \alpha_a \mathcal{P}_2)
\end{aligned}$$

(P.1)

$$\begin{aligned}
e(A_\star + \pi_\sigma + \pi_{mid} \pi_b) &\stackrel{\text{Prove}}{=} e\left(A_0 + \langle \vec{u}, \vec{A} \rangle_{I_\sigma} + \langle \vec{u}, \vec{A} \rangle_{I_\star} + \langle \vec{u}, \vec{A} \rangle_{I_{mid}}, \langle \vec{u}, \vec{B} \rangle\right) \\
&\stackrel{\text{Prove}}{=} e(\langle \vec{u}, \vec{A} \rangle, \langle \vec{u}, \vec{B} \rangle) \\
&\stackrel{\text{Gen}}{=} e\left(\left(\sum_{k=0}^{m+4} u_k a_k(\tau)\right) \rho_a \mathcal{P}_1, \left(\sum_{k=0}^{m+4} u_k b_k(\tau)\right) \rho_b \mathcal{P}_2\right) \\
&\stackrel{\text{Gen}}{=} e\left(\mathcal{P}_1, \mathcal{P}_2\right)^{\rho_a \rho_b a(\tau) b(\tau)} \\
&\stackrel{\text{Gen,Prove}}{=} e\left(\mathcal{P}_1, \mathcal{P}_2\right)^{\rho_c (h(\tau) z(\tau) + c(\tau))} \\
&= e\left(h(\tau) \mathcal{P}_1, \rho_c z(\tau) \mathcal{P}_2\right) \cdot e\left(\rho_c c(\tau) \mathcal{P}_1, \mathcal{P}_2\right) \\
&\stackrel{\text{Prove}}{=} e(H, Z) \cdot e\left(\rho_c \langle \vec{u}, \vec{C} \rangle \mathcal{P}_1, \mathcal{P}_2\right) \\
&\stackrel{\text{Prove}}{=} e(H, Z) \cdot e(\pi_c, \mathcal{P}_2)
\end{aligned}$$

(P.2) We refer to the proof of (A.2), which is very similar to the cases of  $\pi_{mid}$ ,  $\pi_b$ , and  $\pi_c$ .

(P.3)

$$\begin{aligned}
e(\pi_E, \gamma \mathcal{P}_2) &\stackrel{\text{Prove}}{=} e(\langle \vec{u}, \vec{E} \rangle, \gamma \mathcal{P}_2) \stackrel{\text{Gen}}{=} e(\beta \langle \vec{u}, (\rho_a \vec{a}(\tau) + \rho_b \vec{b}(\tau) + \rho_a \rho_b \vec{c}(\tau)) \mathcal{P}_1 \rangle, \gamma \mathcal{P}_2) \\
&= e(\langle \vec{u}, (\rho_a \vec{a}(\tau) + \rho_b \vec{b}(\tau) + \rho_a \rho_b \vec{c}(\tau)) \mathcal{P}_1 \rangle, \beta \gamma \mathcal{P}_2) \\
&= e(\langle \vec{u}, \rho_a \vec{a}(\tau) \mathcal{P}_1 \rangle, \beta \gamma \mathcal{P}_2) \cdot e(\langle \vec{u}, \rho_b \vec{b}(\tau) \mathcal{P}_1 \rangle, \beta \gamma \mathcal{P}_2) \cdot e(\langle \vec{u}, \rho_a \rho_b \vec{c}(\tau) \mathcal{P}_1 \rangle, \beta \gamma \mathcal{P}_2) \\
&= e(\langle \vec{u}, \vec{A} \rangle, \beta \gamma \mathcal{P}_2) \cdot e(\beta \gamma \mathcal{P}_1, \langle \vec{u}, \rho_b \vec{b}(\tau) \mathcal{P}_2 \rangle) \cdot e(\langle \vec{u}, \vec{C} \rangle, \beta \gamma \mathcal{P}_2) \\
&= e(\langle \vec{u}, \vec{A} \rangle + \langle \vec{u}, \vec{C} \rangle, \beta \gamma \mathcal{P}_2) \cdot e(\beta \gamma \mathcal{P}_1, \langle \vec{u}, \vec{B} \rangle) \\
&\stackrel{\text{Prove}}{=} e(A_0 + \langle \vec{u}, \vec{A} \rangle_{I_\sigma} + \langle \vec{u}, \vec{A} \rangle_{I_\star} + \langle \vec{u}, \vec{A} \rangle_{I_{mid}} + \pi_c, \beta \gamma \mathcal{P}_2) \cdot e(\beta \gamma \mathcal{P}_1, \pi_b) \\
&\stackrel{\text{Prove}}{=} e(A_\star + \pi_\sigma + \pi_{mid} + \pi_c, \beta \gamma \mathcal{P}_2) \cdot e(\beta \gamma \mathcal{P}_1, \pi_b)
\end{aligned}$$

□

## 4.2 Adaptive Proof of Knowledge

In the following theorem we prove that the scheme ADSNARK described in Section 4 satisfies the adaptive proof of knowledge property. For this purpose, we base (part of) the security directly on the proof of knowledge property of the SNARK of Parno et al. [PGHR13] (with the adaptation of [BSCTV14], see Appendix C), that is in turn based on the  $q$ -PKE and the  $q$ -DHE assumptions.

**Theorem 4.** *If PGHR is a SNARK,  $F$  is a pseudorandom function, the  $q$ -PKE [Gro10] and the  $q$ -DHE [CKS09] assumptions hold, then ADSNARK is a secretly-verifiable AD-SNARK with adaptive proof of knowledge.*

Before giving the proof, we first recall the  $q$ -DHE and the  $q$ -PKE assumptions.

**Definition 7 ( $q$ -Diffie-Hellman Exponent assumption [CKS09]).** *The  $q$ -DHE problem is defined as follows. Let  $\mathcal{G}$  be a bilinear group generator, and let  $\text{bgpp} = (p, \mathbb{G}_1, \mathbb{G}_2, \mathbb{G}_T, e, \mathcal{P}_1, \mathcal{P}_2) \leftarrow_{\mathcal{R}} \mathcal{G}(1^\lambda)$ . Let  $\tau \leftarrow_{\mathcal{R}} \mathbb{Z}_p$  be chosen uniformly at random. We define the advantage of an adversary  $\mathcal{A}$  in solving the  $q$ -DHE problem as*

$$\begin{aligned}
\text{Adv}_{\mathcal{A}}^{q\text{-DHE}}(\lambda) &= \Pr[\mathcal{A}(\text{bgpp}, \tau \mathcal{P}_1, \tau \mathcal{P}_2, \dots, \tau^q \mathcal{P}_1, \\
&\quad \tau^q \mathcal{P}_2, \tau^{q+2} \mathcal{P}_1, \tau^{q+2} \mathcal{P}_2, \dots, \tau^{2q} \mathcal{P}_1, \tau^{2q} \mathcal{P}_2) = \tau^{q+1} \mathcal{P}_1].
\end{aligned}$$

We say that the  $q$ -DHE assumption holds for  $\mathcal{G}$  if for every PPT algorithm  $\mathcal{A}$  and any polynomially-bounded  $q = \text{poly}(\lambda)$  we have that  $\text{Adv}_{\mathcal{A}}^{q\text{-DHE}}(\lambda)$  is negligible in  $\lambda$ .

**Definition 8 ( $q$ -Power Knowledge of Exponent assumption [Gro10]).** *Let  $\mathcal{G}$  be a bilinear group generator,  $\lambda$  be a security parameter and  $q = \text{poly}(\lambda)$ . The  $q$ -PKE assumption holds for  $\mathcal{G}$  if for every non-uniform PPT adversary  $\mathcal{A}$  there exists a non-uniform PPT extractor  $\mathcal{E}_{\mathcal{A}}$  such that:*

$$\begin{aligned}
&\Pr[\alpha H = \hat{H} \wedge H \neq (\sum_{i=0}^q \tilde{v}_i \tau^i) \mathcal{P}_1 : \\
&\quad (H, \hat{H}; \tilde{v}_0, \dots, \tilde{v}_q) \leftarrow (\mathcal{A} | \mathcal{E}_{\mathcal{A}})(\text{bgpp}, \tau \mathcal{P}_1, \tau \mathcal{P}_2, \dots, \tau^q \mathcal{P}_1, \tau^q \mathcal{P}_2, \alpha \mathcal{P}_1, \alpha \tau \mathcal{P}_1, \dots, \alpha \tau^q \mathcal{P}_1, \text{aux})] = \text{negl}(\lambda)
\end{aligned}$$

where  $\text{bgpp} = (p, \mathbb{G}_1, \mathbb{G}_2, \mathbb{G}_T, e, \mathcal{P}_1, \mathcal{P}_2) \leftarrow_{\mathcal{R}} \mathcal{G}(1^\lambda)$ ,  $\tau, \alpha \leftarrow_{\mathcal{R}} \mathbb{Z}_p$  are chosen uniformly at random, and  $\text{aux}$  is any auxiliary information that is generated independently of  $\alpha$ . The notation  $(H, \hat{H}; \tilde{v}_i) \leftarrow (\mathcal{A} | \mathcal{E}_{\mathcal{A}})(\text{inp})$  means that  $\mathcal{A}$  upon input of  $\text{inp}$  returns  $(H, \hat{H})$  and  $\mathcal{E}_{\mathcal{A}}$  on the same input returns  $\tilde{v}_i$ . In this case,  $\mathcal{E}_{\mathcal{A}}$  has access to  $\mathcal{A}$ 's random tape.



In order to prove Theorem 4, we describe a series of hybrid experiments  $G_0 - G_4$ , where experiment  $G_0$  is identical to the real adaptive proof of knowledge experiment and the remaining experiments  $G_1 - G_4$  are progressively modified in such a way that each consecutive pair is proven to be (computationally) indistinguishable. Some of the games use some flag values  $\text{bad}_i$  that are initially set to **false**. If at the end of a game any of these values is set to **true**, the game simply outputs 0. For notation, we denote with  $G_i$  the event that a run of  $G_i$  with the adversary outputs 1, and we call  $\text{Bad}_i$  the event that  $\text{bad}_i$  is set to **true** during a run of  $G_i$ . Essentially, whenever an event  $\text{Bad}_i$  occurs, the corresponding game may deviate its outcome.

**Game  $G_0$ :** This is the adaptive proof of knowledge experiment described in Section 3 and Figure 5.

**Game  $G_1$ :** This is the same as  $G_0$  except that the PRF  $F_S(\cdot)$  is replaced by a truly random function  $\mathcal{R} : \{0, 1\}^* \rightarrow \mathbb{F}$ . By the security of the PRF,  $G_1$  is computationally indistinguishable from  $G_0$ , i.e.,

$$|\Pr[G_0] - \Pr[G_1]| \leq \text{Adv}_{\mathcal{D}, \mathbb{F}}^{\text{PRF}}(\lambda)$$

**Game  $G_2$ :** This is the same as  $G_1$  except that the procedure **Ver** sets  $\text{bad}_2 \leftarrow \text{true}$  if the adversary makes verification queries that (a) verify correctly with respect to the equation (A.1)<sup>secret</sup>, and in which (b) there is a label  $L \notin T$  (i.e.,  $\mathcal{A}$  never asked to authenticate a value under label  $L$ ). Clearly,  $G_1$  and  $G_2$  are identical until  $\text{Bad}_2$ , i.e.,

$$|\Pr[G_1] - \Pr[G_2]| \leq \Pr[\text{Bad}_2]$$

We show that  $G_2$  is statistically close to  $G_1$ , by proving in Lemma 2 that  $\Pr[\text{Bad}_2]$  is (unconditionally) negligible. Intuitively, this follows from the fact that when  $L \notin T$  the first verification check is an equation with an almost-freshly sampled element  $\phi_L = \mathcal{R}(L) \in \mathbb{F}$ , i.e., the equation will be satisfied only with negligible probability, which is at most  $1/(p - Q)$  where  $Q$  is the number of verification queries made by  $\mathcal{A}$ .

**Game  $G_3$ :** This is the same as  $G_2$  except for the following change in **Ver** when answering Type 2 verification queries, i.e., we assume every label  $L$  was previously used to authenticate a value. Let  $\pi_\mu, \pi_\sigma$  be the elements in the proof  $\pi$  queried by the adversary. In  $G_3$  we compute  $\pi_\sigma^* = \sum_{k \in I_\sigma} x_k A_k \in \mathbb{G}_1$ , as well as its corresponding authentication tag  $\pi_\mu^* = \sum_{k \in I_\sigma} \mu_k A_k$ , where each  $\mu_k$  is the tag previously generated for  $(L_k, x_k)$  upon the respective authentication query. Next, we replace the check of equations (A.1)<sup>secret</sup> with checking whether

$$e(\pi_\mu / \pi_\mu^*, \mathcal{P}_2) = e(\pi_\sigma / \pi_\sigma^*, K_2) \quad (1)$$

is satisfied. Then, if equation (A.2) is satisfied (hence  $\pi'_\sigma = \alpha_a \pi_\sigma$ ), we can run an extractor  $\mathcal{E}_A$  to obtain a polynomial  $\tilde{a}_\sigma(X)$  of degree at most  $d$ . If  $\pi_\sigma \neq \tilde{a}_\sigma(\tau) \rho_a \mathcal{P}_1$ , then we set  $\text{bad}_3 \leftarrow \text{true}$ . First, we observe that by correctness, checking equation (1) is equivalent to checking the verification equation (A.1)<sup>secret</sup>.

Second, to see that we can run the extractor  $\mathcal{E}_A$ , we observe that the input received by the adversary  $\mathcal{A}$  can indeed be expressed as a pair  $(T, aux)$ , where  $T = \{\tau^i \mathcal{P}_j, \alpha \tau^i \mathcal{P}_j\}_{i \in [0, d], j=1,2}$  and  $aux$  is some auxiliary information independent of  $\alpha$  – as in the definition of the  $d$ -PKE assumption.

Hence,  $G_2$  and  $G_3$  are identical up to  $\text{Bad}_3$ , i.e.,

$$|\Pr[G_2] - \Pr[G_3]| \leq \Pr[\text{Bad}_3]$$

and it is easy to see that the  $d$ -PKE assumption immediately implies that the probability of  $\text{Bad}_3$  (i.e., that the extractor outputs a polynomial which is not a correct one) is negligible.

**Game  $G_4$ :** This game proceeds as  $G_3$  except for the following change in the **Ver** procedure. Assume that the equation (1) is satisfied and that  $\text{bad}_3 \leftarrow \text{true}$  is not set (i.e.,  $\pi_\sigma = \tilde{a}_\sigma(\tau)\rho_a\mathcal{P}_1$  holds). Then, compute the polynomials  $a_\sigma^*(X) = \sum_{k \in I_\sigma} x_k a_k(X)$  and  $\delta_a(X) = \tilde{a}_\sigma(X) - a_\sigma^*(X)$ , where  $\tilde{a}_\sigma(X)$  is the polynomial obtained from the extractor. If  $\delta_a(X)$  is *not* divisible by  $z(X)$  then set  $\text{bad}_4 \leftarrow \text{true}$ .  
Clearly,  $G_3$  and  $G_4$  are identical up to  $\text{Bad}_4$ , i.e.,

$$|\Pr[G_3] - \Pr[G_4]| \leq \Pr[\text{Bad}_4]$$

To show that the two games are negligibly close, we prove in Lemma 3 that  $\Pr[\text{Bad}_4]$  is negligible under the  $q$ -DHE assumption, for some  $q = 2d + 1$ .

Finally, we observe that at this point, if  $\text{Bad}_4$  does not occur, we have verified that  $\pi_\sigma$  was computed by using the correct (i.e., authenticated) statement values. Namely, except for having a randomized element  $\pi_\sigma$ , we are almost in the same conditions for breaking the proof of knowledge of PGHR. In fact, in Lemma 4 we show that if any adversary has advantage at most  $\epsilon$  in breaking the adaptive proof of knowledge of PGHR, then  $\Pr[G_4] \leq Q \cdot \epsilon$ , where  $Q$  is the number of **Gen** queries made by the adversary.

**Lemma 2.**  $\Pr[\text{Bad}_2] \leq Q/(p - Q)$ .

*Proof.* Let  $Q$  be the number of verification queries made by the adversary in  $G_2$ , and let  $B_i$  be the event that  $\text{bad}_2$  is first set from false to true in the  $i$ -th verification query. Clearly, we have:

$$\Pr[\text{Bad}_2] = \Pr\left[\bigvee_{i=1}^Q B_i\right] \leq \sum_{i=1}^Q \Pr[B_i]$$

To prove the lemma we will bound the probability  $\Pr[B_i]$  for any  $1 \leq i \leq Q$ , where the probability is taken over the random choices of the function  $\mathcal{R}(\cdot)$ .

By definition of  $B_i$  we have  $\Pr[B_i] = \Pr[B_i | \bar{B}_1 \wedge \dots \wedge \bar{B}_{i-1}]$ . Also, observe that  $\text{bad}_2$  is set to true if  $\exists k \in I_\sigma$  such that  $(L_k, \cdot) \notin T$  and the equation

$$\pi_\mu = \left[ \sum_{k \in I_\sigma} \mathcal{R}(L_k) A_k \right] + \kappa \cdot \pi_\sigma \quad (2)$$

is satisfied.

Let us fix one such index  $\bar{k} \in I_\sigma$  such that  $(L_{\bar{k}}, \cdot) \notin T$ . If  $\phi_{\bar{k}} = \mathcal{R}(L_{\bar{k}})$  is sampled uniformly at random in the  $i$ -th query, then the equation above will be satisfied with probability  $1/p$ . However, the adversary might have asked  $L_{\bar{k}}$  in some previous verification query, and such a query might have leaked some information about  $\phi_{\bar{k}} = \mathcal{R}(L_{\bar{k}})$ . Yet, since it holds  $\bar{B}_1 \wedge \dots \wedge \bar{B}_{i-1}$ , the only information leaked to the adversary is that a bunch of equations involving  $\phi_{\bar{k}}$  were not satisfied. For each of these unsatisfied equations, one can exclude at most one possible value of  $\phi_{\bar{k}}$ . In conclusion, we have that in the  $i$ -th query, the equation (2) is satisfied with probability at most  $\frac{1}{p-(i-1)}$ . Hence,

$$\Pr[\text{Bad}_2] \leq \sum_{i=1}^Q \frac{1}{p-(i-1)} \leq \frac{Q}{p-Q}.$$

□

**Lemma 3.** *If the  $q$ -DHE assumption [CKS09] holds for  $\mathcal{G}$ , we have that  $\Pr[\text{Bad}_4]$  is negligible for any PPT adversary  $\mathcal{A}$ .*

*Proof.* Assume that there is an adversary  $\mathcal{A}$  such that  $\Pr[\text{Bad}_4] \geq \epsilon$  is non-negligible. We show how to build an adversary  $\mathcal{B}$  that breaks the  $q$ -DHE assumption with probability  $\epsilon/DQ - 1/|\mathbb{F}|$  such that: (a)  $D = \text{poly}(\lambda)$  is an upper bound on the number of multiplication gates (and thus the degree of the corresponding QAP) in the  $Q$  circuits  $C_1, \dots, C_Q$  queried by  $\mathcal{A}$  to **Gen**, and (b)  $q = 2d^* + 1$  for some  $d^* \leq D$ , which is the degree of the QAP in the circuit  $C^*$  for which  $\text{Bad}_4$  occurs.

$\mathcal{B}$  takes as input an instance of the  $q$ -DHE assumption  $(\text{bgpp}, \tau\mathcal{P}_1, \tau\mathcal{P}_2, \dots, \tau^q\mathcal{P}_1, \tau^q\mathcal{P}_2, \tau^{q+2}\mathcal{P}_1, \tau^{q+2}\mathcal{P}_2, \dots, \tau^{2q}\mathcal{P}_1, \tau^{2q}\mathcal{P}_2)$  and its goal is to compute the missing element  $\tau^{q+1}\mathcal{P}_1$ . To do so,  $\mathcal{B}$  simulates  $\mathbf{G}_4$  to  $\mathcal{A}$  as described in the following. Assume that  $\text{Bad}_4$  occurs for the circuit  $C^*$  which is the  $j$ -th circuit queried to **Gen**.

#### Game setup:

- $\mathcal{B}$  sets up the experiment for  $\mathcal{A}$  as in  $\mathbf{G}_4$  with the following modifications.
- It picks random  $j^* \leftarrow_{\mathcal{R}} \{1, \dots, Q\}$ ,  $d^* \leftarrow_{\mathcal{R}} \{1, \dots, D\}$  to guess the query's index of  $C^*$  and its QAP's degree respectively.
- $\mathcal{B}$  sets  $q \leftarrow 2d^* + 1$ , and takes as input an instance  $(\text{bgpp}, \tau\mathcal{P}_1, \tau\mathcal{P}_2, \dots, \tau^q\mathcal{P}_1, \tau^q\mathcal{P}_2, \tau^{q+2}\mathcal{P}_1, \tau^{q+2}\mathcal{P}_2, \dots, \tau^{2q}\mathcal{P}_1, \tau^{2q}\mathcal{P}_2)$  of the  $q$ -DHE assumption.
- It defines the degree- $d^*$  polynomial  $z^*(X) = \prod_{k=1}^{d^*} (X - r_k)$  where  $\{r_k\}$  is a set of canonical roots used to build the QAP.<sup>10</sup>
- $\mathcal{B}$  chooses  $\kappa^*(X)$  as a random polynomial in  $\mathbb{F}[X]$  of degree  $d^* + 1$  such that the polynomial  $\kappa^*(X)z^*(X)$  of degree  $2d^* + 1$  has a zero coefficient in front of  $X^{d^*+1}$ .
- $\mathcal{B}$  simulates the secret  $\kappa$  with  $\kappa^*(\tau)$  by computing  $K_j = \kappa^*(\tau)\mathcal{P}_j$ , for  $j = 1, 2$ . Observe that  $\kappa^*(\tau)\mathcal{P}_j$  can be computed efficiently using  $\{\tau^i\mathcal{P}_j\}_{i=0}^{d^*+1}$  contained in the  $q$ -DHE instance.
- $\mathcal{B}$  generates a key pair  $(\text{sk}', \text{vk}') \leftarrow_{\mathcal{R}} \Sigma.\text{KG}(1^\lambda)$  for the regular signature scheme and gives to the adversary  $\text{pap} = (\text{pp}, \text{prfpp}, K_1, K_2)$  and  $\text{vk} = (\text{vk}', K_2)$ .

#### Gen( $C$ )

$\mathcal{B}$  proceeds as follows to simulate the  $i$ -th query.

- [Case  $i \neq j^*$ ]  $\mathcal{B}$  runs the real  $\text{Gen}(\text{pap}, C)$  algorithm and returns its output.
- [Case  $i = j^*$ ] Let us call  $C^*$  the queried circuit.  $\mathcal{B}$  simulates the answer to this query as follows. First, it builds the QAP for  $C^*$  and if its degree  $d$  is not the  $d^*$  guessed earlier, then  $\mathcal{B}$  aborts the simulation. Otherwise, we have  $d = d^*$  and hence  $z(X) = z^*(X)$  and  $\mathcal{B}$  can proceed as follows. For the value  $\tau$ , instead of randomly choosing it,  $\mathcal{B}$  implicitly uses the same value  $\tau$  from the  $q$ -DHE assumption. Namely,  $\mathcal{B}$  implicitly sets  $\rho_a = \rho'_a \tau^{d+1}$  and  $\rho_c = \rho'_a \rho_b \tau^{d+1} \rho_a \rho_b$ , where  $\rho'_a, \rho_b \leftarrow_{\mathcal{R}} \mathbb{F}$ , by computing, for  $k = 0, \dots, m$ :

$$A_k = \tau^{d+1} a_k(\tau) \rho'_a \mathcal{P}_1, \quad C_k = \tau^{d+1} c_k(\tau) \rho'_a \rho_b \mathcal{P}_1, \quad A_{m+1} = \tau^{d+1} z(\tau) \rho'_a \mathcal{P}_1, \quad C_{m+3} = \tau^{d+1} z(\tau) \rho'_a \rho_b \mathcal{P}_1.$$

Notice that these values can be computed efficiently since all the polynomials  $\tau^{d+1} a_k(\tau)$  and  $\tau^{d+1} c_k(\tau)$  have degree at most  $2d^* < q$ , while  $\tau^{d+1} z(\tau)$  has degree  $2d^* + 1 = q$ . Similarly, all the remaining values  $\{B_k\}$  can be simulated as the degree of the polynomials encoded in the exponent is at most  $d^* < q$ . The simulation of the remaining elements  $Z, A'_k, B'_k, C'_k, E_k$  can be done in a very similar way.

<sup>10</sup> The roots of the QAP target polynomial can be chosen arbitrarily.

Finally,  $K_a = (A_{m+1})^\kappa$  is simulated by computing  $\rho'_a(\tau^{d+1} \kappa^*(\tau) z(\tau) \mathcal{P}_1)$ . In particular, note that  $(\tau^{d+1} \kappa^*(\tau) z(\tau) \mathcal{P}_1)$  can be computed since  $\tau^{d+1} \kappa^*(\tau) z(\tau)$  has degree  $3d+2$  and has a zero coefficient in front of  $\tau^{2d+2} = \tau^{q+1}$ , by construction of  $\kappa^*(X)$ .

#### **Auth**( $L, x$ )

To simulate authentication queries,  $\mathcal{B}$  samples a random  $\mu \leftarrow_{\mathcal{R}} \mathbb{F}$ , computes  $\Phi = \mu \mathcal{P}_2 - x K_2$ , generates  $\sigma' \leftarrow_{\mathcal{R}} \Sigma.\text{Sign}(\text{sk}', \Phi | L)$ , updates  $\mathsf{T} \leftarrow \mathsf{T} \cup \{(L, x)\}$ , and returns  $\sigma = (\mu, \Phi, \sigma')$ . Observe that such  $\sigma$  is identically distributed as an authentication tag returned by **Auth** in  $\mathsf{G}_4$ . Also, although  $\mathcal{B}$  is not explicitly generating  $\phi \leftarrow \mathcal{R}(L)$ , as one can notice, these values are no longer used to answer the verification queries.

#### **Ver**( $C, L, \{x_i\}_{L_i \neq *}, \tilde{\pi}$ )

Finally, we describe how  $\mathcal{B}$  handles verification queries. First, note that for those queries that fall in the Type 1 branch,  $\mathcal{B}$  can directly answer  $\perp$  (reject), and it does not have to use the values  $\mathcal{R}(L)$ . Clearly, due to definition of game  $\mathsf{G}_4$  and since  $\text{Bad}_2$  does not occur, answers to these queries are correctly distributed. Second, for queries in the Type 2 branch, we distinguish two cases according to whether the queried circuit  $C$  is  $C^*$  or not.

- If  $C \neq C^*$ , then  $\mathcal{B}$  can answer as is done in game  $\mathsf{G}_4$ . In particular, note that equation (A.1)<sup>secret</sup> has been replaced by equation (1) that requires only public values to be checked.
- If  $C = C^*$ , then  $\mathcal{B}$  proceeds as in  $\mathsf{G}_4$ . First, set  $\delta_a(X) = \tilde{a}_\sigma(X) - a_\sigma^*(X)$ . Now, since we assume that  $\text{Bad}_4$  occurs in the experiment, this means that  $\delta_a(X)$  is not divisible by  $z^*(X)$ , i.e.,  $\delta_a \notin \text{Span}(z^*(X))$ . Then  $\mathcal{B}$  checks whether  $\omega(X) = \delta_a(X) \kappa^*(X)$  is such that its coefficient  $\omega_{d+1}$  is zero. If so,  $\mathcal{B}$  aborts the simulation (however, by Lemma 10 [GGPR13], this happens with probability at most  $1/|\mathbb{F}|$ ). Otherwise, if  $\omega_{d+1} \neq 0$ ,  $\mathcal{B}$  returns

$$\Omega = (\omega_{d+1} \rho'_a)^{-1} \left[ \pi_\mu - \pi_\mu^* - \sum_{k=0, k \neq d+1}^{2d+1} \rho'_a \omega_k (\tau^{k+d+1} \mathcal{P}_1) \right]$$

Notice that  $\mathcal{B}$ 's simulation to  $\mathcal{A}$  is perfect except if  $\mathcal{B}$  aborts. However,  $\mathcal{B}$  can abort only in three cases: if its guess on  $j^*$  is wrong, i.e., if  $j \neq j^*$  (which happens with probability  $1 - 1/Q$ ); if its guess on  $d^*$  is wrong, i.e., if  $d \neq d^*$  (which happens with probability  $1 - 1/D$ ); if  $\omega_{d+1} = 0$  (which holds unconditionally with probability at most  $1/|\mathbb{F}|$ ). Also, it is not hard to see that if  $\text{Bad}_4$  occurs, then  $\mathcal{B}$  returns

$$\begin{aligned} \Omega &= (\omega_{d+1} \rho'_a)^{-1} \left[ \kappa(\pi_\sigma - \pi_\sigma^*) - \sum_{k=0, k \neq d+1}^{2d+1} \rho'_a \omega_k (\tau^{k+d+1} \mathcal{P}_1) \right] \\ &= (\omega_{d+1} \rho'_a)^{-1} \left[ \rho'_a \tau^{d+1} \delta_a(\tau) \kappa^*(\tau) \mathcal{P}_1 - \sum_{k=0, k \neq d+1}^{2d+1} \rho'_a \omega_k (\tau^{k+d+1} \mathcal{P}_1) \right] \\ &= (\omega_{d+1} \rho'_a)^{-1} \left[ \rho'_a \tau^{d+1} \omega(\tau) - \rho'_a \tau^{d+1} (\omega(\tau) - \omega_{d+1} \tau^{d+1}) \right] \mathcal{P}_1 \\ &= (\omega_{d+1} \rho'_a)^{-1} \left[ \rho'_a \tau^{d+1} \omega_{d+1} \tau^{d+1} \right] \mathcal{P}_1 \\ &= \tau^{2d+2} \mathcal{P}_1 \end{aligned}$$

and breaks the  $q$ -DHE assumption, as desired.

Therefore, by putting together the probability that  $\mathcal{B}$  does not abort, with our assumption that  $\Pr[\text{Bad}_4] \geq \epsilon$ , then we obtain that  $\mathcal{B}$  breaks the  $q$ -DHE assumption with probability  $\geq \epsilon/DQ - 1/|\mathbb{F}|$ .  $\square$

**Lemma 4.** *If PGHR satisfies adaptive proof of knowledge, and the  $q$ -PKE assumption holds, then for any PPT adversary  $\mathcal{A}$  we have that  $\Pr[\mathbf{G}_4]$  is negligible.*

*Proof.* Assume by contradiction that there exists an adversary  $\mathcal{A}$  such that  $\Pr[\mathbf{G}_4] \geq \epsilon$  is non-negligible. We show how to build an adversary  $\mathcal{B}$  that breaks the security of PGHR with probability at least  $\epsilon/Q_1Q_2$ , where  $Q_1$  is the number of circuits  $C_1, \dots, C_{Q_1}$  queried by  $\mathcal{A}$  to **Gen** during game  $\mathbf{G}_4$ , and  $Q_2$  is the number of verification queries. Without loss of generality, assume that  $\mathcal{B}$  receives the parameters **bgpp** of the bilinear groups before choosing the circuit  $C^*$  to attack.<sup>11</sup>

**Game setup:**

- $\mathcal{B}$  picks a random  $j^* \leftarrow_{\mathcal{R}} \{1, \dots, Q_1\}$  to guess the query's index of  $C^*$ , the circuit for which  $\mathcal{A}$  will break the security of our ADSNARK scheme in game  $\mathbf{G}_4$ .
- $\mathcal{B}$  generates a key pair  $(\text{sk}', \text{vk}') \leftarrow_{\mathcal{R}} \Sigma.\text{KG}(1^\lambda)$  for the regular signature scheme, and then samples a random  $\kappa \leftarrow_{\mathcal{R}} \mathbb{F}$ . It gives to  $\mathcal{A}$   $\text{pap} = (\text{bgpp}, \text{prfpp}, K_1 = \kappa\mathcal{P}_1, K_2 = \kappa\mathcal{P}_2)$  and  $\text{vk} = (\text{vk}', K_2)$ .

**Gen( $C$ )**

$\mathcal{B}$  proceeds as follows to simulate the  $i$ -th generation query.

- [Case  $i \neq j^*$ ]  $\mathcal{B}$  runs the real **Gen**(**pap**,  $C$ ) algorithm and returns its output.
- [Case  $i = j^*$ ] Let us call  $C^*$  the queried circuit.  $\mathcal{B}$  forwards  $C^*$  to its challenger and receives a pair of keys  $(\text{VK}_P^*, \text{EK}_P^*)$  of the PGHR scheme.  $\mathcal{B}$  then uses  $\kappa$  to compute  $K_a = \kappa A_{m+1}$ , sets the key pair of the ADSNARK scheme to  $(\text{VK}^*, \text{EK}^*)$ , where  $\text{VK}^* = \text{VK}_P^*$  and  $\text{EK}^*$  consists of  $\text{EK}_P^*$  and the additional value  $K_a$ .

**Auth( $L, x$ )**

$\mathcal{B}$  runs **Auth** as in  $\mathbf{G}_4$ , i.e.,  $\mathcal{B}$  outputs  $\sigma = (\mu = \mathcal{R}(L) + \kappa x, \Phi = \mathcal{R}(L)\mathcal{P}_2, \sigma' = \Sigma.\text{Sign}(\text{sk}', \Phi|L))$ .

**Ver( $C, L, \{x_i\}_{L_i \neq *}, \tilde{\pi}$ )**

Finally, we describe how  $\mathcal{B}$  simulates verification queries to  $\mathcal{A}$ . Notice that all the equation checks require only public values. Also, observe that in  $\mathbf{G}_4$  the adversary  $\mathcal{A}$  can win only by returning a Type 2 forgery, and by returning a proof  $\pi$  containing values  $\pi_\sigma, \pi'_\sigma$  of the “correct form”, i.e.,  $\pi_\sigma = (a_\sigma^*(\tau) + \delta_a^\sigma z(\tau))\rho_a \mathcal{P}_1$  and  $\pi'_\sigma = (a_\sigma^*(\tau) + \delta_a^\sigma z(\tau))\rho_a \alpha_a \mathcal{P}_1 = \alpha_a \pi_\sigma$  respectively, for some  $\delta_a^\sigma \in \mathbb{F}$ .

So, for every verification query that passes the verification checks and that involves the circuit  $C^*$ ,  $\mathcal{B}$  translates the given proof  $\pi$  into a proof  $\pi_P$  as described below.

**Translation of  $\pi$  to  $\pi_P$ .** Let  $\pi = (\pi_\mu, \pi_\sigma, \pi'_\sigma, \pi_{mid}, \pi'_{mid}, \pi_b, \pi'_b, \pi_c, \pi'_c, \pi_E, H)$ . First,  $\mathcal{B}$  computes  $\hat{\pi}_{mid} = \pi_{mid} + (\pi_\sigma - \pi_\sigma^*)$  and  $\hat{\pi}'_{mid} = \pi'_{mid} + (\pi'_\sigma - \pi_\sigma^*)$ , where  $\pi_\sigma^* = \langle \vec{x}, \vec{A} \rangle_{I_\sigma}$  and  $\pi'_\sigma^* = \langle \vec{x}, \vec{A}' \rangle_{I_\sigma}$ . Then,  $\mathcal{B}$  computes  $\hat{\pi}_E = \pi_E + \delta_a^\sigma E_{m+1}$  where  $\delta_a^\sigma = (\tilde{a}_\sigma(X) - a_\sigma^*(X))/z(X)$ . Next,  $\mathcal{B}$  changes the (accepting) proof  $\pi$  produced by  $\mathcal{A}$  by: replacing  $\pi_{mid}, \pi'_{mid}$  and  $\pi_E$  with the values  $\hat{\pi}_{mid}, \hat{\pi}'_{mid}$  and  $\hat{\pi}_E$  (as computed above) respectively; removing  $\pi_\sigma, \pi'_\sigma, \pi_\mu$ . Let  $\pi_P$  be such modified proof.  $\mathcal{B}$  stores the tuple  $(\{x_k\}_{k \in \mathcal{I}_x}, \pi_P)$  into a list  $\Omega$ .

First, observe that the proof  $\pi_P$  is identical to a proof in the scheme PGHR, and in particular it has the same distribution. Second, we claim that if  $\pi$  is accepted in  $\mathbf{G}_4$  for the circuit  $C^*$  and labels

<sup>11</sup> We note that this reduction to the security of PGHR is done for ease of exposition. Indeed, we could have included in our simulator  $\mathcal{B}$  the same code of the simulator in the security proof of the PGHR scheme, where the parameters of the bilinear groups are received at the very beginning.

$\{\mathbf{L}_k\}_{k \in \mathcal{I}_\sigma}$  (used to authenticate  $\{x_k\}_{k \in \mathcal{I}_\sigma}$ ), then  $\pi_P$  is accepted for statement  $\{x_k\}_{k \in \mathcal{I}_x}$  in the given instance of the PGHR scheme for circuit  $C^*$ .

The first claim follows by inspection and by observing that since **Bad**<sub>4</sub> does not occur, the value  $(\pi_\sigma - \pi_\sigma^*)$  contains a multiple of  $z(\tau)$ , i.e., the correct form of  $\pi_{mid}$  is preserved. In particular, the value  $\delta_a^\sigma$  is a scalar value since  $(\tilde{a}_\sigma(X) - a_\sigma^*(X))$  is divisible by  $z(X)$  which has degree  $d$ , and  $\deg(\tilde{a}_\sigma(X)), \deg(a_\sigma^*(X)) \leq d$ .

The second claim follows from the fact that the value  $A = \pi_\sigma + A_\star + \pi_{mid}$  computed to verify the proof  $\pi$  in the ADSNARK scheme, and the value  $A_P = \langle \vec{x}, \vec{A} \rangle_{[0,n]} + \hat{\pi}_{mid}$  computed to verify the proof  $\pi_P$  in PGHR are identical – as  $\hat{\pi}_{mid} = \pi_{mid} + (\pi_\sigma - \pi_\sigma^*)$ . Since **Bad**<sub>4</sub> does not occur, the value  $\delta_a^{(\sigma)}$  is exactly the coefficient used by  $\mathcal{A}$  for the randomization of  $\pi_\sigma$ .

After  $\mathcal{A}$  stops running,  $\mathcal{B}$  picks a random tuple  $(\{x_k\}_{k \in \mathcal{I}_x}, \pi_P)$  from the list  $\Omega$  (which contains at most  $Q_2$  elements) and returns this tuple to its challenger.

To complete the proof we analyze  $\mathcal{B}$ 's success probability. We claim that if  $\mathcal{A}$  breaks the security of the ADSNARK scheme in game  $\mathbf{G}_4$ , then  $\mathcal{B}$  breaks the adaptive proof of knowledge property of PGHR with probability at least  $1/Q_1 Q_2$ . It is not hard to see that  $\mathcal{B}$ 's simulation has a distribution which is statistically close to the distribution of game  $\mathbf{G}_4$ . Also, if  $\mathcal{A}$  breaks the scheme it means that for at least one of its verification queries that accepts, say the  $\ell$ -th query, we have that  $x \notin \mathcal{R}_C$ . Assume that  $C$  was the  $j$ -th circuit queried to **Gen**, and that  $\mathcal{B}$  returns the  $\ell^*$ -th tuple in the list  $\Omega$ . Since the simulation does not leak any information on  $j^*$  and  $\ell^*$ , we have that  $\Pr[j^* = j \wedge \ell^* = \ell] \geq 1/Q_1 Q_2$ . Therefore, if  $\mathcal{A}$  breaks the security of the ADSNARK scheme in game  $\mathbf{G}_4$  with probability at least  $\epsilon$ , then  $\mathcal{B}$  breaks the security of PGHR with probability  $\geq \epsilon/Q_1 Q_2$ .  $\square$

**Adaptive Proof of Knowledge with Public Verifiability.** It is easy to adapt the proof of Theorem 4 in order to show that our scheme satisfies adaptive proof of knowledge even in the case where the proof is made publicly verifiable. Hence, it is possible to prove the following theorem:

**Theorem 5.** *If PGHR is a SNARK,  $F$  is a pseudorandom function,  $\Sigma$  is a secure signature scheme, the  $d$ -PKE [Gro10] and the  $q$ -DHE [CKS09] assumptions hold, then the scheme described above is a publicly-verifiable AD-SNARK with adaptive proof of knowledge.*

In the publicly verifiable case, since the adversary can verify the proofs on its own, we can assume that it makes a single verification query to **Ver**. To obtain the proof of Theorem 5, we use the same games as those for Theorem 4. The only difference is that the probability  $\Pr[\mathbf{Bad}_2]$  is now shown to be negligible under the assumption that the regular signature scheme is secure. Such claim is rather straightforward: an adversary which returns a proof involving a statement value with label  $\mathbf{L}_k$  that had not been queried to the **Auth** oracle, has to show at least one signature  $\sigma'_k$  that verifies correctly for some non-queried label  $\mathbf{L}$ .

### 4.3 Proof of the Zero-Knowledge Property

**Theorem 6.** *The ADSNARK scheme described in Section 4 is statistically zero-knowledge in the sense of Definition 6.*

*Proof.* To see that our scheme satisfies zero-knowledge, our first observation is that the group elements  $\pi_\sigma$ ,  $\pi_{mid}$ , and  $\pi_c$ , are statistically uniform over  $\mathbb{G}_1$  and the same holds for  $\pi_b$  over  $\mathbb{G}_2$ . Indeed, as long as  $z(\tau) \neq 0$ , each of these elements is uniformly randomized.

$\text{Sim}_1(\text{pp}, C, \text{sk}, \text{vk}, \text{pap})$ Run $\text{Gen}(\text{pap}, C)$ to obtain $(\text{EK}_C, \text{VK}_C)$ and also store $\text{sk}, \tau, \beta, \alpha_a, \alpha_b, \alpha_c, \rho_a, \rho_b, \rho_c$ in $\text{td}$ Return $(\text{EK}_C, \text{VK}_C, \text{td})$	$\text{Sim}_2(\text{td}, L, \{x_i\}_{L_i=\star})$ let $a_\star(X) = a_0(X) + \sum_{k \in I_\star} x_k a_k(X), \{\phi_k \leftarrow F_S(L_k)\}_{k \in I_\sigma}$ Choose random $a_\sigma(X), a_{\text{mid}}(X) \leftarrow_{\mathcal{R}} \mathbb{F}[X]$ $a(X) \leftarrow a_\sigma(X) + a_\star(X) + a_{\text{mid}}(X)$ Choose random $b(X), c(X) \leftarrow_{\mathcal{R}} \mathbb{F}[X]$ , such that $z(Z) \mid a(X)b(X) - c(X)$ $h(X) \leftarrow (a(X)b(X) - c(X))/z(X)$ $\pi_\mu \leftarrow \langle \vec{\phi}, \vec{A} \rangle_{I_\sigma} + \rho_a a_\sigma(\tau) \kappa \mathcal{P}_1$ $\pi_\sigma \leftarrow \rho_a a_\sigma(\tau) \mathcal{P}_1, \pi'_\sigma \leftarrow \alpha_a \pi_\sigma, \pi_{\text{mid}} \leftarrow \rho_a a_{\text{mid}}(\tau) \mathcal{P}_1, \pi'_{\text{mid}} \leftarrow \alpha_a \pi_{\text{mid}}$ $\pi_b \leftarrow \rho_b b(\tau) \mathcal{P}_2, \pi'_b \leftarrow \alpha_b \rho_b b(\tau) \mathcal{P}_1, \pi_c \leftarrow \rho_c c(\tau) \mathcal{P}_1, \pi'_c \leftarrow \alpha_c \pi_c,$ $\pi_E \leftarrow \beta[\pi_\sigma + \pi_{\text{mid}} + (\rho_a a_\star(\tau) + \rho_b b(\tau)) \mathcal{P}_1 + \pi_c]$ $H \leftarrow h(\tau) \mathcal{P}_1$ Return $\pi = (\pi_\mu, \pi_\sigma, \pi'_\sigma, \pi_{\text{mid}}, \pi'_{\text{mid}}, \pi_b, \pi'_b, \pi_c, \pi'_c, \pi_E, H)$
---	--

Figure 6. Simulator Sim.

Second, we notice that once the elements  $\pi_\sigma, \pi_{\text{mid}}, \pi_b, \pi_c$ , are fixed, the values of all the remaining elements in  $\pi$ , i.e.,  $\pi_\mu, \pi'_\sigma, \pi'_{\text{mid}}, \pi'_b, \pi'_c, \pi_E$ , and  $H$  get determined according to the constraints of the verification equations (A.1), (A.2), (P.1), (P.2), (P.3).

Finally, we show that there is a simulator  $(\text{Sim}_1, \text{Sim}_2)$ , formally described in Figure 6, that satisfies Definition 6. The simulated keys generated by  $\text{Sim}_1$  are distributed as in the real experiment. Regarding  $\text{Sim}_2$ , it is not hard to see that the simulated values  $\pi_\sigma, \pi_{\text{mid}}, \pi_b, \pi_c$  are statistically uniform. Also, given the trapdoor,  $\text{Sim}_2$  can generate (without knowing inputs  $\{x_k\}_{k \in I_\sigma}$ ) all remaining elements of  $\pi$  with the correct distribution, i.e., such that verification equations (A.1), (A.2), (P.1), (P.2), (P.3) are satisfied.  $\square$

## 5 Evaluation

We now describe our implementation of the ADSNARK scheme proposed in Section 4 and then present the experimental results we obtained to support the efficiency and practical applicability claims for our construction.

### 5.1 Implementation

We have implemented our ADSNARK scheme as an extension to the `libsnark` library<sup>12</sup> [BSCG<sup>+</sup>13, BSCTV14]. Our scheme extends the PGHR SNARK implementation offered by this library and supports the same class of statements expressed in the NP-complete language R1CS (rank-1 constraint systems), which is similar to arithmetic circuit satisfiability. The resulting implementation is totally generic, following the `libsnark` code writing policies, and can be instantiated with arbitrary digital signatures and PRF constructions (in addition to the various parameterization options already offered by the `libsnark` library). The source code is available upon request.

The modifications to the original PGHR SNARK implementation required by our extensions were relatively small.<sup>13</sup> In the global parameter generation algorithm, the modifications were limited to one additional exponentiation. In the symmetric verification algorithm, we replaced the computations performed on the (known) inputs with (essentially equivalent) computations on the

<sup>12</sup> <https://github.com/scipr-lab/libsnark>

<sup>13</sup> This would be expected from the theoretical description of our scheme, but praise should also go to the developers of the `libsnark` library, who produced a nice, modular and well documented implementation on which it was easy to build upon.

corresponding authentication elements.<sup>14</sup> In the prover algorithm, the extra code comprises the three multi-exponentiations required to compute the extra authentication elements. Finally, our extensions are most visible in the public verification algorithm where, in addition to the digital signature verification operations, the number of pairings to be computed also increases linearly with the number of authenticated inputs. Our implementation strategy was to employ the optimizations available in the `libsark` codebase whenever possible, taking advantage of the existing multi- and batch- exponentiation algorithms. The additional pairing computations required in public verification are performed two-by-two, exploiting the available *double Miller loop* optimization.

For the extra cryptographic components required by our construction, i.e., the generic signature scheme and the PRF mapping labels to field elements, we have turned to the state-of-the-art implementations offered by the most recent version of the `Supercop` framework.<sup>15</sup> For the signature scheme, we have used the `ed25519`<sup>16</sup> implementation described in [BDL<sup>+</sup>12], which offers extremely fast batch verification that we incorporated in the `ADSNARK` public verification algorithm (recall that one signature per input must be verified). For the PRF implementation, we have fixed labels to be 128-bit binary strings and the PRF key to be a 256-bit string partitioned as two AES keys. The PRF construction uses one AES computation to map the input label to a 128-bit pseudorandom seed, applies an independent instance of AES in counter mode to expand the seed to 384 pseudorandom bits, and then uses modular reduction to obtain a pseudorandom 254-bit field element.<sup>17</sup> To select the best `ed25519` and AES implementations, we have simply run `Supercop` on our target machine to exhaustively evaluate all available implementations, and then used the recommendations that this framework produced for the fastest implementations and corresponding compilation options.

**Microbenchmarks.** All measurements were taken in a modest machine with two Dual-Core AMD Opteron 2218 processors clocked at 1 GHz, with 12 GB RAM. The reported values for every parameter correspond to the median of measurements computed over at least 100 runs. Following the original implementation of the `libsark` library, we have equipped our implementation of the verification algorithm with the capability to perform part of the computation off-line. However, all our results pessimistically report the full verification time. The security level was set at 128-bits.

## 5.2 Experiments Setup

We have conducted experiments to carry out two types of performance evaluation: the first targeting general circuits, and the second focusing on a concrete application.

**General circuits.** To obtain our first set of experimental results, we have relied on the `libsark` functionality that permits generating random instances of constraint systems of arbitrary sizes. This allowed us to evaluate the performance of our protocol when dealing with proof goals corresponding to computations of growing complexity and with a varying number of inputs. Our goal here was to corroborate the theoretical analysis presented in Section 4, by benchmarking our protocol

<sup>14</sup> We deviate slightly from the original implementation in the way we store these input authentication elements. We use a simple (dense) vector representation as opposed to the more elaborate (sparse) map representation in the original. This originated a slight improvement in verification times in the experiments we conducted, but this is simply due to the fact that we did not explore more complex input handling scenarios, where our representation of inputs data might prove less adequate.

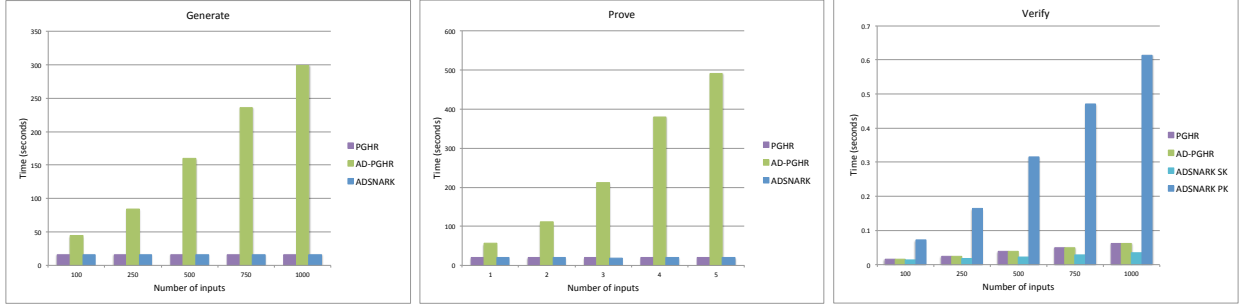
<sup>15</sup> <http://bench.cr.yp.to/supercop.html>

<sup>16</sup> <http://ed25519.cr.yp.to/>

<sup>17</sup> It is straightforward to prove that this construction yields a secure PRF, assuming that AES is itself a secure PRF.



against both the original (unauthenticated) PGHR SNARK protocol and the generic AD-SNARK construction described in Section 3.2 instantiated with PGHR, that we call AD-PGHR.



	Generation Time (seconds)			Proving Time (seconds)			Verification Time (seconds)			
Inputs	PGHR	AD-PGHR	ADSNARK	PGHR	AD-PGHR	ADSNARK	PGHR	AD-PGHR	ADSNARK SK	ADSNARK PK
100	16.259	44.441	16.269	19.600	56.349	19.558	0.017	0.017	0.014	0.073
250	16.312	84.695	16.358	19.651	111.008	19.597	0.025	0.025	0.017	0.165
500	16.317	159.943	16.335	19.561	212.162	19.473	0.038	0.038	0.023	0.316
750	16.344	236.379	16.307	19.602	380.563	19.672	0.050	0.050	0.029	0.470
1 000	16.350	299.314	16.276	19.513	490.852	19.612	0.062	0.062	0.035	0.613

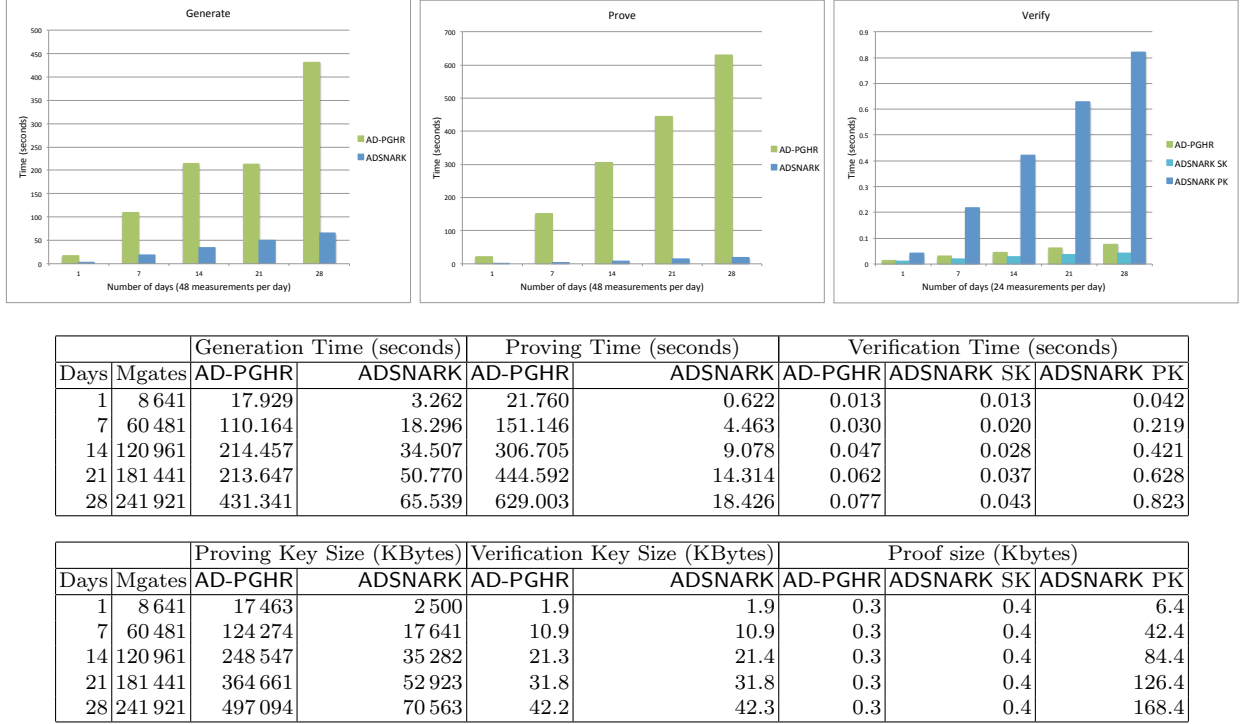
	Proving Key Size (KBytes)			Verification Key Size (KBytes)			Proof size (Kbytes)			
Inputs	PGHR	AD-PGHR	ADSNARK	PGHR	AD-PGHR	ADSNARK	PGHR	AD-PGHR	ADSNARK SK	ADSNARK PK
100	15 650	45 944	15 657	3.5	3.5	3.5	0.3	0.3	0.4	12.9
250	15 640	91 885	15 657	8.2	8.2	8.2	0.3	0.3	0.4	31.6
500	15 622	167 092	15 657	16.0	16.0	16.0	0.3	0.3	0.4	62.9
750	15 605	250 459	15 657	23.8	23.8	23.8	0.3	0.3	0.4	94.1
1 000	15 587	318 590	15 657	31.5	31.5	31.5	0.3	0.3	0.4	125.4

**Figure 7.** Experimental results showing generation, proving and verification times for random constraint systems of size 50K and varying number of inputs. For AD-PGHR, the number of multiplication gates is  $50K + 1000 \times \text{\#inputs}$ . For ADSNARK in the public verification variant, the proof size is equal to the SNARK proof size plus the size of the authentication data, which is 128 bytes per input.

We have arbitrarily fixed the complexity of the computation associated with the proof goal to involve 50K restrictions (or equivalent, roughly 50K multiplication gates), which typically corresponds to a computation of intermediate complexity according to the state of the art (see for example [PGHR13]). The concrete size of the computation is not important, since we will be concerned with the relative degradation of the performance of the various protocols, as we gradually increase the number of (possibly authenticated) inputs to the computation from 100 to 1000. For the generic construction AD-PGHR, we have (very optimistically) taken the penalty for including the signature verification circuit in the proof goal to be only of 1000 multiplications per signature. The fact that, in practice, the cost will probably be higher only strengthens our claims.

**Concrete Application.** Our second set of experimental results targets a real-world scenario, where the security guarantees provided by an AD-SNARK are highly relevant: a concrete smart-metering application like the one described in the introduction. Analogous results can be obtained for similar applications such as the pay-as-you-drive insurance or the health risk assessment. Our goal here is to indeed demonstrate the practical applicability of our ADSNARK implementation and to show that the overhead incurred by the generic construction can be prohibitive in practice, as it may

lead to a significant increase in the complexity of the proof goal. This is particularly true if the proof goal is reasonably simple to start with, as is the case in the application that follows.



**Figure 8.** Experimental results showing generation, proving, and verification times for the smart metering application, with the number of measurements varying from 1 day to 28 days (with 48 measurements per day). For AD-PGHR, the number of multiplication gates is  $\#Mgates + 1000 \times \#days \times 48$ . For ADSNARK in the public verification variant, the proof size is equal that of the SNARK proof plus the size of the authentication data (128 bytes per input).

We focus on the smart-metering application described in [RD11, FKDL13] where a (non-linear) cumulative price function is applied to the consumption measurements in order to determine the aggregated cost. The idea here is that the smart meter is able to authenticate the measurements, and that the client locally computes the monetary value corresponding to the measured consumption. The client can then use an AD-SNARK protocol to demonstrate to the supplier that the computation is correct and based on legitimate measurements, without divulging the details of the individual values. As a simple example of a cumulative policy [RD11], one may think of a non-linear function defined by the following list of threshold/price pairs:  $[(0, 2), (3, 5), (7, 8)]$ . This policy establishes four consumption intervals and their corresponding prices, as follows:  $[0, 3] \rightarrow 2$ ,  $(3, 7] \rightarrow 5$ ,  $(7, \infty) \rightarrow 8$ . For a measured consumption of 9, the price due is  $3 \times 2 + 4 \times 5 + 2 \times 8 = 42$ .

In this application, the complexity of the price computation depends on both the number of measurements and the number of intervals prescribed by the cost function.

We have implemented a generator of R1CS statements that, for a specified number of measurements and a concrete cumulative cost function, is able to construct a constraint system for an arithmetic circuit that checks the correctness of the computed cost, for any given set of measurements. The number of multiplication gates in (i.e., the number of constraints associated to) the

resulting circuits is  $36 \times \# \text{measurements} \times \# \text{intervals} + 1$ .<sup>18</sup> For the generic construction AD-PGHR, we have again used the estimate of 1000 additional multiplications per signature verification. We set the number of thresholds to 5 (a coarse level of granularity in specifying the non-linear policy) so that we obtain a moderately sized circuit even for a month’s worth of readings. We then take the indicative value of 48 measurements per day, and vary the number of days separating the price computation to be 1, 7, 14, 21, and 28 days. The policy is defined by thresholds 5, 10, 15, 20, and 25. The measurement values were sampled at random in the range 0 to 100.

### 5.3 Performance for General Circuits

Figure 7 shows the results we obtained in terms of execution time. It is clear from the graphs the rapid degradation of the global generation and proving times in the case of AD-PGHR. This is a direct consequence of increasing the size of the circuit and corresponding increase in the size of the proving key, which for 1000 inputs in AD-PGHR approaches 320 MB, as opposed to 15 MB for ADSNARK and PGHR.<sup>19</sup> The (relatively) small penalty paid for using public verification in ADSNARK is visible in the verification times. Furthermore, it is interesting to observe that the secret-key verification of ADSNARK is as fast as the one of AD-PGHR or the (unauthenticated) PGHR. The size of the proof is under 500 bytes for all protocols except the public verification version of AD-PGHR, where the authentication data takes an additional 128 bytes per input. Even so, for 1000 inputs, the proof size is under 126Kbytes.<sup>20</sup>

### 5.4 Performance for Smart Metering Billing

Figure 8 shows the results we obtained in terms of execution time. It is clear from the graphs that ADSNARK yields proving times that are compatible with real-world deployment: even for one month’s worth of measurements, the proving time is around 18 seconds, the proof size is under 0.5 KB for secret verification and under 170 KB for public verification. The contrast to AD-PGHR is evident, where the proof size is essentially the same as ADSNARK with secret verification, but the running time of the AD-PGHR’s prover goes up to over 10 minutes. Moreover, even for a month’s worth of readings, ADSNARK would pay little more time for public verification (around 0.8 seconds vs. 0.08 seconds of AD-PGHR). Although this may not be very important for smart-metering, it shows, once more, that the public verification time scales very well.

## 6 Further Related Work

As we mentioned earlier, our work extends the notion of succinct non-interactive arguments of knowledge (SNARKs) [Mic94, BCCT12], which in turn build on (succinct) interactive proofs [GMR89] and interactive arguments [Kil92, Kil95]. In particular, we focus on the so-called *preprocessing model* where the verifier is required to run an expensive but re-usable key generation phase. In this preprocessing model, several works [Gro10, Lip12, GGPR13, BCI<sup>+</sup>13] proposed efficient realizations of SNARKs, and more recent works [PGHR13, BSCG<sup>+</sup>13, BSCTV14] have shown efficient,

<sup>18</sup> The circuit implementation assumes that measurements and thresholds are represented as 32-bit integer values.

<sup>19</sup> For PGHR and ADSNARK the variations in generation and proving times with the increasing number of inputs are barely visible due to the fact that the number of constraints in the circuit is fixed at 50K.

<sup>20</sup> In our implementation each signature and public key takes 64 bytes, and the group element takes 64 bytes per input.

highly-optimized, implementations that support general-purpose computations. These schemes can also support zero-knowledge proofs. It is worth mentioning that all known SNARKs are either in the random oracle model or rely on non-standard non-falsifiable assumptions [Nao03]. Assumptions from this class have been shown [GW11a] likely to be inherent for SNARKs for  $\mathcal{NP}$ .

The notion of SNARKs is also related to *verifiable computation* [GGP10], in which a (computationally weak) client delegates the computation of a function to a powerful server and wants to verify the result efficiently. As noted in previous work, by using SNARKs for  $\mathcal{NP}$ , it is possible to construct a verifiable computation scheme, and several works [GGPR13, PGHR13, BSCG<sup>+</sup>13] indeed follow this approach. However, alternative approaches to realizing verifiable computation have been proposed, notably based on fully homomorphic encryption [GGP10, CKV10, AIK10] or attribute-based encryption [PRV12].

Another line of work which is closely related to ours is the one on *homomorphic authentication* (comprising both homomorphic/malleable signatures [JMSW02, BF11, ABC<sup>+</sup>12, CKLM14] and MACs [GW13, CF13, BFR13]). The main idea of homomorphic authenticators is that, given a set of messages  $(\sigma_1, \dots, \sigma_n)$  authenticated using a secret key  $\mathbf{sk}$ , anyone can evaluate a program  $P$  on such authenticated messages in a way that the result  $\sigma \leftarrow P(\{\sigma_i\})$  is again authenticated with respect to the same key  $\mathbf{sk}$  (or some public key  $\mathbf{vk}$  in the case of signatures). Some works in this area [ABC<sup>+</sup>12, CKLM14] considered various privacy notions (called context-hiding) to model that signatures on the outputs of a computation should not reveal information about the inputs. In this sense, AD-SNARKs are closely related to the notion of multi-input malleable signatures [CKLM14]. However, to the best of our knowledge, none of these schemes achieves practical efficiency for arbitrary computations.

The recent work  $Z\emptyset$  [FL14] aimed to combine the best of different zero-knowledge proof systems by doing an efficiency cost analysis to use the best one for every application. In particular,  $Z\emptyset$  relies on both ZQL and Pinocchio [PGHR13]. However, when using Pinocchio with authenticated data,  $Z\emptyset$  does not provide any guarantee on the integrity of this data, i.e., on the validity of the corresponding signatures.

## 7 More Applications

In this section we describe three more applications that fit our three-party model.

**Pay-as-you-drive Insurance.** Similarly to the smart-metering scenario, a trusted black-box installed in the client’s car collects information on the driving habits; the driver receives the information and needs to pay a premium to the insurance company according to the driving information (distances, speed, safety, etc.). For privacy, the driver may not want to reveal her personal driving habits to the insurance company. For integrity, the company wants to be sure that every driver pays the correct premium. The solution is similar to the one for smart-metering: the black-box plays the role of the trusted source, the driver keeps the collected information locally, sends to the company only the computed premium and uses AD-SNARK to attest its correctness.

**Loyalty Cards.** Many large retailers use customer loyalty cards to encourage repeat visits. Typically, the customer must enroll in a loyalty program, and receive a card that can be shown to receive discounts in future visits. However, this has the great disadvantage of allowing the retailer to keep track of the purchase history of its clients. One solution [FL14] would be to let the point of sale become the trusted source by transferring to the client’s mobile phone a signed purchase transaction. The client should then be able to compute the discount claim locally, and

use AD-SNARK to prove to the retailer that this is correct *and* performed on *legitimate* purchase transactions, without revealing the exact details of its prior purchases.

**Health Statistics.** Governments and states must periodically publish health statistics in order to inform the public of the status of healthcare systems. Obviously, the original data cannot be made public because it will contain sensitive information pertaining to the people receiving health care. However, this raw information can be authenticated by medical practitioners, who can operate as trusted sources. In this case, the general public (playing the role of a multitude of service providers) can be given the assurance that the statistics computed by the government (playing the role of the data owner) are correct and originated in legitimate medical data by using AD-SNARK.

## 8 Conclusions

This paper presents and addresses the problem of enabling privacy-preserving (aka zero-knowledge) data processing with a specific focus on the case where the input data is authenticated, and solely the authentication guarantees “percolate” to the resulting proof, without disclosing information on the original data. Current approaches to solve this problem are limited in either the class of computations that can be supported [FKDL13], or in the prover’s scalability (as we show in our experiments).

In this paper, we propose a formal approach to this three-party problem via a new cryptographic primitive, AD-SNARK, of which we propose an efficient realization. Starting from our realization, we build and evaluate a nearly practical system, ADSNARK, for proving arbitrary computations over authenticated data in a privacy-preserving way.

Our experimental evaluations show that ADSNARK performs essentially as well as non-authenticated state of the art solutions [PGHR13, BSCTV14], which means that it scales excellently for modest computations. Moreover, ADSNARK dramatically improves over generic solutions to the input authentication problem. Furthermore, since ADSNARK leverages the recent developments in zero-knowledge proof systems, it permits handling arbitrary computations in an easy and usable way. Indeed, any of the available compilers (e.g., [PGHR13]) can be used as a front-end tool for translating from high-level languages (e.g., C++) into arithmetic circuit satisfaction problems that can later be passed to the zero-knowledge backend, in our case to ADSNARK.

ADSNARK also inherits some of the limitations of existing SNARKs, such as the use of the circuit computation model. Recent work [BSCTV14] have shown how to move to more efficient representations such as RAM. We leave it as future work to study the extension of AD-SNARKs to more convenient and efficient computation models.

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## A AD-SNARK Extensions

In this section we discuss two extensions of AD-SNARKs. The first one is a generalization of AD-SNARKs to the setting in which one proves statements authenticated by *multiple data sources*. As a second extension we show how to obtain a scheme in which the verification algorithm runs in time independent of the number of authenticated inputs. This second extension supports only secret-key verification and assumes labels with a specific structure.

### A.1 Multi-Source AD-SNARKs

A multi-source AD-SNARK is an AD-SNARK where: the **Gen** algorithm takes in a tuple of  $k$  public authentication parameters; the **Prove** may receive inputs authenticated using different authentication keys (i.e., from multiple data sources); the **Ver** algorithm takes as input a set of authentication verification keys and extended labels  $L$  where each  $L_i$  specifies if the statement value  $x_i$  is authenticated and under which key. The definition of completeness is the straightforward generalization of the one in Section 5. Adaptive proof of knowledge is similar to the one of Definition 5 except that in the multi-source setting the adversary is allowed to obtain values authenticated under all possible keys.

**AN EFFICIENT MULTI-SOURCE AD-SNARK SCHEME.** We briefly show how to adapt our AD-SNARK construction of Section 4 to work in the multi-source setting.

The algorithms **Setup**, **AuthKG**, **Auth** and **AuthVer** are identical. The remaining algorithms work as follows.

**Gen**( $\{\text{pap}_j\}, C$ ) takes as input a circuit  $C$  and  $k$  public authentication parameters  $\text{pap}_1, \dots, \text{pap}_k$ .

It proceeds exactly as in **Gen** of Section 4 except that now it computes a  $K_a$  value for each authentication key. Namely, it computes (and includes in  $\text{EK}_C$ )  $K_{a,j} = z(\tau)\rho_a K_{1,j}$ , for  $j = 1, \dots, k$ .

**Prove**( $\text{EK}_C, \vec{x}, \vec{w}, \vec{\sigma}$ ): here each authentication tag  $\sigma_i$  in  $\vec{\sigma}$  also specifies under which authentication key  $\text{vk}_{j_i}$  it verifies. The set  $I_\sigma$  is then further partitioned in several subsets  $I_{\sigma,j}$ , one for every authentication key used in the statement. Without loss of generality, assume there are  $k$  of such sets. The algorithm proceeds as follows:

1. Compute  $\vec{s} = \text{QAPwit}(C, \vec{x}, \vec{w}) \in \mathbb{F}^m$ .
2. Randomly sample  $\delta_a^{(1)}, \dots, \delta_a^{(k)}, \delta_a^{mid}, \delta_b, \delta_c \leftarrow_{\mathcal{R}} \mathbb{F}$ , and set  $\delta_a = \sum_{j=1}^k \delta_a^{(j)} + \delta_a^{mid}$ . Also, define the vector  $\vec{u} = (1, \vec{s}, \delta_a, \delta_b, \delta_c) \in \mathbb{F}^{m+4}$  as before.
3. Solve the QAP  $Q_C$  exactly as in **Prove** of Section 4. Then compute  $H = h(\tau) \mathcal{P}_1$  using the values  $\tau^i \mathcal{P}_1$  contained in the evaluation key  $\text{EK}_C$ .



4. For  $j = 1$  to  $k$ , compute:

$$\begin{aligned}\pi_{\sigma,j} &= \langle \vec{u}, \vec{A} \rangle_{I_{\sigma,j}} + \delta_a^{(j)} A_{m+1}, \quad \pi'_{\sigma,j} = \langle \vec{u}, \vec{A}' \rangle_{I_{\sigma,j}} + \delta_a^{(j)} A'_{m+1} \\ \pi_{mid} &= \langle \vec{u}, \vec{A} \rangle_{I_{mid}} - \sum_{j=1}^k \delta_a^{(j)} A_{m+1}, \\ \pi'_{mid} &= \langle \vec{u}, \vec{A}' \rangle_{I_{\sigma}} - \sum_{j=1}^k \delta_a^{(j)} A'_{m+1}\end{aligned}$$

and then compute  $\pi_b, \pi'_b, \pi_c, \pi'_c, \pi_E$  as in Section 4.

5. Authenticate each value  $\pi_{\sigma,j}$  by computing

$$\pi_{\mu,j} = \langle \vec{\mu}, \vec{A} \rangle_{I_{\sigma,j}} + \delta_a^{(j)} K_{a,j}$$

6. Output  $\pi = (\{\pi_{\mu,j}, \pi_{\sigma,j}, \pi'_{\sigma,j}\}_{j=1}^k, \pi_{mid}, \pi'_{mid}, \pi_b, \pi'_b, \pi_c, \pi'_c, \pi_E, H)$ . To make the proof publicly verifiable, include also  $\{\Phi_k, \sigma'_k\}_{k \in I_{\sigma}}$  in  $\pi$ .

$\text{Ver}(\{\text{vk}_j\}, \text{VK}_C, \text{L}, \{x_i\}_{\text{L}_i=\star}, \pi)$ : it proceeds as the verification algorithm of Section 4 except that it runs the verification equations (A.1) and (A.2) for every triple  $(\pi_{\mu,j}, \pi_{\sigma,j}, \pi'_{\sigma,j})$  in the proof.

The completeness of this scheme follows from the same arguments used to argue the completeness of our AD-SNARK. The security of the multi-source AD-SNARK described above holds under the same assumptions used for ADSNARK. The difference in the security proof is that one needs to define more hybrid games as the “bad events” can now occur for either one of the  $k$  authentication keys.

As an efficiency remark, note that while the size of the proof  $\pi$  depends on the number  $k$  of authentication keys used to sign the statement, in several applications one should think of  $k$  as a rather small constant. For instance, one may think of a variation of the pay-as-you-drive insurance application in which there may be  $k = 2$  distinct trusted devices acting as data sources, e.g., a GPS collecting geographic data and a car sensor collecting driving information.

## A.2 A Zero-Knowledge AD-SNARK with Constant-Time Verification.

Here we show a variant of the scheme proposed in Section 4 which allows for a verification algorithm whose efficiency does *not* depend on the number of authenticated values, in an amortized sense. In order to achieve this appealing property, we trade efficiency for usability in making the previous scheme only secretly verifiable.

The **Setup** algorithm is identical. The remaining algorithms work as follows.

**AuthKG(pp)**: Run  $(S, \text{prfpp}) \leftarrow_{\mathcal{R}} \text{F.KG}(1^\lambda)$  to obtain the seed  $S$  and the public parameters **prfpp** of a pseudorandom function  $F_S : \{0, 1\}^* \rightarrow \mathbb{G}_2$ . Choose a random value  $\kappa \leftarrow_{\mathcal{R}} \mathbb{F}$ . Compute  $K = e(\mathcal{P}_1, \mathcal{P}_2)^\kappa \in \mathbb{G}_T$ . Return the secret key  $\text{sk} = \text{vk} = (S, \kappa)$ , and the public authentication parameters  $\text{pap} = (\text{pp}, \text{prfpp}, K)$ .

**Auth(sk, L, x)**: Let  $\text{sk} = (S, \kappa)$ . To authenticate a value  $x \in \mathbb{F}$  with label  $\text{L}$ , use the PRF to compute  $\Phi \leftarrow F_S(\text{L})$ , then compute  $\sigma = \Phi + x \kappa \mathcal{P}_2$  and output  $\sigma$ .

**AuthVer(vk,  $\sigma$ , L, x)**: Let  $\text{vk} = (S, \kappa)$  be the (secret) verification key. To verify that  $\sigma$  is a valid authentication tag for a value  $x \in \mathbb{F}$  with respect to label  $\text{L}$ , output  $\top$  if  $\sigma = F_S(\text{L}) + x \kappa \mathcal{P}_2$  and  $\perp$  otherwise.

$\text{Gen}(\text{pap}, C)$ : is the same as in Section 4 except that here  $K_a = (K)^{z(\tau)} \in \mathbb{G}_T$ .

$\text{Prove}(\text{EK}_C, \vec{x}, \vec{w}, \vec{\sigma})$ : is the same as in Section 4 except that here  $\pi_\mu = [\prod_{k \in I_\sigma} e(A_k, \Phi_k)] \cdot (K_a)^{\delta_a^\sigma} \in \mathbb{G}_T$ .

$\text{Ver}(\text{vk}, \text{VK}_C, \mathbf{L}, \{x_i\}_{\mathbf{L}_i=\star}, \pi)$ : is the same as in Section 4 except for the first verification equation. Let  $\text{vk} = (S, \kappa)$ .

(A.1) Check the authenticity of  $\pi_\sigma$ , against labels  $\mathbf{L}$  by checking if the following equation is satisfied over  $\mathbb{G}_T$ :

$$\pi_\mu = \prod_{k \in I_\sigma} e(A_k, F_S(\mathbf{L}_k)) \cdot e(\pi_\sigma, \kappa \mathcal{P}_2)$$

**HOW TO ACHIEVE EFFICIENT VERIFICATION.** By assuming a proper labeling of the data and a suitable pseudorandom function  $F$ , the scheme described above can allow for an improved verification algorithm whose running time does not depend on the number  $|I_\sigma|$  of authenticated values. Following the ideas in [BFR13], we assume that every input  $x$  is authenticated by using a multi-label  $\mathbf{L} = (\Delta, \tau)$ , where  $\Delta$  is a data set identifier, and  $\tau$  is an input identifier. As an example, the input identifiers  $\tau_1, \dots, \tau_n$  can be *specific* canonical information like date and time (e.g., day 05, 11:12:42), and the data set identifier  $\Delta$  can be more *general* information describing the category (e.g., “energy consumption for March 2014”).

As for the pseudorandom function, we can instantiate  $F_S$  by using the specific ACF-efficient PRF of [BFR13]  $F_S : \{0, 1\}^* \times \{0, 1\}^* \rightarrow \mathbb{G}_2$  such that:  $F_S(\Delta, \tau) = (a_\Delta u_\tau + b_\Delta v_\tau) \mathcal{P}_2$ , where the values  $(a_\Delta, b_\Delta)$  and  $(u_\tau, v_\tau)$  are derived by applying two standard PRFs (each mapping into  $\mathbb{F}^2$ ) to  $\Delta$  and  $\tau$ , respectively. This function is pseudorandom under the Decision Linear assumption [BFR13]. To achieve efficient verification one proceeds as follows:

- Offline phase: precompute  $\omega_u = e(\sum_{k \in I_\sigma} u_k A_k, \mathcal{P}_2)$  and  $\omega_v = e(\sum_{k \in I_\sigma} v_k A_k, \mathcal{P}_2)$  where each  $(u_k, v_k)$  is derived from  $\tau_k$  for all  $k \in I_\sigma$ . Store  $(\omega_u, \omega_v)$ .
- Online phase: given  $\Delta$ , derive  $(a_\Delta, b_\Delta)$  from  $\Delta$ , and compute  $\Omega = (\omega_u)^{a_\Delta} \cdot (\omega_v)^{b_\Delta} \in \mathbb{G}_T$ . Finally, use  $\Omega$  to check the verification equation (A.1) described above, i.e., check that  $\pi_\mu = \Omega \cdot e(\tilde{\pi}_\sigma, \kappa \mathcal{P}_2)$ .

The correctness of this efficient verification follows from  $\Omega = [\prod_{k \in I_\sigma} e(A_k, F_S(\Delta, \tau_k))]$ .

## B Definition of Zero Knowledge SNARKs

We recall the definition of SNARKs for arithmetic circuit satisfiability [Mic94, GW11b]. A *succinct non-interactive argument* (SNARG) for arithmetic circuit satisfiability is a triple of algorithms  $\Pi = (\text{Gen}, \text{Prove}, \text{Ver})$  working as follows:

- Given a circuit  $C$ , the generation algorithm  $\text{Gen}(1^\lambda, C)$  generates a (public) reference string  $\text{EK}_C$  and a corresponding verification key  $\text{VK}_C$  for  $C$ .
- Given statement  $\vec{x}$  and witness  $\vec{w}$  such that  $C(\vec{x}, \vec{w}) = 0$ , the prover produces a proof  $\pi \leftarrow \text{Prove}(\text{EK}_C, \vec{x}, \vec{w})$ .
- The verifier runs  $\{\perp, \top\} \leftarrow \text{Ver}(\text{VK}_C, \vec{x}, \pi)$  to verify the validity of  $\pi$ . The following three properties need to be satisfied.
- **Completeness.** For all  $(\vec{x}, \vec{w}) \in \mathcal{R}_C$ , we have that

$$\begin{aligned} \Pr[\text{Ver}(\text{VK}_C, \vec{x}, \pi) = \perp : (\text{EK}_C, \text{VK}_C) \leftarrow \text{Gen}(1^\lambda, C), \\ \pi \leftarrow \text{Prove}(\text{EK}_C, \vec{x}, \vec{w})] = \text{negl}(\lambda) \end{aligned}$$

- **Soundness.** (Adaptive case) For all PPT  $\text{Prove}^*$ , we have

$$\Pr[\text{Ver}(\text{VK}_C, \vec{x}, \pi) = \top \wedge \vec{x} \notin \mathcal{L}_C : (\text{EK}_C, \text{VK}_C) \leftarrow \text{Gen}(1^\lambda, C), (\vec{x}, \pi) \leftarrow \text{Prove}^*(\text{EK}_C)] = \text{negl}(\lambda)$$

- (Non-adaptive case) For all PPT  $\text{Prove}^*$ , and  $\vec{x} \notin \mathcal{L}_C$ :

$$\Pr[\text{Ver}(\text{VK}_C, \vec{x}, \pi) = \top : (\text{EK}_C, \text{VK}_C) \leftarrow \text{Gen}(1^\lambda, C), \pi \leftarrow \text{Prove}^*(\text{EK}_C, \vec{x})] = \text{negl}(\lambda)$$

- **Succinctness.** The length of a proof  $\pi$  is given by  $|\pi| = \text{poly}(\lambda) \text{polylog}(|\vec{x}|, |\vec{w}|)$ .

A SNARG is called *adaptive* if the prover can choose the statement  $\vec{x}$  after seeing the reference string  $\text{EK}_C$ .

A SNARG of knowledge (SNARK) is a SNARG where soundness is replaced by the following property:

- **Adaptive Proof of Knowledge.** For all efficient  $\text{Prove}^*$  there exists a polynomial-size extractor  $E$  such that for every auxiliary input  $\text{aux} \in \{0, 1\}^{\text{poly}(\lambda)}$ , and every circuit  $C$  of polynomial size,

$$\Pr[\text{Ver}(\text{VK}_C, \vec{x}, \pi) = \top \wedge (\vec{x}, \vec{w}) \notin \mathcal{R}_C : (\text{EK}_C, \text{VK}_C) \leftarrow \text{Gen}(1^\lambda, C), (\vec{x}, \pi) \leftarrow \text{Prove}^*(\text{aux}, \text{EK}_C), \vec{w} \leftarrow E(\text{aux}, \text{EK}_C)] = \text{negl}(\lambda)$$

## C The PGHR Zero-Knowledge SNARK

We review a version of the zero-knowledge SNARK scheme of Parno et al. [PGHR13] which was described in the recent work of Ben-Sasson et al. [BSCTV14].

**Setup**( $1^\lambda$ ): generate the public parameters consisting of a bilinear group description  $\text{pp} = (p, \mathbb{G}_1, \mathbb{G}_2, \mathbb{G}_T, e, \mathcal{P}_1, \mathcal{P}_2) \leftarrow_{\mathcal{R}} \mathcal{G}(1^\lambda)$ . Let  $\mathbb{F}$  be the finite field  $\mathbb{F}_p$ .

$(\text{EK}_C, \text{VK}_C) \leftarrow \text{Gen}(\text{pp}, C)$ : Let  $C : \mathbb{F}^n \times \mathbb{F}^h \rightarrow \mathbb{F}^l$  be an arithmetic circuit.

1. Run  $Q_C = (\vec{a}, \vec{b}, \vec{c}, z) = \text{QAPInst}(C)$  to build a QAP  $Q_C$  of size  $m$  and degree  $d$  for  $C$ . Extend  $\vec{a}, \vec{b}, \vec{c}$  with 3 more polynomials each, by setting:

$$\begin{aligned} a_{m+1}(X) &= b_{m+2}(X) = c_{m+3}(X) = z(X), \\ a_{m+2}(X) &= a_{m+3}(X) = b_{m+1}(X) = b_{m+3}(X) = c_{m+1}(X) = c_{m+2}(X) = 0. \end{aligned}$$

2. Pick  $\rho_a, \rho_b, \tau, \alpha_a, \alpha_b, \alpha_c, \beta, \gamma \leftarrow_{\mathcal{R}} \mathbb{F}$ , set  $\rho_c = \rho_a \cdot \rho_b$ .
3. Compute  $Z = z(\tau) \rho_c \mathcal{P}_2$ , and  $\forall k \in \{0, \dots, m+3\}$ :

$$\begin{aligned} A_k &= a_k(\tau) \rho_a \mathcal{P}_1, & A'_k &= \alpha_a a_k(\tau) \rho_a \mathcal{P}_1, \\ B_k &= b_k(\tau) \rho_b \mathcal{P}_2, & B'_k &= \alpha_b b_k(\tau) \rho_b \mathcal{P}_1, \\ C_k &= c_k(\tau) \rho_c \mathcal{P}_1, & C'_k &= \alpha_c c_k(\tau) \rho_c \mathcal{P}_1, \\ E_k &= \beta(a_k(\tau) \rho_a + b_k(\tau) \rho_b + c_k(\tau) \rho_c) \mathcal{P}_1. \end{aligned}$$

4. Output the *evaluation key*  $\text{EK}_C$  and the *verification key*  $\text{VK}_C$  which are defined as follows:

$$\begin{aligned}\text{EK}_C &= \left( Q_C, \vec{A}, \vec{A}', \vec{B}, \vec{B}', \vec{C}, \vec{C}', \vec{E}, \{\tau^i \mathcal{P}_1\}_{i \in \{0, \dots, d\}} \right) \\ \text{VK}_C &= \left( \mathcal{P}_1, \mathcal{P}_2, \alpha_a \mathcal{P}_2, \alpha_b \mathcal{P}_1, \alpha_c \mathcal{P}_2, \gamma \mathcal{P}_2, \beta \gamma \mathcal{P}_1, \beta \gamma \mathcal{P}_2, Z, \{A_k\}_{k=0}^n \right)\end{aligned}$$

$\text{Prove}(\text{EK}_C, \vec{x}, \vec{w})$ : given a statement  $\vec{x} \in \mathbb{F}^n$  and witness  $\vec{w} \in \mathbb{F}^h$ , proceed as follows:

1. Compute  $\vec{s} = \text{QAPwit}(C, \vec{x}, \vec{w}) \in \mathbb{F}^m$ .
2. Randomly sample  $\delta_a, \delta_b, \delta_c \leftarrow_{\mathcal{R}} \mathbb{F}$ . Also, define the vector  $\vec{u} = (1, \vec{s}, \delta_a, \delta_b, \delta_c) \in \mathbb{F}^{m+4}$ .
3. Solve the QAP  $Q_C$  by computing the coefficients  $(h_0, \dots, h_d) \in \mathbb{F}^{d+1}$  of  $h \in \mathbb{F}[X]$  such that  $h(X)z(X) = a(X)b(X) - c(X)$ , where  $a, b, c \in \mathbb{F}[X]$  are

$$\begin{aligned}a(X) &= a_0(X) + \sum_{k \in [m]} s_k \cdot a_k(X) + \delta_a \cdot z(x) = \langle \vec{u}, \vec{a} \rangle \\ b(X) &= b_0(X) + \sum_{k \in [m]} s_k \cdot b_k(X) + \delta_b \cdot z(x) = \langle \vec{u}, \vec{b} \rangle \\ c(X) &= c_0(X) + \sum_{k \in [m]} s_k \cdot c_k(X) + \delta_c \cdot z(x) = \langle \vec{u}, \vec{c} \rangle\end{aligned}$$

Compute  $H = h(\tau) \mathcal{P}_1$  using the values  $\tau^i \mathcal{P}_1$  in  $\text{EK}_C$ .

4. Use the elements in  $\text{EK}_C$  to compute the following values:

$$\begin{aligned}\pi_{mid} &= \langle \vec{u}, \vec{A} \rangle_{I_{mid}} + \delta_a A_{m+1}, \\ \pi'_{mid} &= \langle \vec{u}, \vec{A}' \rangle_{I_{\sigma}} + \delta_a A'_{m+1} \\ \pi_b &= \langle \vec{u}, \vec{B} \rangle, \quad \pi_c = \langle \vec{u}, \vec{C} \rangle, \quad \pi_E = \langle \vec{u}, \vec{E} \rangle \\ \pi'_b &= \langle \vec{u}, \vec{B}' \rangle, \quad \pi'_c = \langle \vec{u}, \vec{C}' \rangle.\end{aligned}$$

5. Output  $\pi = (\pi_{mid}, \pi'_{mid}, \pi_b, \pi'_b, \pi_c, \pi'_c, \pi_E, H)$ .

$\text{Verify}(\text{VK}_C, \vec{x}, \pi)$ : in order to verify a proof  $\pi$  (as defined above) for statement  $\vec{x} \in \mathbb{F}^n$ , first compute  $A_x = A_0 + \langle \vec{x}, \vec{A} \rangle_{[1, n]}$  and then perform the following steps:

(P.1) Check the satisfiability of the QAP:

$$e(A_{\star} + \pi_{\sigma} + \pi_{mid}, \pi_b) = e(H, Z) \cdot e(\pi_c, \mathcal{P}_2)$$

(P.2) Check the validity of knowledge commitments:

$$e(\pi'_{mid}, \mathcal{P}_2) = e(\pi_{mid}, \alpha_a \mathcal{P}_2) \wedge e(\pi'_b, \mathcal{P}_2) = e(\alpha_b \mathcal{P}_1, \pi_b) \wedge e(\pi'_c, \mathcal{P}_2) = e(\pi_c, \alpha_c \mathcal{P}_2)$$

(P.3) Check that all the QAP linear combinations use the same coefficients:

$$e(\pi_E, \gamma \mathcal{P}_2) = e(A_{\star} + \pi_{\sigma} + \pi_{mid} + \pi_c, \beta \gamma \mathcal{P}_2) \cdot e(\beta \gamma \mathcal{P}_1, \pi_b)$$

If all the checks above are satisfied, then return  $\top$ ; otherwise return  $\perp$ .