

# An algorithm for MD5 single-block collision attack using high-performance computing cluster

Anton A. Kuznetsov

Program Systems Institute of Russian Academy of Sciences  
aakuznetsoff@gmail.com  
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**Abstract.** The parallel algorithm and its implementation for performing a single-block collision attack on MD5 are described. The algorithm is implemented as MPI program based upon the source code of Dr Marc Stevens' collision search sequential program. In this paper we present the parallel single-block MD5 collision searching algorithm itself and details of its implementation together with optimizations. We believe that this algorithm can be further used to derive a program parallelizing method, and for implementing an efficient parallel implementation for an arbitrary collision search program that is based on Wang et al's differential method. We also disclose a pair of new single-block messages colliding under MD5 that were found using our algorithm on the high-performance computing cluster.

## 1. Introduction

Hash functions are the one-way functions that map arbitrary input messages to a fixed-length hash values. Hashes can be considered as signatures of the original message, and can be used to check the message integrity and authenticity after it was delivered by network communication. Hash functions are designed to be fast and to be resistant to preimage attacks. MD5 [1] belongs to the MD\* family of hash functions based on Merkle-Damgård structure. It is one of the most widely-adopted hash functions, although it is being gradually replaced by more secure counterparts. MD5 is proven to be not collision resistant, that leads to different security vulnerabilities in its applications.

A pair of different messages (M,M') is called a *collision* if hashes of both messages are equal. In 2004 Wang et al. [2] have disclosed a differential method for finding MD5 collisions, and presented a collision with input messages of size 1024 bit (two-block collision). In 2010 Xie and Feng presented *single-block* colliding messages [3] but for security reasons haven't disclosed any detail about the collision searching method. They posted a challenge to cryptology community to construct a different MD5 single-block collision. In 2012 Dr Marc Stevens have answered that challenge [4] by presenting a single-block collision attack for MD5 and an example colliding message pair.

In this paper we describe a parallel algorithm for finding single-block MD5 collisions, and its MPI implementation that is based upon Dr Marc Stevens' collision searching method. We also present a new single-block colliding message pair that was found using our algorithm on the high-performance computing cluster. The search took only 11 hours, as opposed to 3 weeks by the original program.

So far only one inherently parallel collision search method exists. Citation from [5]: "it is a simple technique of parallelizing methods for solving search problems which seek collisions in pseudo-random walks. According to that method, to perform a parallel collision search, each processor proceeds as follows. Select a starting point  $x_0 \in S$  and produce the trail of points  $x_i = f(x_{i-1})$ , for  $i = 1, 2, \dots$  until a distinguished point  $x_d$  is reached based on some easily testable distinguishing property such as a fixed number of leading zero bits. Add the distinguished point to a single common list for all processors and start producing a new trail from a new starting point. Depending on the application of collision search, other information must be stored with the distinguished point (e.g., one must store  $x_0$  and  $d$  in order to quickly locate the points  $a$  and  $b$  such

that  $f(a) = f(b)$ ). A collision is detected when the same distinguished point appears twice in the central list."

The original program by Dr Marc Stevens can also make use of parallelism in searching collisions. It was purposely created single-threaded to be parallelized by using multiple computers and different random seeds for different instantiations of the program. Each instantiation has a different initialization and thus operates on a different part of the search space. Each has in principle the same probability of succeeding at any time. Thus by running the single-threaded program  $N$  times on  $N$  different CPU-cores, one gains an immediate speed-up by a factor  $N$ . The overall complexity remains the same.

In this paper we describe the multi-process collision searching algorithm that reuses the same random seed for different instantiations of the program running on HPC cluster nodes.

## 2. Differential method for collision attack

In 2004 Wang et al. published the details of the differential method of collision attack on MD5. The attack is committed using differential cryptanalysis techniques based on XOR and modular ( $\text{mod } 2^{32}$ ) differences. Using these techniques the researchers have found the way to construct the differential paths for MD5 compression function that describe how the difference between input messages can affect the function's internal states  $Q_i$  and  $Q_i'$ , which in turn affects the MD5 output hashes. To make sure the differential path will happen it is required that  $Q_i$  and  $Q_i'$  satisfy the so-called sufficient conditions.

## 3. The sequential program structure

Dr Marc Stevens' one-threaded algorithm for finding a single-block collision is roughly as follows (citation from [4]):

- 1) *Instantiation*: randomly choose values for  $Q_{14}$  up to  $Q_{21}$  satisfying the given bitconditions. These values directly imply values for  $m_6$ ,  $m_{11}$ ,  $m_0$ ,  $m_5$  and  $Q_1$ .
- 2) *Precomputations*: first a lookup table is generated containing tuples of valid values for  $Q_2$  up to  $Q_7$  and  $Q_{13}$  that satisfy equations for steps that use  $m_1$ ,  $m_5$ ,  $m_6$ , and the given bitconditions. The lookup table is indexed by the values of the bits of  $Q_7$  and  $Q_{13}$  that are involved with  $Q_8$  and  $Q_{12}$  due to indirect bitconditions.
- 3) *Main loop*: iterate over valid values for  $Q_8$  up to  $Q_{12}$  satisfying bitconditions and the step equation for step using  $m_{11}$ . Find all values in the lookup table that satisfy all indirect bitconditions between  $Q_7$  and  $Q_8$ , and between  $Q_{12}$  and  $Q_{13}$  using the index. For each of these values, the variables  $Q_3$  up to  $Q_{16}$  are known and thus the entire message block is determined. Compute  $Q_{22}$  and  $Q_{23}$  and if they satisfy the given bitconditions then do the last part.
- 4) *Tunnels*: use the three known best tunnels to make very precise corrections to the message block pair such that all bitconditions up to  $Q_{23}$  remain fulfilled. For all message block pairs that satisfy bitconditions  $q_3$  up to  $q_{29}$ , check whether the message block pair forms a collision.

The general structure of the sequential program written by Dr Marc Stevens is as follows (in pseudocode):

```

main():
    filltables();
    while (true) collinit();

collinit():
    // Instantiation
    compute Q1, Q4, Q5, Q12–Q18, m0, m5, m6, m11;
    compute Q3Q6cnt value;
    if Q3Q6cnt < 224: return;
    four nested 'do..while' loops:
        in the innermost 'do..while' loop:
            compute all other Qi;
            compute M and M';
            if md5compress(M) = md5compress(M'):
                // collision was found
                print (M,M') and Qi to stdout;
                exit(0);

```

**Algorithm 1:** Sequential program structure

## 4. The parallel program structure

MPI (Message-passing interface) is a message-passing system designed to function on a variety of parallel computing architectures. MPI standard defines the syntax and semantics of a number of library routines used for writing parallel programs in different programming languages.

In MPI standard a program is executed in parallel by running the copy of the program on each core of each node of compute cluster. Each running process is assigned a rank – a decimal number from 0 to N-1, where N is the number of CPU cores in the cluster.

To develop MPI version we made a few modifications:

- 1) added a number of MPI calls into the original source code;
- 2) optimized program execution.

At the beginning of parallel program development for the convenience we wrote a wrapper library for MPI routines with a set of primitives:

- `mpi_init()` – initialize the MPI computing environment;
- `mpi_final()` – finalize the MPI computing environment;
- `mpi_send()` – send an array of unsigned integers to a specified rank;
- `mpi_recv()` – receive an array of unsigned integers (on slave ranks);
- `mpi_barrier()` – synchronize execution, wait until all computing processes have reached this routine;
- `mpi_size()` – return the total number of ranks;
- `mpi_rank()` – return the index of current rank;
- `mpi_headrank()` – check whether the current process has rank 0.

Note: a head rank is a rank with index 0, while a slave rank has non-zero index.

Each call is a wrapper for the genuine MPI call with error-checking added. For instance `mpi_barrier()` is defined as follows:

```

void mpi_barrier() {
    int rc = MPI_Barrier(MPI_COMM_WORLD);
    if (rc != MPI_SUCCESS) {
        printf("Error in MPI_Barrier\n");
        exit(1);
    }
}

```

**Listing 1:** mpi\_barrier() wrapper subroutine

After all the modifications were applied the developed parallel MPI program now has the following structure (in pseudocode):

```

main():
    mpi_init();
    filltables();
    while (true) collinit();
    mpi_final();

collinit():
    mpi_barrier();
    if mpi_headrank(): // node 0: Instantiation
        // this is where random number generator is used:
        compute Q1, Q4, Q5, Q12–Q18, m0, m5, m6, m11;
    if mpi_headrank():
        mpi_send(Q); // broadcast Qi
        mpi_send(M); // broadcast M
    else:
        mpi_recv(Q); // slave ranks receive Qi
        mpi_recv(M); // slave ranks receive M
    compute Q3Q6cnt value on each rank;
    if Q3Q6cnt < 224: return;
    mpi_barrier();
    four nested 'do..while' loops:
        in the innermost 'do..while' loop:
            if numIter mod mpi_size() = mpi_rank():
                // numIter - the counter of loop iterations
                compute all other Qi;
                compute M and M';
                if md5compress(M) = md5compress(M'):
                    // collision was found on some rank
                    print (M,M') and Qi to stdout;
                    mpi_final();
                    exit(0);

```

**Algorithm 2:** Parallel program structure

One can notice that in parallel implementation the *Instantiation* step is executed only on the head rank, then result Q and M arrays are broadcast to the slave ranks. After that *Precomputation* step is executed on all ranks, followed by synchronization primitive (mpi\_barrier()). Other

computation steps are divided equally among the ranks. After collision is found on some rank, the resulting Q and M arrays are printed to standard output and computation stops.

## 5. Optimizations to the source code

Following is the list of optimizations applied to the parallel (MPI) version of single-block collision search program.

- Do not declare vector and numeric variables in every iteration of the inner loop; declare these before the outermost 'do..while'. This is due to the fact that innermost loop is executed several billion times (in worst scenario), thus declaration of variables consume an amount of CPU time.
- Using a simple yet powerful free code profiler we made a conclusion that during the program run most of the CPU clock is consumed by calls to the four routines:
  - rotate\_right()
  - rotate\_left()
  - md5\_ff()
  - md5\_gg()

The former two were optimized using Intel compiler intrinsics:

- rotate\_right() was rewritten using \_rotr()
- rotate\_left() was rewritten using \_rotl()

md5\_ff() routine is optimized like this:

rewrite from:

- $D \wedge (B \& (C \wedge D))$

to:

- $(B \& C) \mid (\sim B \& D)$

md5\_gg() routine is optimized like this:

rewrite from:

- $C \wedge (D \& (B \wedge C))$

to:

- $(D \& B) \mid (\sim D \& C)$

- md5compress() C++ function was rewritten in pure assembler. This yields about 20% speed-up.
- The following command is used to compile program source:  

```
mpic++ *.cpp md5compress.S -O3 -xhost -ipo -o md5sbc
```

It results in a faster binary for the host Intel Xeon processor with interprocedural optimizations and aggressive loop unrolling applied.
- Source code was refactored by running a small Tcl script on it. All substrings in the source code that match the "offset+%i" mask were replaced by the actual sum of the 'offset' constant (that equals to 3) and the integer %i. This was done solely to improve code readability and examine data dependencies between program subroutines.

## 6. HPC cluster run

For the testing we have used the "Tornado" cluster that resides in South Ural State University [6] in the city of Chelyabinsk, Russia. We had made several MPI program launches about 24 hours duration each. The final launch that took **11 hours** was successful – a collision was found (see next section). In that launch 30 nodes were used with 12 processes on each node (number of ranks – 360).

Note: the original collision searching program [4] by Dr Marc Stevens took about 3 weeks to find a single-block collision. The estimated time based on complexity analysis and on a number of computers was 5 weeks.

It is obvious that the more nodes used in computations the higher possibility of finding collision within reasonable timeslot (e.g. 24 hours). Program execution time differs from launch to launch because random number generator is used to calculate some of the  $Q_i$  values at the initialization stage.

The parallel algorithm is highly scalable due to the fact that in the inner loop all iterations are split equally among ranks. Total number of iterations is very high. In the worst scenario even 10-petaflop/s HPC cluster could take days to find collision.

We did not use any accelerator devices like Intel Xeon Phi, that are present on the cluster, but this is feasible for our implementation. We actually implemented multi-threaded version and the CUDA version to be run on a single computing node, but testing these did not yield any success in finding the collision.

## 7. The colliding message pair

Here we present a new single-block message pair colliding under MD5. It was found by running our parallel implementation of the collision finding program on the HPC cluster:

M	5D 11 69 3E 1E 33 4B 2C B3 88 EF AA F0 D0 EC F3 91 2D 73 0A 1C DD 7A AC 6E 3C E0 E4 CE 06 7B B1 8E 73 C7 <b>BA</b> A2 6A A8 19 66 C2 86 16 B3 4F 3D 07 AA B7 C8 1E 32 94 89 <b>64</b> 7C 11 73 4A 3F AF 03 EA
M'	5D 11 69 3E 1E 33 4B 2C B3 88 EF AA F0 D0 EC F3 91 2D 73 0A 1C DD 7A AC 6E 3C E0 E4 CE 06 7B B1 8E 73 C7 <b>BC</b> A2 6A A8 19 66 C2 86 16 B3 4F 3D 07 AA B7 C8 1E 32 94 89 <b>E4</b> 7C 11 73 4A 3F AF 03 EA
Common MD5 hash: 746c4e219320eae3fd23bcf3ebb7d71d	

**Table 1:** The single-block colliding messages

The messages are available for download at [7].

We also present here the list of  $Q_i$  values that was found by the parallel program and was used to generate message M:

Q <sub>-3</sub> =0x67452301	Q <sub>8</sub> =0x29F20526	Q <sub>19</sub> =0x410F3F70
Q <sub>-2</sub> =0x10325476	Q <sub>9</sub> =0x3E1893ED	Q <sub>20</sub> =0x71936434
Q <sub>-1</sub> =0x98BADCFE	Q <sub>10</sub> =0x00000040	Q <sub>21</sub> =0xF7D2E265
Q <sub>0</sub> =0xEFCDAB89	Q <sub>11</sub> =0xFFFFFDFE	Q <sub>22</sub> =0x09D6ECD5
Q <sub>1</sub> =0xD9A89593	Q <sub>12</sub> =0xB62EA109	Q <sub>23</sub> =0xF8B84FB6
Q <sub>2</sub> =0xDA361481	Q <sub>13</sub> =0x062DA1C8	Q <sub>24</sub> =0xBCCE16A3
Q <sub>3</sub> =0x0660DFEA	Q <sub>14</sub> =0x1661D7EA	Q <sub>25</sub> =0x463268A8
Q <sub>4</sub> =0x04812801	Q <sub>15</sub> =0x00050621	Q <sub>26</sub> =0x34EFF95F
Q <sub>5</sub> =0xEB78D1DC	Q <sub>16</sub> =0x14810A21	Q <sub>27</sub> =0x5E7E0F7D
Q <sub>6</sub> =0x77D76EFF	Q <sub>17</sub> =0xA8009748	Q <sub>28</sub> =0xE8514E70
Q <sub>7</sub> =0xBE675C82	Q <sub>18</sub> =0xADABC8E8	Q <sub>29</sub> =0xC677D867

**Table 2:** Q values list

Note: all the rest Q values (Q<sub>30</sub>—Q<sub>64</sub>) are equal to 0.

Message M = (m<sub>0</sub> ... m<sub>15</sub>) is derived from Q values as follows:

$$m_t = RR(Q_{t+1} - Q_t, RC_t) - AC_t - f_t(Q_t, Q_{t-1}, Q_{t-2}) - Q_{t-3}$$

where:

RR(X, n) is a cyclic right rotation of X by n bit positions;

RC<sub>t</sub> is a rotation constant: (RC<sub>t</sub>, RC<sub>t+1</sub>, RC<sub>t+2</sub>, RC<sub>t+3</sub>) = (7, 12, 17, 22) for t = (0, 4, 8, 12);

AC<sub>t</sub> is an additive constant:  $AC_t = \lfloor 2^{32} |\sin(t+1)| \rfloor$ ;

$f_t(X, Y, Z) = F(X, Y, Z) = (X \wedge Y) \oplus (\bar{X} \wedge Z)$ .

Message M' is derived from M as follows:

$$M' = M + (0,0,0,0,0,0,0,0,2^{25},0,0,0,0,2^{31},0,0)$$

Q<sub>i</sub> values presented here generally satisfy bitconditions (see Table 3 in [4]) but not all. There are four values that do not satisfy bitconditions: Q<sub>3</sub>, Q<sub>4</sub>, Q<sub>9</sub> and Q<sub>14</sub>. It is an open question why this divergence occurred.

## 8. Conclusion

We presented the collision searching parallel algorithm that was derived from Dr Marc Stevens' original method. It was implemented using MPI standard and successfully used to find a pair of single-block messages colliding under MD5.

We believe that this algorithm can be further used to write an efficient parallel implementation of an arbitrary collision search program that is based on Wang et al's differential method.

Dr Marc Stevens' algorithm has a runtime cost of 2<sup>50</sup> md5compress() calls. We believe that a single-block collision searching algorithm can be substantially improved, so that it requires much less computational power. This is the subject for further research.

The collision search program can be adapted to run on other massively parallel devices: multi-core CPUs, Nvidia CUDA devices, Intel Xeon Phi accelerators. This can greatly speed up collision search on the workstation and/or computational cluster.

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