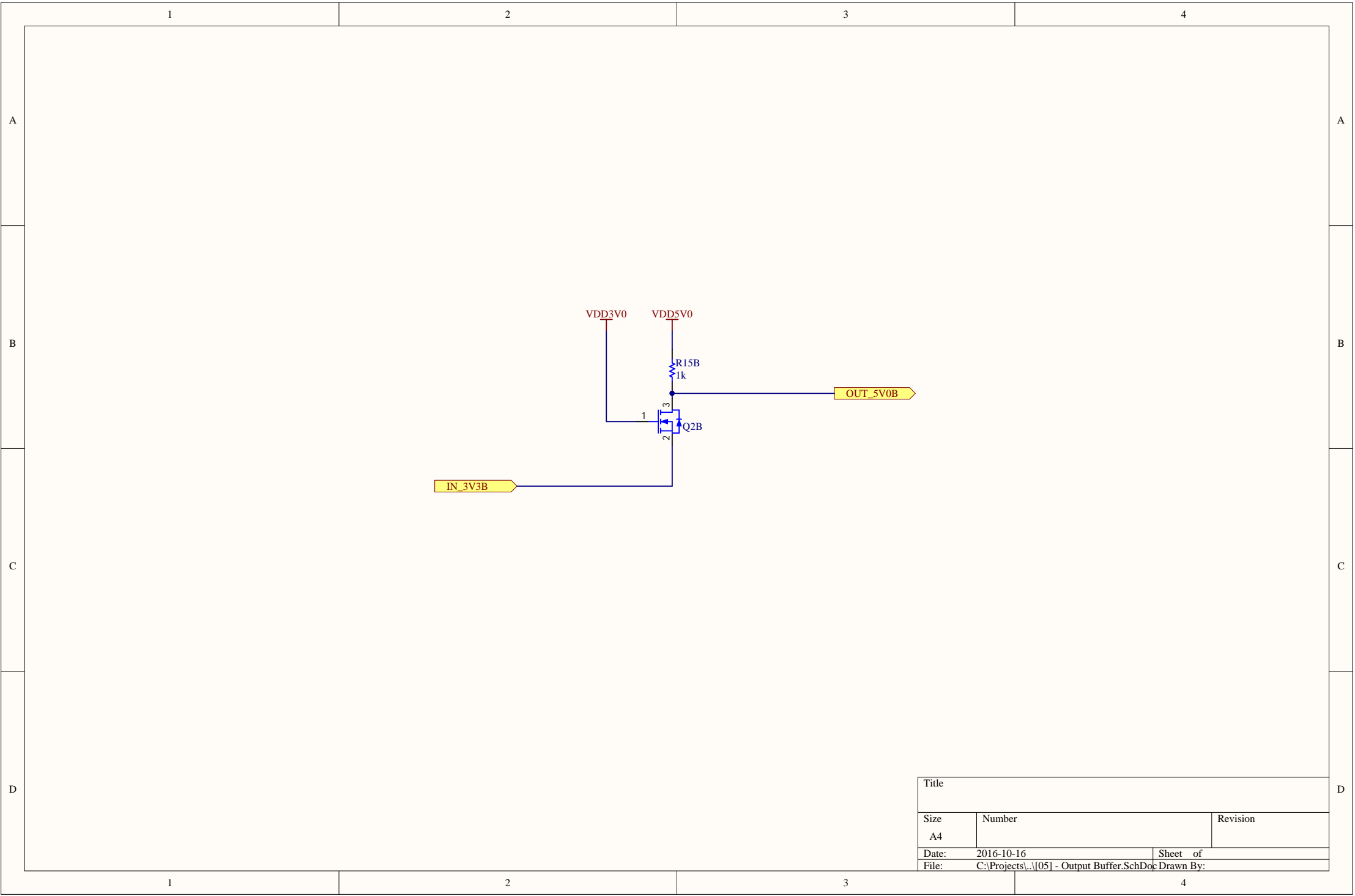
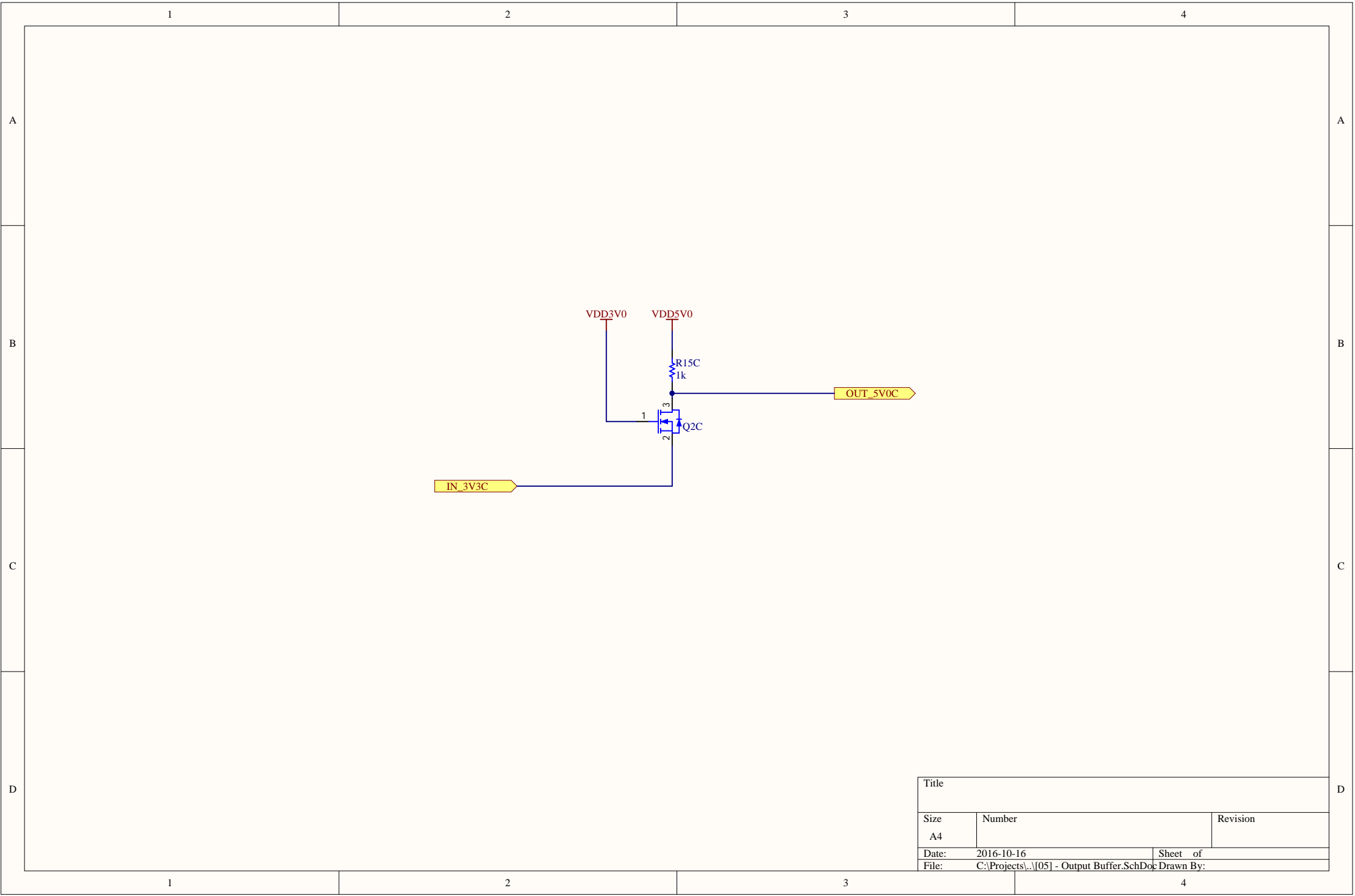


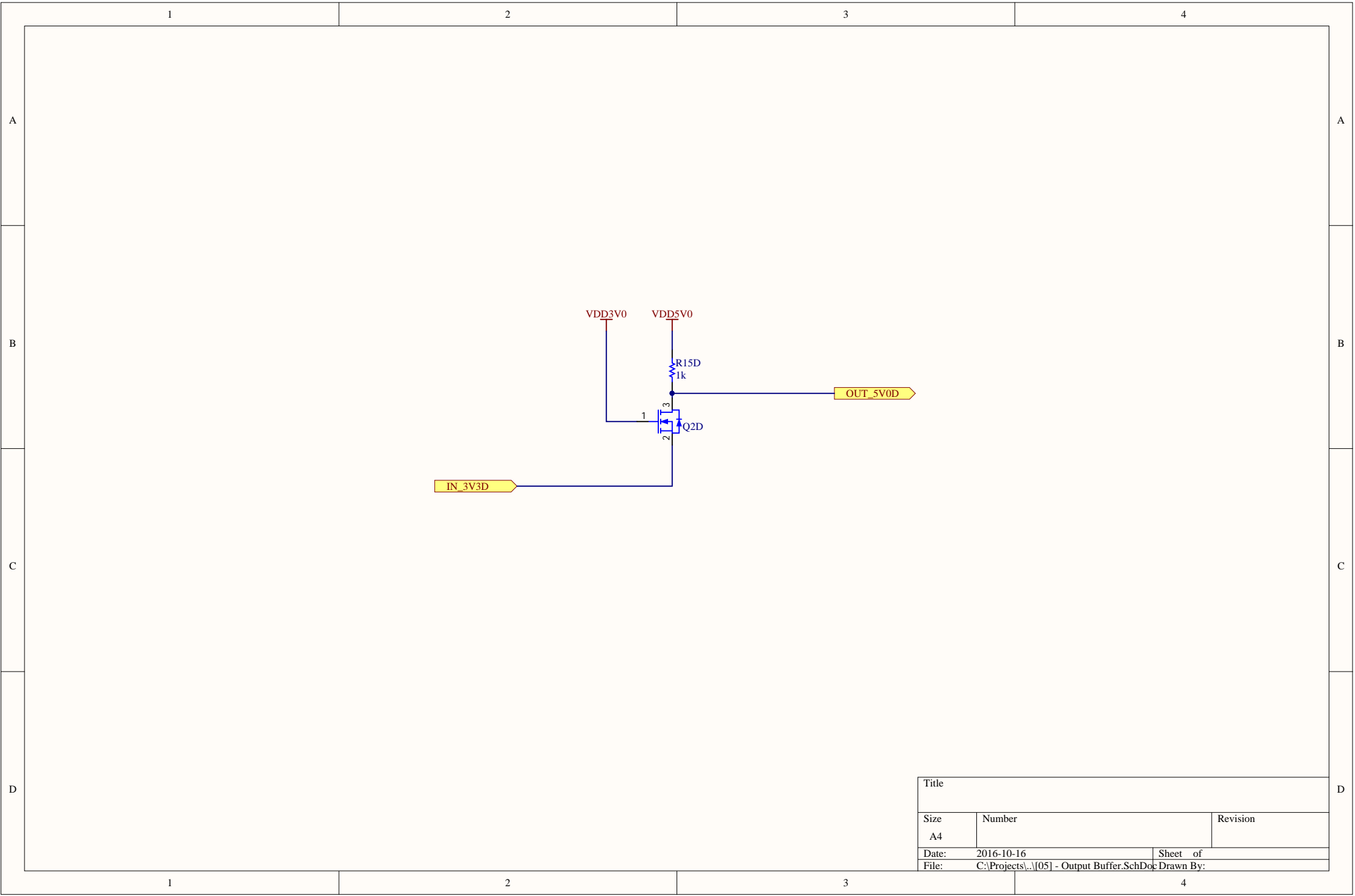
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Date:	2016-10-16	Sheet of
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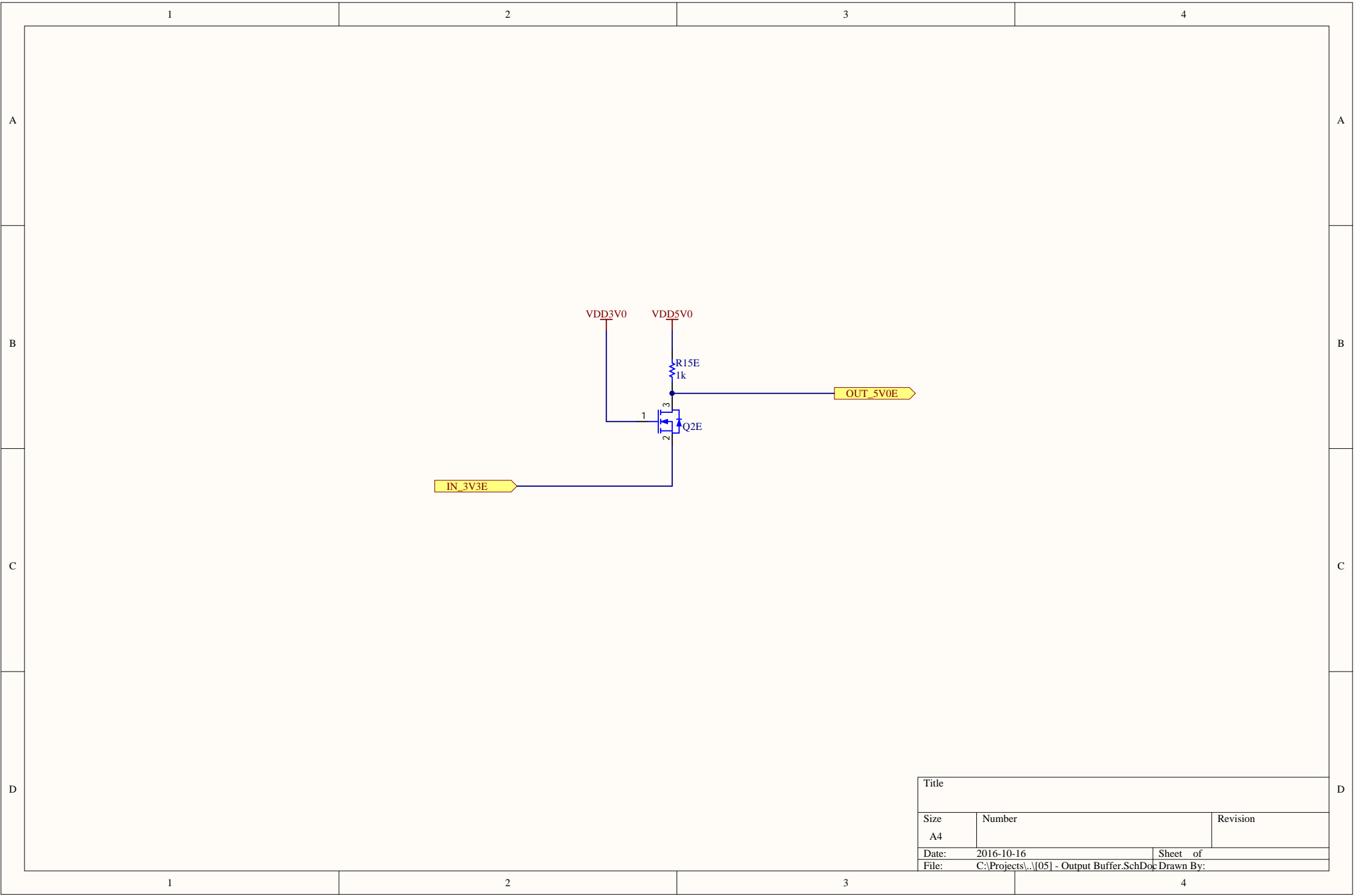
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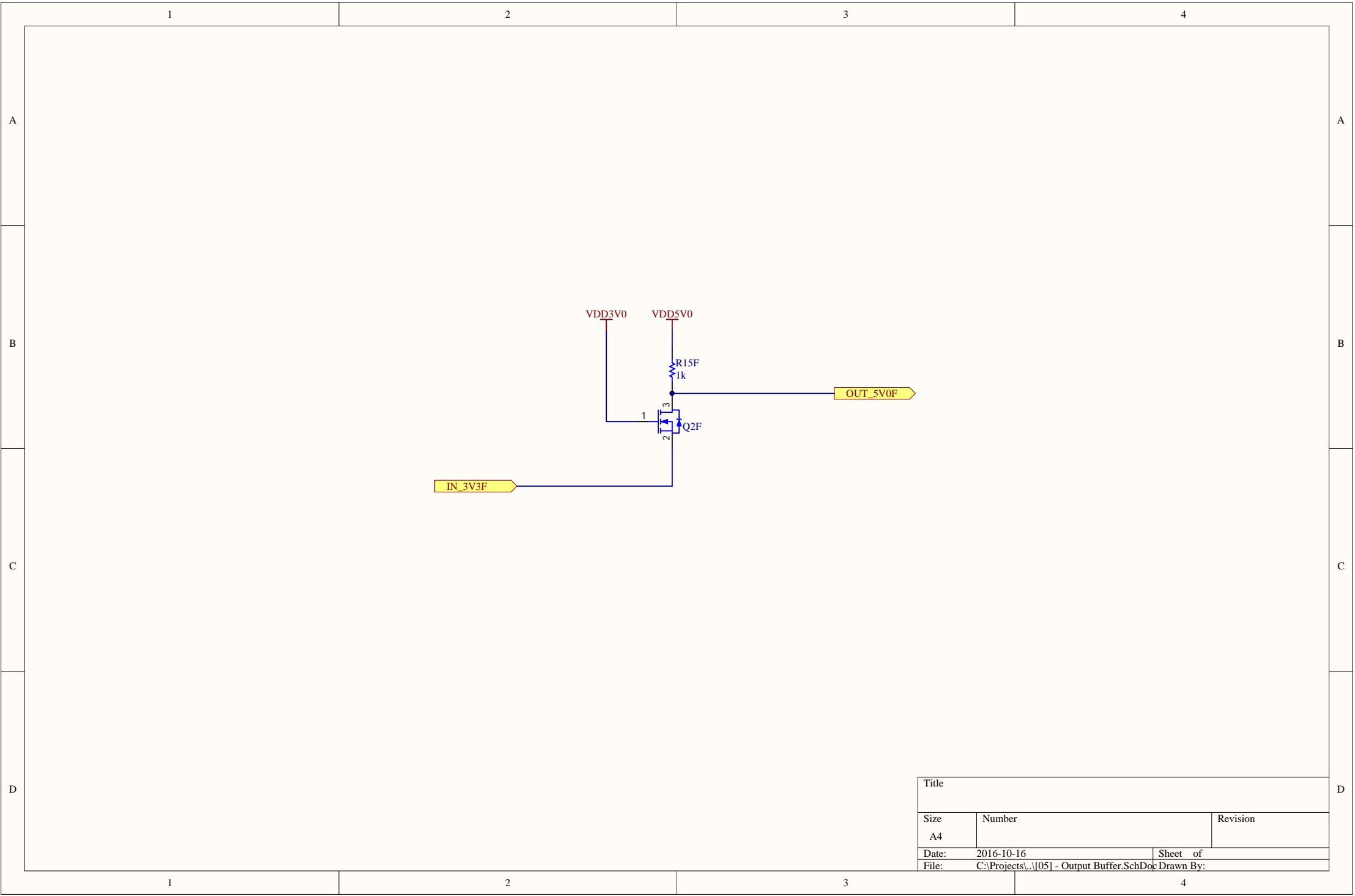


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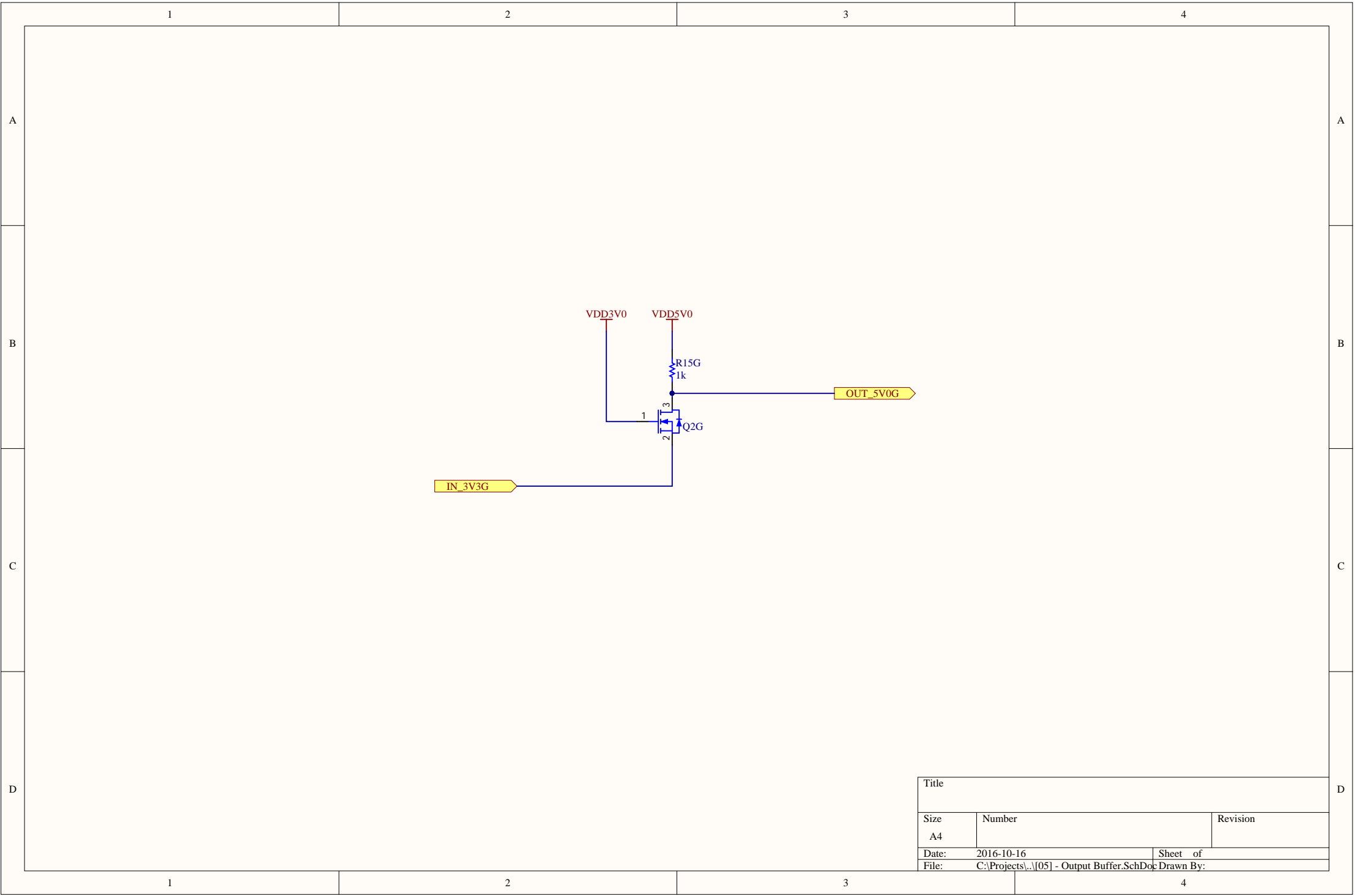


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Title		
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A4		
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Title		
Size	Number	Revision
A4		
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open source
hardware

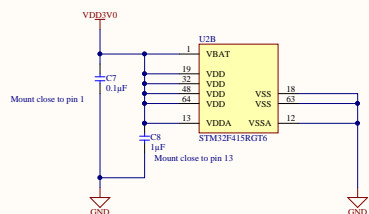
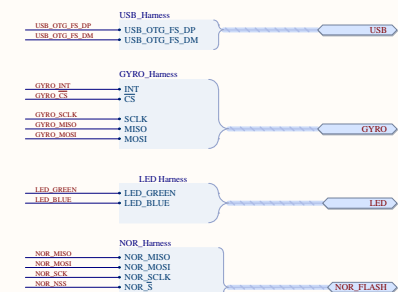
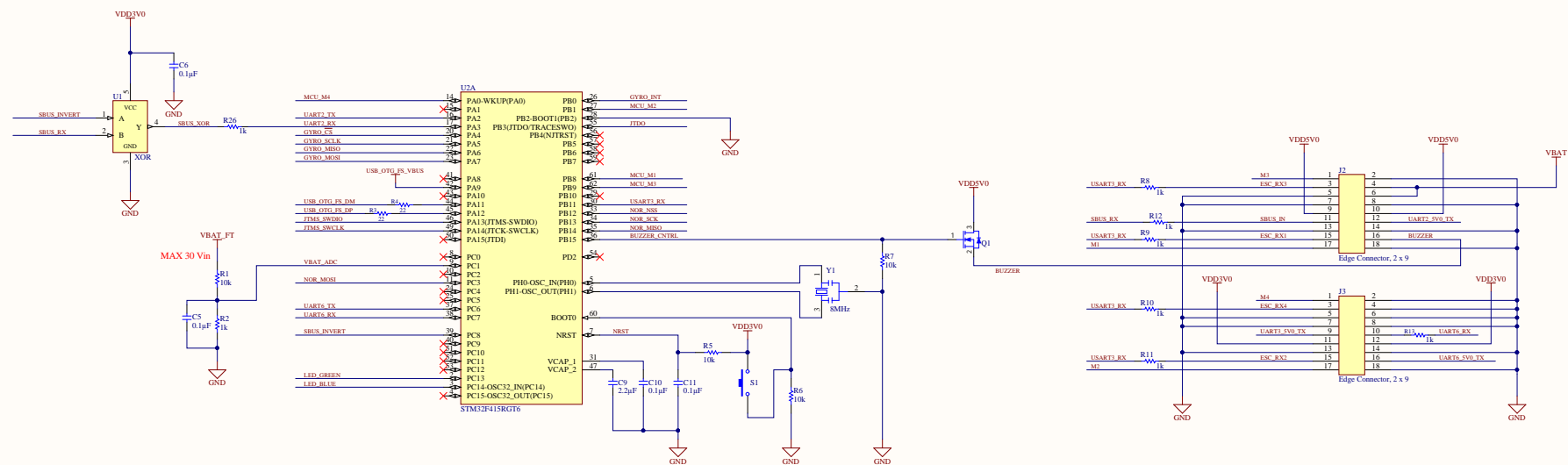
- * FIX NOR FLASH
- * Remove VIAS in pads
- * Reduce footprint FN301



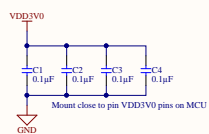
This schematic is *NOT SUPPORTED* and DOES NOT constitute a reference design. Only "community" support is allowed via resources at flighttronics.se

THERE IS NO WARRANTY FOR THIS DESIGN , TO THE EXTENT PERMITTED BY APPLICABLE LAW. EXCEPT WHEN OTHERWISE STATED IN WRITING THE COPYRIGHT HOLDERS AND/OR OTHER PARTIES PROVIDE THE DESIGN "AS IS" WITHOUT WARRANTY OF ANY KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE. THE ENTIRE RISK AS TO THE QUALITY AND PERFORMANCE OF THE DESIGN IS WITH YOU. SHOULD THE DESIGN PROVE DEFECTIVE, YOU ASSUME THE COST OF ALL NECESSARY SERVICING, REPAIR OR CORRECTION.

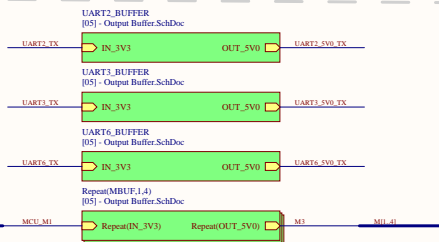
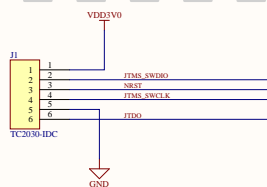
Title		
Size	Number	Revision
A3		
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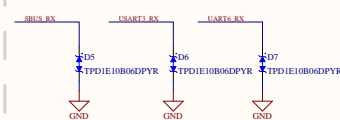
3.3V TO 5.0V OUTPUT BUFFERS



JTAG DEBUG

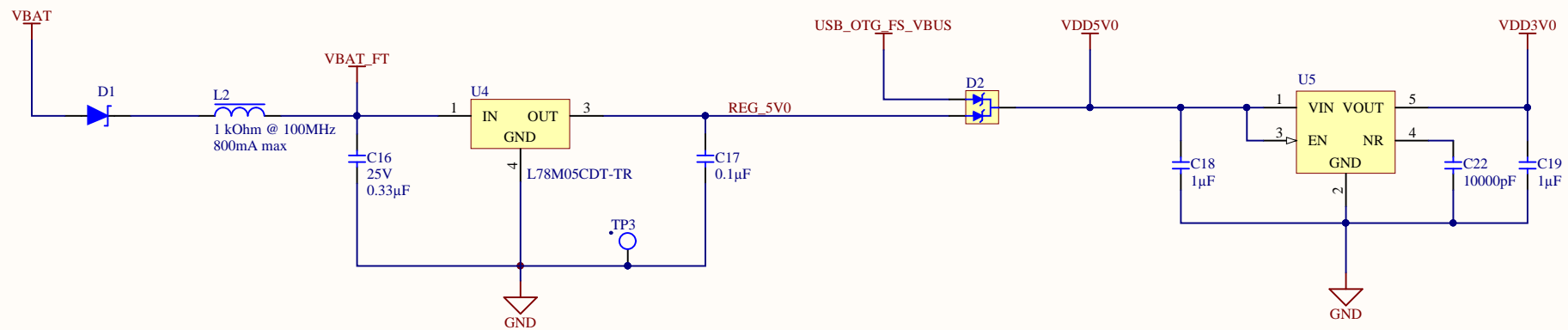


INPUT PROTECTION

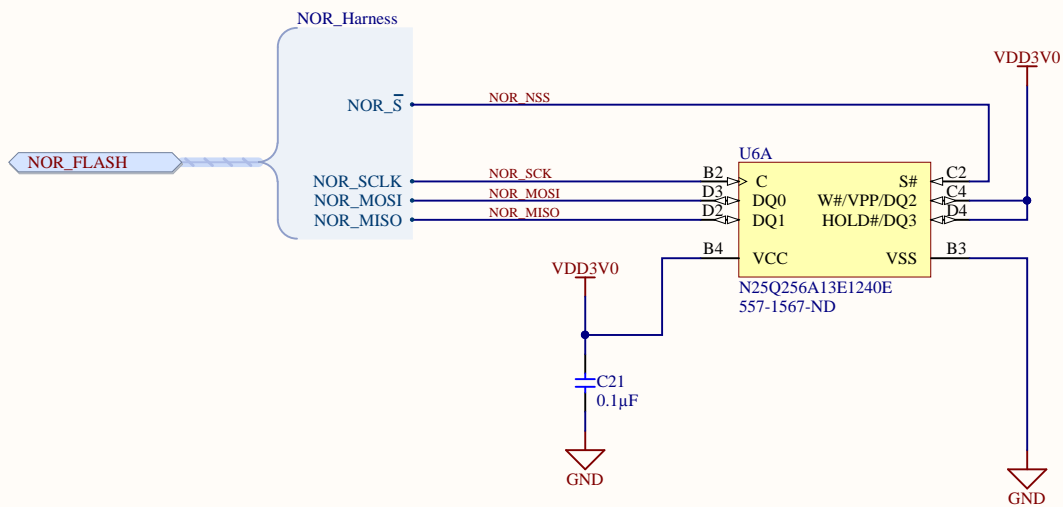


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Date:	2016-10-16	Sheet of	
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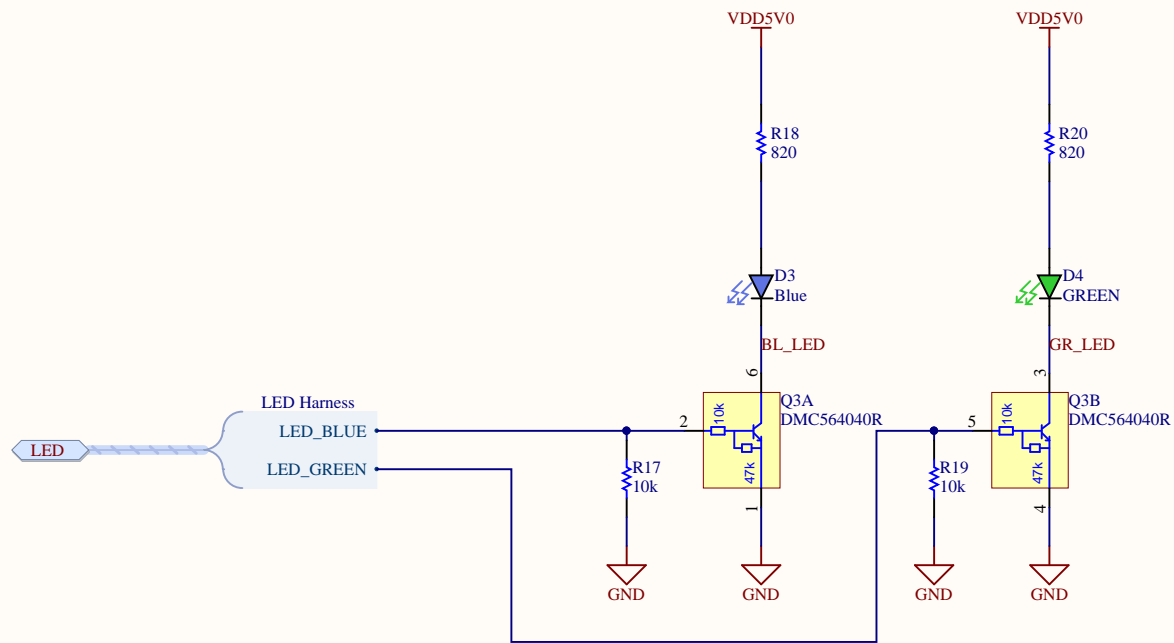
VBAT MAX 24V



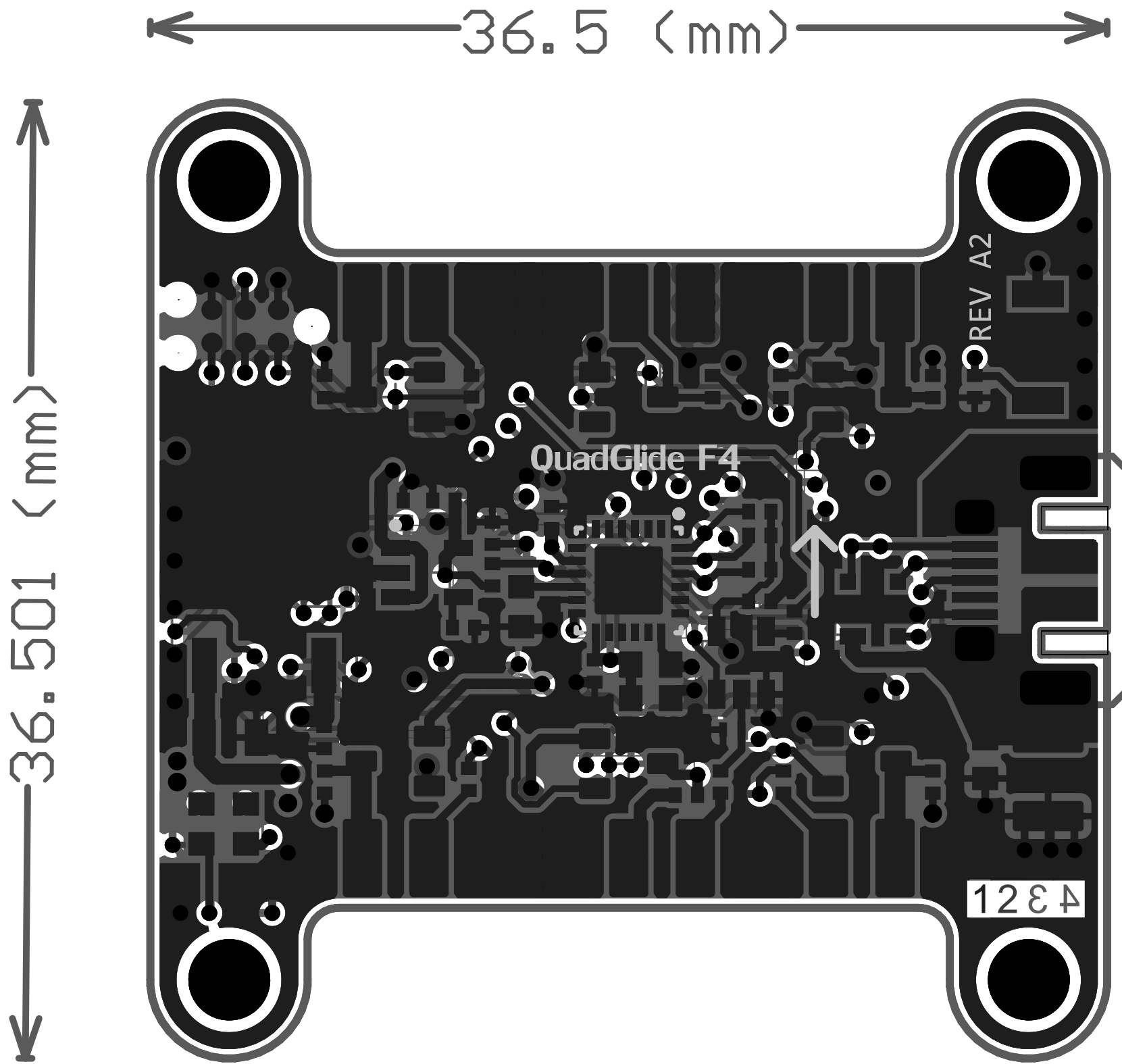
Title		
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Date:	2016-10-16	Sheet of
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Title		
Size	Number	Revision
A4		
Date:	2016-10-16	Sheet of
File:	C:\Projects\...\[07] - Flash.SchDoc	Drawn By:



Title		
Size	Number	Revision
A4		
Date:	2016-10-16	Sheet of
File:	C:\Projects\...\[08] - LED.SchDoc	Drawn By:



Boards