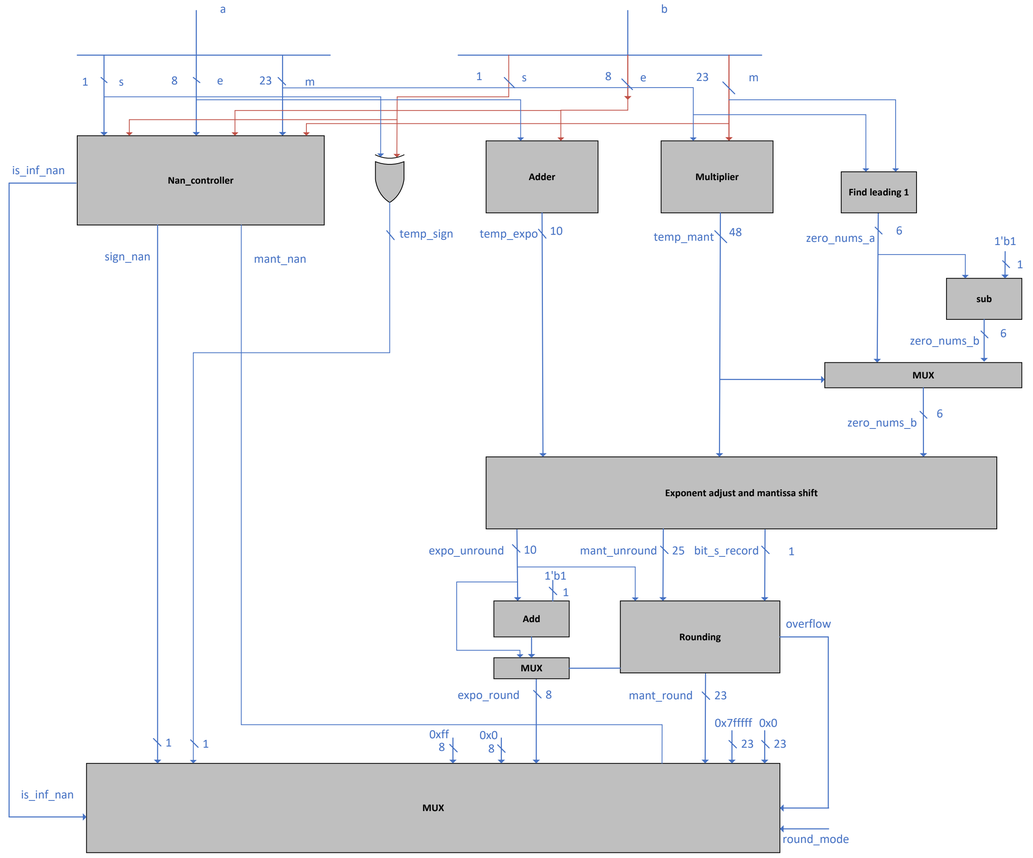
a
b
1
s
8
e
23
m
Nan_controller
Adder
Multiplier
Find leading 1
is_inf_nan
temp_sign
temp_expo
10
temp_mant
48
zero_nums_a
6
1'b1
1
sub
zero_nums_b
6
MUX
sign_nan
mant_nan
Exponent adjust and mantissa shift
expo_unround
10
mant_unround
25
bit_s_record
1
Add
Rounding
overflow
expo_round
8
mant_round
23
0xff
0x0
0x7fffff
MUX
round_mode