Coarrays on HPC: A Path to
Parallel Discrete Event Simulation?

Daniel Topa

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Abstract

Contents

section0   Introduction

subsection0.0   Overview of the Problem

This subsection provides a detailed description of the problem or challenge being addressed in this project.

subsection0.0   The Promise of Coarrays

Portability of performance. Can a human do better? References: [**numrich2018parallel**, **ray2019fortran**], ([**markus2012modern**, **clerman2011modern**, **Hanson2013**]) as well as the invaluable language reference guide [**metcalf2024modern**].

subsection0.0   Objectives

This subsection outlines the main objectives of the project, including key research goals or development targets.

subsection**0.0** **Methodology**

This subsection describes the methodology or approach taken to address the problem and achieve the objectives.

section**0** **Introduction**

subsection**0.0** **The Promise of Coarrays**

subsection**0.0** **B**

Second subsection.

subsection**0.0** **C**

Third subsection.