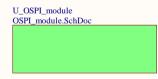
BGA24 OSPI / Hyperbus memory module

MB1242

Table of contents

Sheet 1: Project overview (this page)

Sheet 2: OSPI module



BOARD CPN
CPN

Legend

General comment such as function title, configuration, ..

Text to be added to silkscreen.

Warning text.

Notes to generate the board layout.

Open Platform License Agreement

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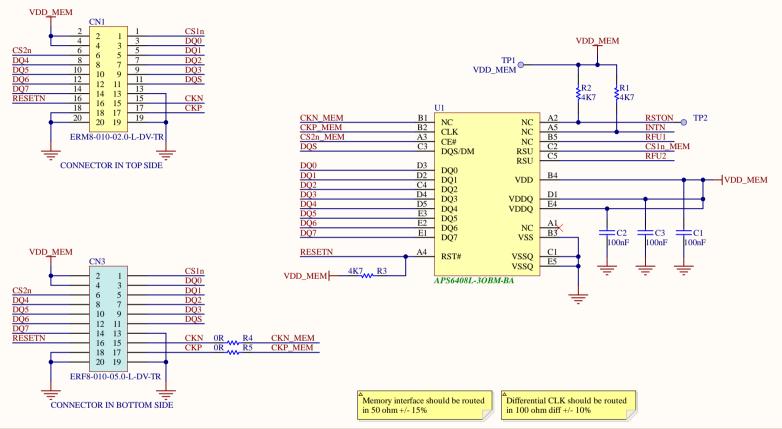
By using the enclosed reference designs, schematics, PC board layouts, and documentation, in hardcopy or CAD tool file format (collectively, the "Reference Material"), You are agreeing to be bound by the terms and conditions of this Agreement. Do not use the Reference Material until You have read and agreed to this Agreement terms and conditions. The use of the Reference Material automatically implies the acceptance of the Agreement terms and conditions.

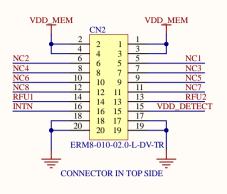
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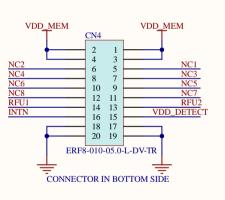
Title: Proje c					
Project: BGA24 OSPI / Hyperbus memory module					
Variant: APS6408L					
Revision: B-01		Reference: MB1242			
Size: A4	Date: 20-AUG-20	Sheet: 1 of 1			

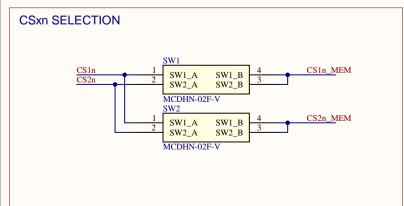
BGA24 OSPI / Hyperbus memory module

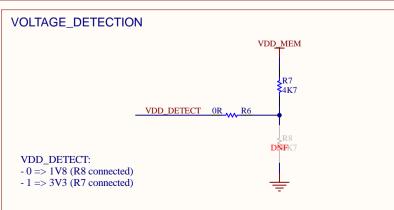
Operating range: 1.65<VDD<3.3V (IOs)



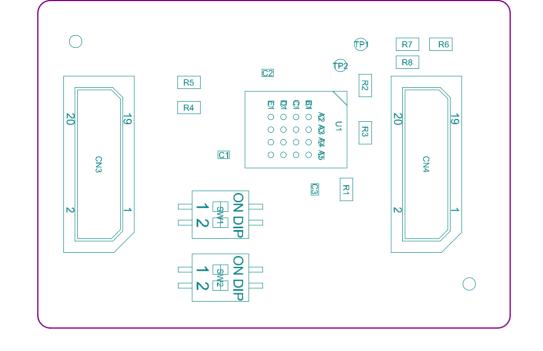








Title: LCD			
Project: BGA24 OSPI / Hyperbus memory module			
Variant: APS6408L			
Revision: B-01		Reference: MB1242	111
Size: A4	Date: 20-AUG-20	Sheet: 7 of 23	life.augment



Project: BGA24 OSPI / Hyperbus memory module

Layer: M14-Top Assembly

bly Gerber: .GM14

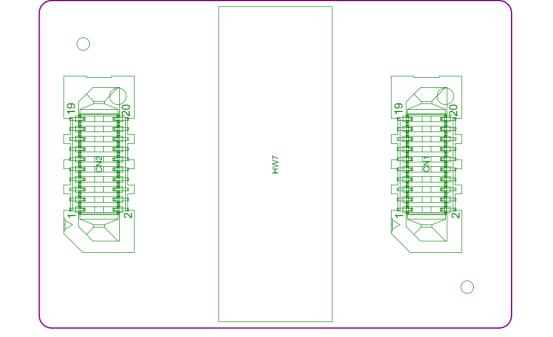
Variant: [No Variations]

Ref: MB1242

Date: 20-AUG-20

Rev: B





Project: BGA24 OSPI / Hyperbus memory module

Gerber: GM15 Layer: M15-Bottom Assembly Ref: MB1242 Variant: [No Variations]

Date: 20-AUG-20 Rev: B

