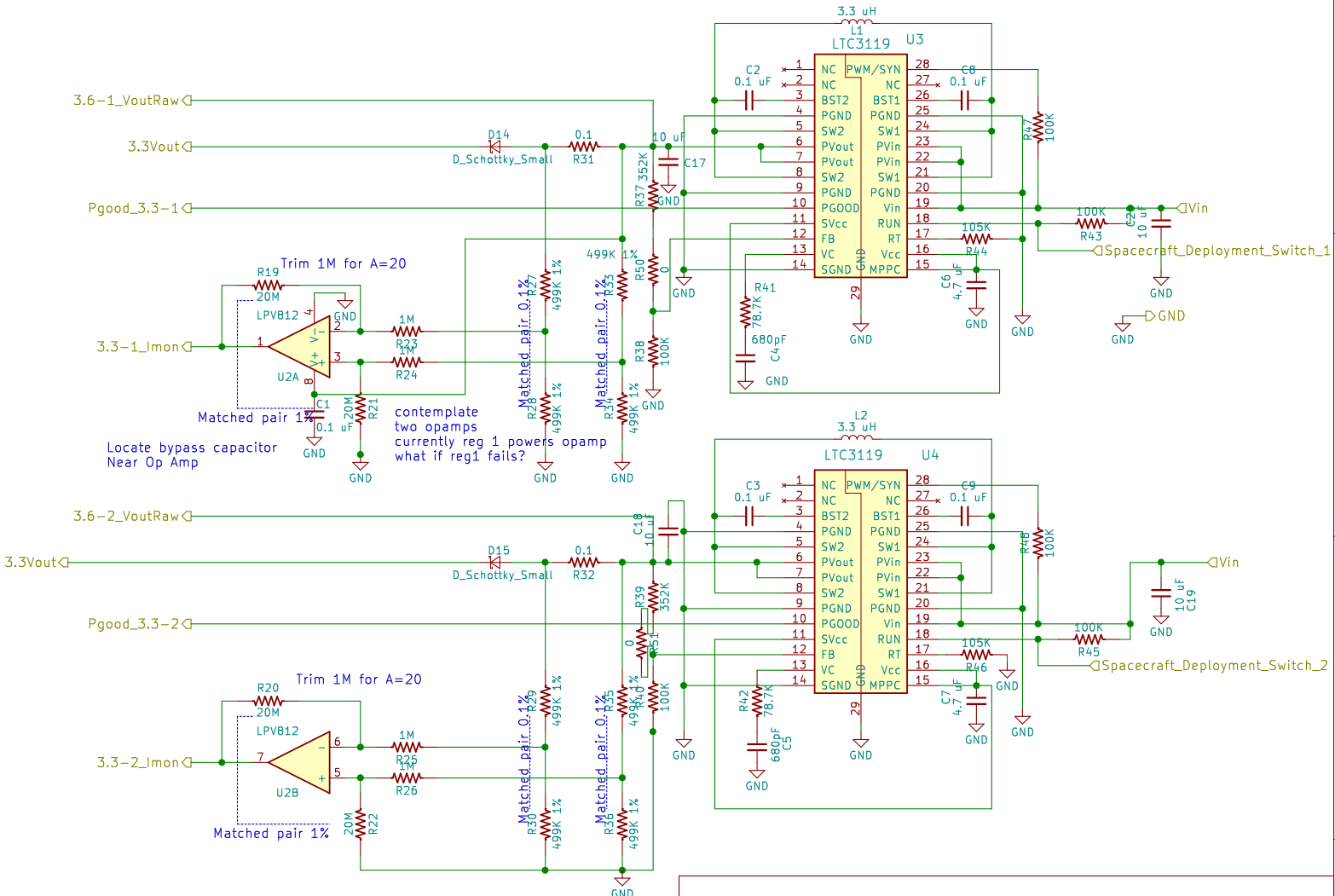
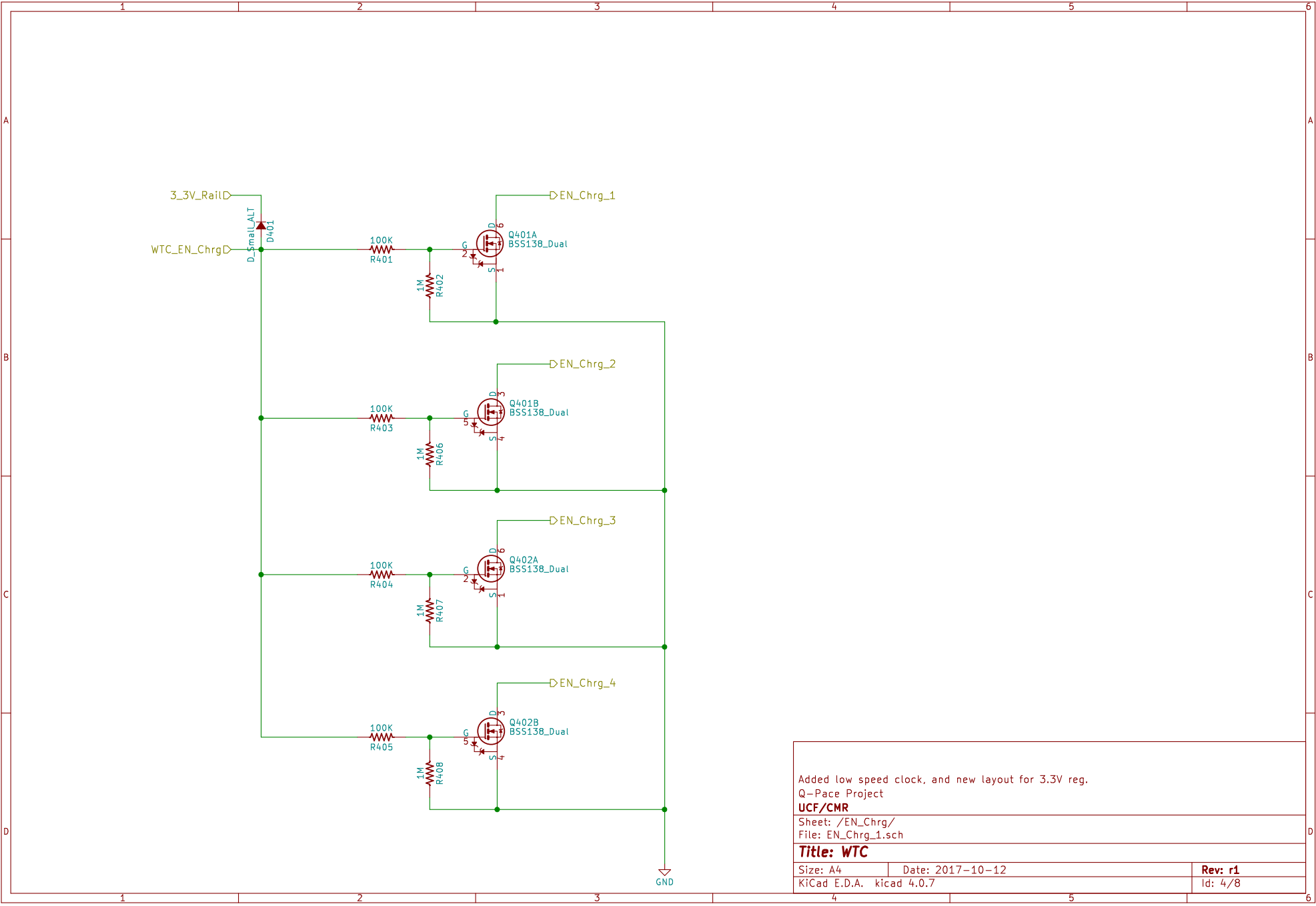
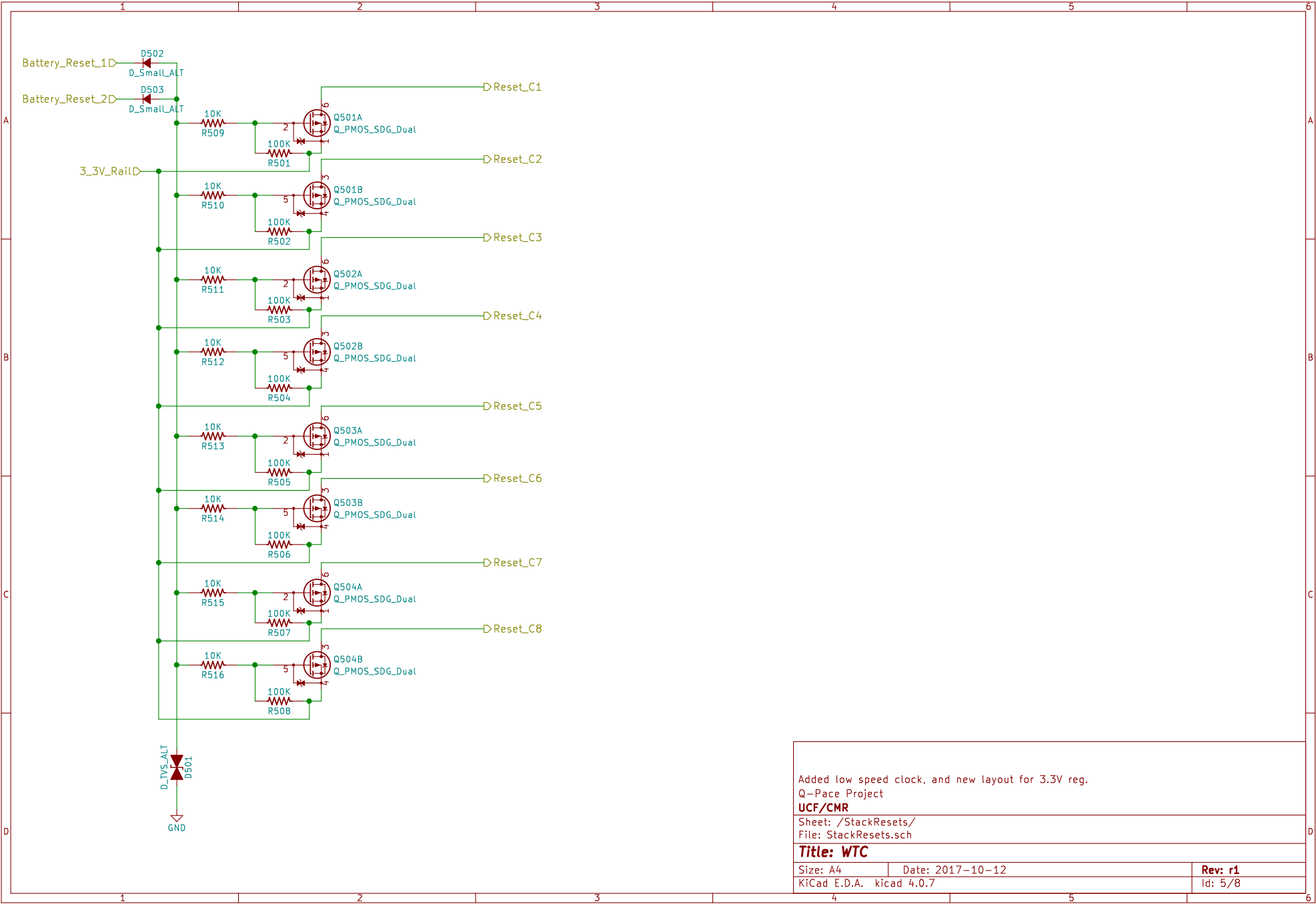


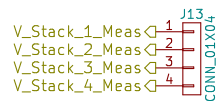
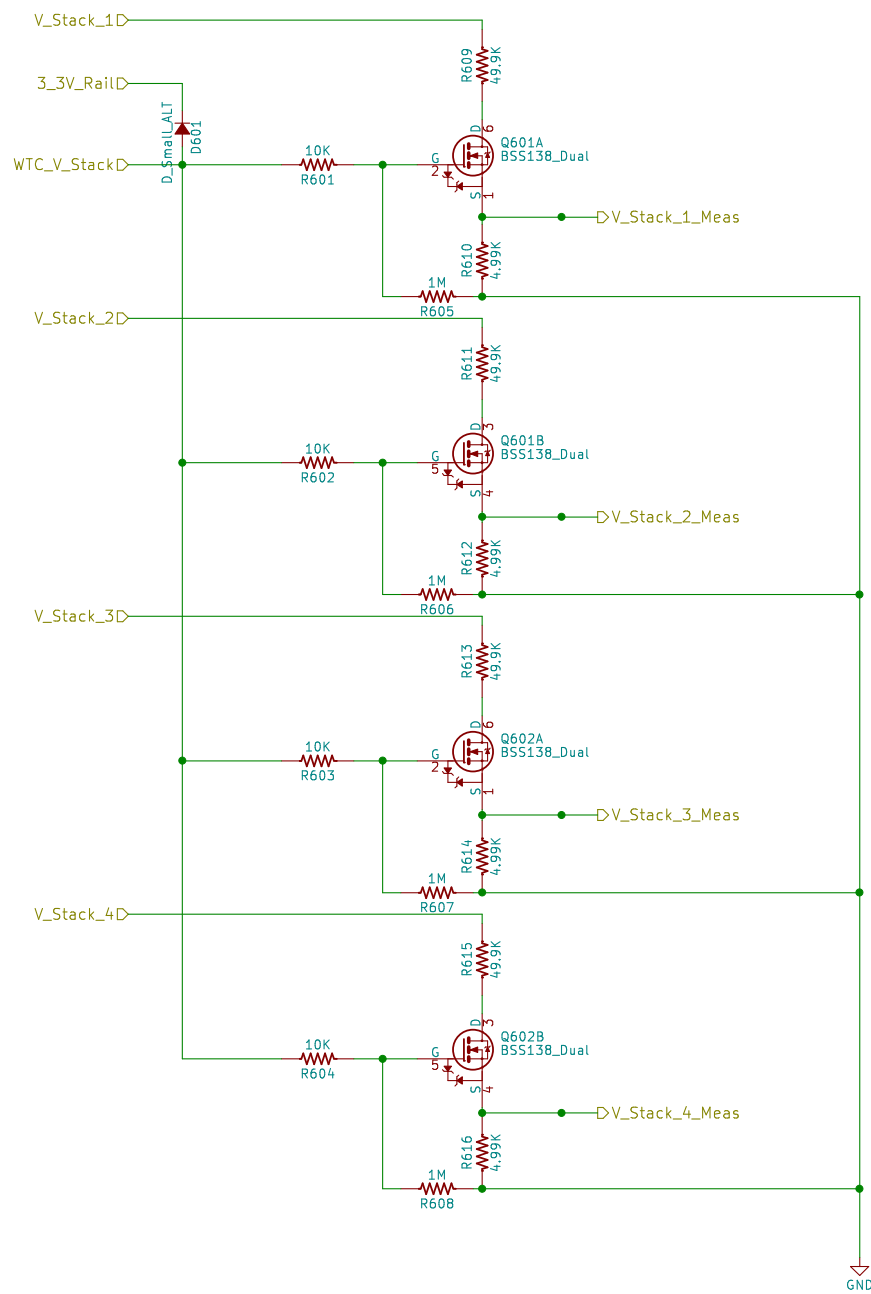
A  
B  
C  
D



Added low speed clock, and new layout for 3.3V reg.  
Q-Pace Project  
**UCF/CMR**  
Sheet: /3v3 Rail Regulator/  
File: 3v3Rail.sch  
**Title: WTC**  
Size: A4 | Date: 2017-10-12 | Rev: r1  
KiCad E.D.A. kicad 4.0.7 | Id: 3/8







Added low speed clock, and new layout for 3.3V reg.

## Q-Pace Project

UCF/CMR

Sheet: /V\_Stacks/

File: V\_Stacks.sch

**Title:** WTC

Size: A4	Date: 2017-10-12
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Size: A4	Date: 2025-08-27
KiCad E.D.A.	kicad 4.0.7

Rev: r1

Id: 6/8



