

LOUIS Fabien
29 ans
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EDUCATION

2011 Master IUP ISME Toulouse : Ingénierie des Systèmes microsystemes embarqués.
2009 Licence IUP ISME Toulouse : Ingénierie des Systèmes et Microsystemes embarqués.
2007 BTS SE Lycée polyvalent de Baimbridge : Système électroniques.

TECHNICAL KNOWLEDGE

Systems	Windows, MAC OSX, LINUX (Ubuntu, Fedora, Yocto), Android
Programming Languages	C, C++, Python, Makefile, JAVA, TCL (Tool Command Language)
Network	Bus CAN, OSI model
Methodology	DO178B, GIT
Tools	Bugzilla, JIRA, Buildbot, Gerrit, Synopsys
English	written, read and spoken
Android	Android debug bridge (ADB), repo, fastboot, Android Build system, Android Studio

WORK EXPERIENCE

Since 01/2015 at **INTEL on behalf of CELAD**

Systems integration engineer: New Product introduction (NPI) team

General context:

New Product introduction (NPI) team:

- Is System Integration representative during “Power On” for New SoC introduction.
- Takes cares about build target for new product, and also for additional variants for existing products.
- Manage Android Build Framework (Fix build issues, Ensure the porting of build targets on new dessert).

Role : System integrator

Activity :

- Create new variants
- Kernel 3.10 rebase for android wear
- Kernel 4.4 enablement for arch x86 env
- Fix build issues (kernel, AOSP, BSP, GIT)
- Enable new targets (Modify BSP to Boot to UI)
- Code review (Makefile, C, C++, JAVA)
- Work on enabling android N (Ninja, Clang, Fix AOSP issues for upstream)
- Pre-silicon Activity ((SOC Simulation done with Synopsys):
 - o Create the target and make it build on Android M
 - o Make kernel boot (kernel 4.1)
 - o Boot to UI (Setup wizard)
 - o Create a test Bench for test automation (Python, Bash, Shell, TCL)

From 07/2013 to 12/2014 at **INTEL on behalf of CELAD**

Validation engineer: Android system integration

General context: Android System Integration (SI) provide a set of processes and tools dedicated to the integration of the Android Software with Intel Platforms.

Role : Daily Debugger

The daily debugger is part of Test-House team, which is responsible for testing daily builds to ensure the stability of Intel platforms.

Test-House is composed of testers (performs manual tests, create tickets for new issues), and debuggers (analyze crashes, assign tickets , ensure non-regression).

Activity :

- Follow tests
- Analyze bugs (Hardware, AOSP, BSP, Kernel)
- Follow tickets (Bugzilla, JIRA)
- Find culprite patch in case of regression
- Train new debuggers (france, China)

Specification engineer : Communication Architecture & Datalink group (EYAC1)

General context: The Air Traffic Control (ATC) system enables data link communication between the aircraft and the ATC centers. This communication is made through the communication system composed of HF Data Radio (HFDR), VHF Data Radio (VDR) and SATCOM systems.

Role : Specification engineer

Activity :

- Write, support and validate system specifications
- Specify division of activities between Airbus and the equipment manufacturer
- Write test specifications
- Write operating instructions