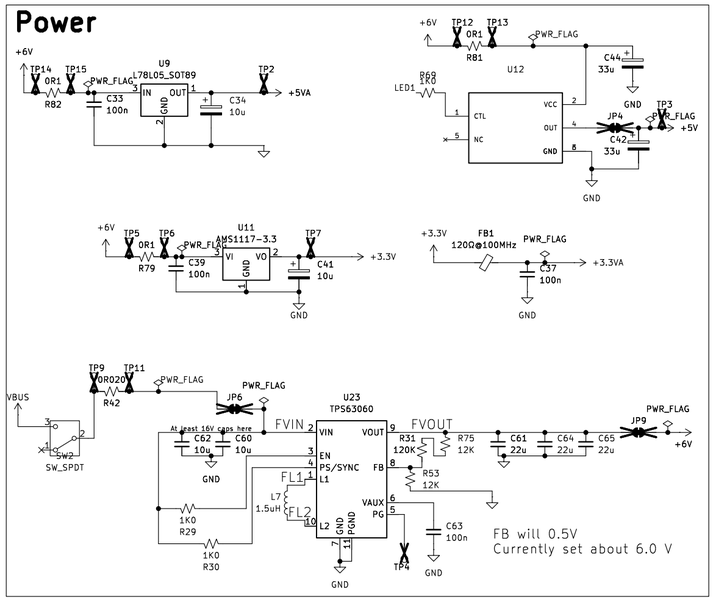
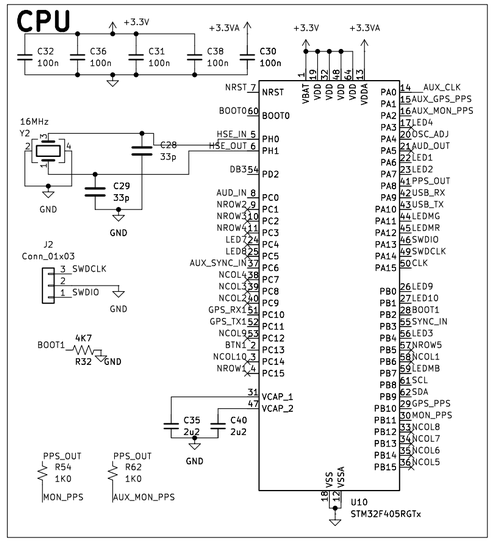
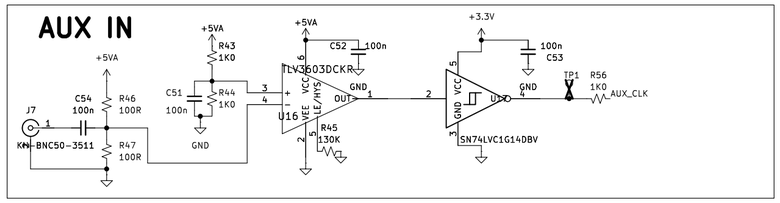
Power



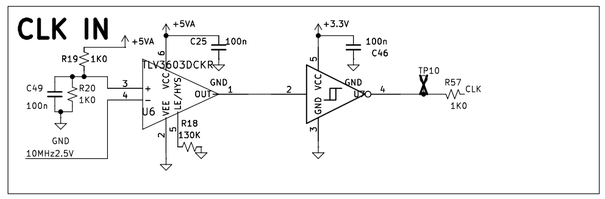
CPU



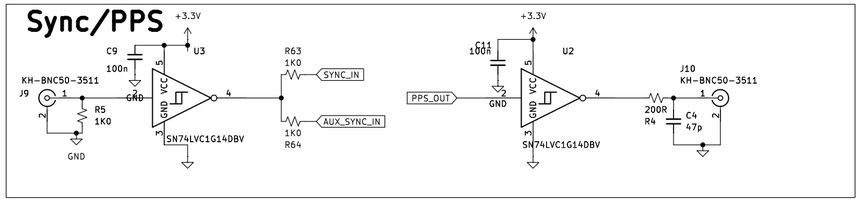
AUX IN



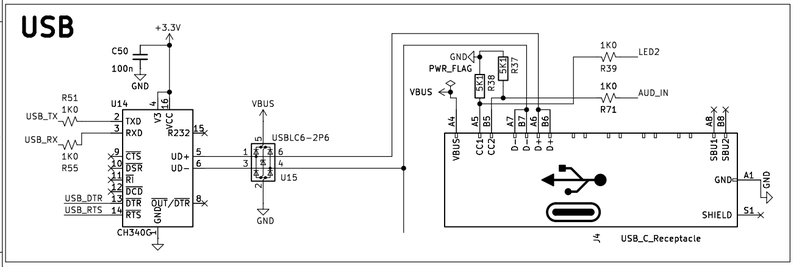
CLK IN



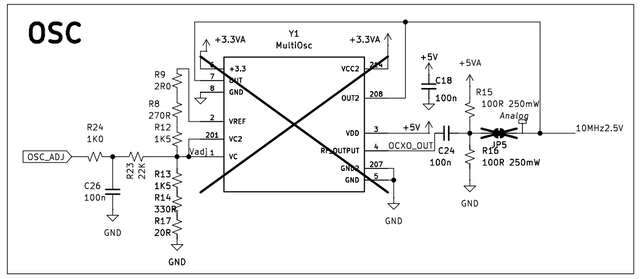
Sync/PPS



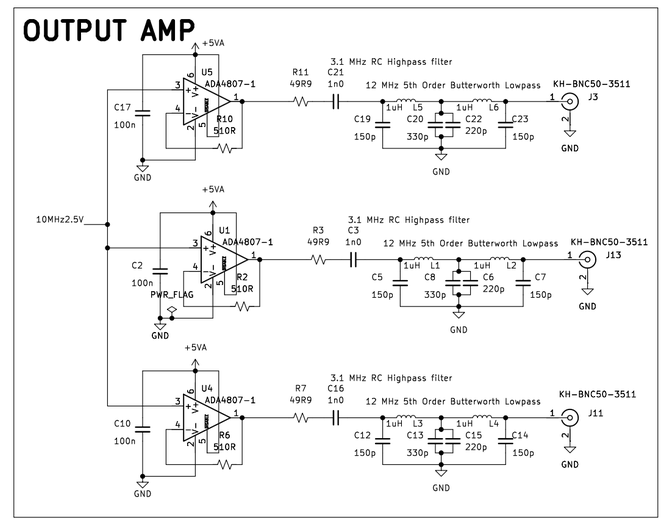
USB



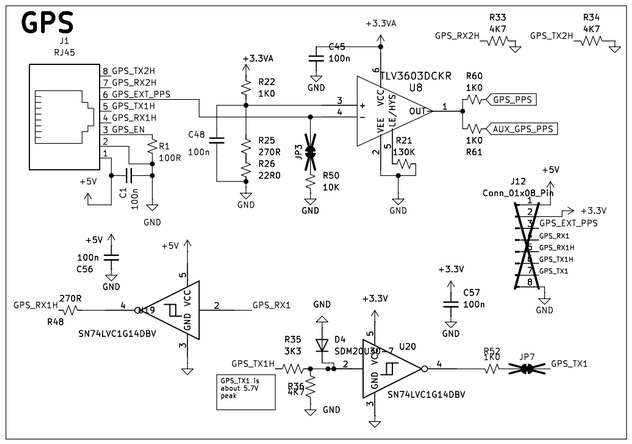
OSC



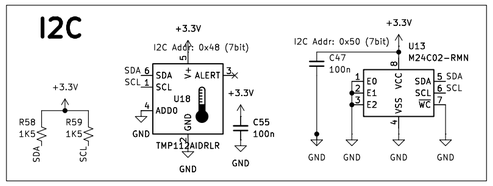
OUTPUT AMP



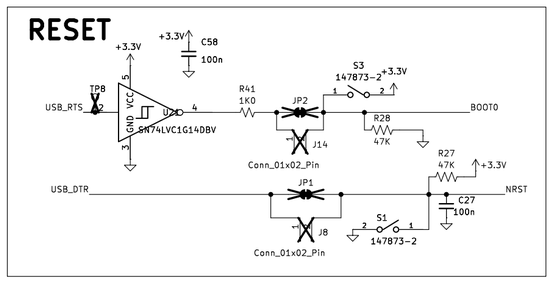
GPS



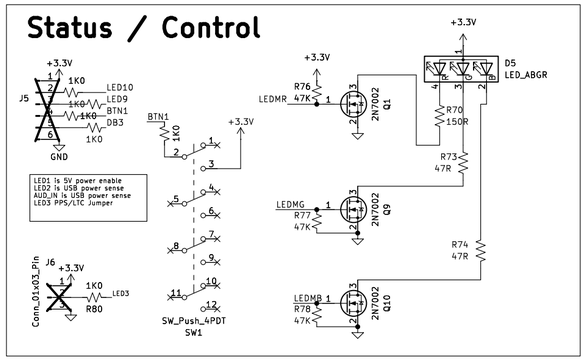
I2C



RESET



Status / Control



MOUNTING

H1 MountingHole	H3 MountingHole
H2 MountingHole	H4 MountingHole

Cullen Jennings

Sheet: /

File: clock_A.kicad_sch

Title: Clock

Size: C	Date: 2024-05-15	Rev: A
KiCad E.D.A. 8.0.2-1		Id: 1/1