

A

A

B

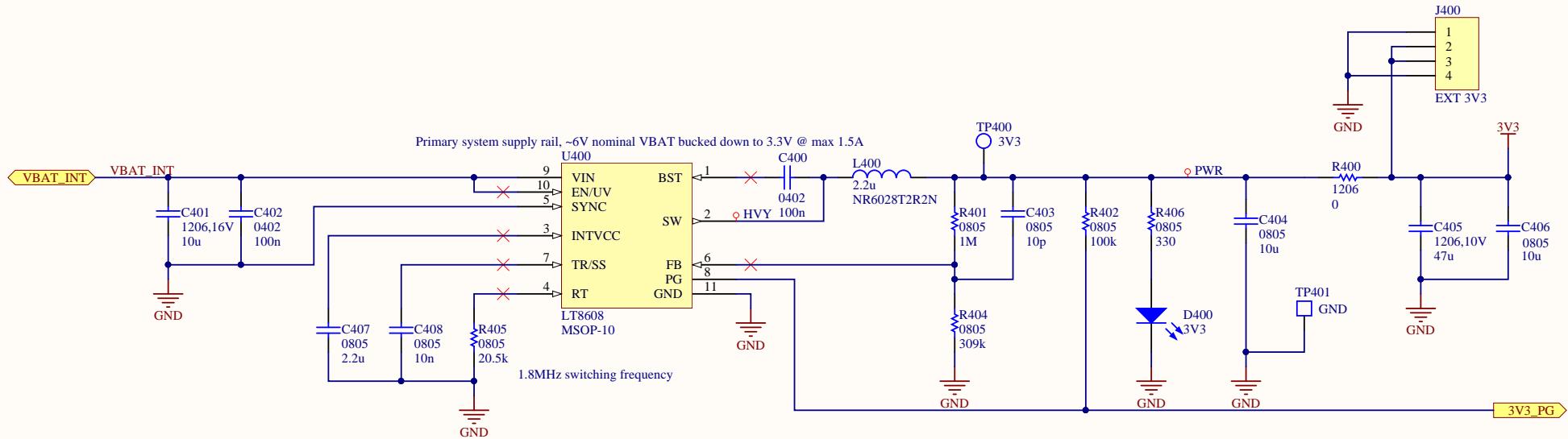
B

C

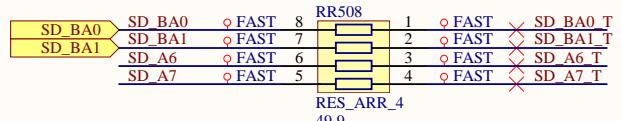
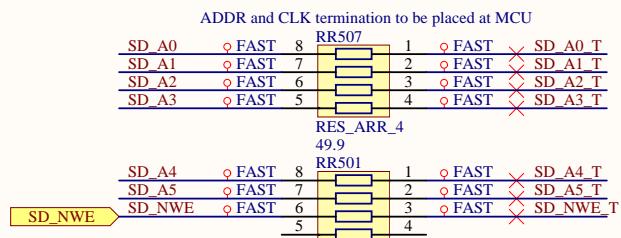
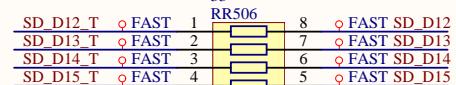
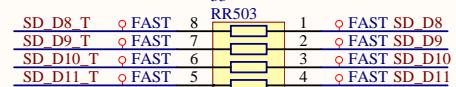
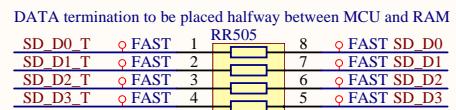
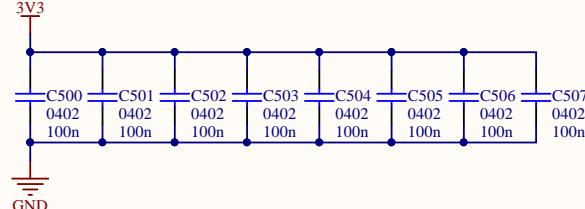
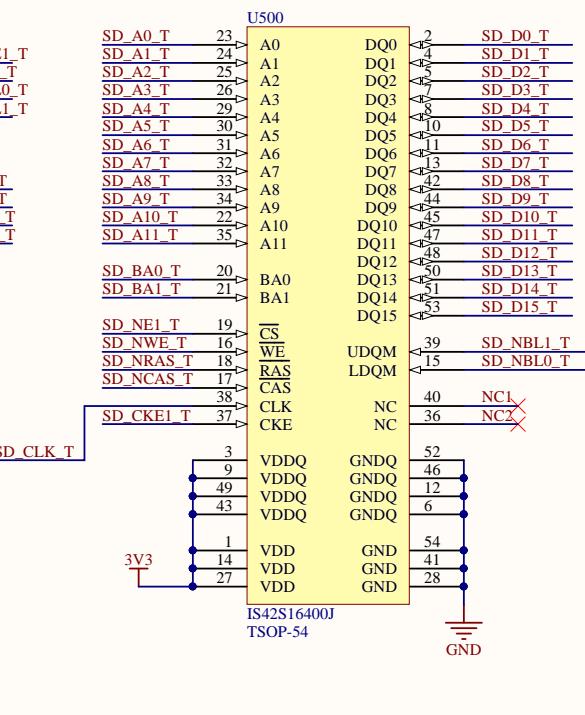
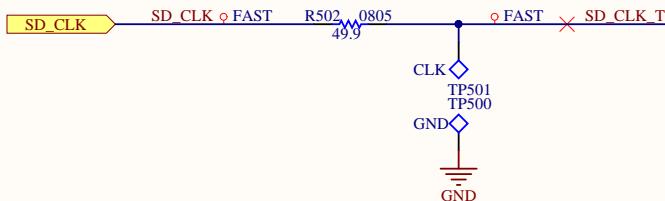
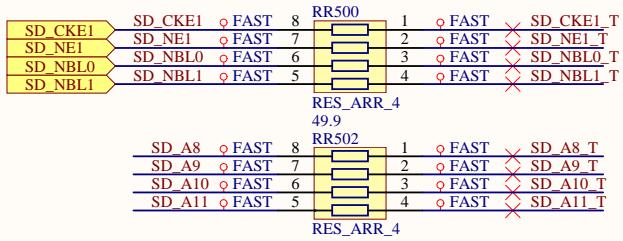
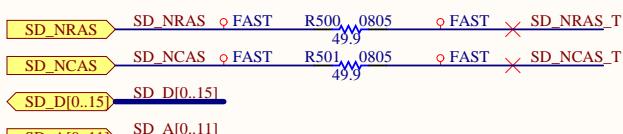
C

D

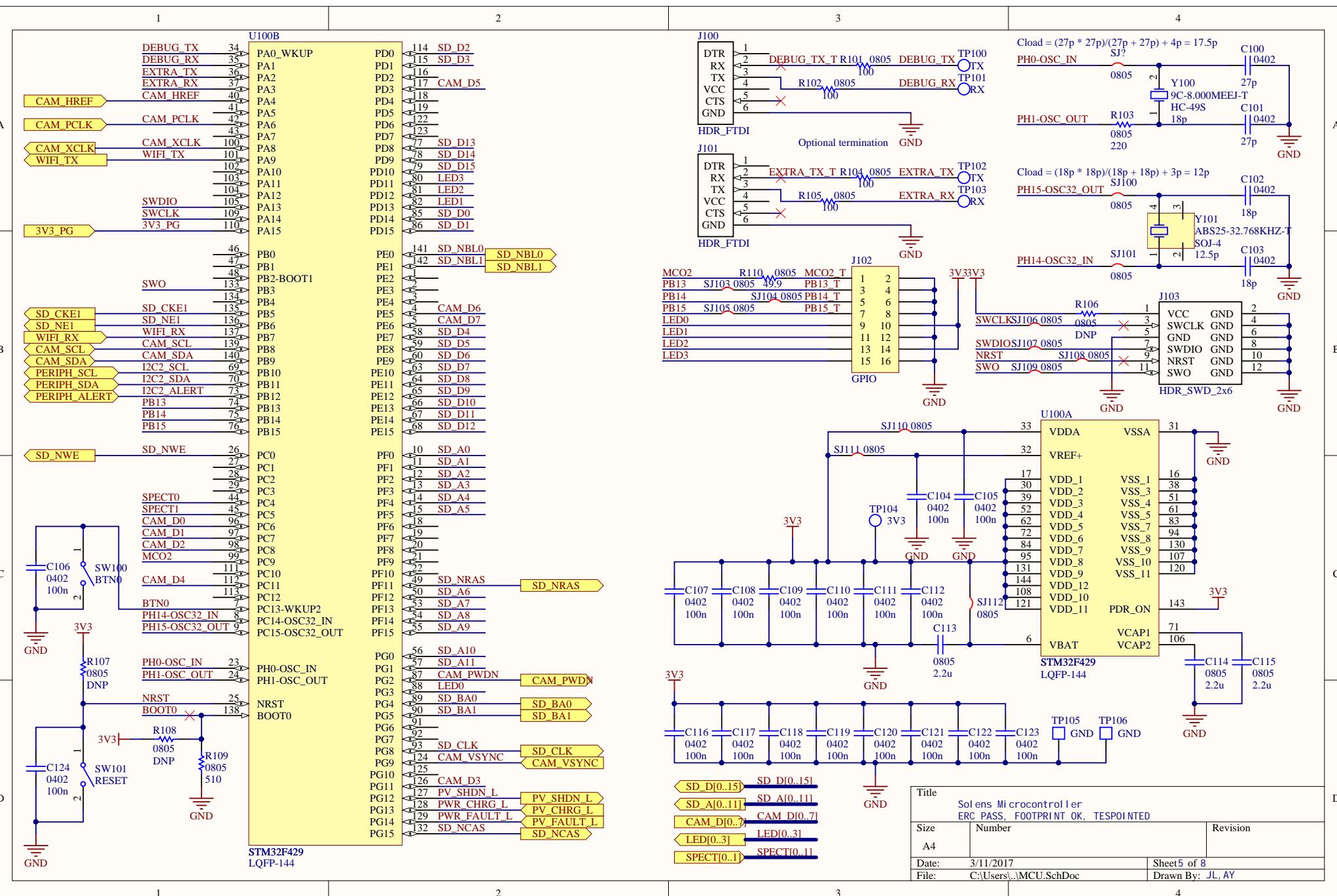
D



Title		
Solens Voltage Regulators		ERC PASS, FOOTPRINT OK, TESPOINTED
Size		Number
A4		Revision
Date:	3/11/2017	Sheet 3 of 8
File:	C:\Users...\Regulators.SchDoc	Drawn By: TN, AY



Title		
Size	Number	Revision
A4		
Date:	3/11/2017	Sheet 4 of 8
File:	C:\Users\...\SDRAM.SchDoc	Drawn By: JL, AY



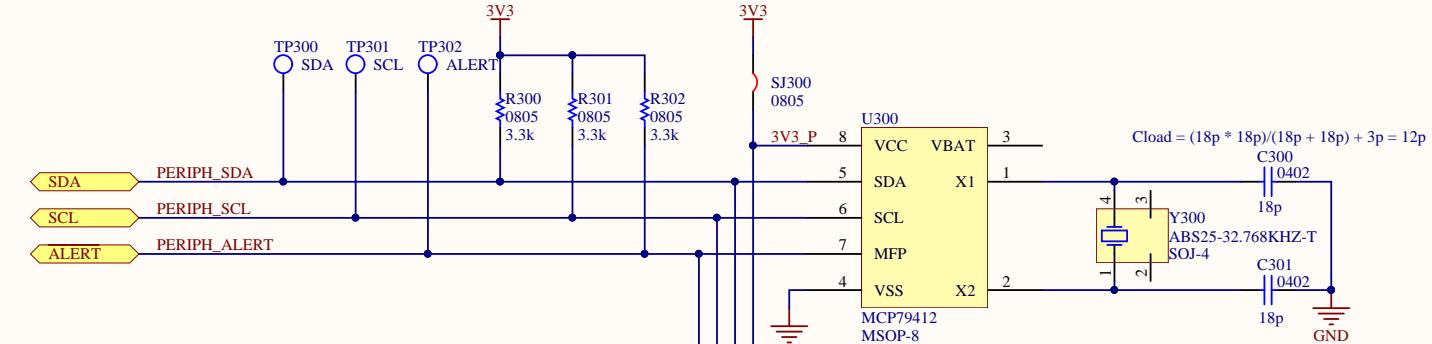
1

2

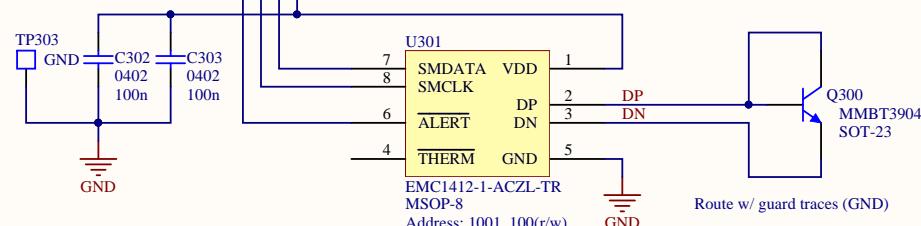
3

4

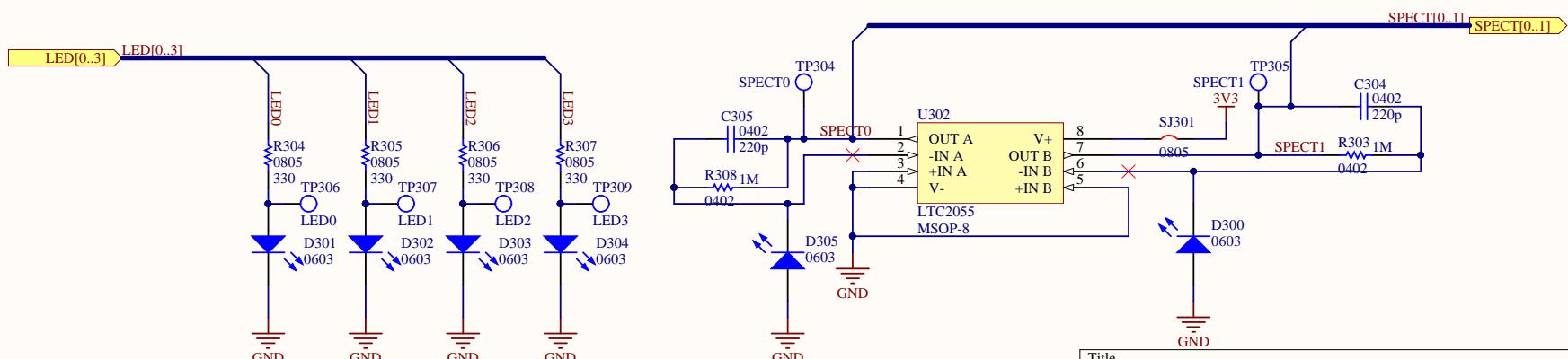
A



B



C



D

Title		
Solens Peripherals ERC PASS, FOOTPRINT OK, TESPOINTED		
Size	Number	Revision
A4		
Date:	3/11/2017	Sheet 6 of 8
File:	C:\Users\...\Peripherals.SchDoc	Drawn By: AY

1

2

3

4

A

A

B

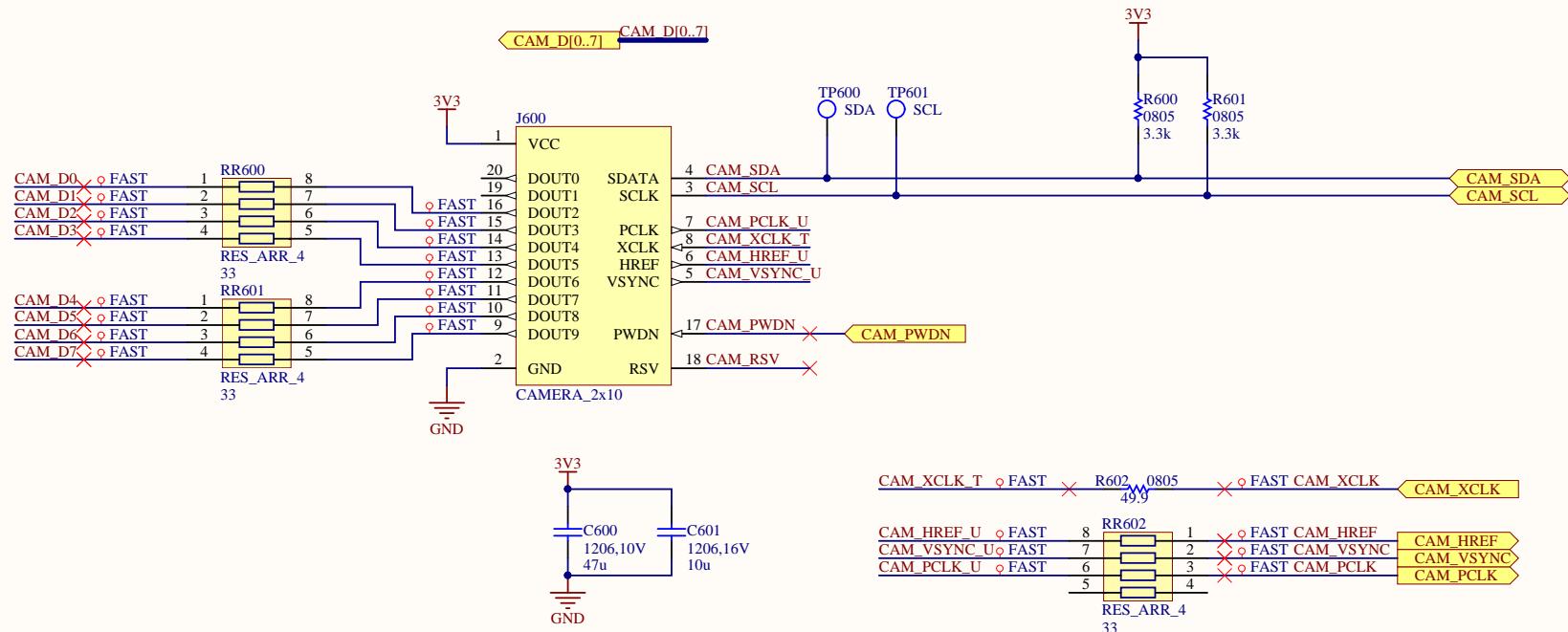
B

C

C

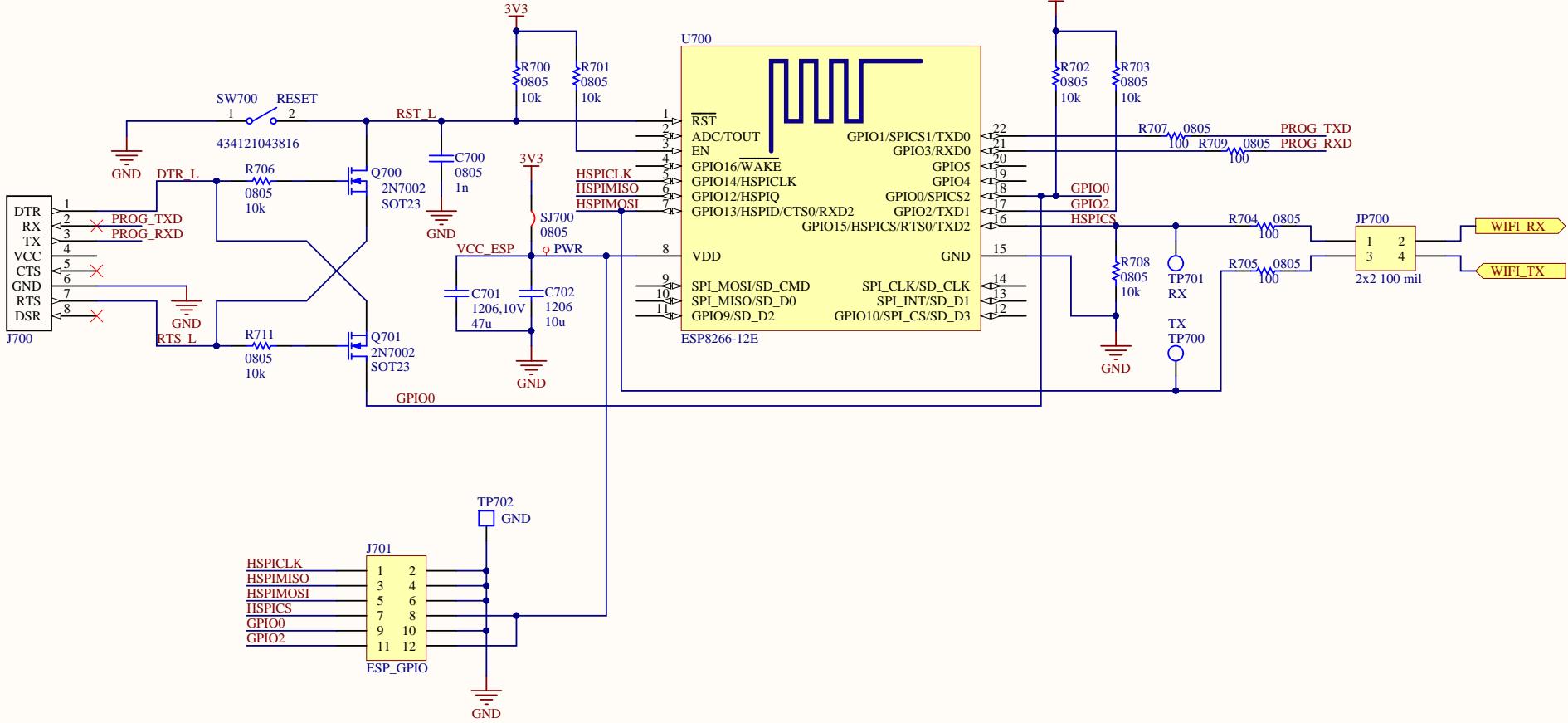
D

D



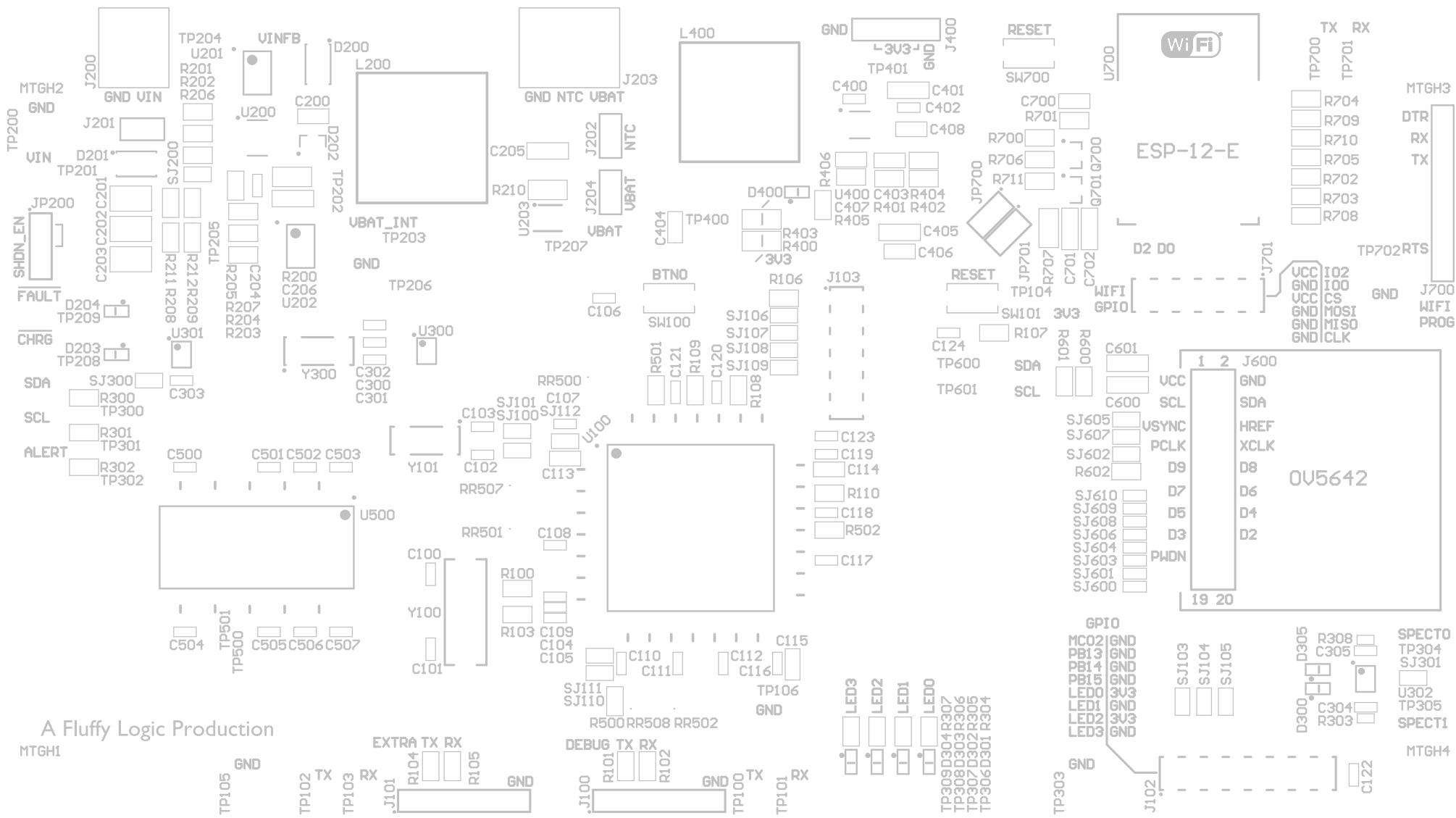
Title Solens Camera Subsystem ERC PASS, FOOTPRINT OK, TESTPOINTED		
Size A4	Number	Revision
Date: 3/11/2017	Sheet 7 of 8	
File: C:\Users...\Camera.SchDoc		Drawn By: JL, AY

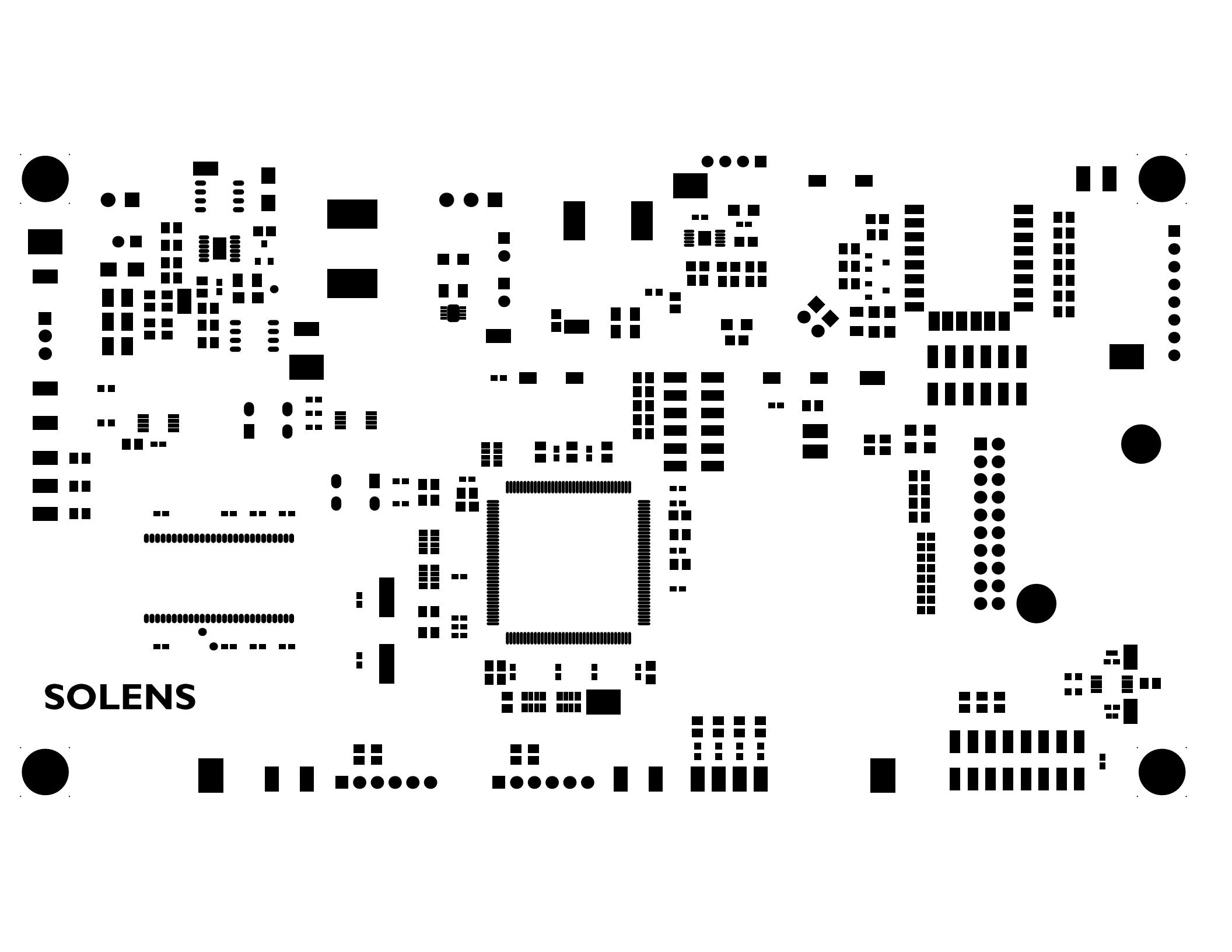
A



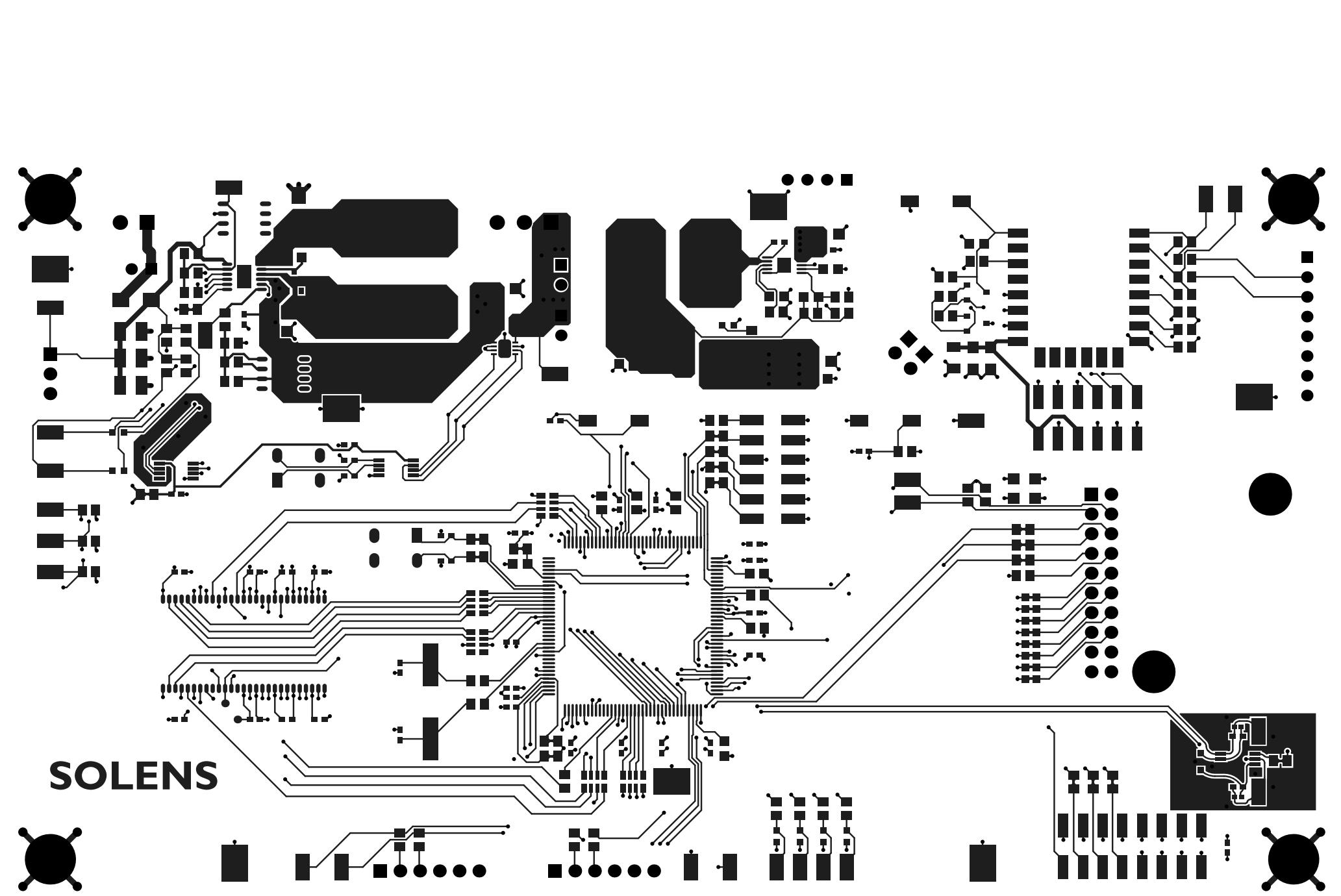
Title		
Solens WiFi Subsystem ERC PASS, FOOTPRINT OK, TESTPOINTED		
Size	Number	Revision
A4		
Date:	3/11/2017	Sheet 8 of 8
File:	C:\Users\...\WiFi.SchDoc	Drawn By: JL, AY

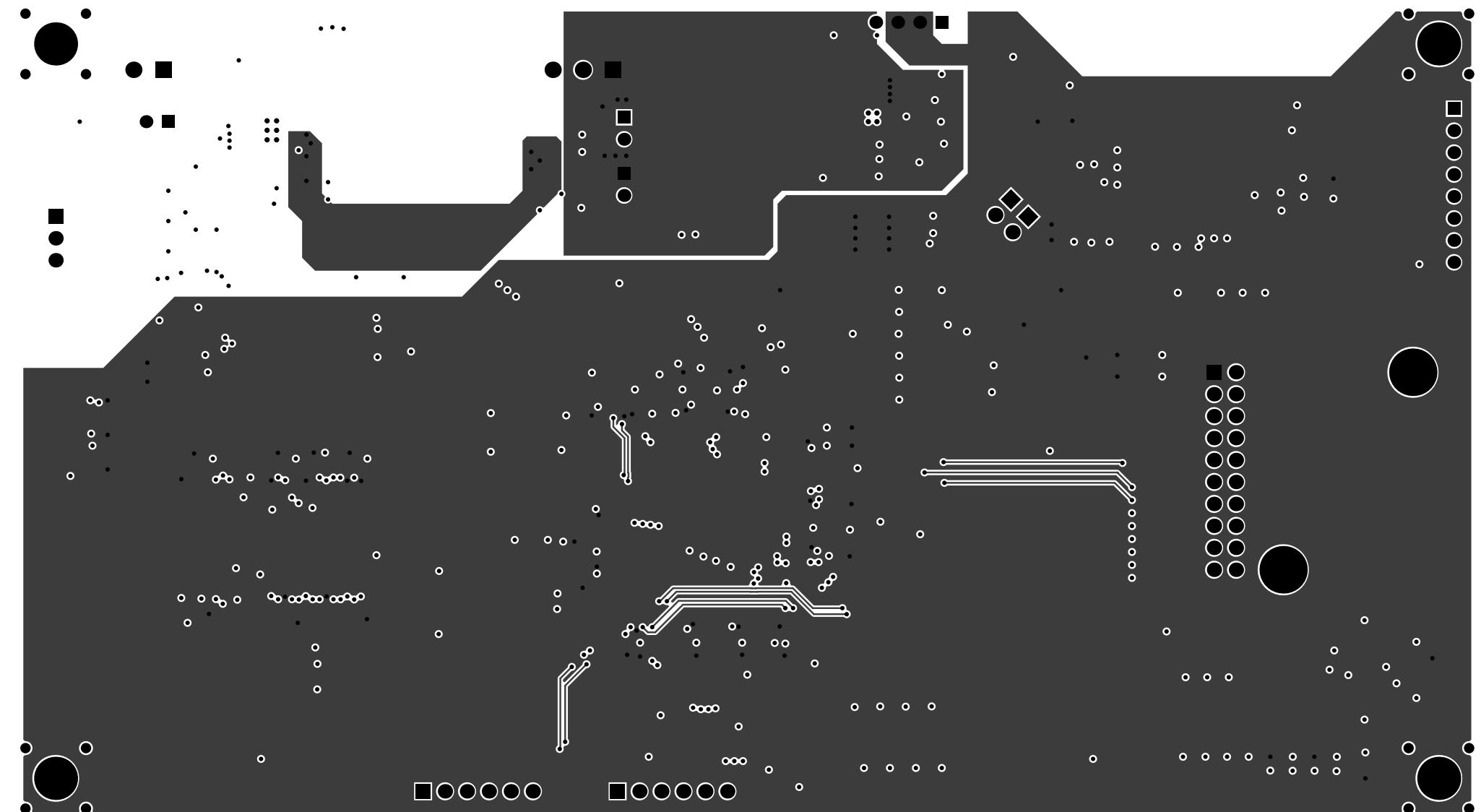


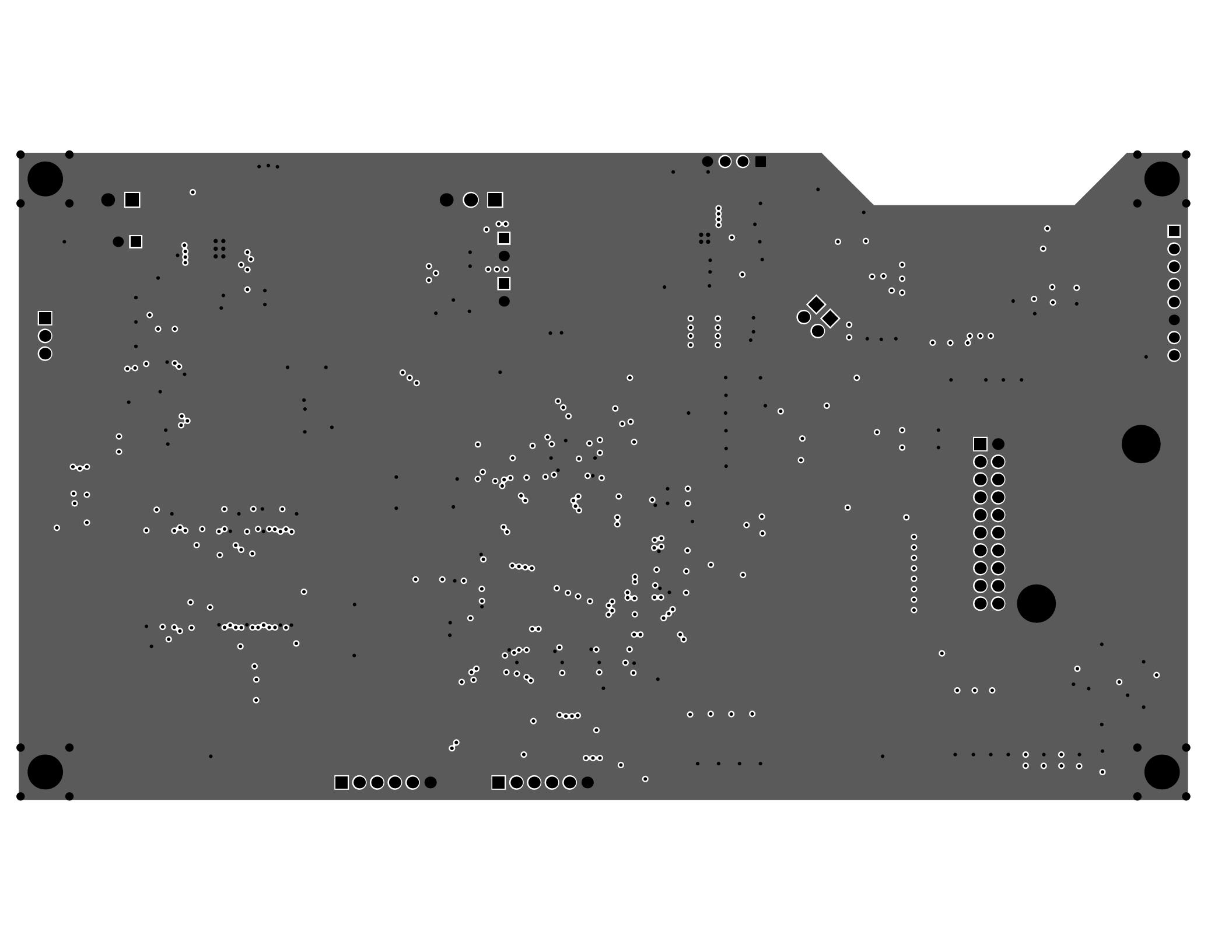


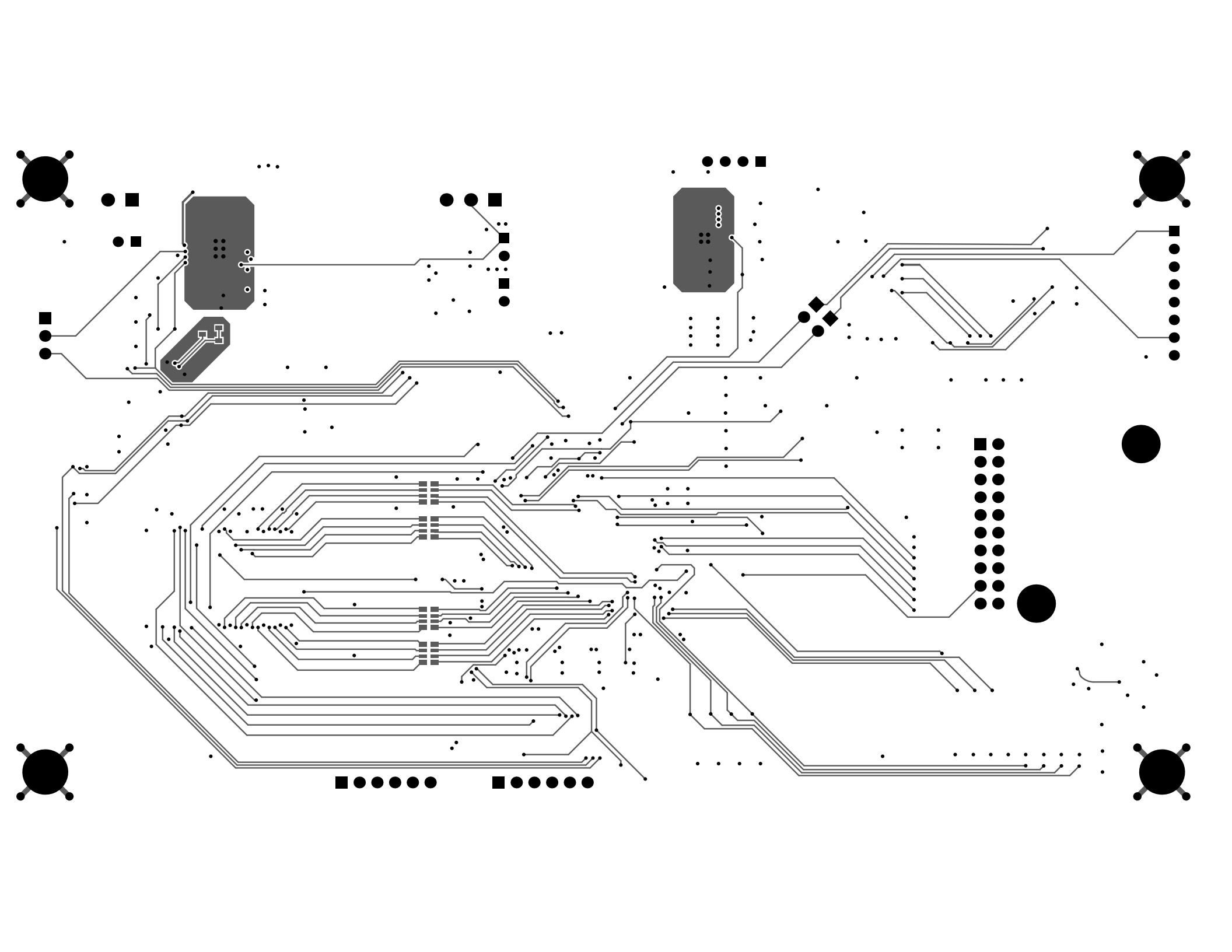


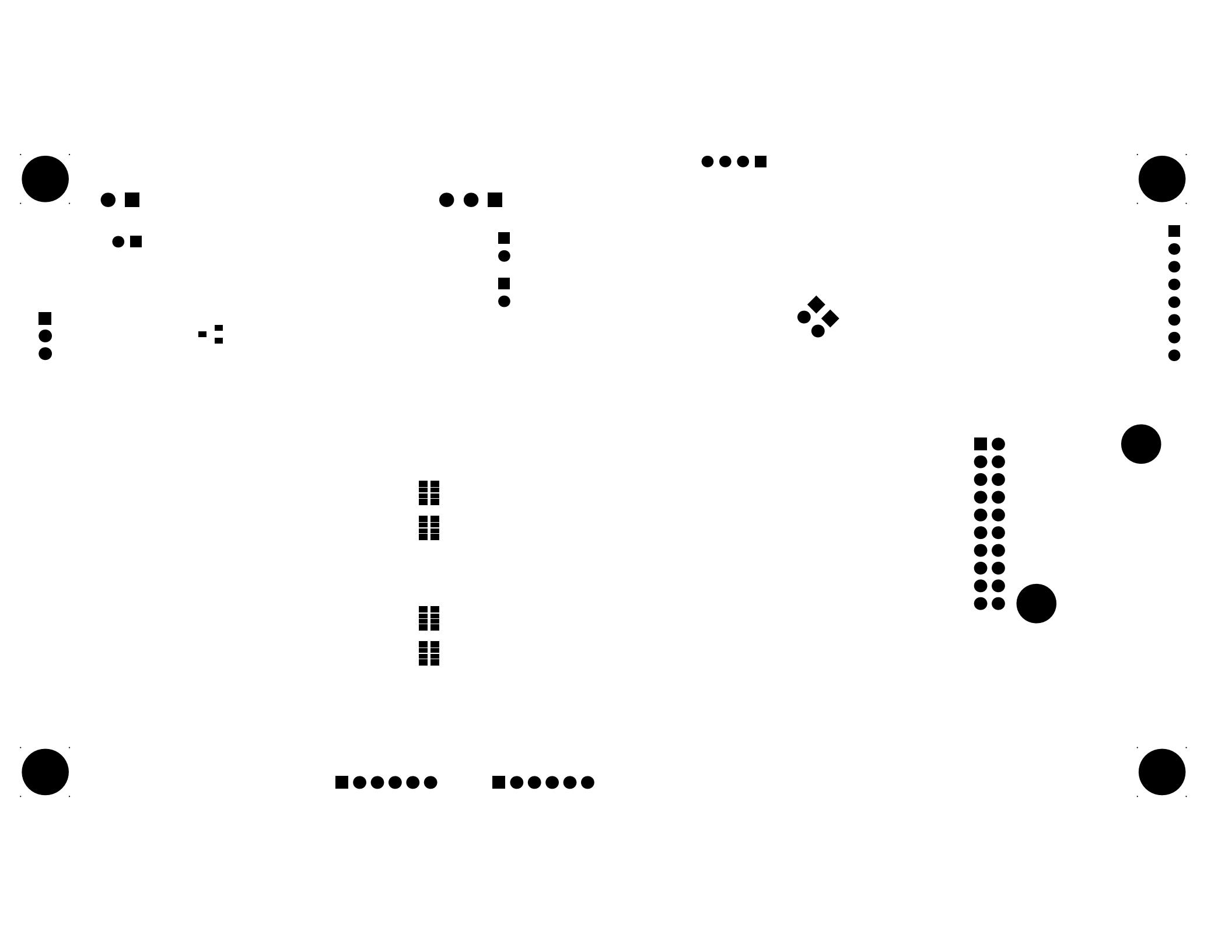
**SOLENS**

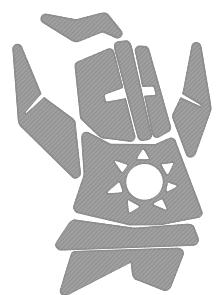












00300  
L  
L

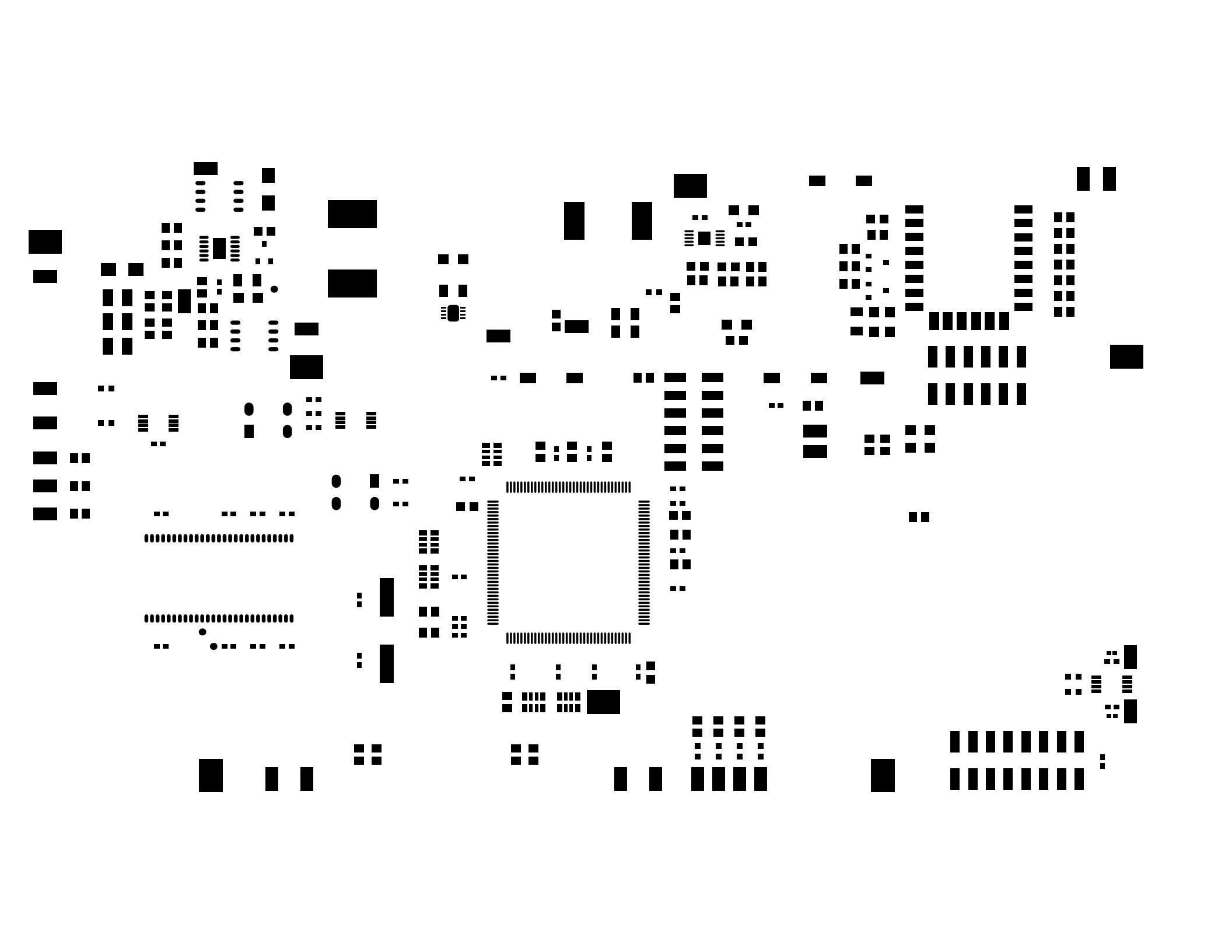
RR505

RR504

RR503

RR506

AKIRA Youngblood  
JEREMY Rapp  
TAYLOR Nguyen  
JEFFERY LIM  
BEN Heberlein

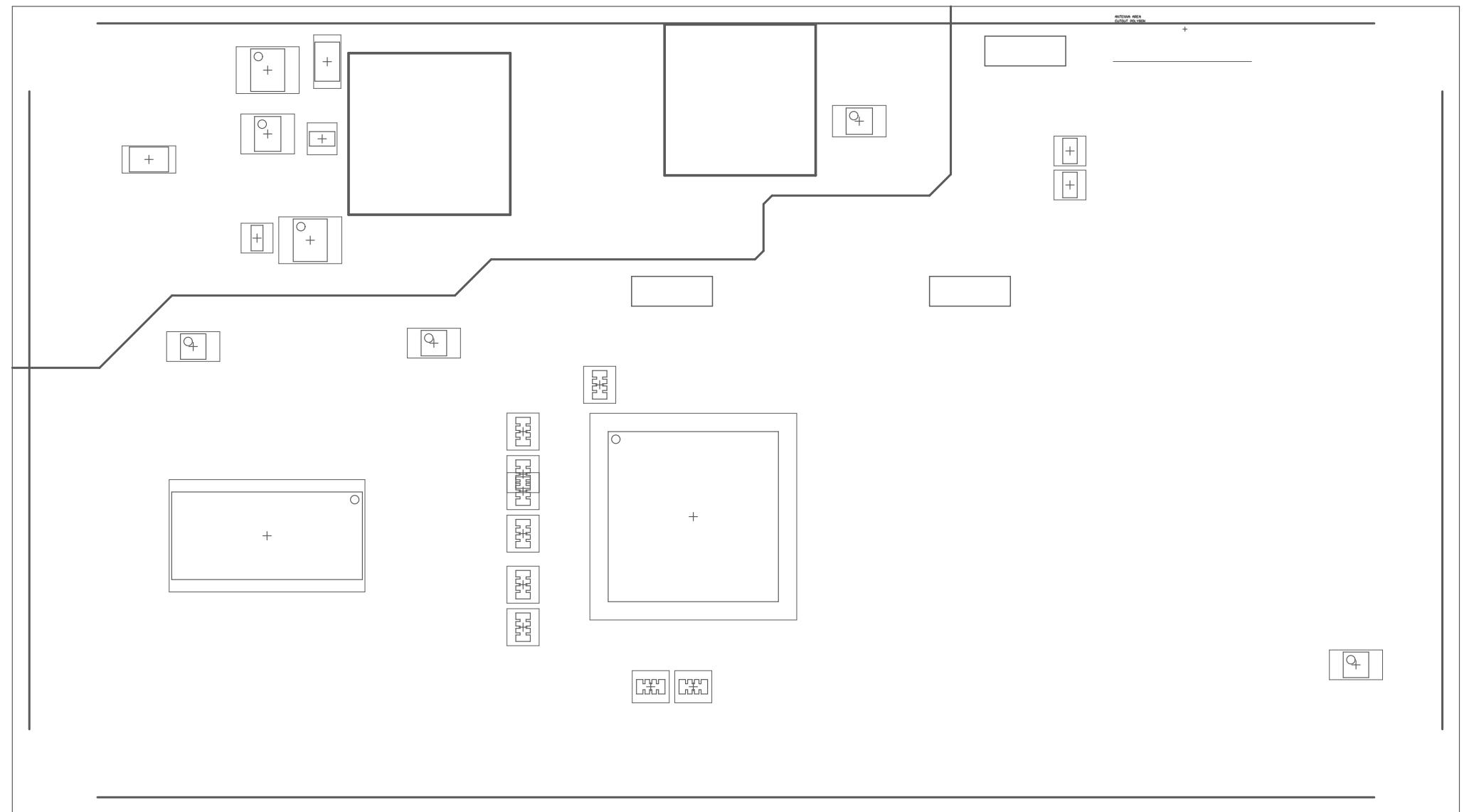


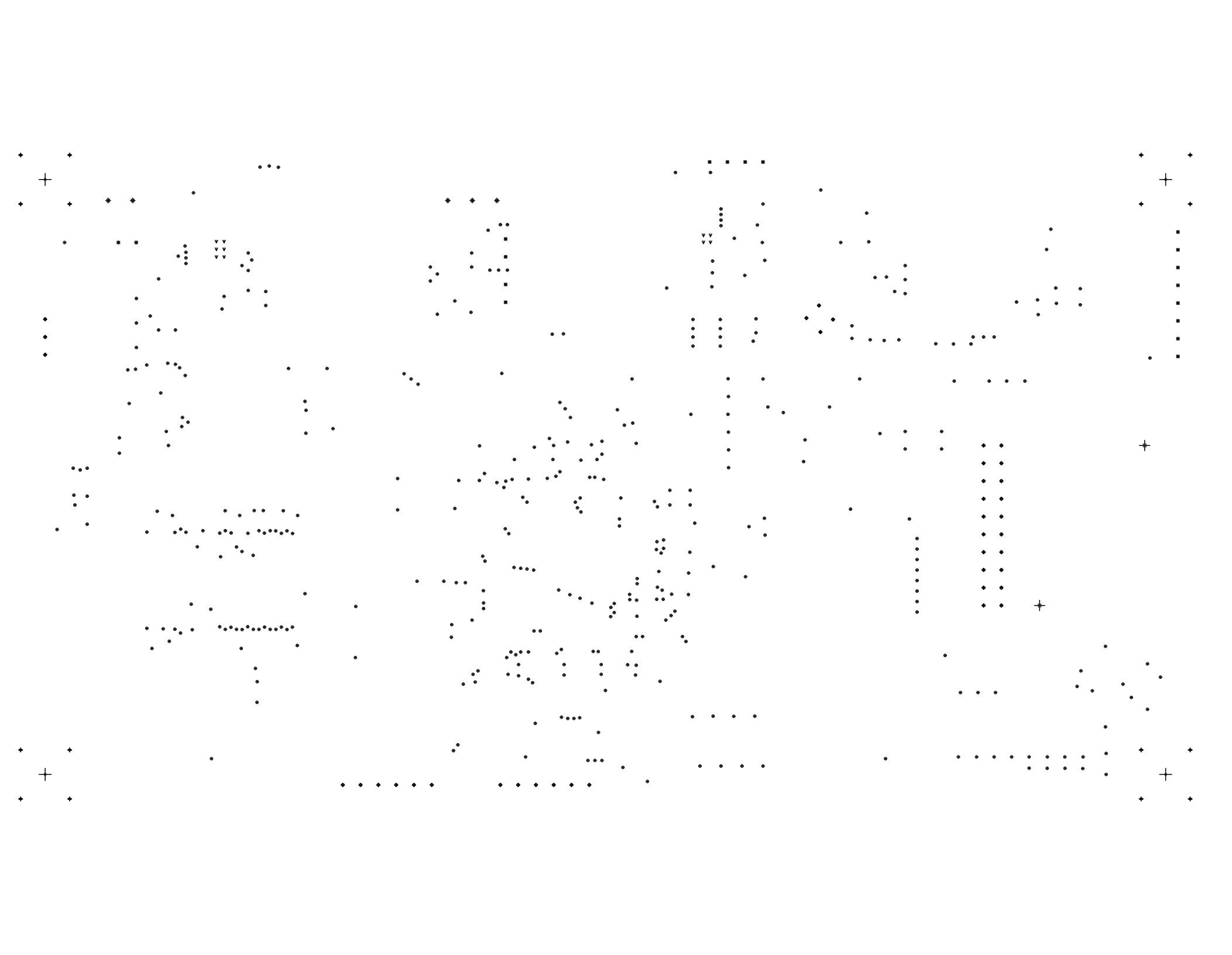
- :

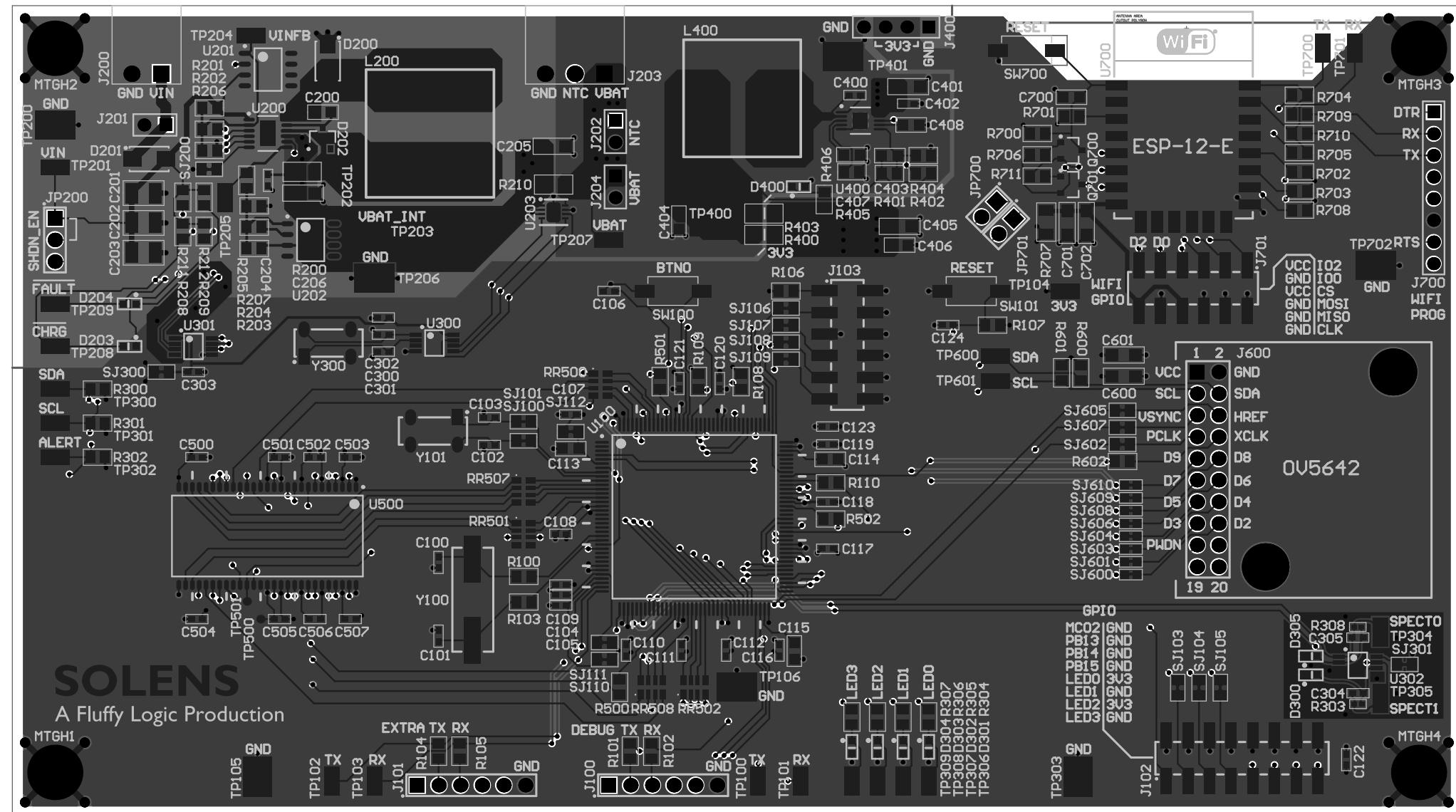
■ ■ ■

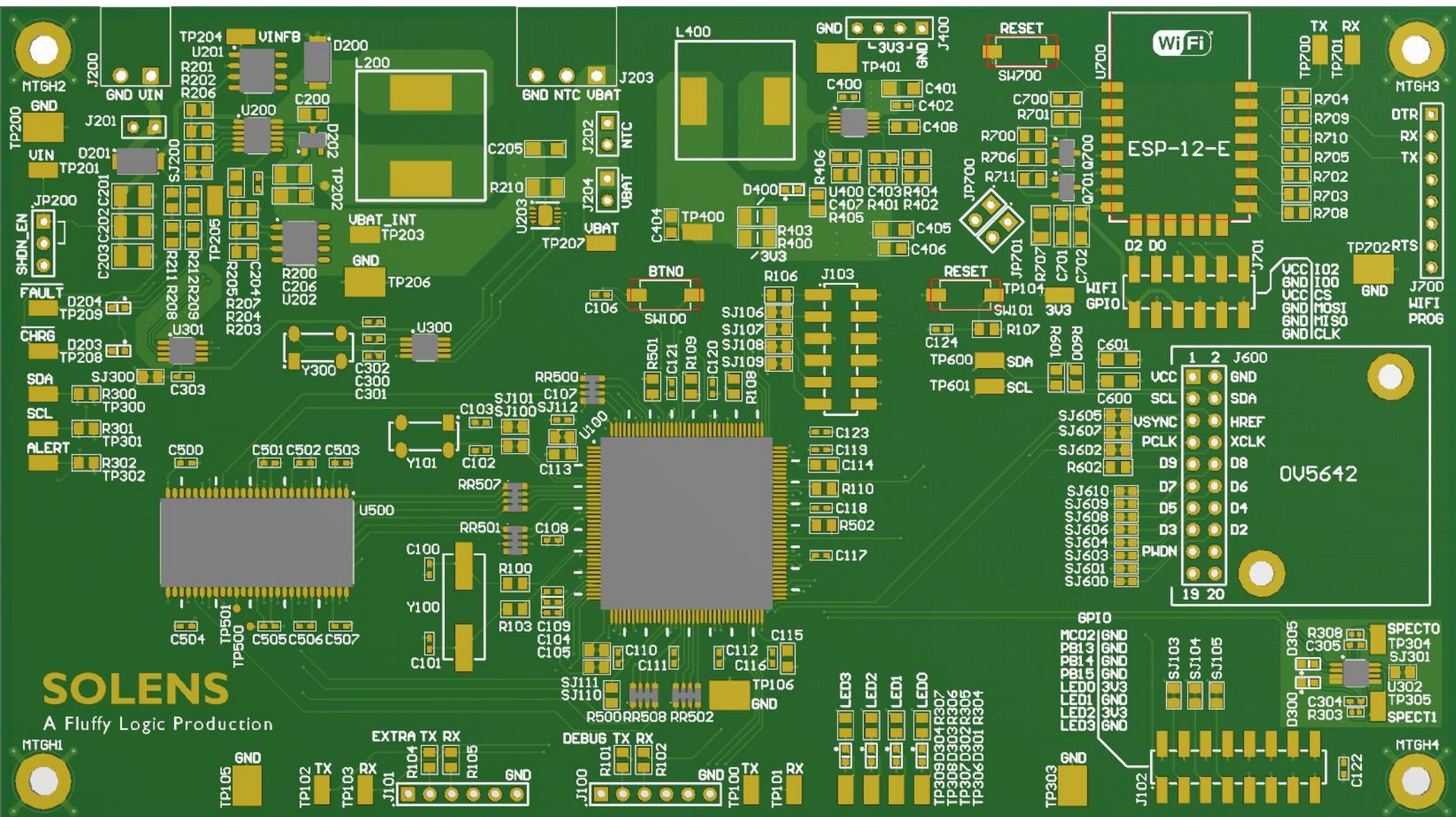
■ ■ ■

■ ■ ■











BEN HEBERLEIN  
JEFFERY LIM  
TAYLOR NGUYEN  
JEREMY RAPP  
AKIRA YOUNGBLOOD

RR505

RR504

RR503

RR506

Q300