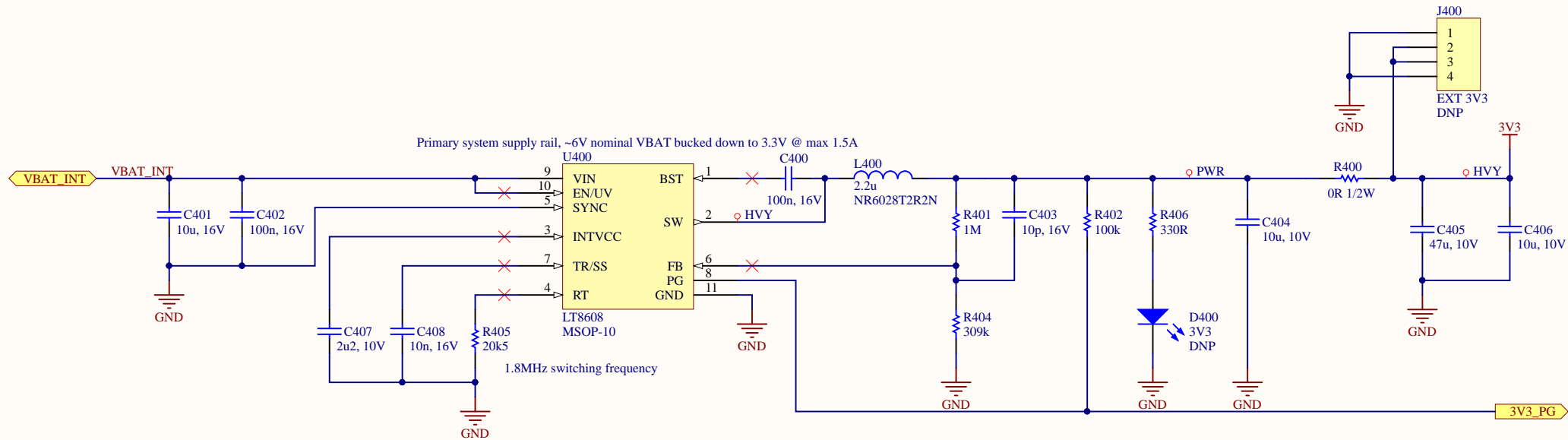
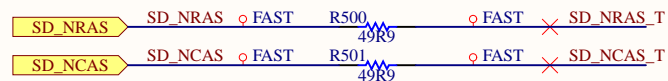


Title		
Solens Camera Subsystem		
ERC WARN, FOOTPRINT N/A		
Size	Number	Revision
A4		
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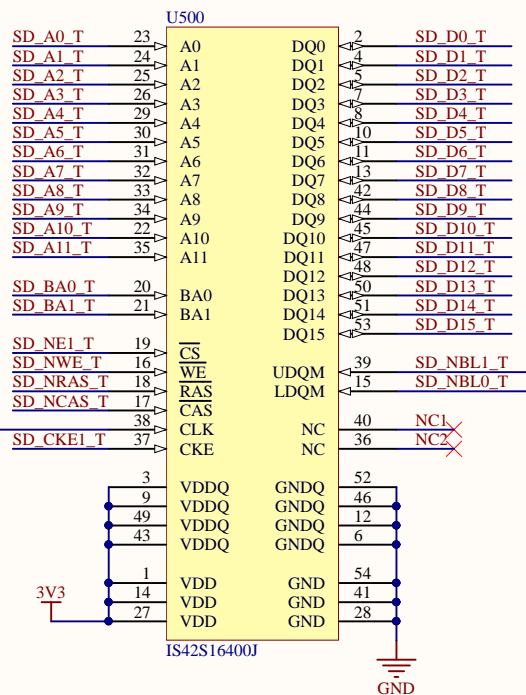
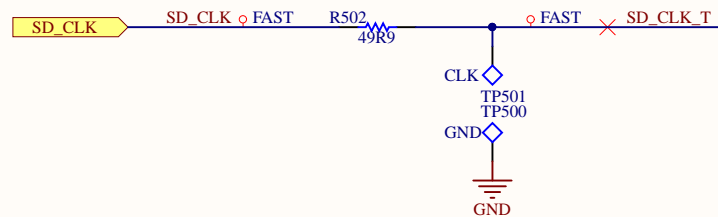
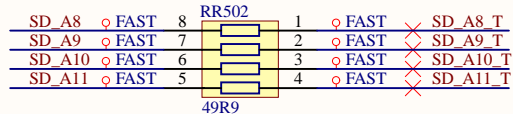
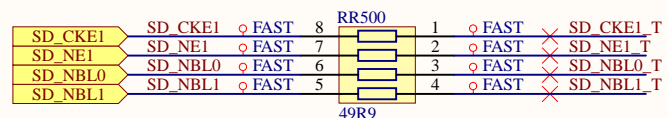
Title		
Sol ens Vol tage Regulators		
ERC PASS, FOOTPRINT OK, TESPOINTED		
Size	Number	Revision
A4		
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File:	C:\Users\...\Regulators.SchDoc	Drawn By: TN, AY

A

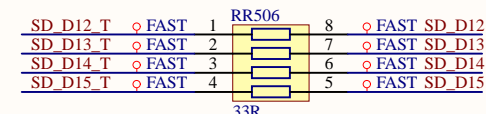
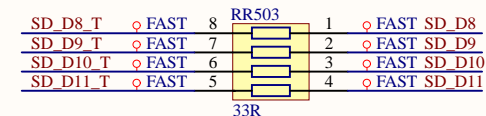
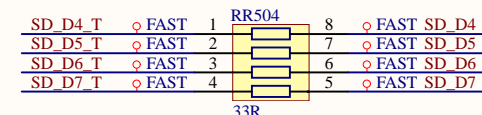
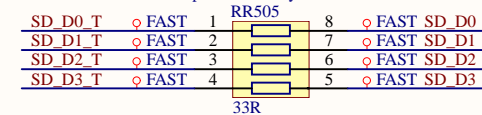


SD_D[0..15] SD_D[0..15]

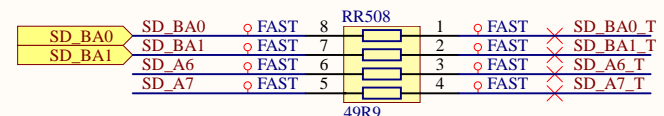
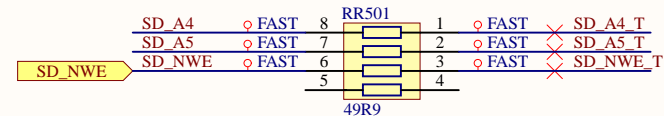
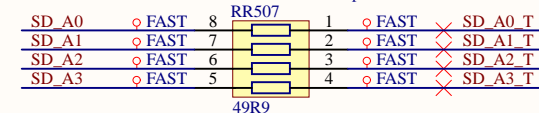
SD_A[0..11] SD_A[0..11]



DATA termination to be placed halfway between MCU and RAM



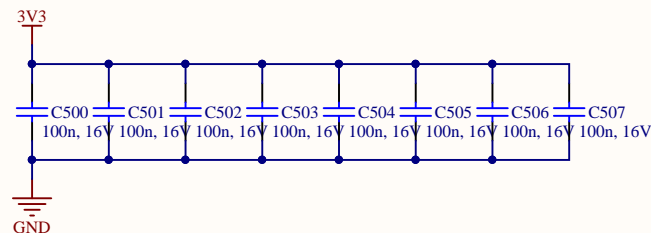
ADDR and CLK termination to be placed at MCU



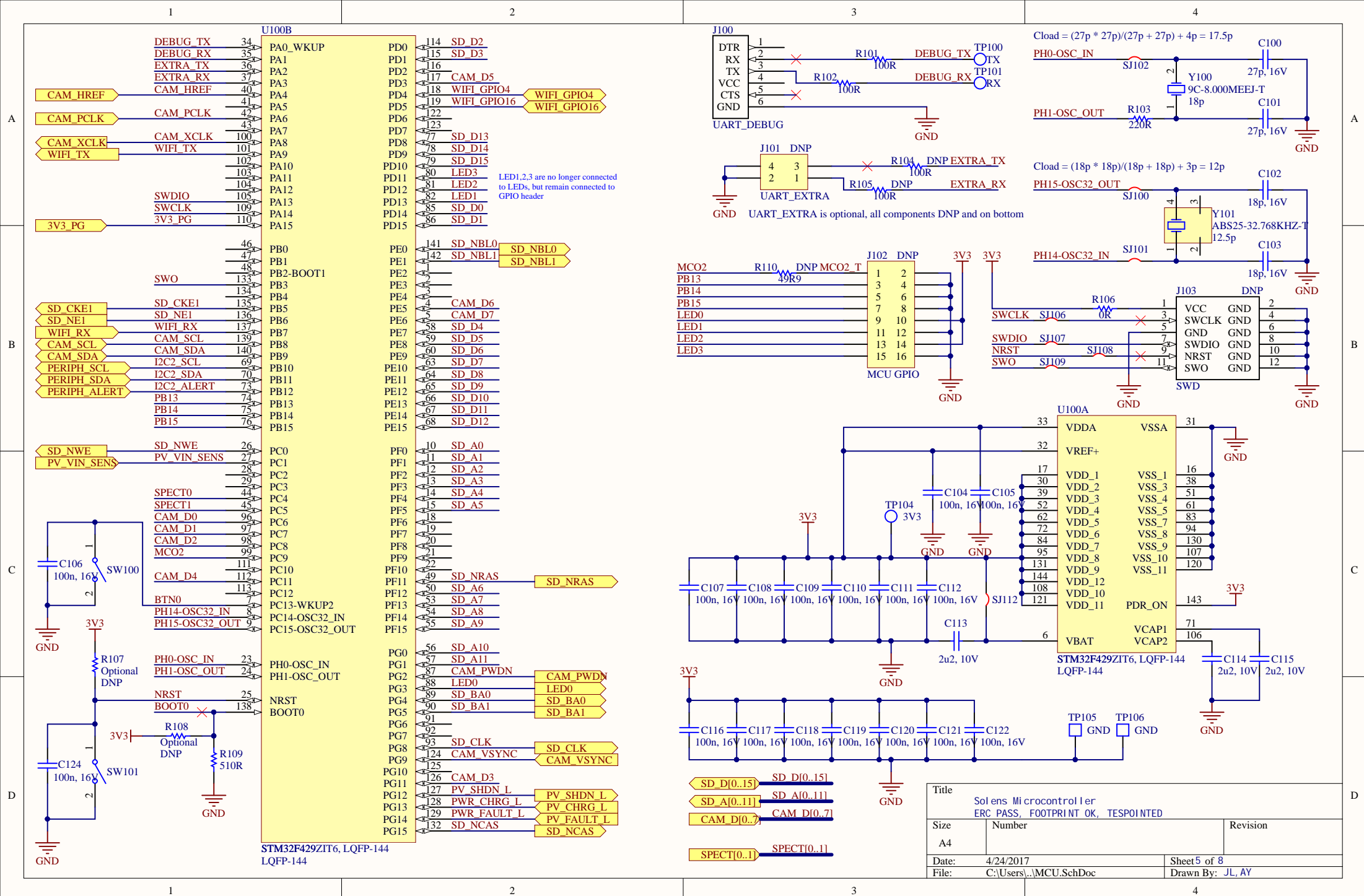
B

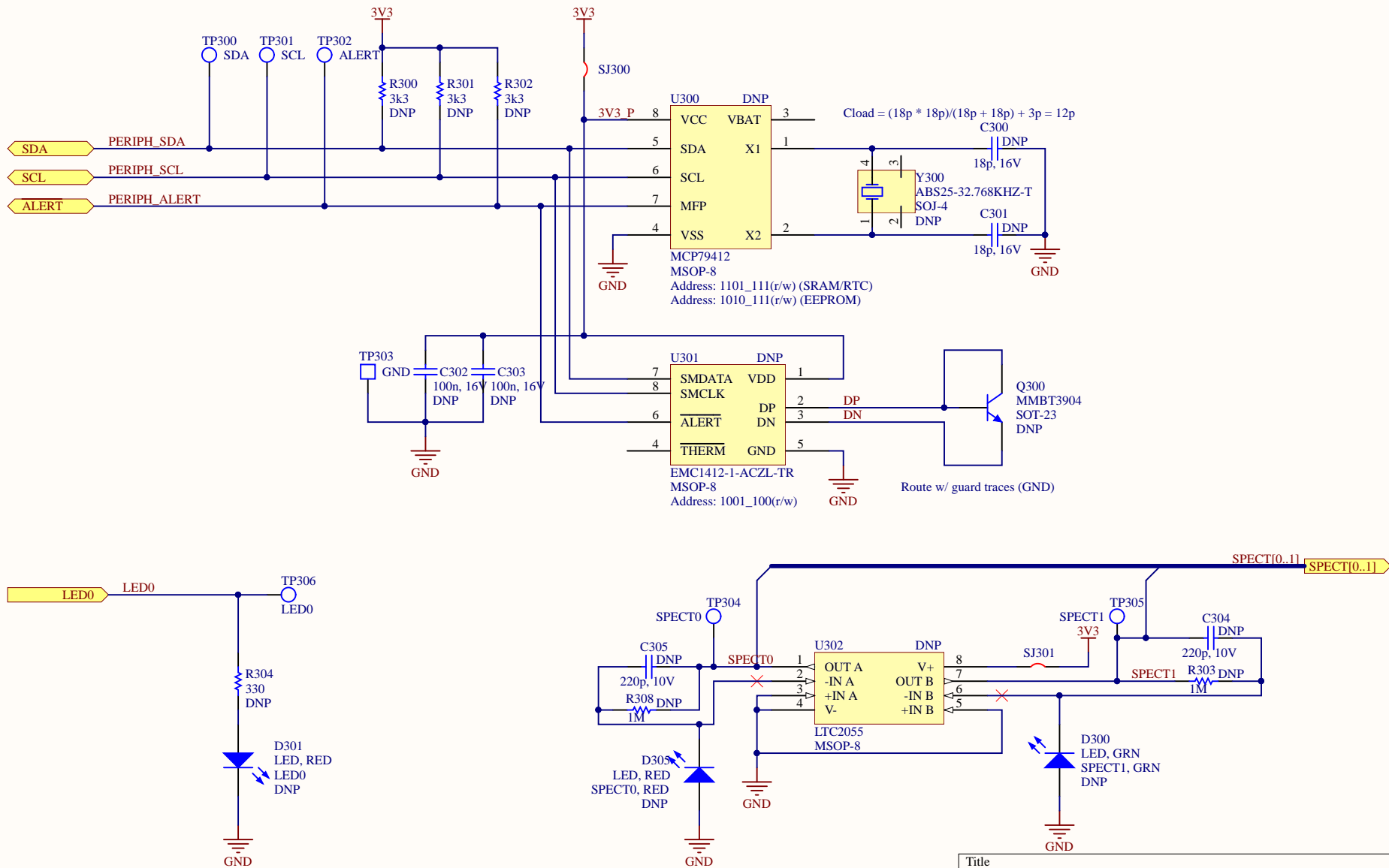
C

D

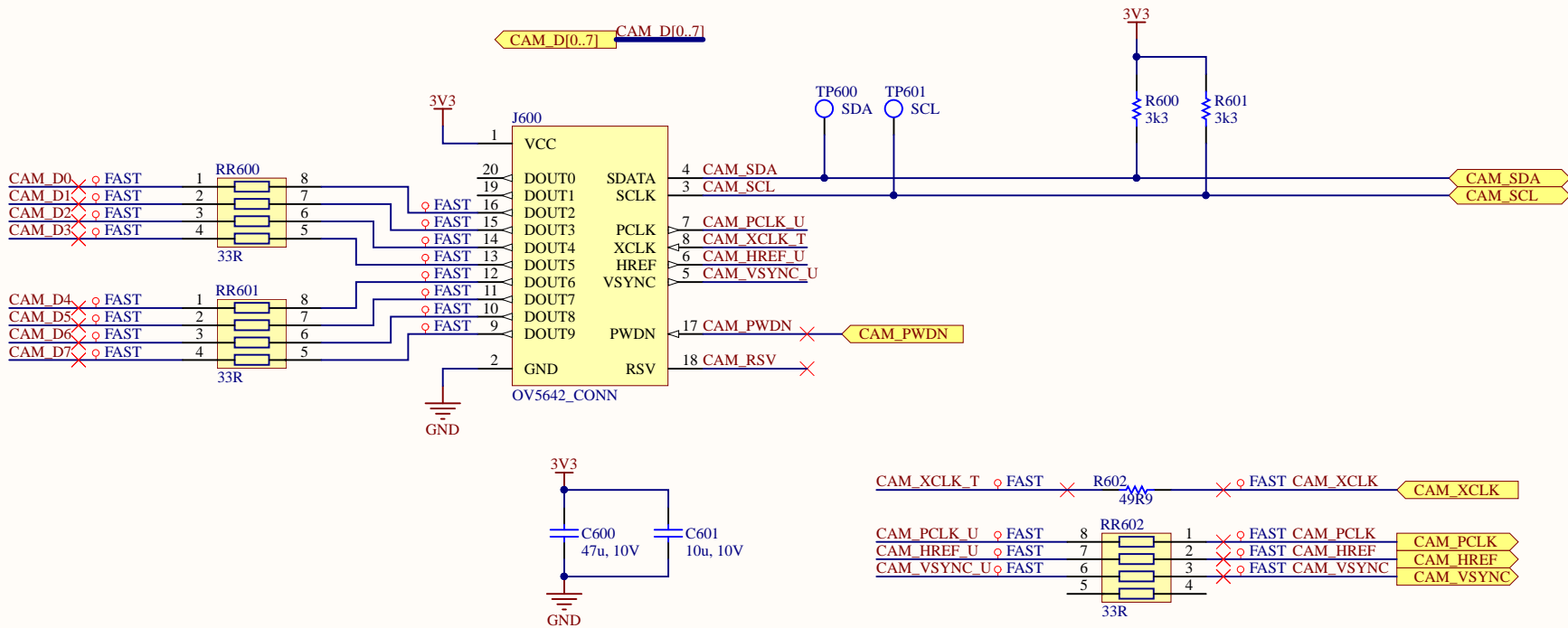


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Sol ens SDRAM		
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