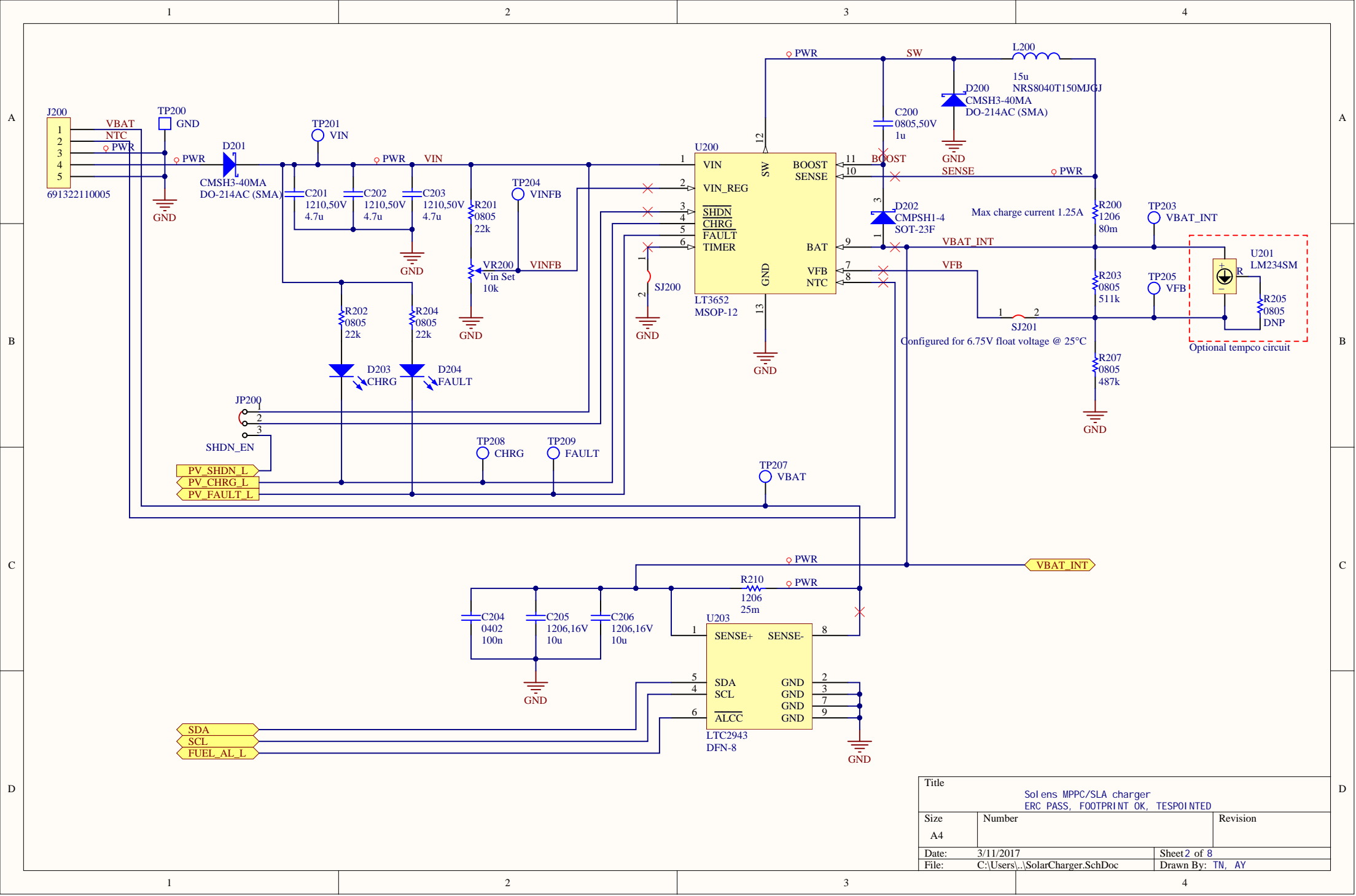
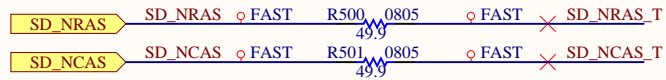


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Solens Camera Subsystem		
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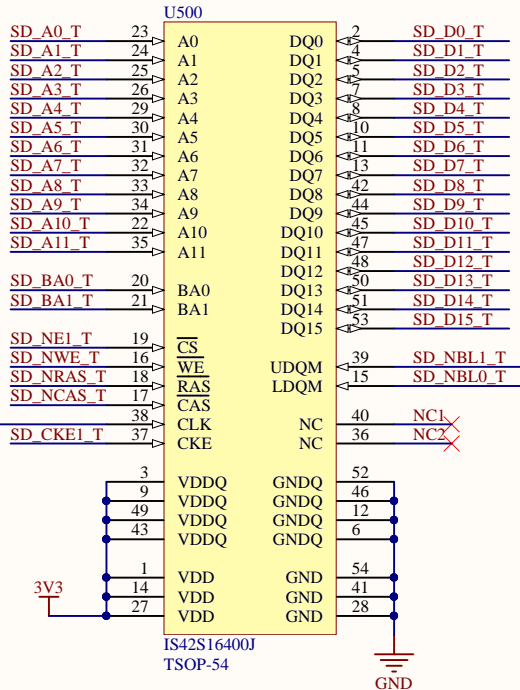
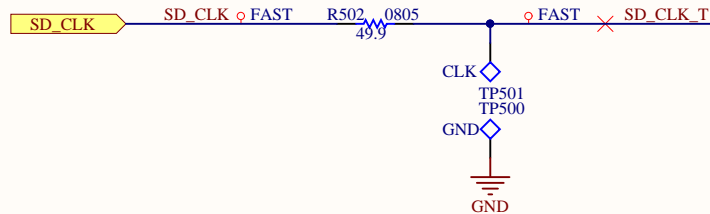
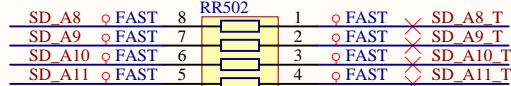
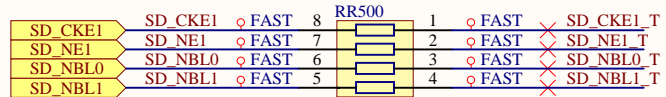


A

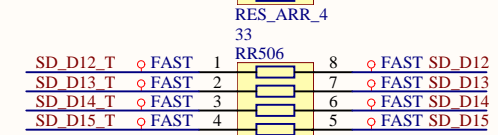
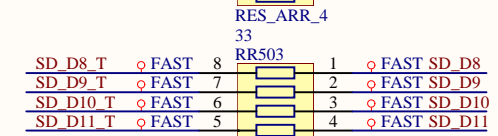
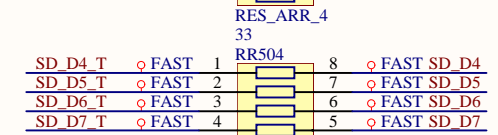
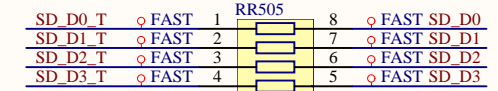


SD\_D[0..15] SD\_D[0..15]

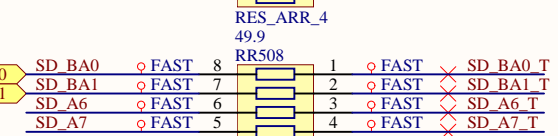
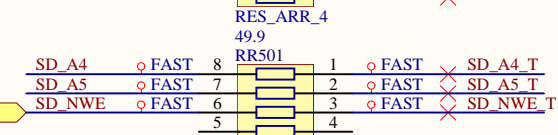
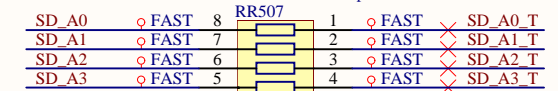
SD\_A[0..11] SD\_A[0..11]



DATA termination to be placed halfway between MCU and RAM



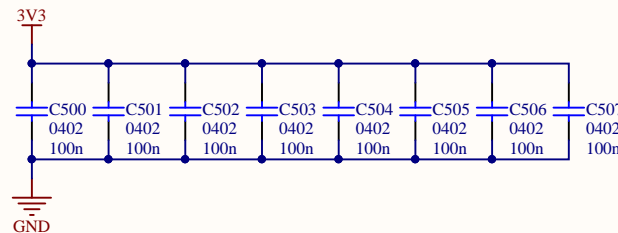
ADDR and CLK termination to be placed at MCU



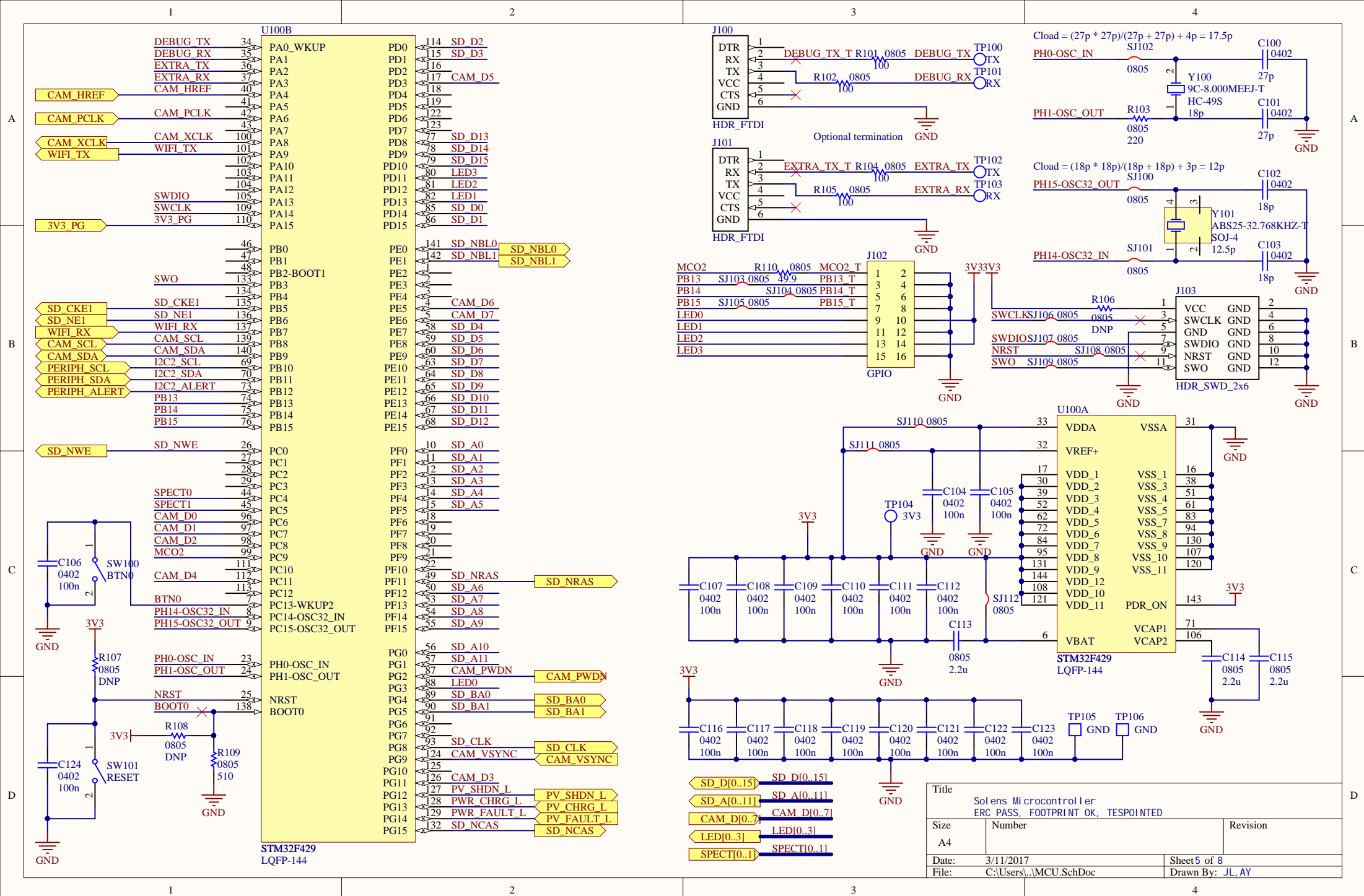
B

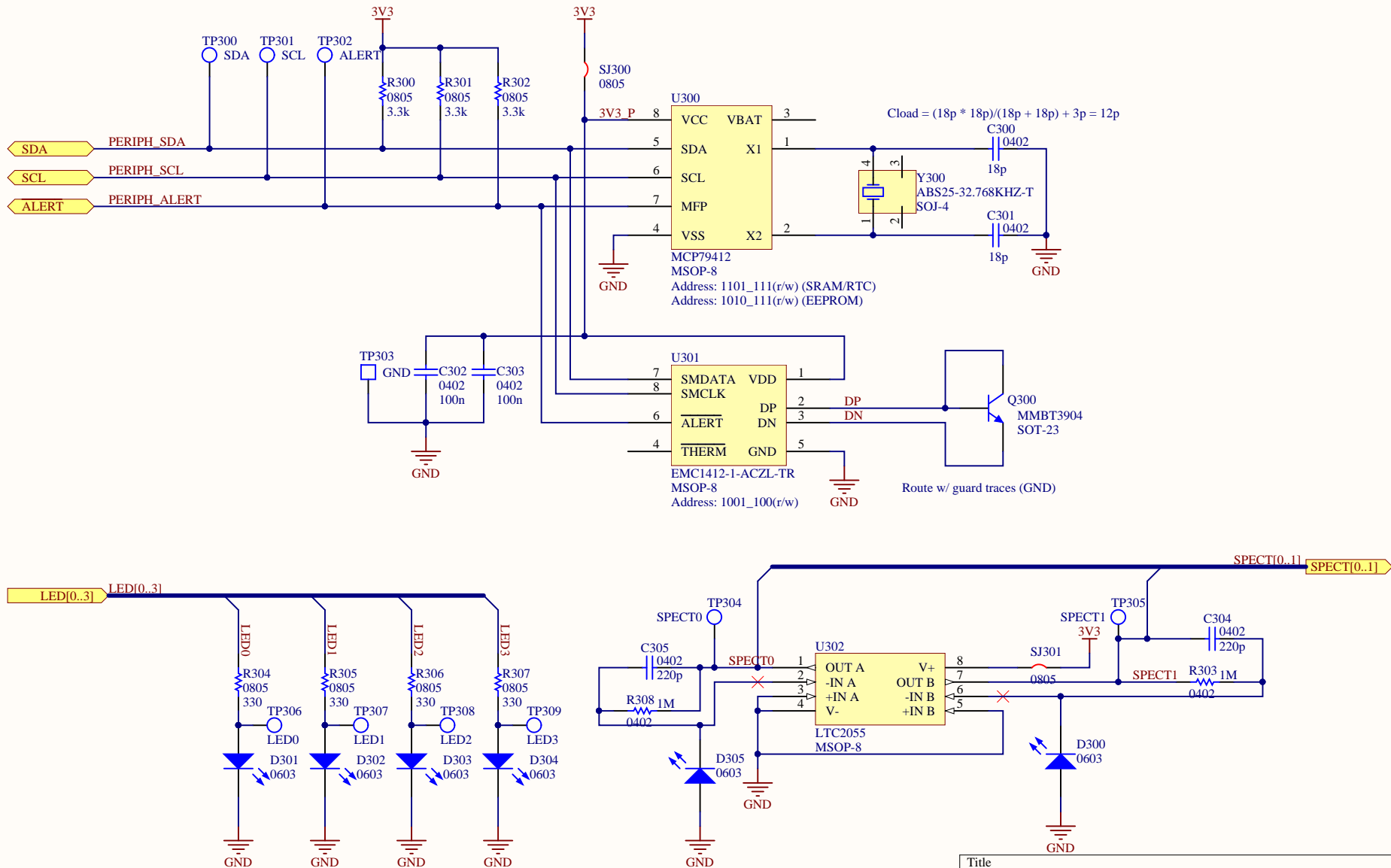
C

D

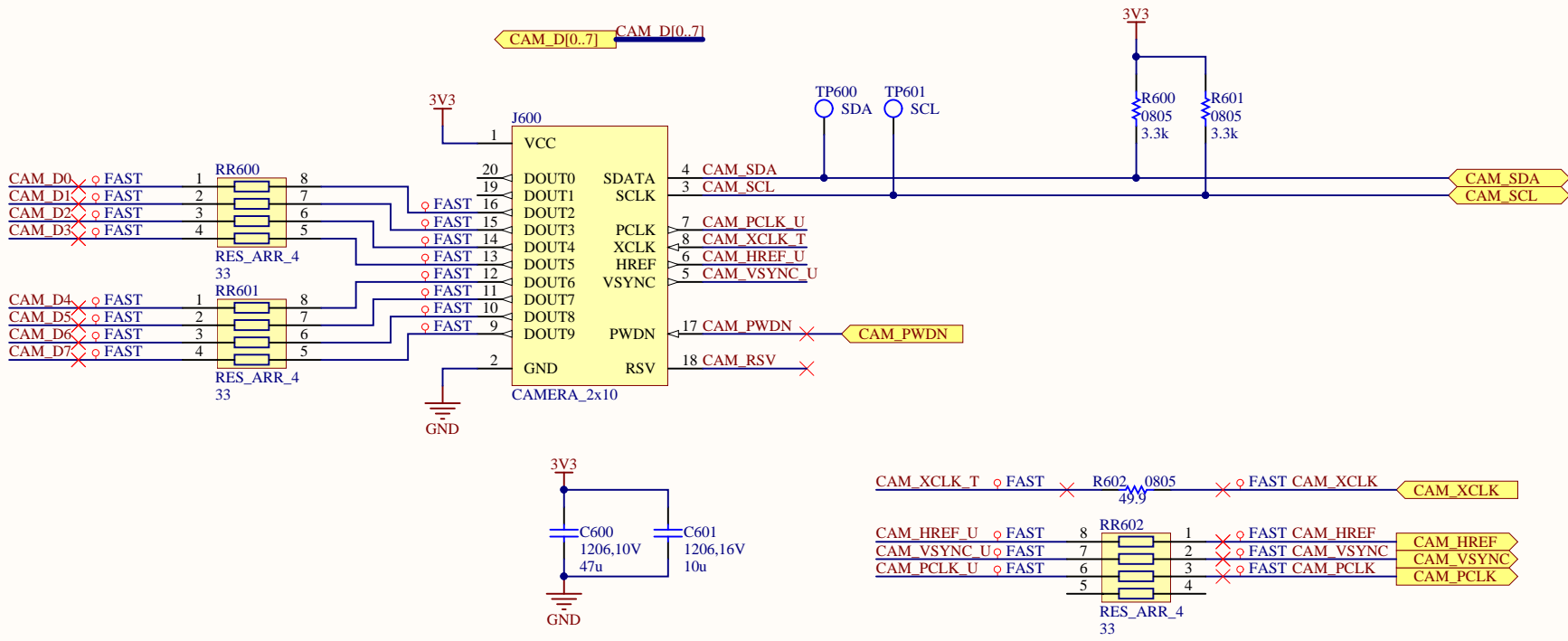


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Solens SDRAM		
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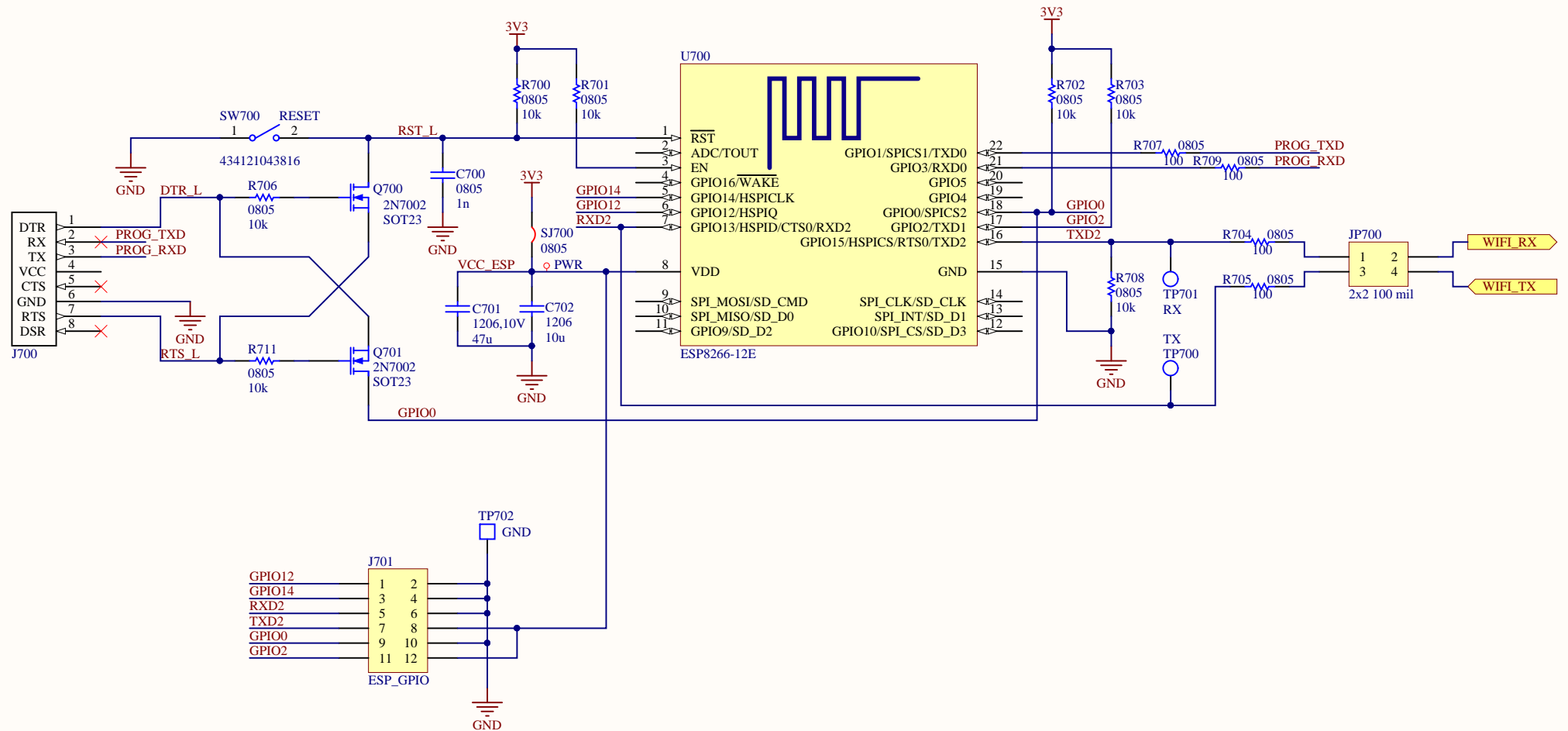




Title		
Solens Peripherals		
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Solens Camera Subsystem			
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Solens WiFi Subsystem		
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