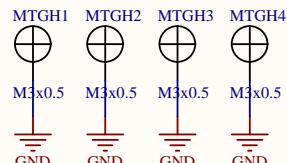
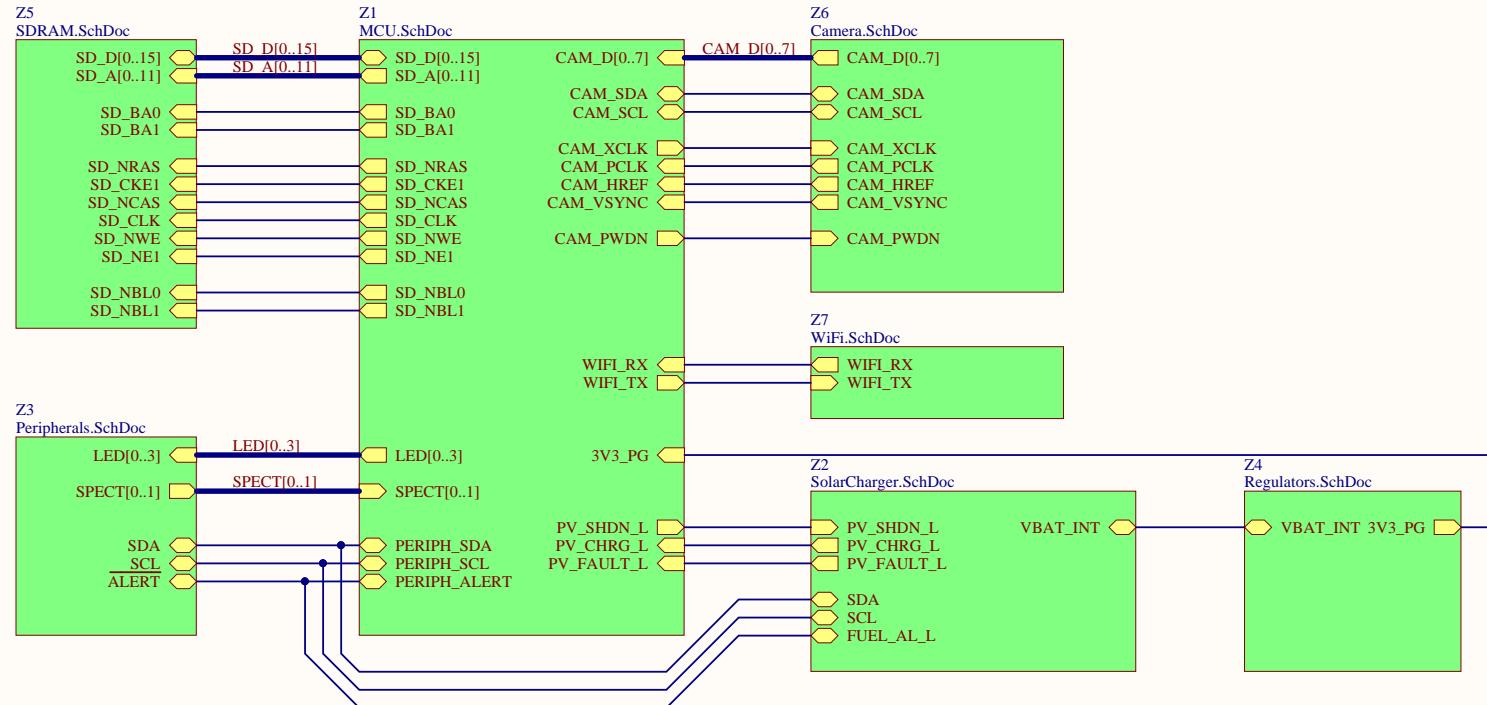


A

A



Title

Solens Camera Subsystem
ERC_WARN, FOOTPRINT_N/A

Size
A4

Number

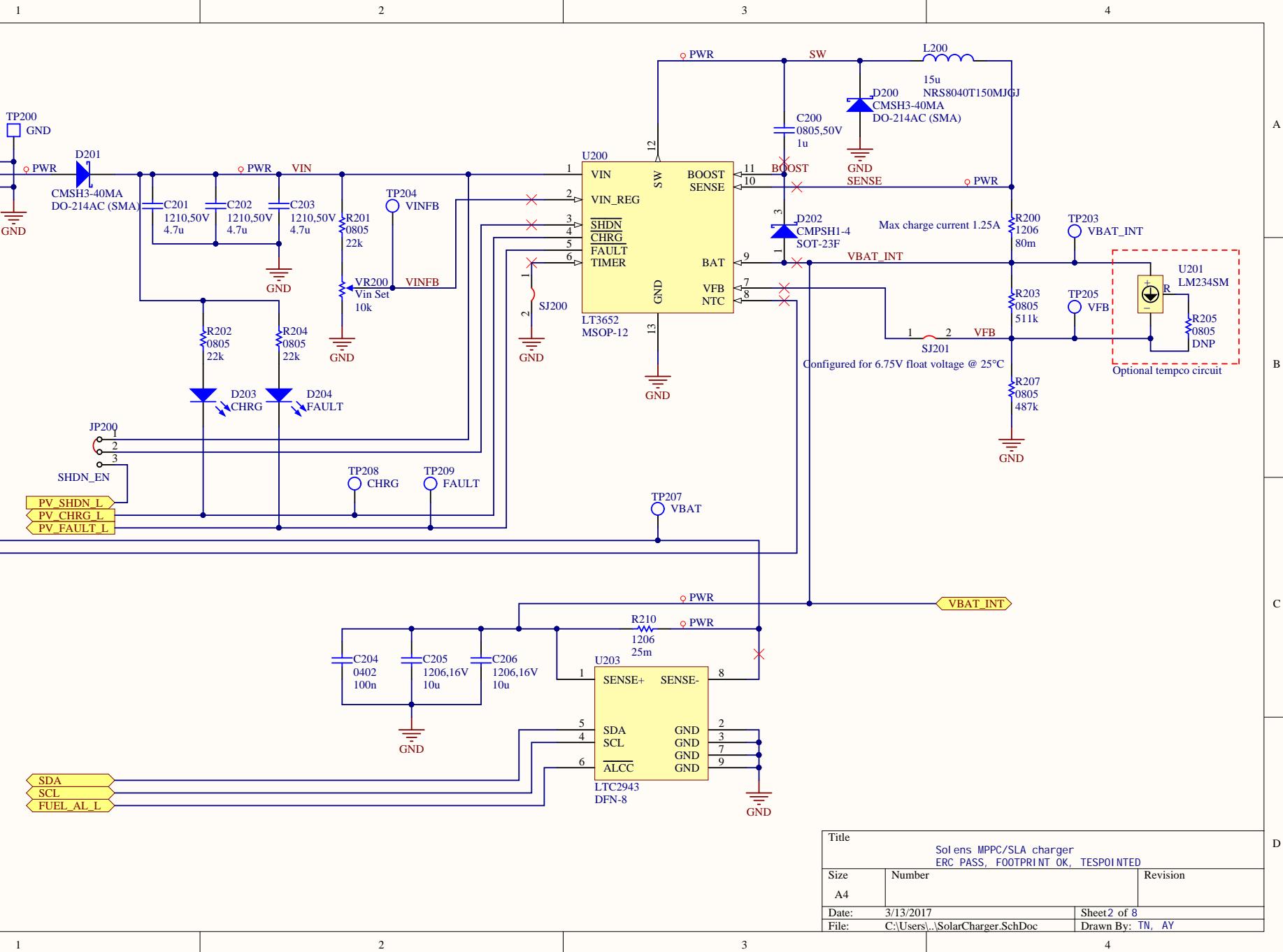
Revision

Date: 3/13/2017

Sheet 1 of 8

File: C:\Users\...\Solens.SchDoc

Drawn By: JL, AY



Title		
Solens MPPC/SLA charger		
ERC PASS, FOOTPRINT OK, TESPOINTED		
Size	Number	Revision
A4		
Date: 3/13/2017		Sheet 2 of 8
File: C:\Users..\SolarCharger.SchDoc		Drawn By: TN, AY

A

A

B

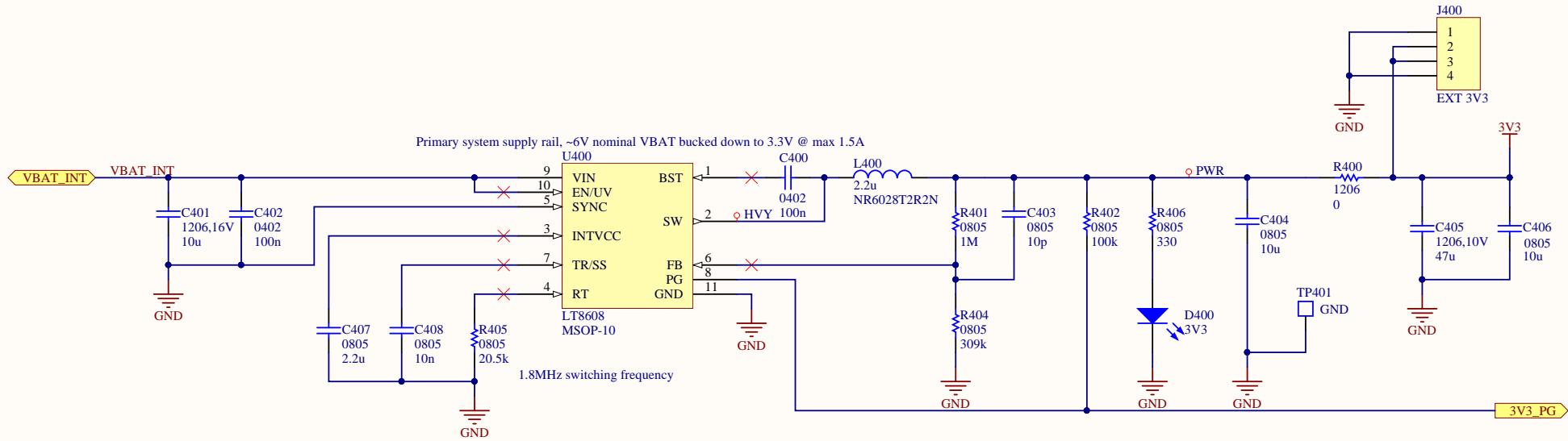
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C

C

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D



Title		
Solens Voltage Regulators		ERC PASS, FOOTPRINT OK, TESPOINTED
Size		Revision
A4		
Date:	3/13/2017	Sheet 3 of 8
File:	C:\Users...\Regulators.SchDoc	Drawn By: TN, AY

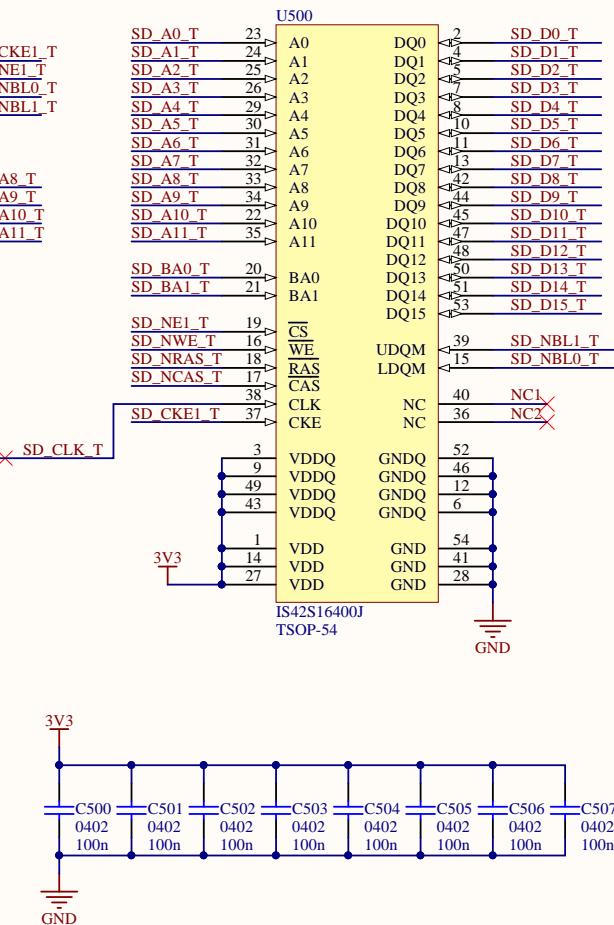
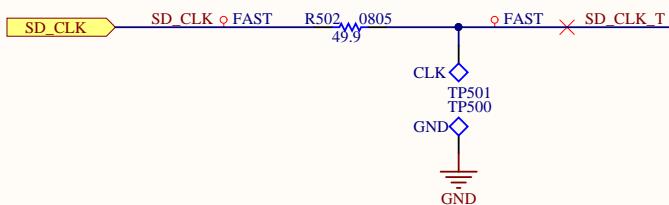
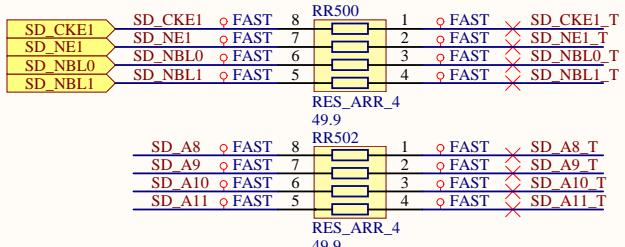
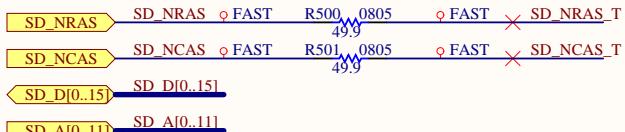
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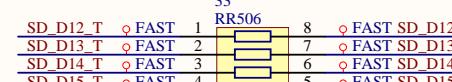
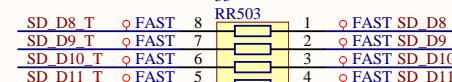
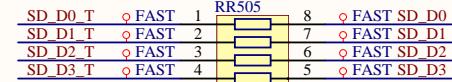
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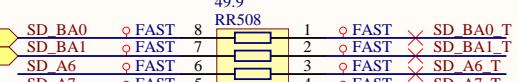
A



DATA termination to be placed halfway between MCU and RAM



ADDR and CLK termination to be placed at MCU



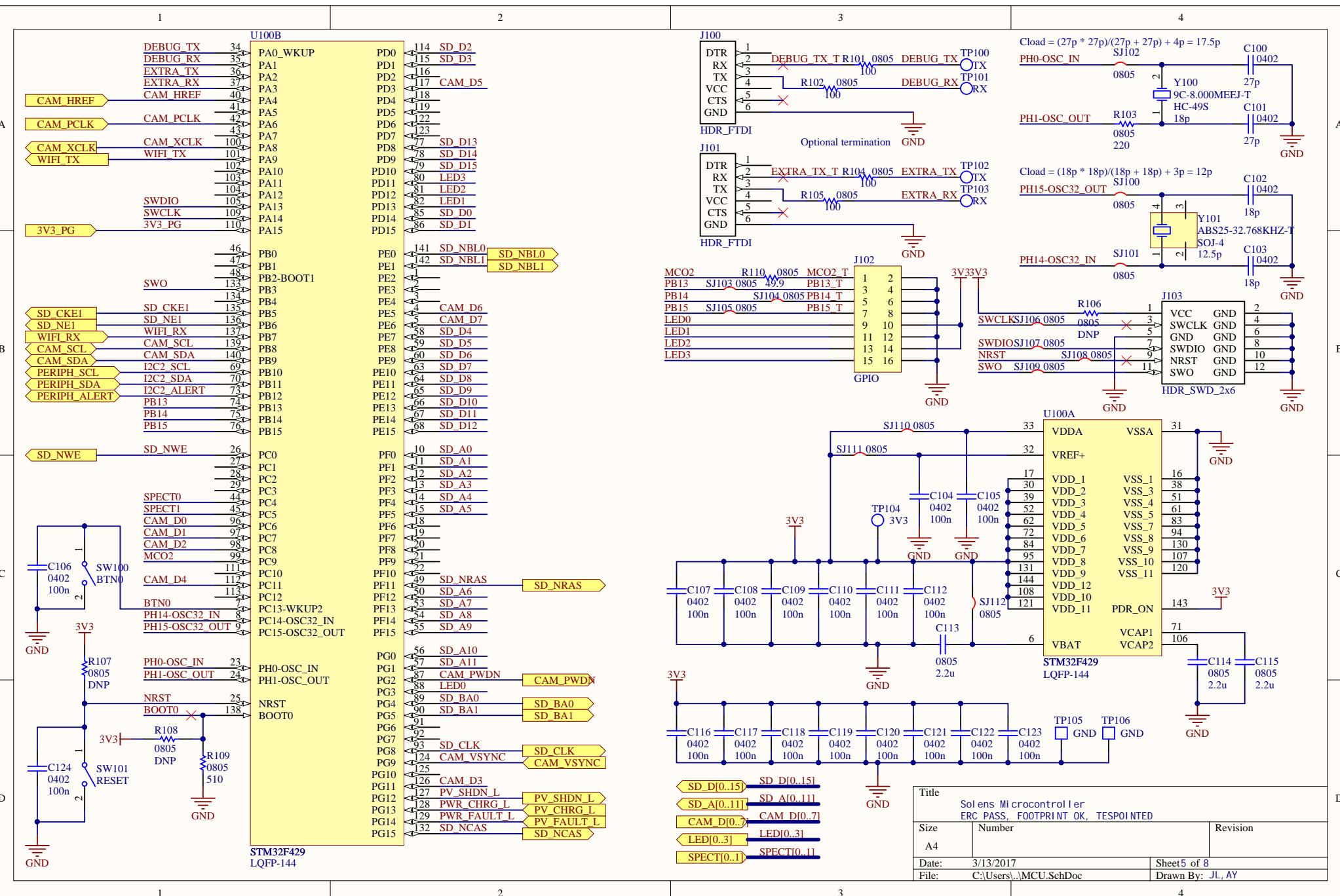
Title		
Solens_SDRAM		
Size	Number	Revision
A4		
Date:	3/13/2017	Sheet 4 of 8
File:	C:\Users\...\SDRAM.SchDoc	Drawn By: JL, AY

1

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3

4



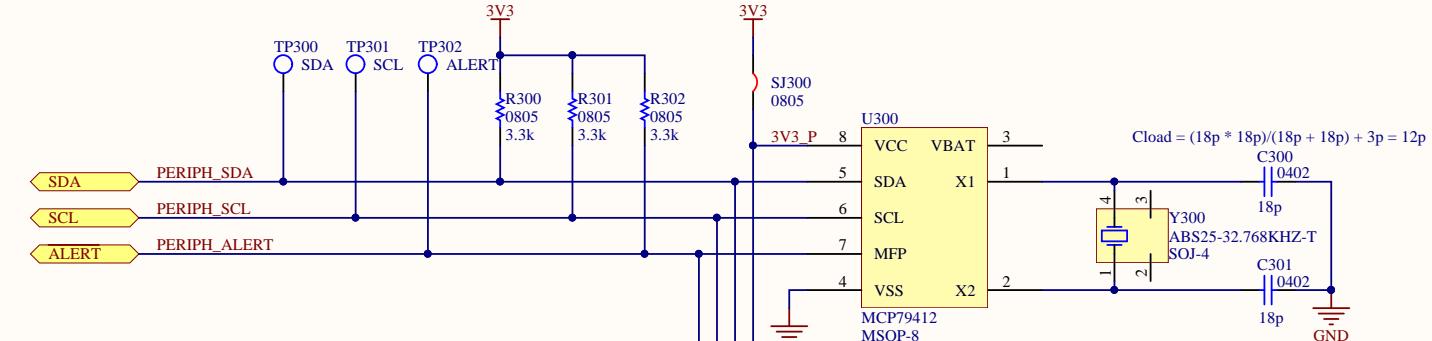
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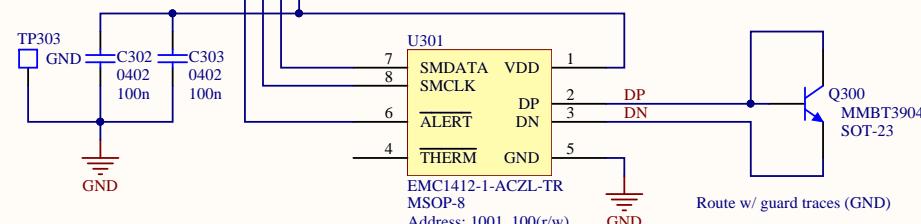
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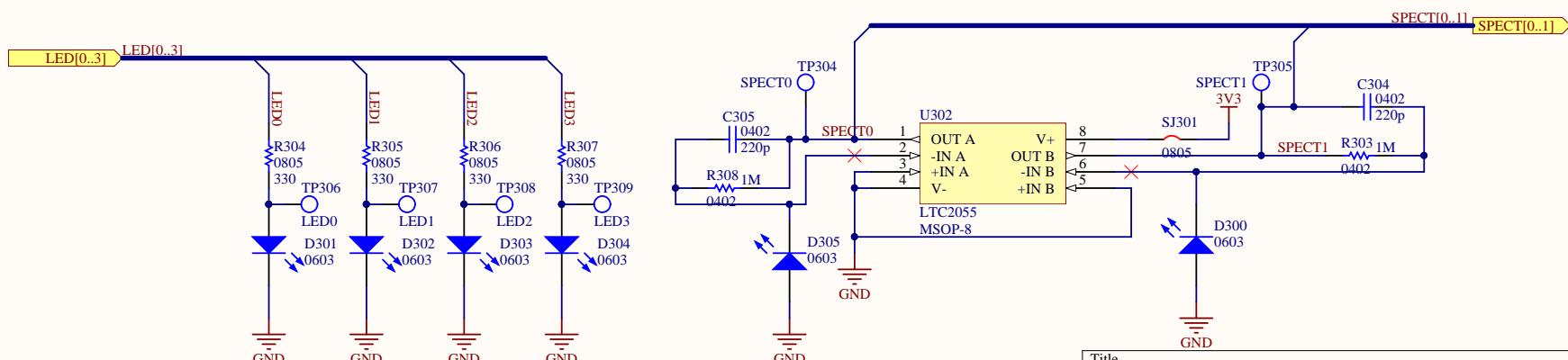
A



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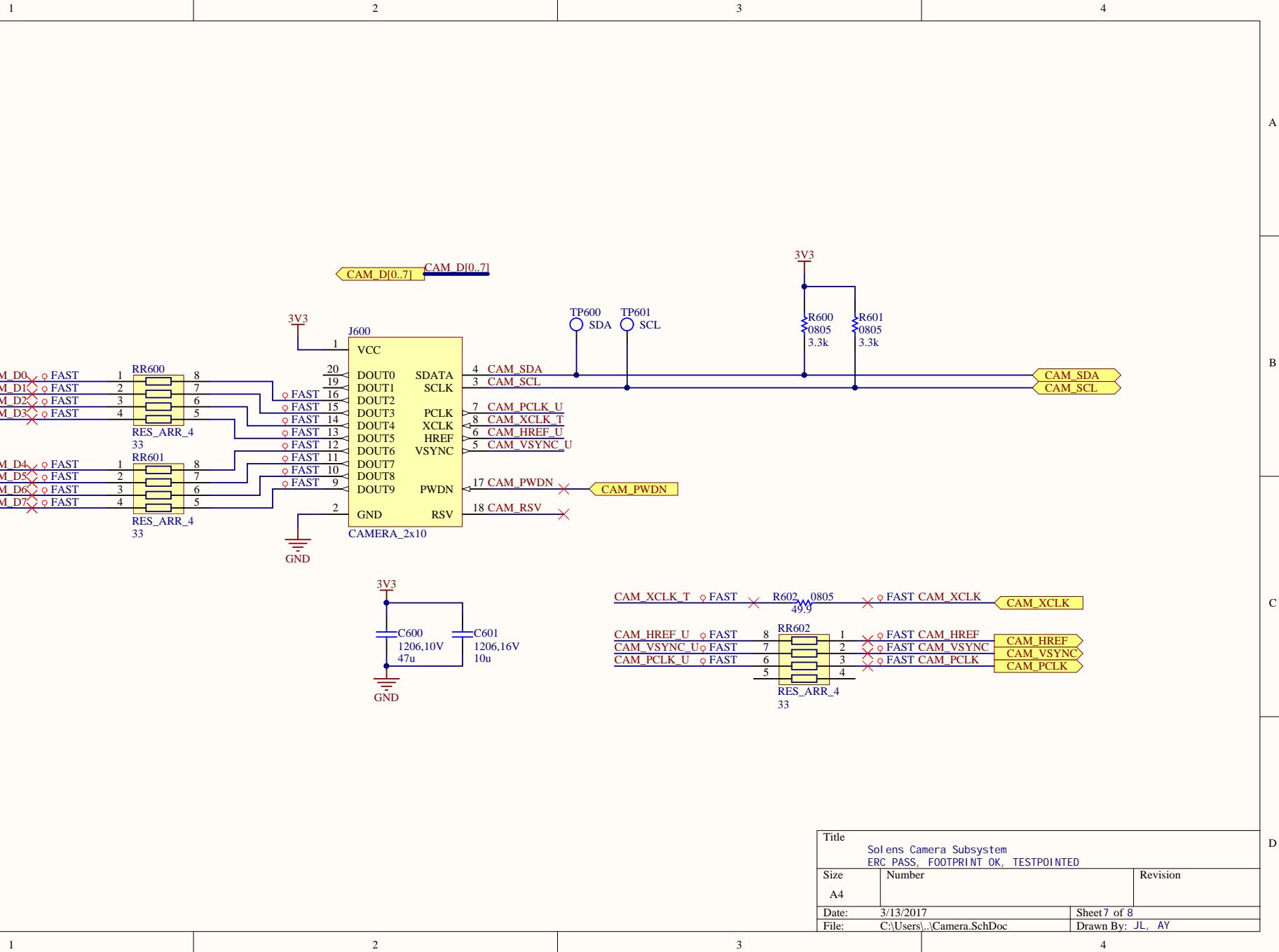
Title		
Solens Peripherals ERC PASS, FOOTPRINT OK, TESPOINTED		
Size	Number	Revision
A4		
Date:	3/13/2017	Sheet 6 of 8
File:	C:\Users\...\Peripherals.SchDoc	Drawn By: AY

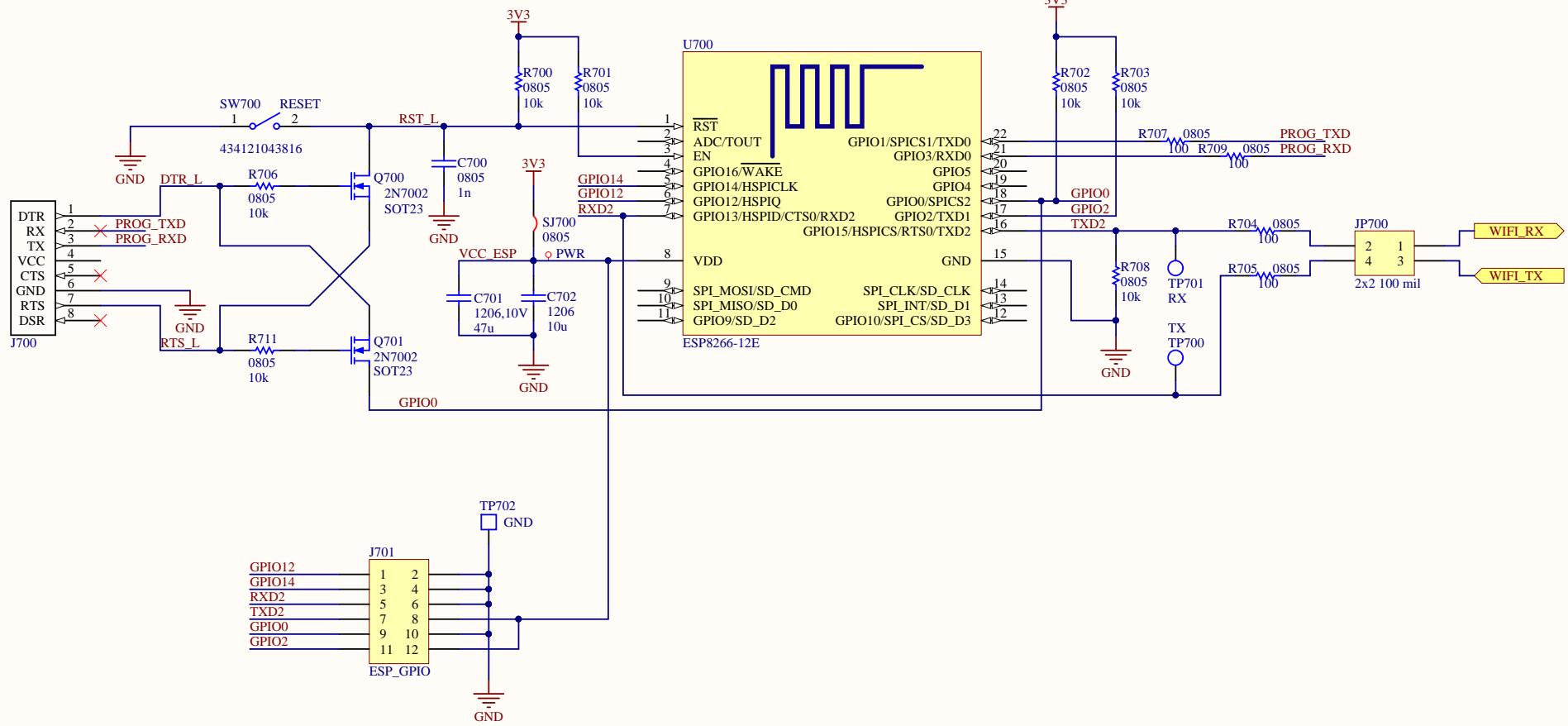
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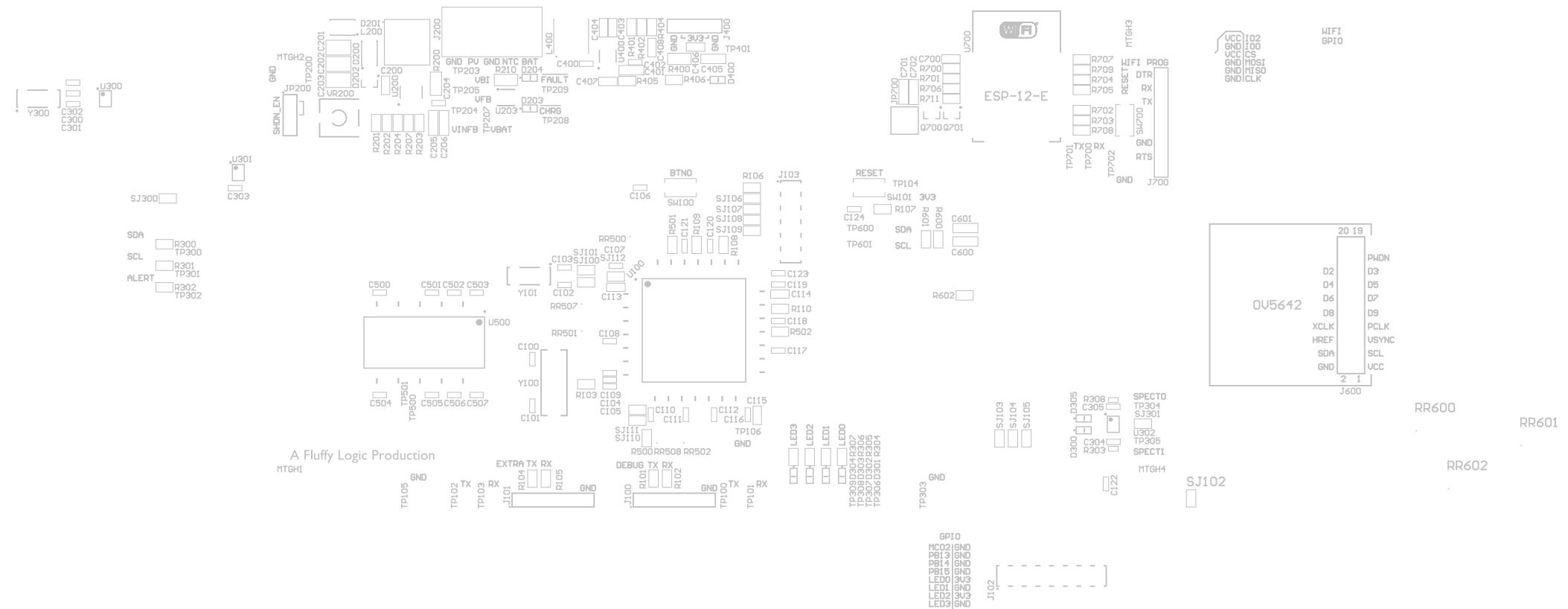
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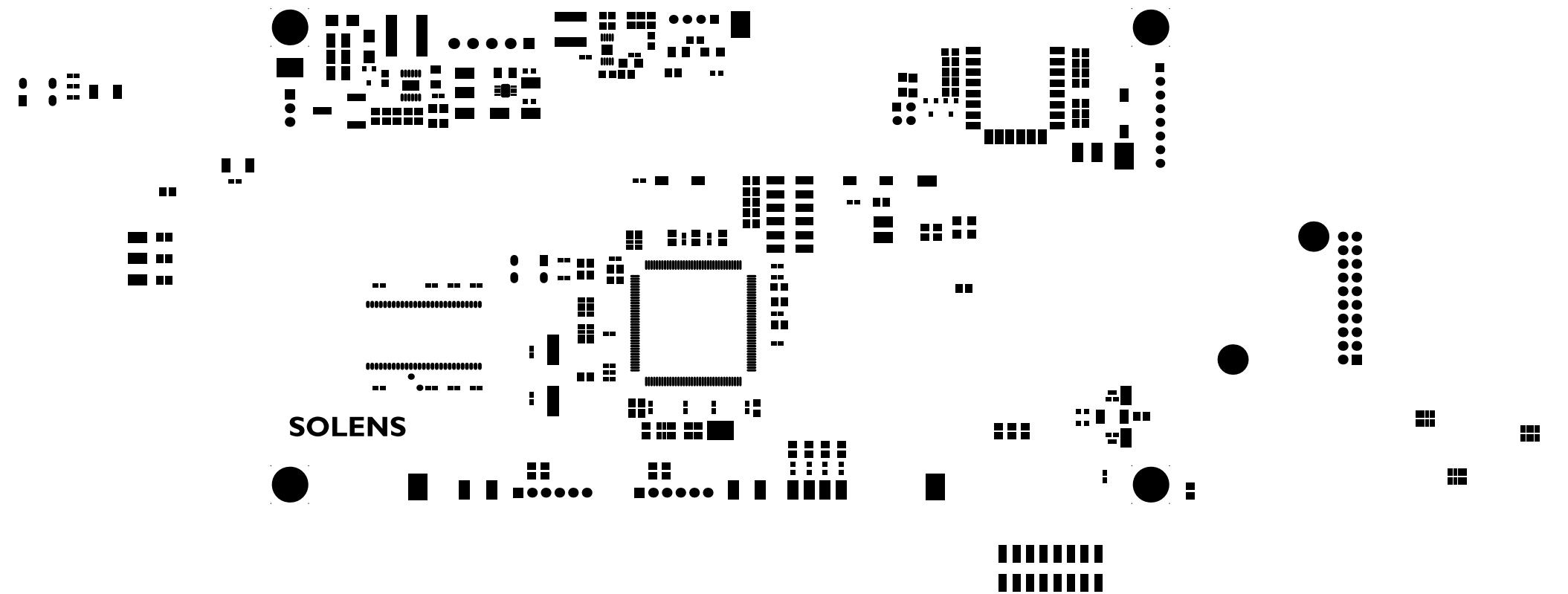


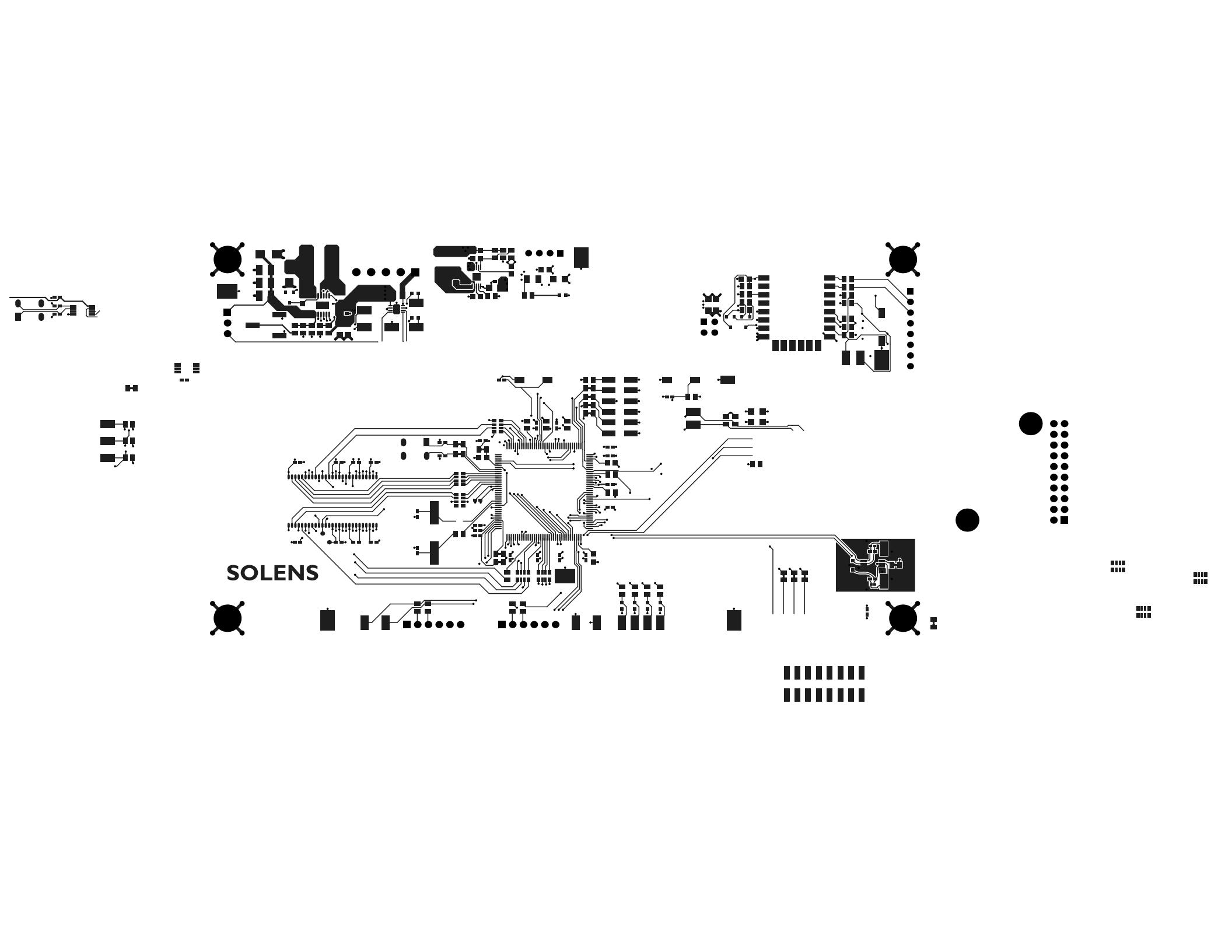


Title		
Solens WiFi Subsystem		
ERC PASS, FOOTPRINT OK, TESTPOINTED		
Size		Revision
A4		
Date: 3/13/2017	Sheet 8 of 8	
File: C:\Users\...\WiFi.SchDoc	Drawn By: JL, AY	

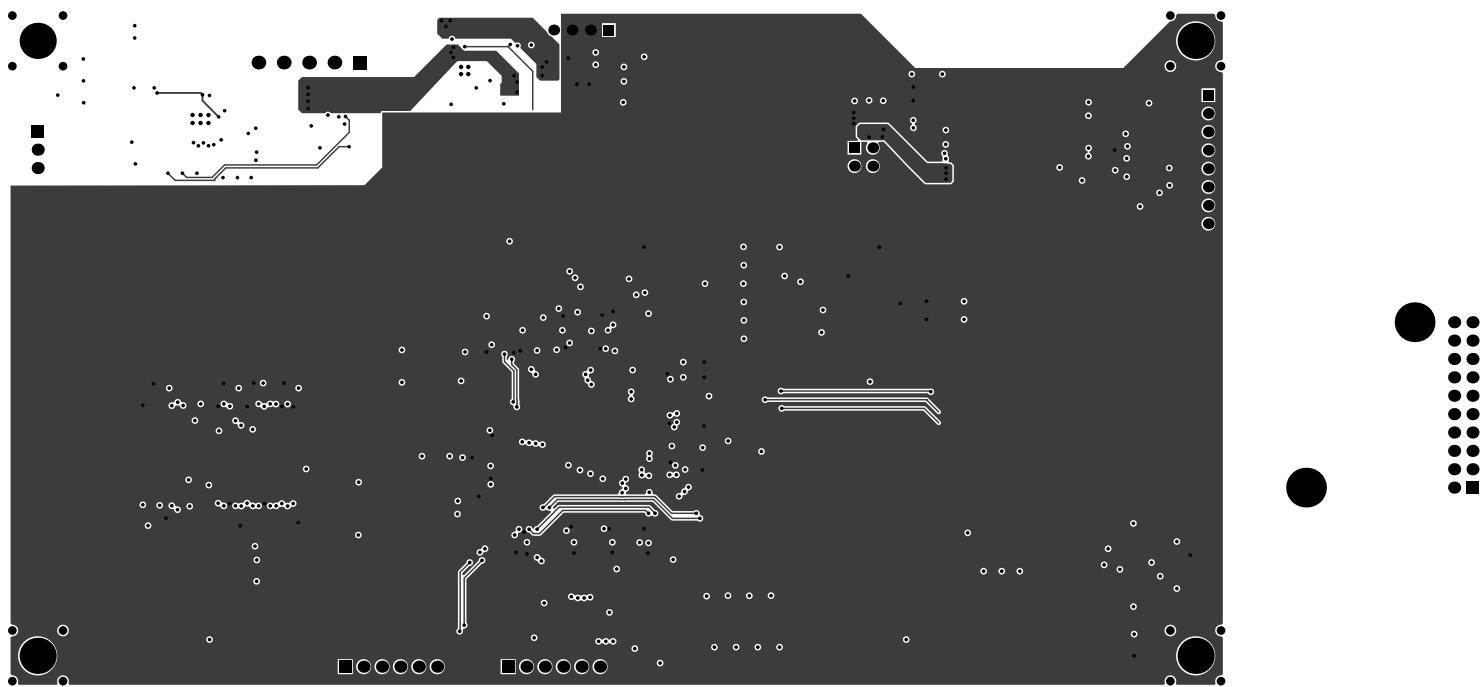


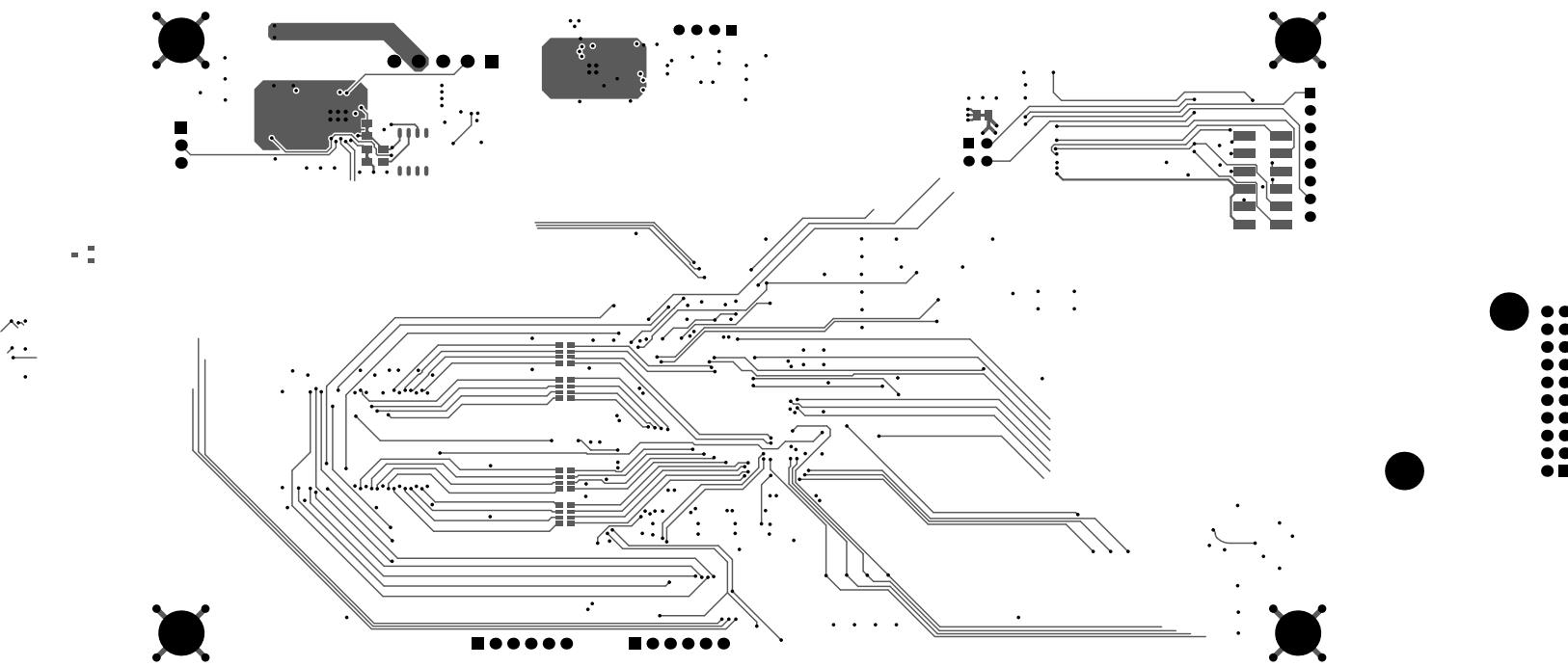
SOLENS

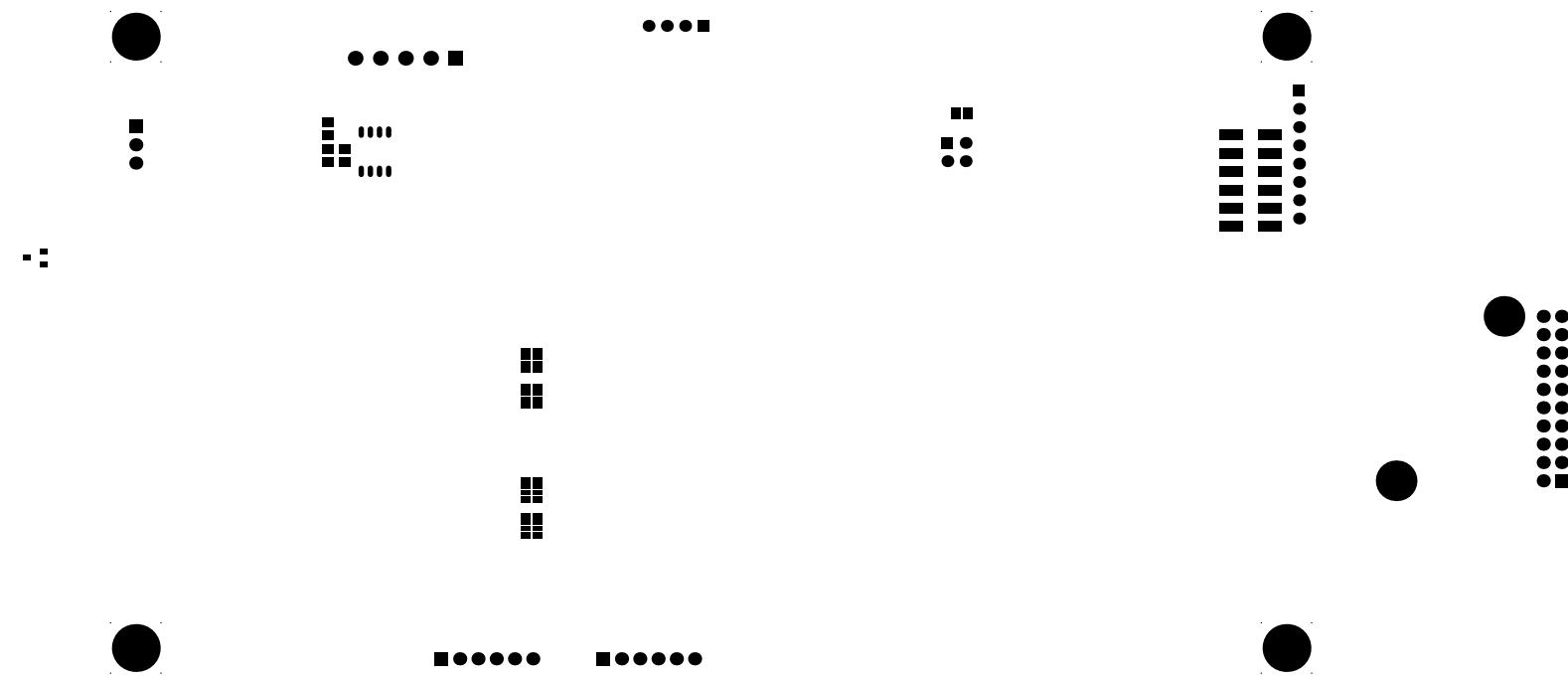




SOLENS









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0300

21201
21202
21203
21204
21205

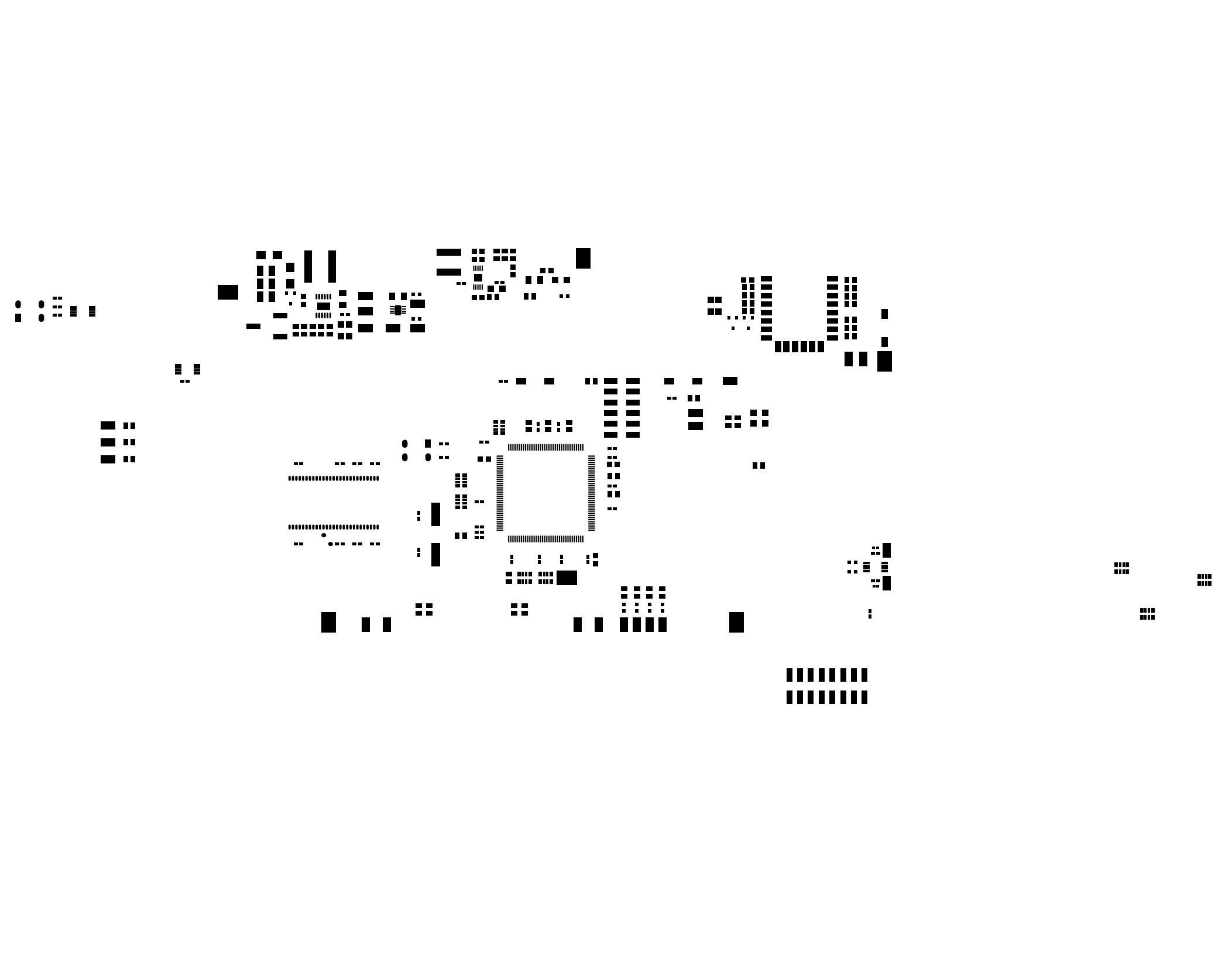
RR202

RR204

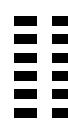
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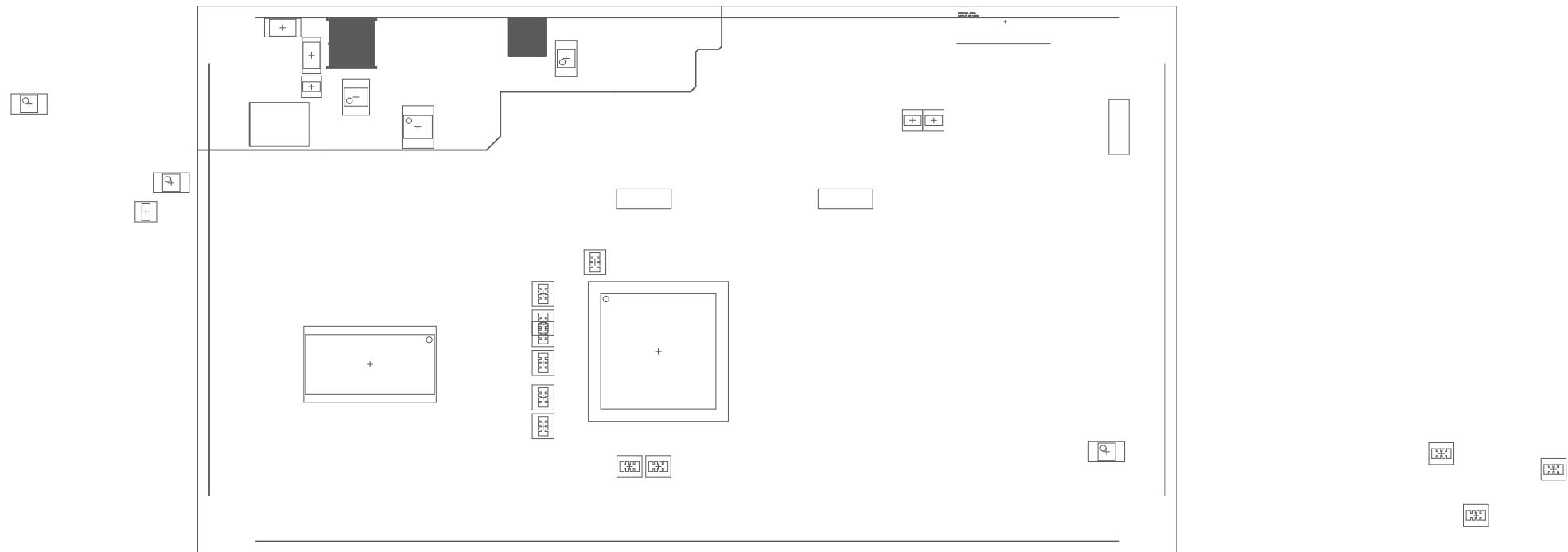
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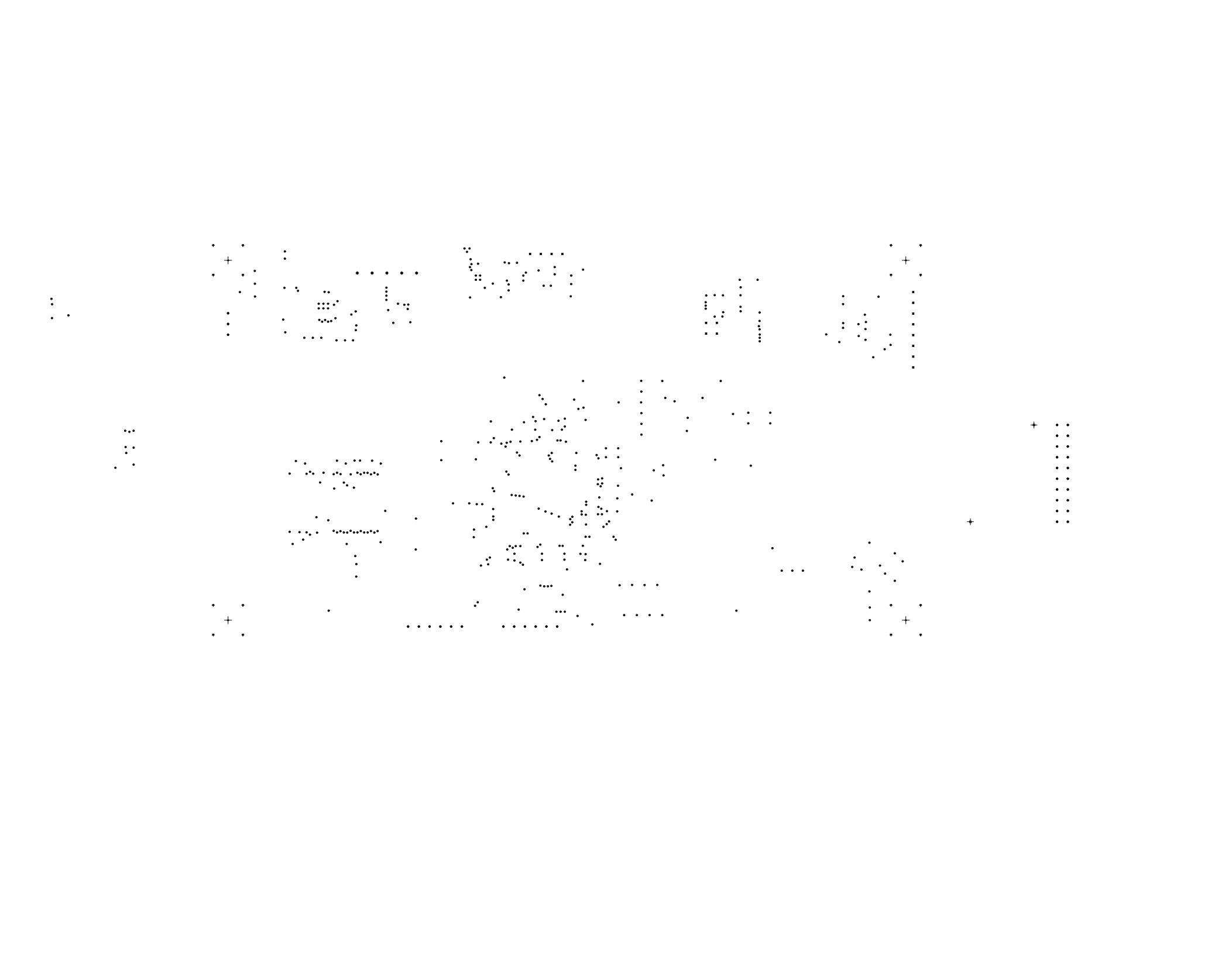
AKIRA Youngblood
JEREMY Rapp
TAYLOR Neuyuen
JEFFERY Tim
Ben Heberlein

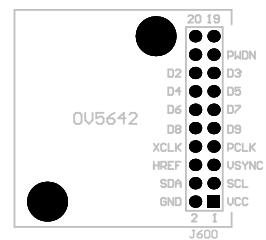
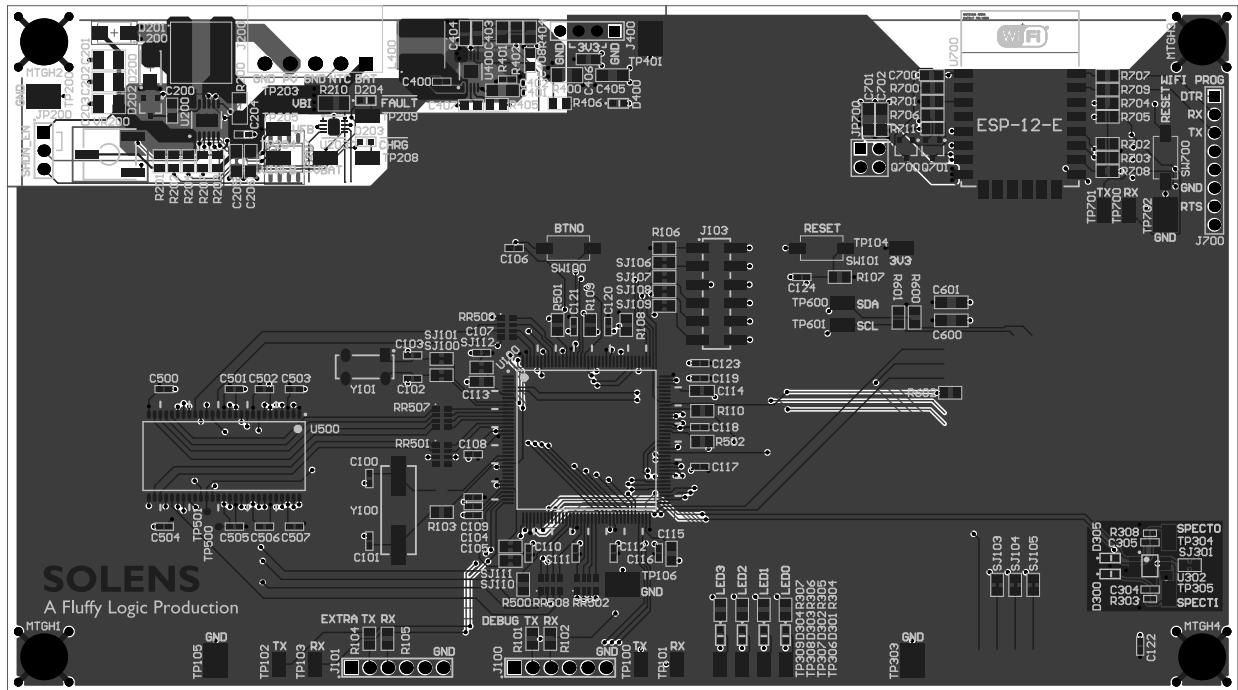


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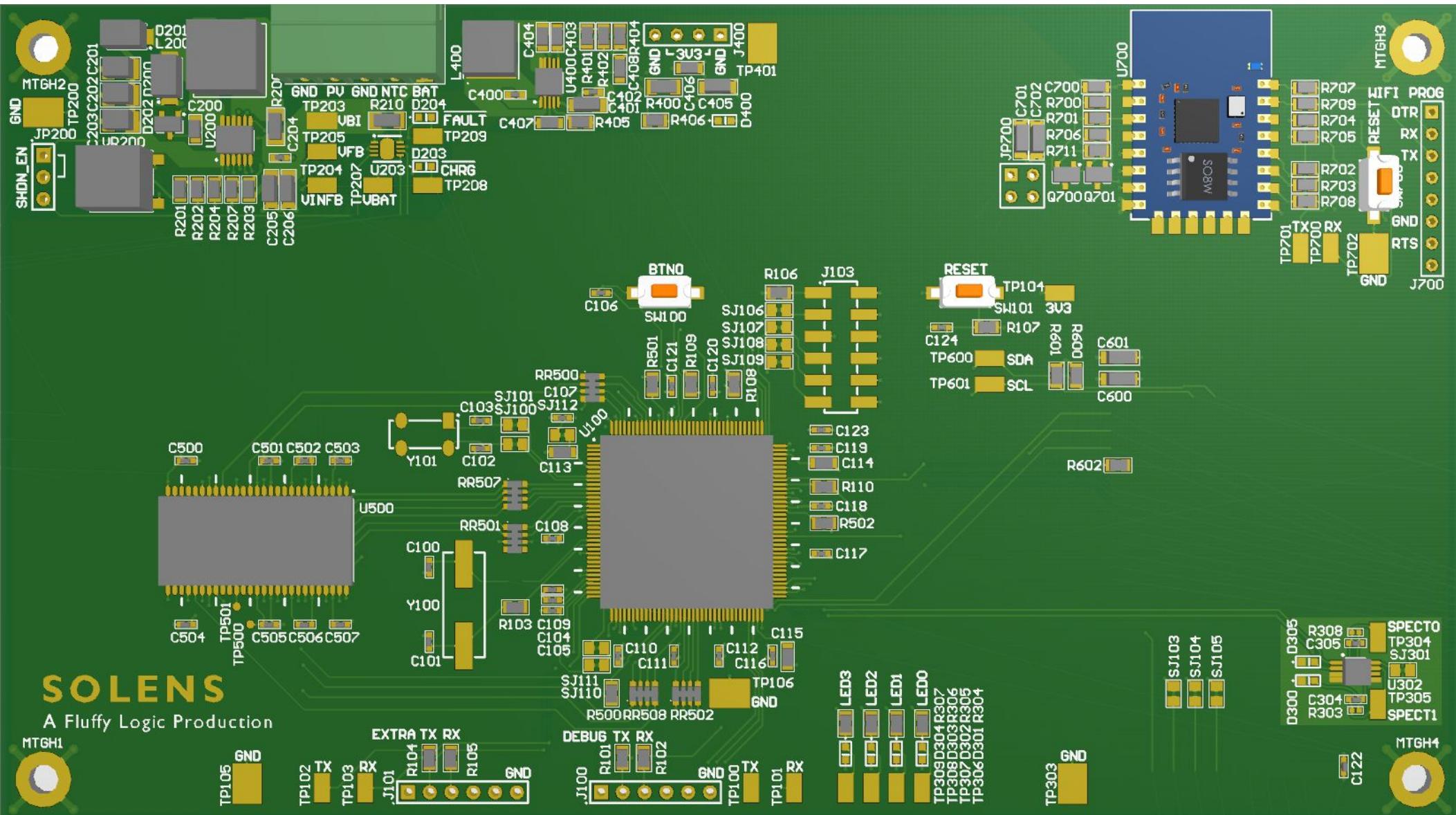


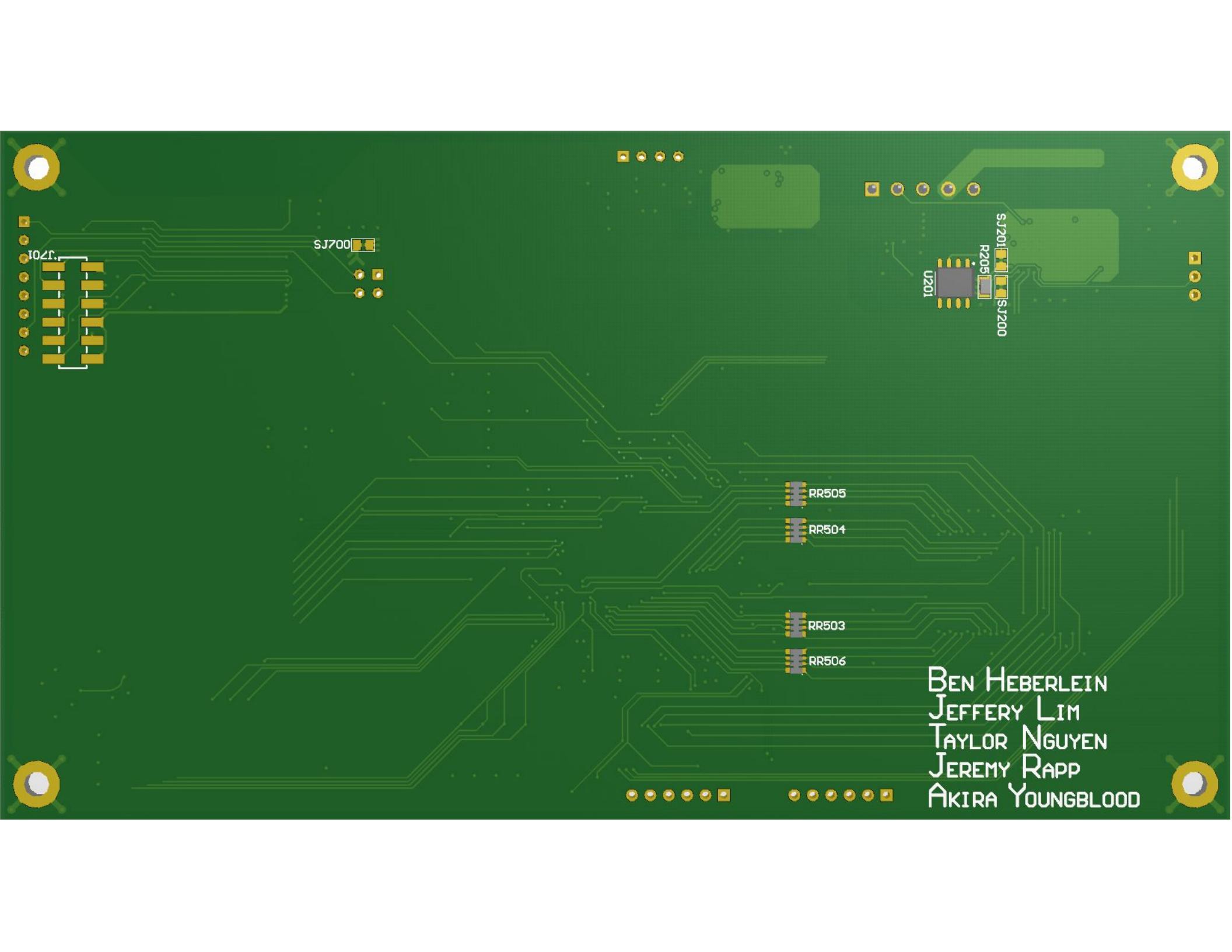


RR600

R602







BEN HEBERLEIN
JEFFERY LIM
TAYLOR NGUYEN
JEREMY RAPP
AKIRA YOUNGBLOOD