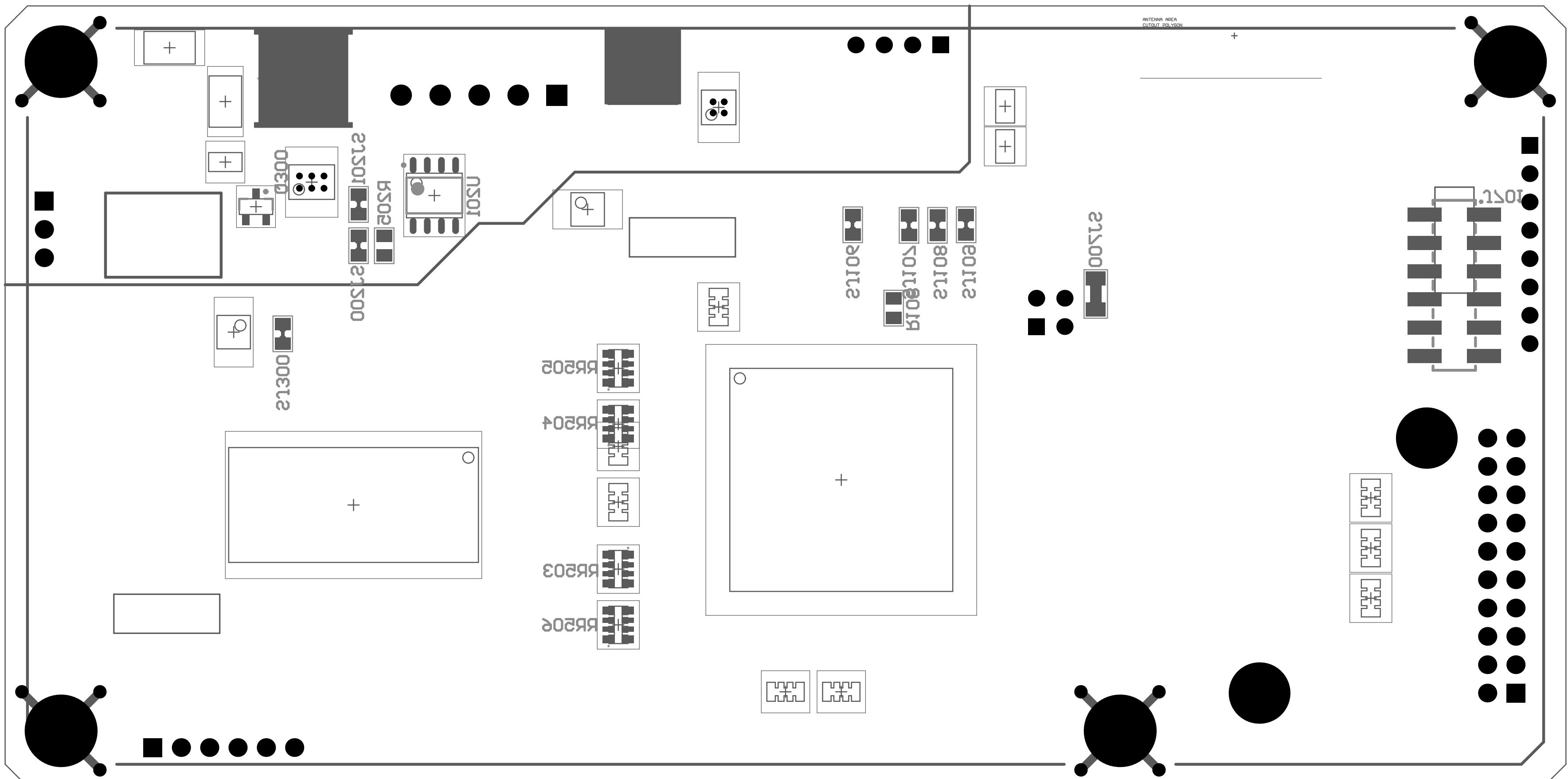


A Fluffy Logic Production

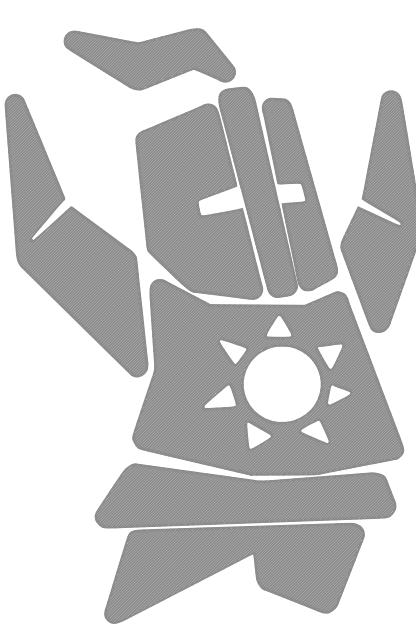
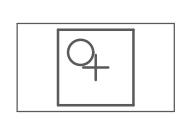
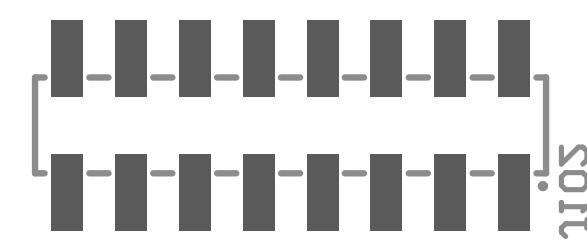
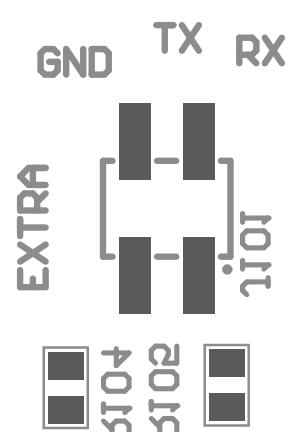
The schematic diagram illustrates the physical layout of components on a printed circuit board. Key components include a central integrated circuit labeled 'TP106' with a bounding box of [150, 250, 350, 350], a ground connection 'GND' at [300, 60, 400, 160], and three resistors labeled 'R304' [250, 780, 350, 880], 'D301' [150, 600, 250, 700], and 'TP306' [150, 550, 250, 650]. A large black rectangle [450, 10, 650, 150] likely represents a metal shield or ground plane.

GPIO	
MC02	GND
PB13	GND
PB14	GND
PB15	GND
LED0	3V3
LED1	GND
LED2	3V3
LED3	GND

The image displays a grid of 12 different SPECTO logo designs. The top row contains the main SPECTO logo (black rectangle with white text) and a smaller version of it. The middle row contains the TP304 logo (black rectangle with white text) and the SJ301 logo (black rectangle with white text). The bottom row contains the R308 logo (black rectangle with white text), the C305 logo (black rectangle with white text), the U302 logo (black rectangle with white text), the TP305 logo (black rectangle with white text), the C304 logo (black rectangle with white text), and the R303 logo (black rectangle with white text). There are also several smaller, semi-transparent versions of the logos scattered throughout the grid.

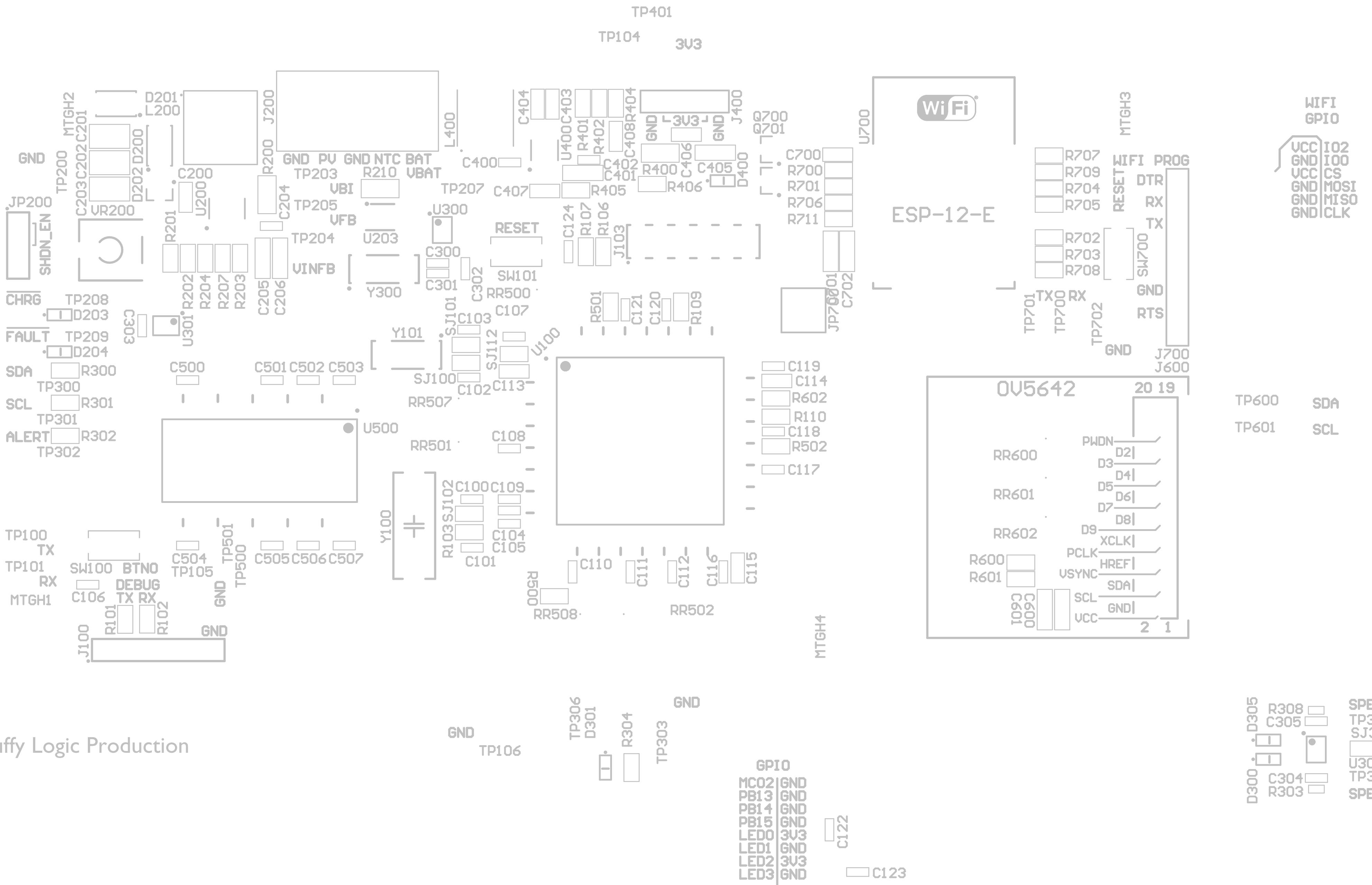


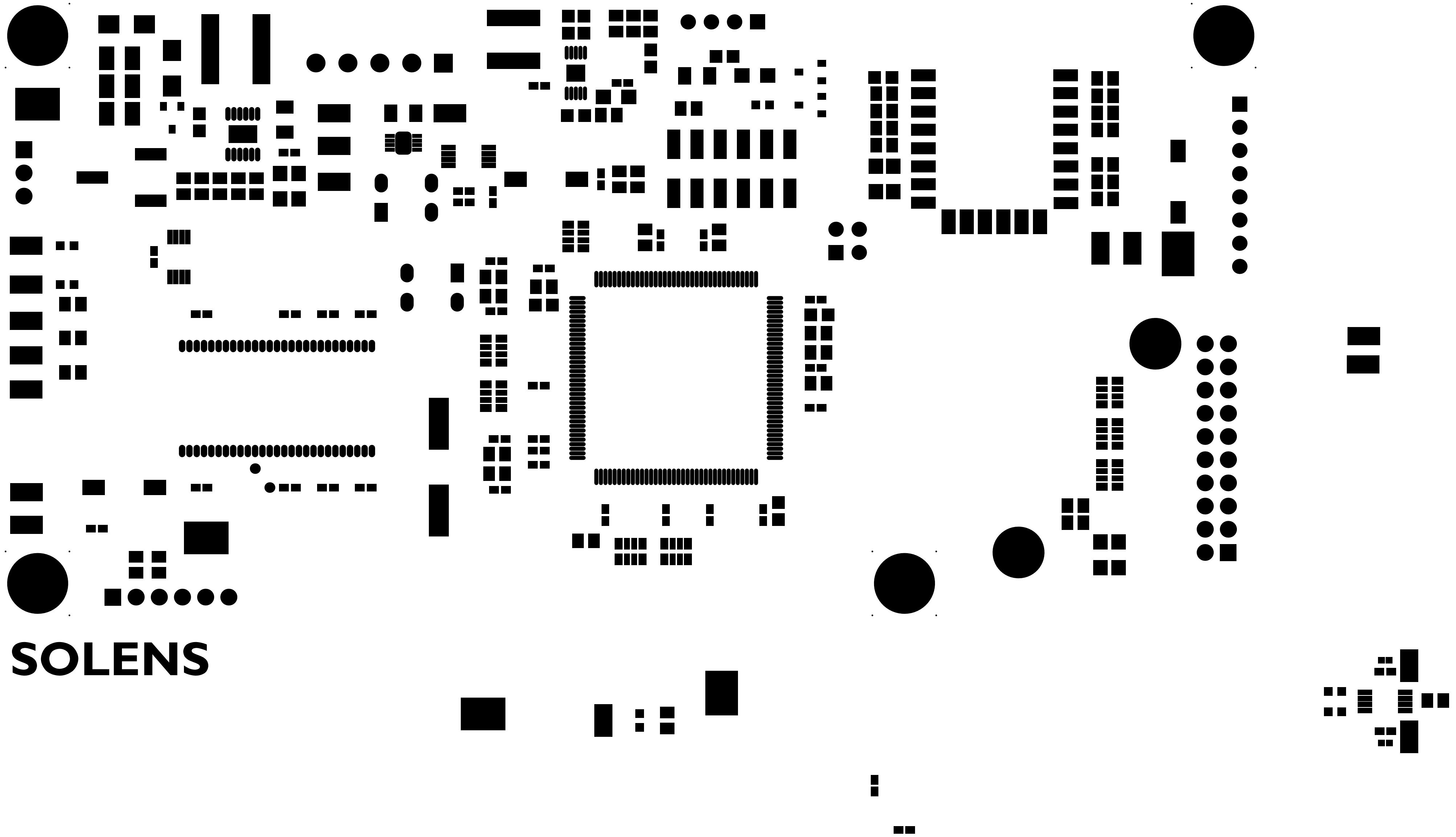
BEN HEBERLEIN
JEFFERY LIM
TAYLOR NGUYEN
JEREMY RAPP
AKIRA YOUNGBLOOD



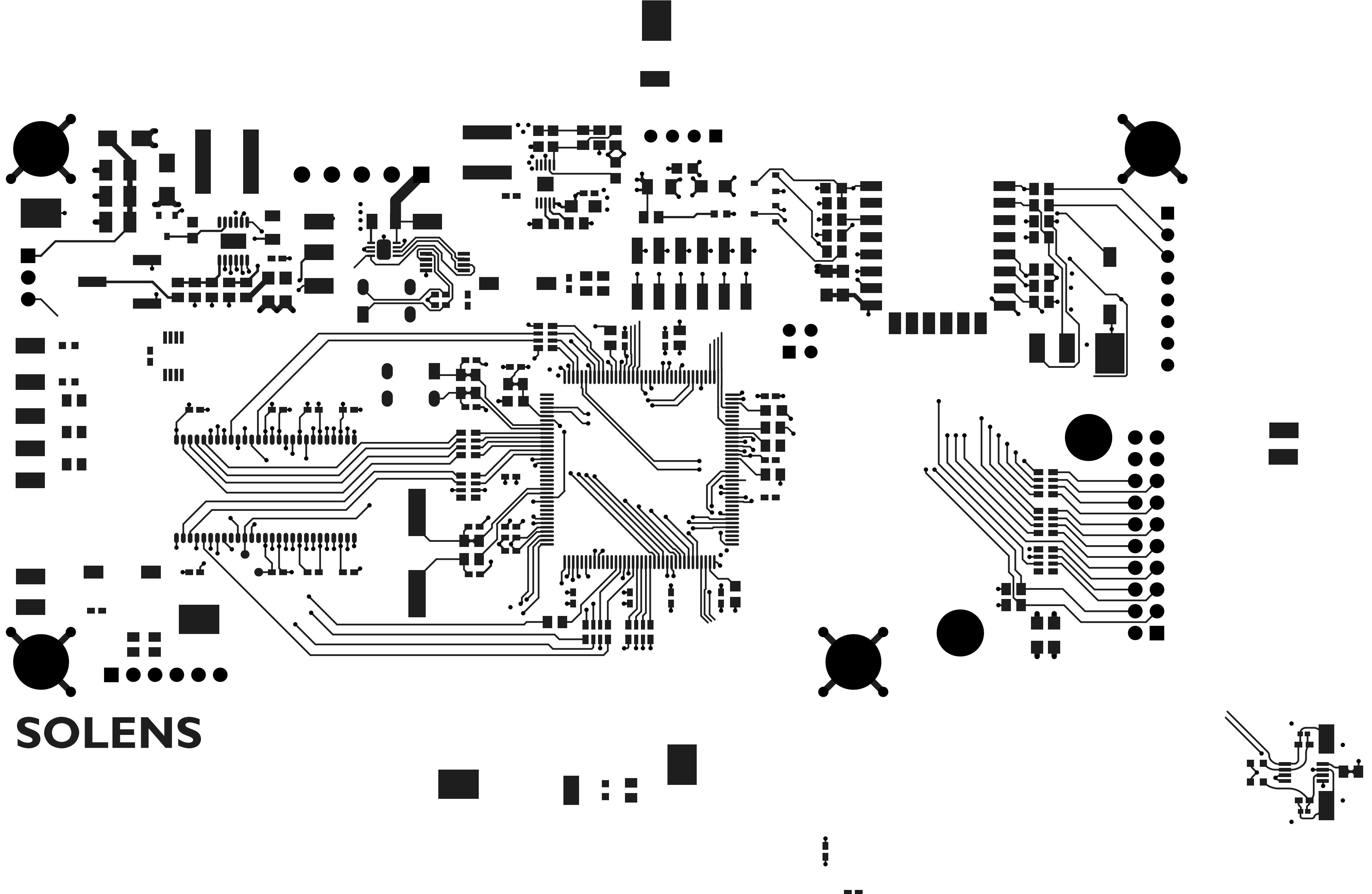
Board Stack Report

Stack Up		Layer Stack			
Layer	Board Layer Stack	Name	Material	Thickness	Constant
1		Top Paste			
2		Top Overlay			
3		Top Solder	Solder Resist	0.40mil	3.5
4		Top Layer	Copper	1.37mil	
5		Dielectric 1	2x2116	9.80mil	4.2
6		Signal Layer 2	Copper	1.37mil	
7		Dielectric 4		40.00mil	4.2
8		Signal Layer 1	Copper	1.37mil	
9		Dielectric 3	2x2116	9.80mil	4.2
10		Bottom Layer	Copper	1.37mil	
11		Bottom Solder	Solder Resist	0.40mil	3.5
12		Bottom Overlay			
13		Bottom Paste			
Height : 65.88mil					





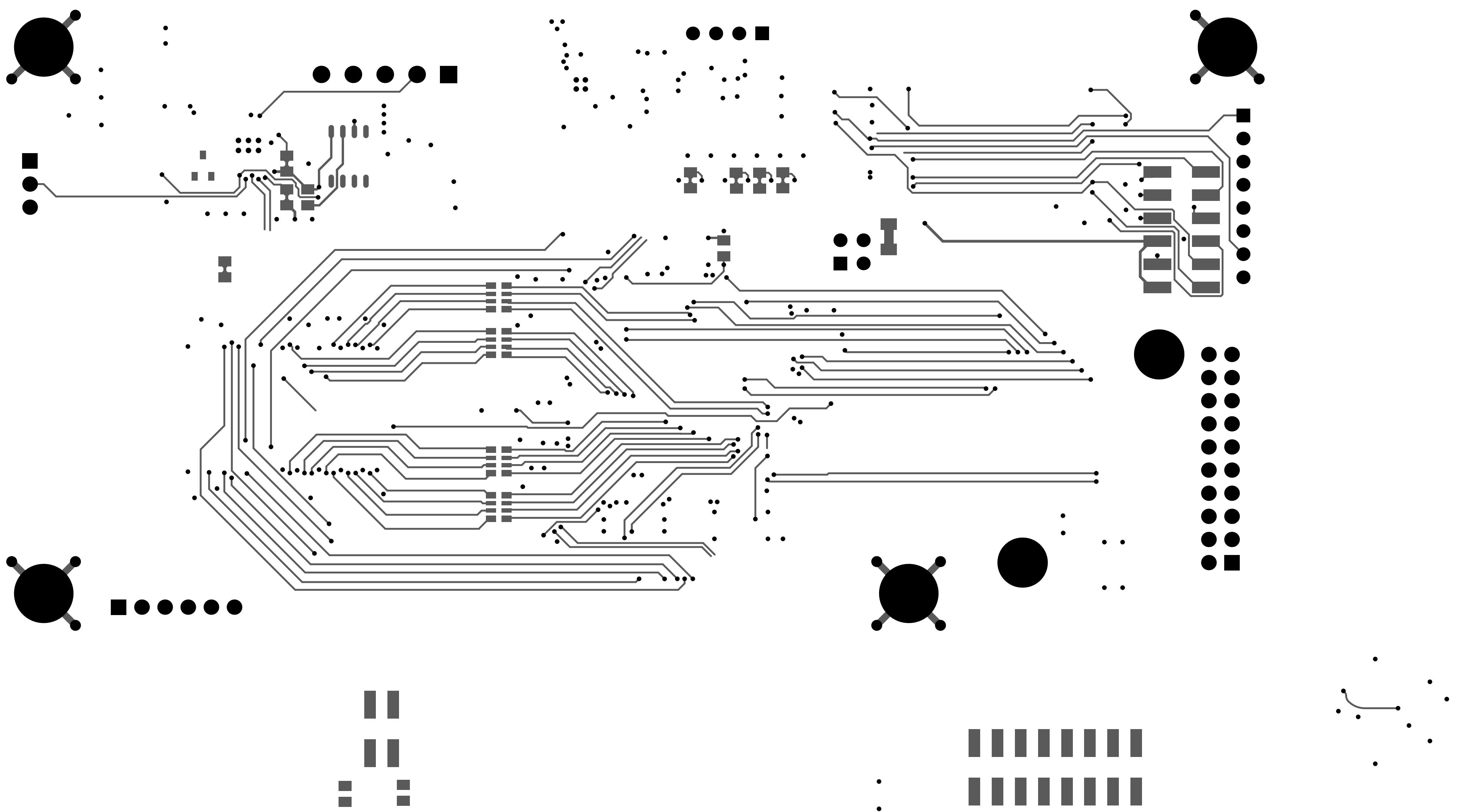
SOLENS

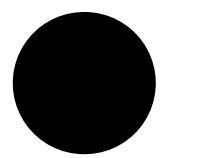
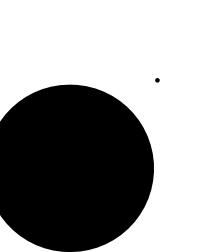
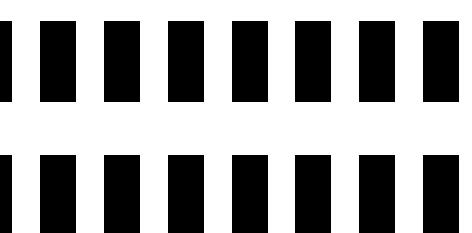
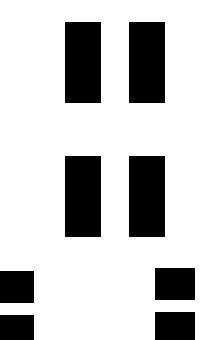
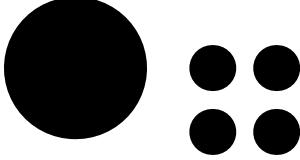
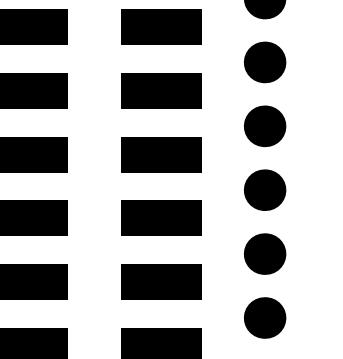
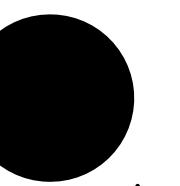
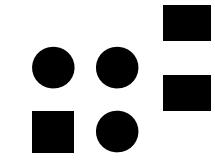
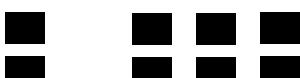
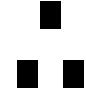
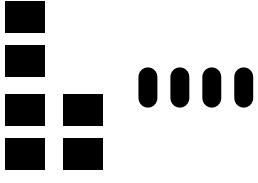
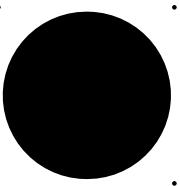
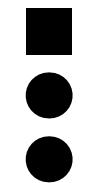
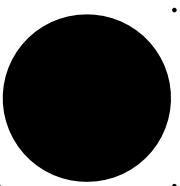


SOLENS







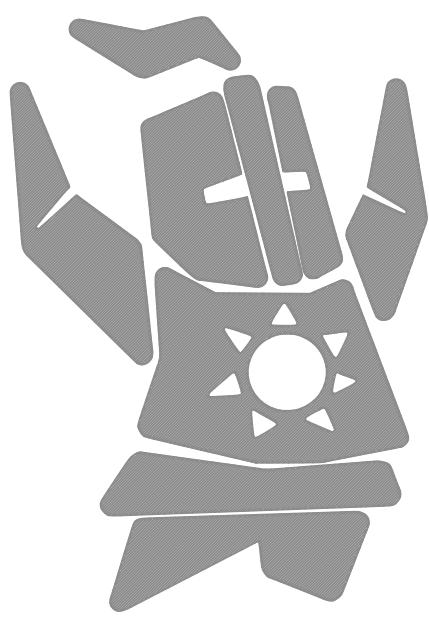


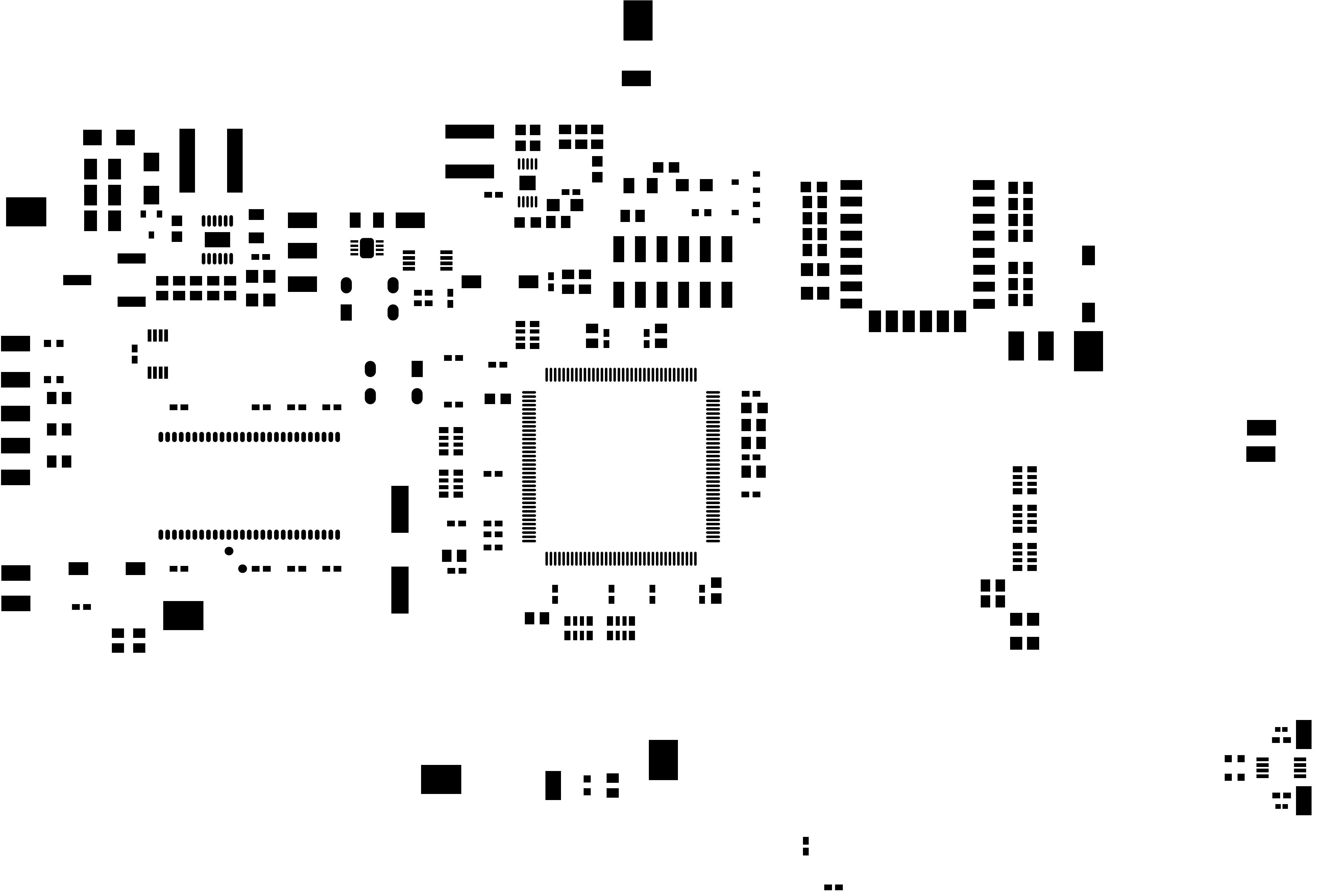
AKIRA Youngblood JEREMY RAPP TAYLOR Nguyen JEFFERY LIM Ben Hebrewlein

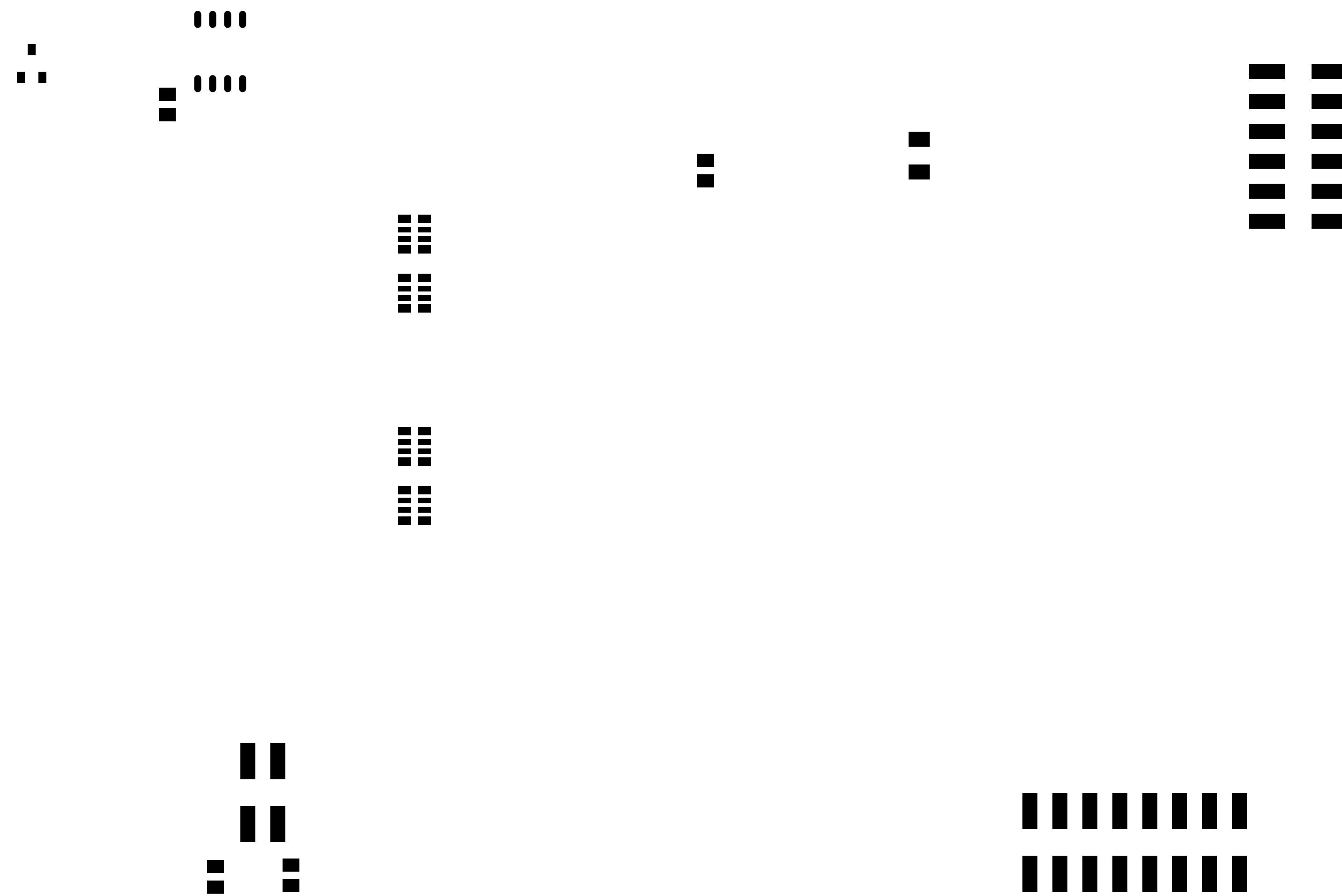
The diagram illustrates a coaxial cable assembly. At the top, four labels are positioned: 'GND' on the left, 'TX' in the center, 'RX' on the right, and 'EXTRA' on the far left. Below these labels, a central vertical line represents the coaxial axis. From this axis, two horizontal lines extend downwards to form a bracket. The left end of this bracket is labeled 'GND'. The right end is labeled 'TX' above 'RX'. Between the 'TX' and 'RX' labels, there is a small gap. To the left of the 'EXTRA' label, another horizontal line extends downwards from the coaxial axis, ending in a rectangular box. To the right of the 'RX' label, another horizontal line extends downwards from the coaxial axis, ending in a rectangular box. These two boxes represent BNC connectors for the TX and RX signals.

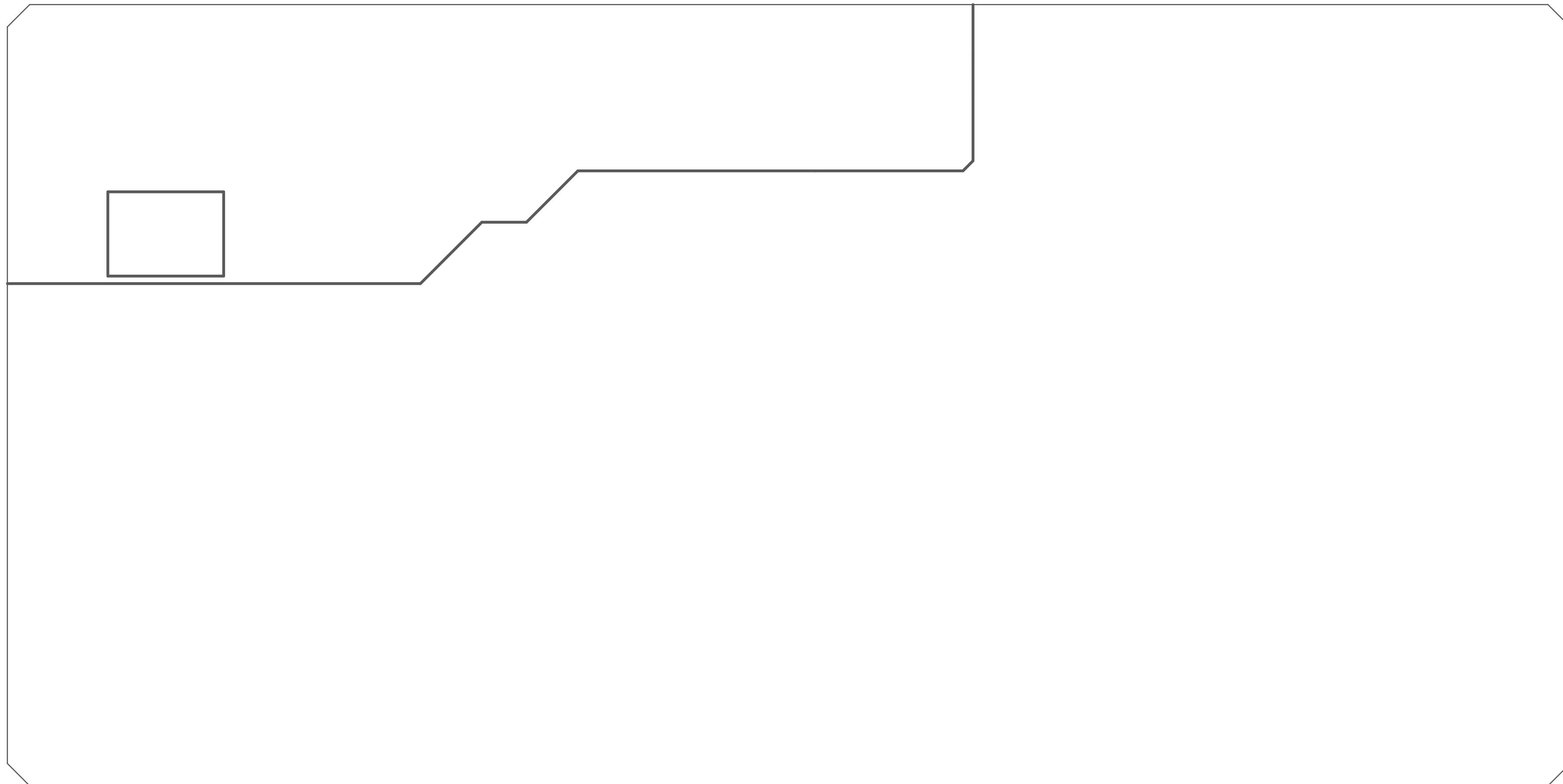
RR503
RR504
RR505
RR506

21300 0300

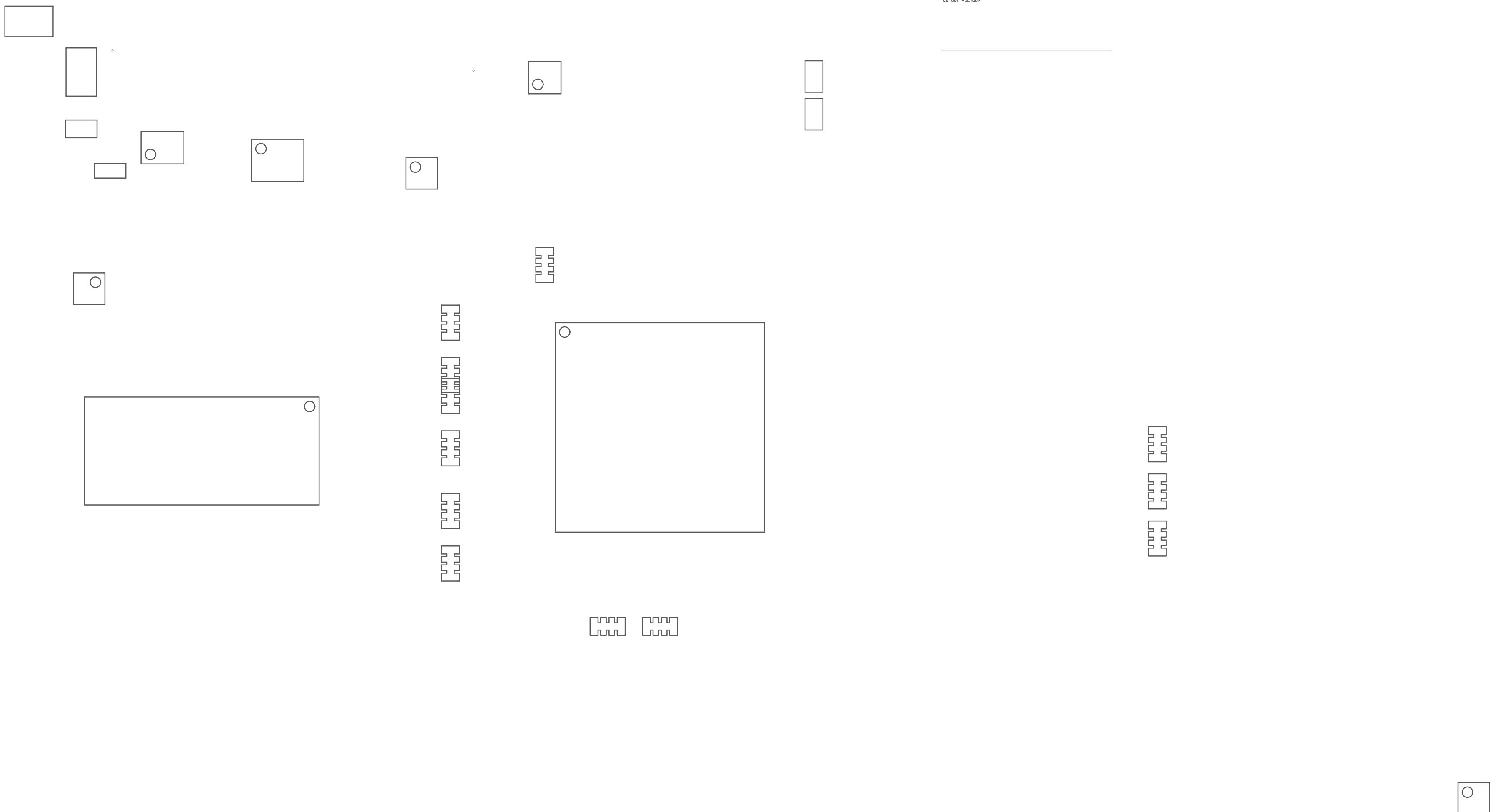


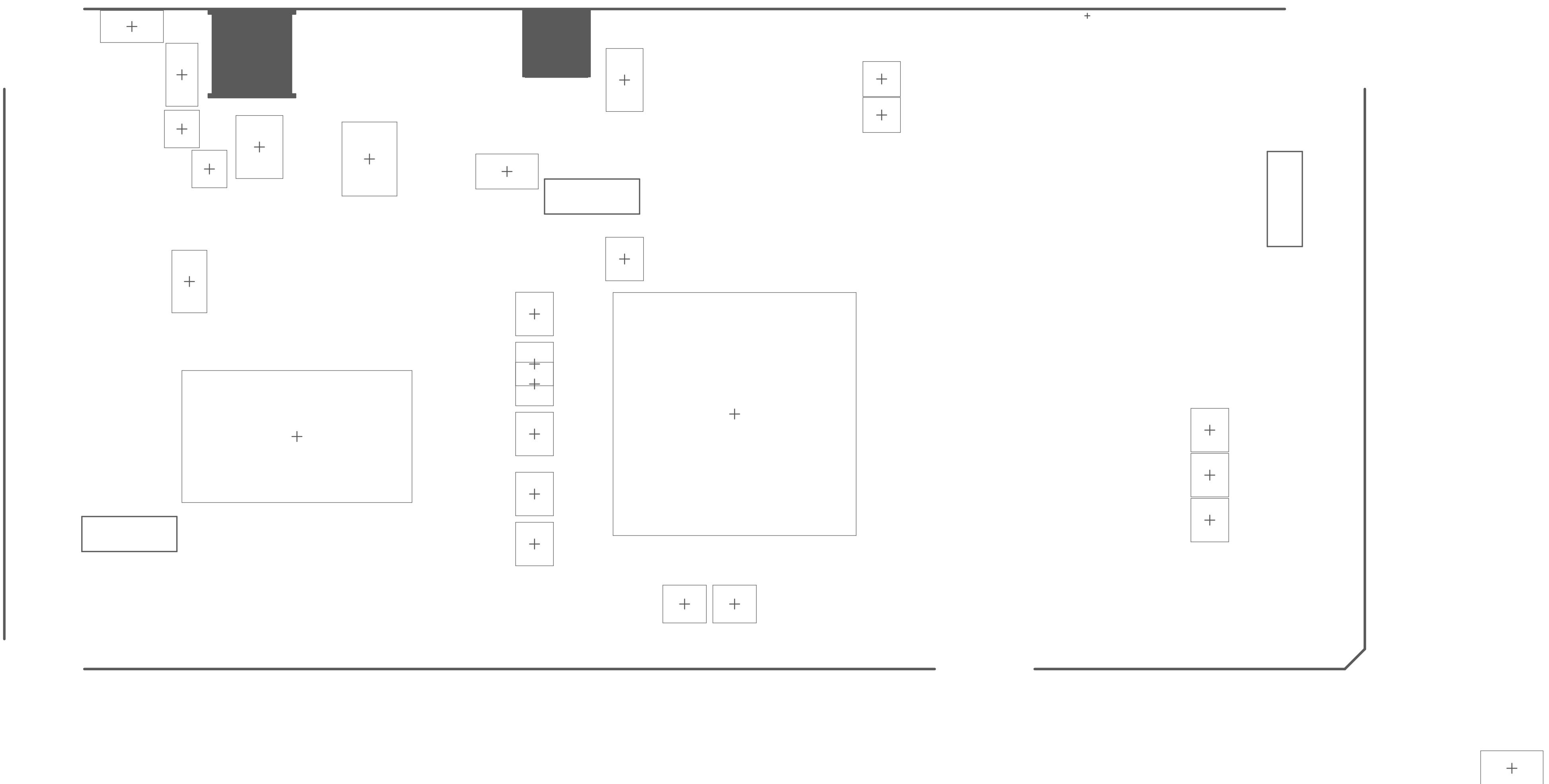






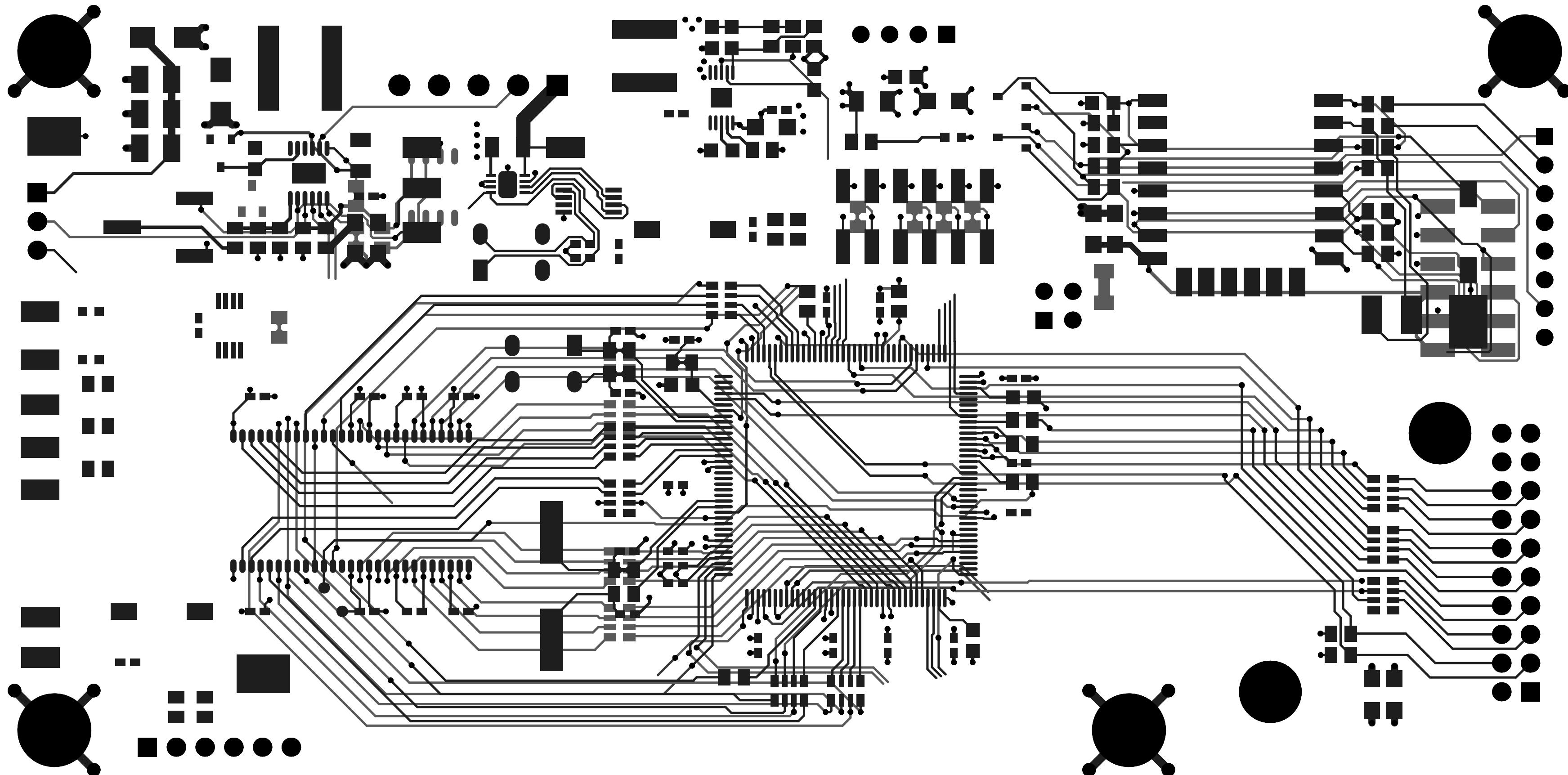
ANTENNA AREA
CUTOUT POLYGON











SOLENS

