

A

B

C

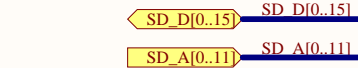
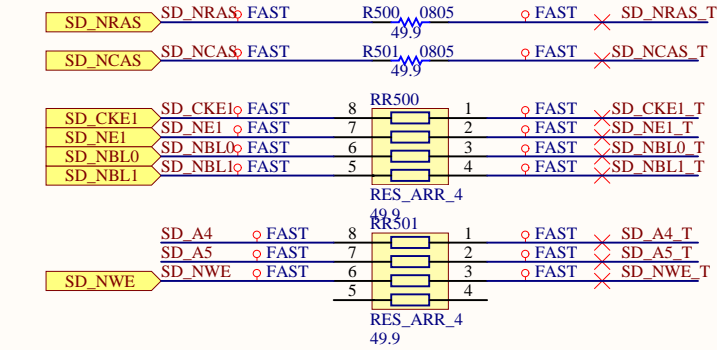
D

A

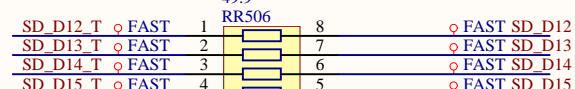
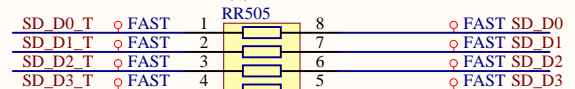
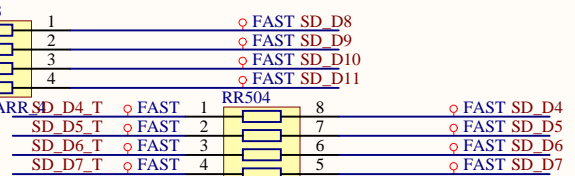
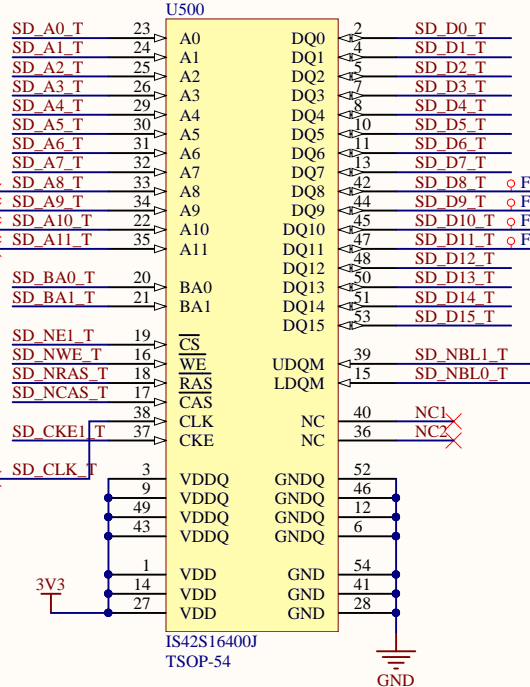
B

C

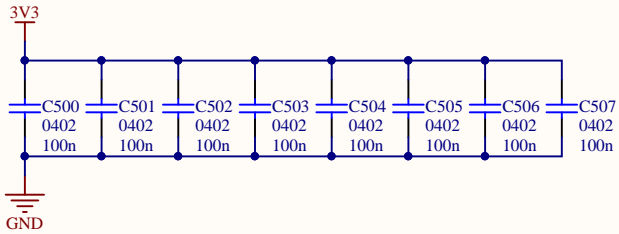
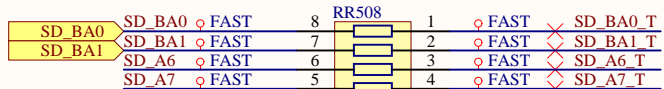
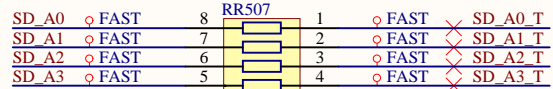
D



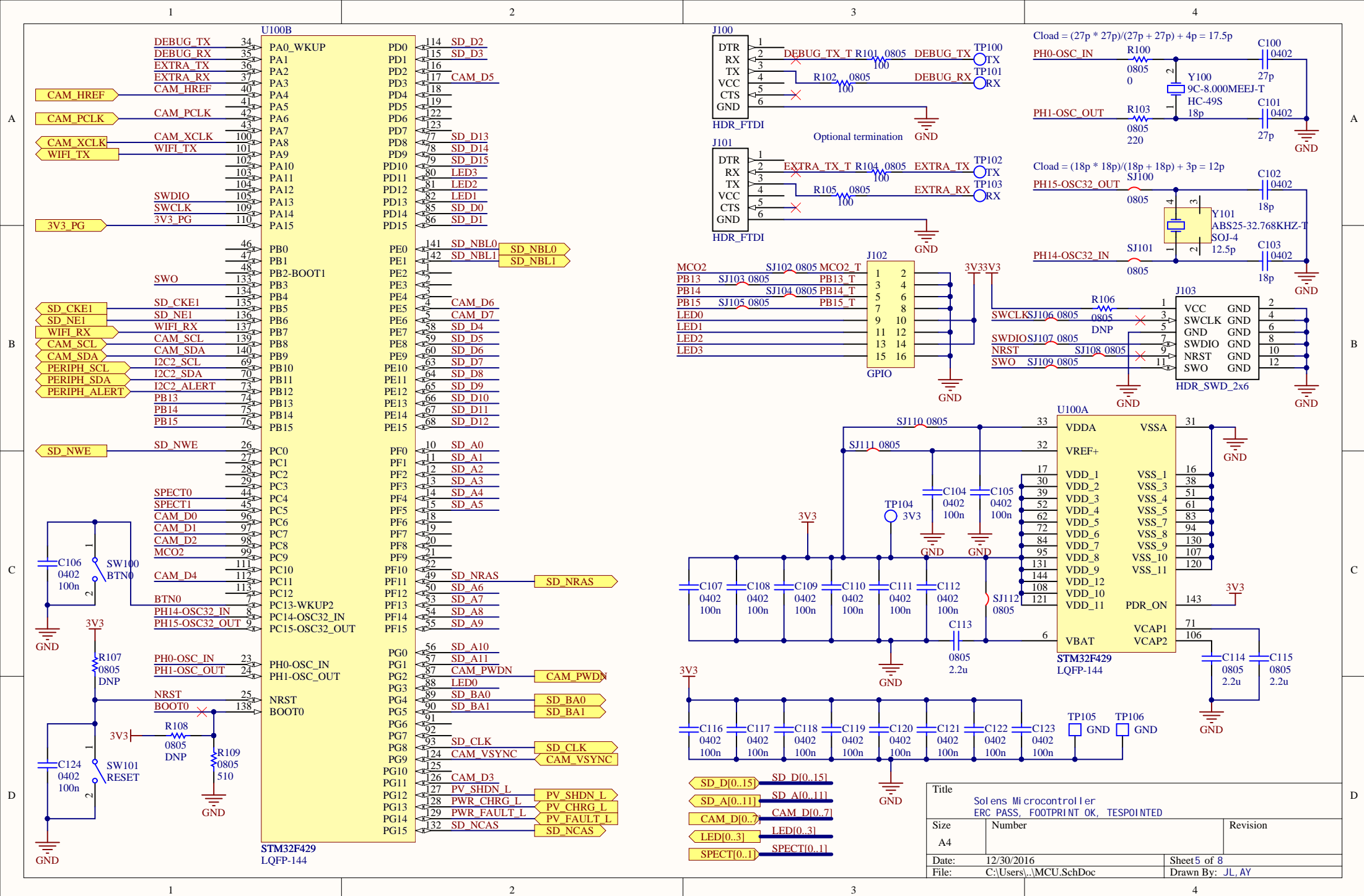
DATA termination to be placed halfway between MCU and RAM

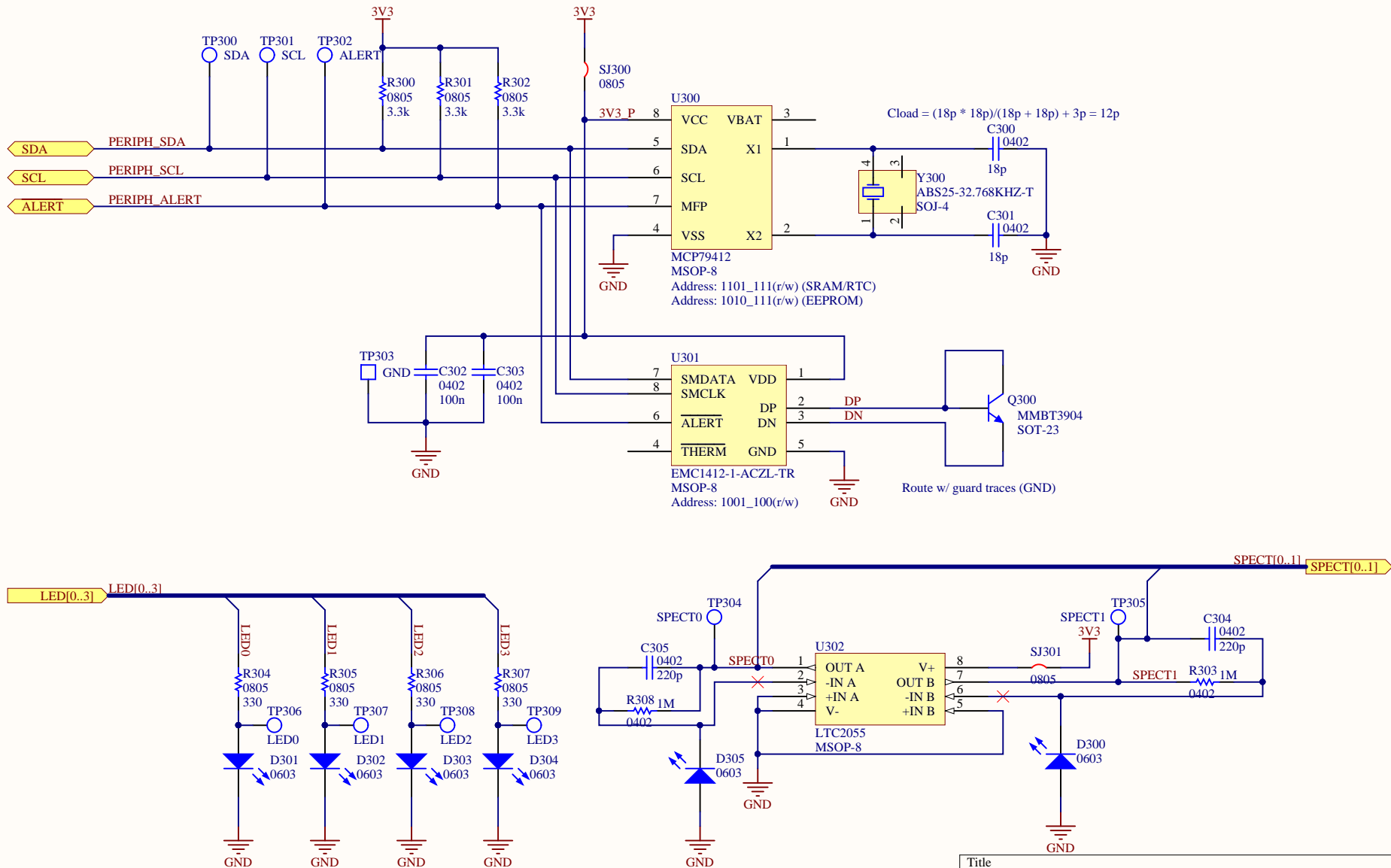


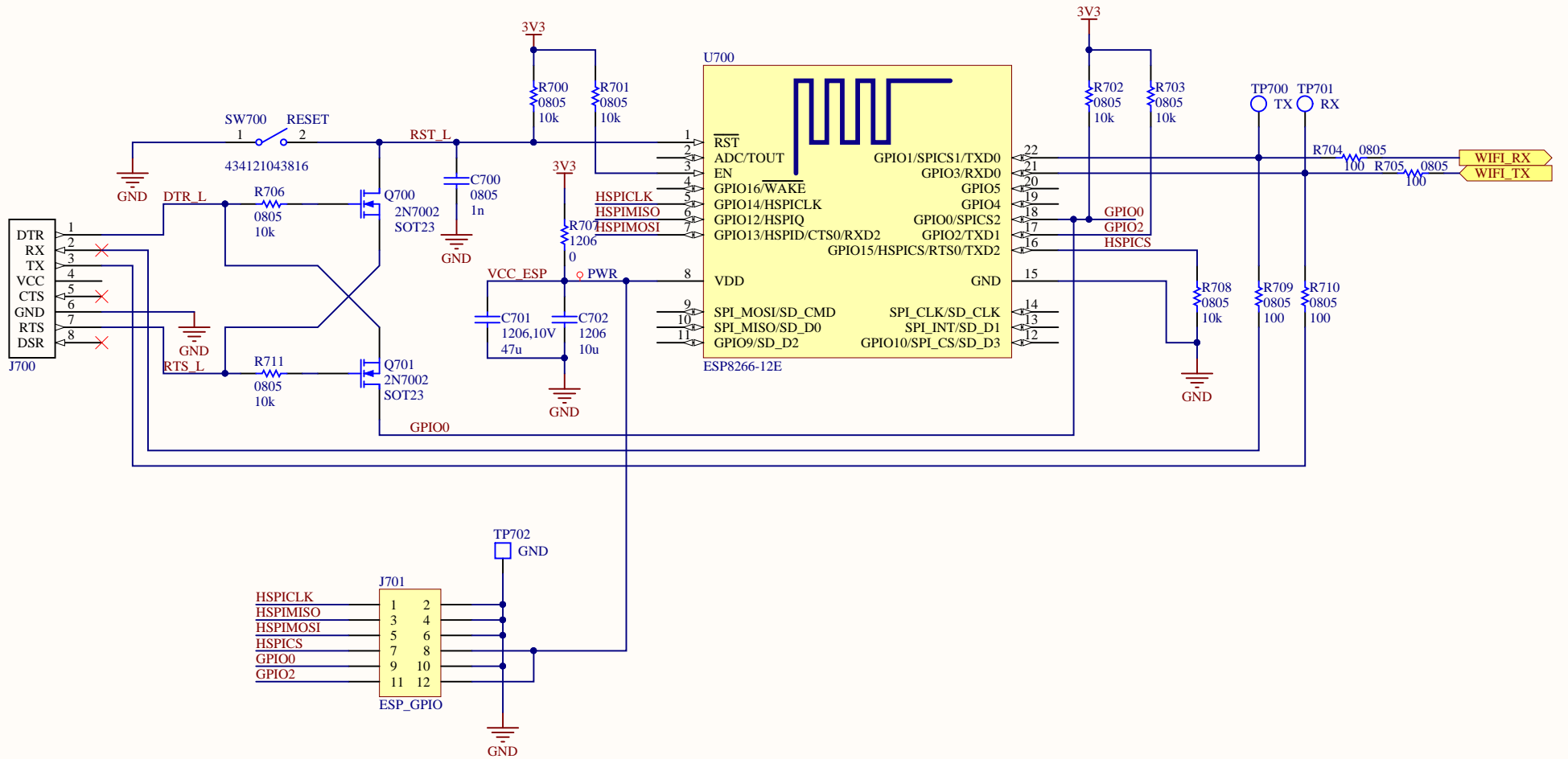
ADDR and CLK termination to be placed at MCU



Title		
Solens SDRAM		
ERC PASS, FOOTPRINT OK, TESPOINTED		
Size	Number	Revision
A4		
Date:	12/30/2016	Sheet 4 of 8
File:	C:\Users\...\SDRAM.SchDoc	Drawn By: JL, AY







Title		
Solens WiFi Subsystem		
ERC PASS, FOOTPRINT OK, TESTPOINTED		
Size	Number	Revision
A4		
Date:	12/30/2016	Sheet 8 of 8
File:	C:\Users\...\WiFi.SchDoc	Drawn By: JL, AY