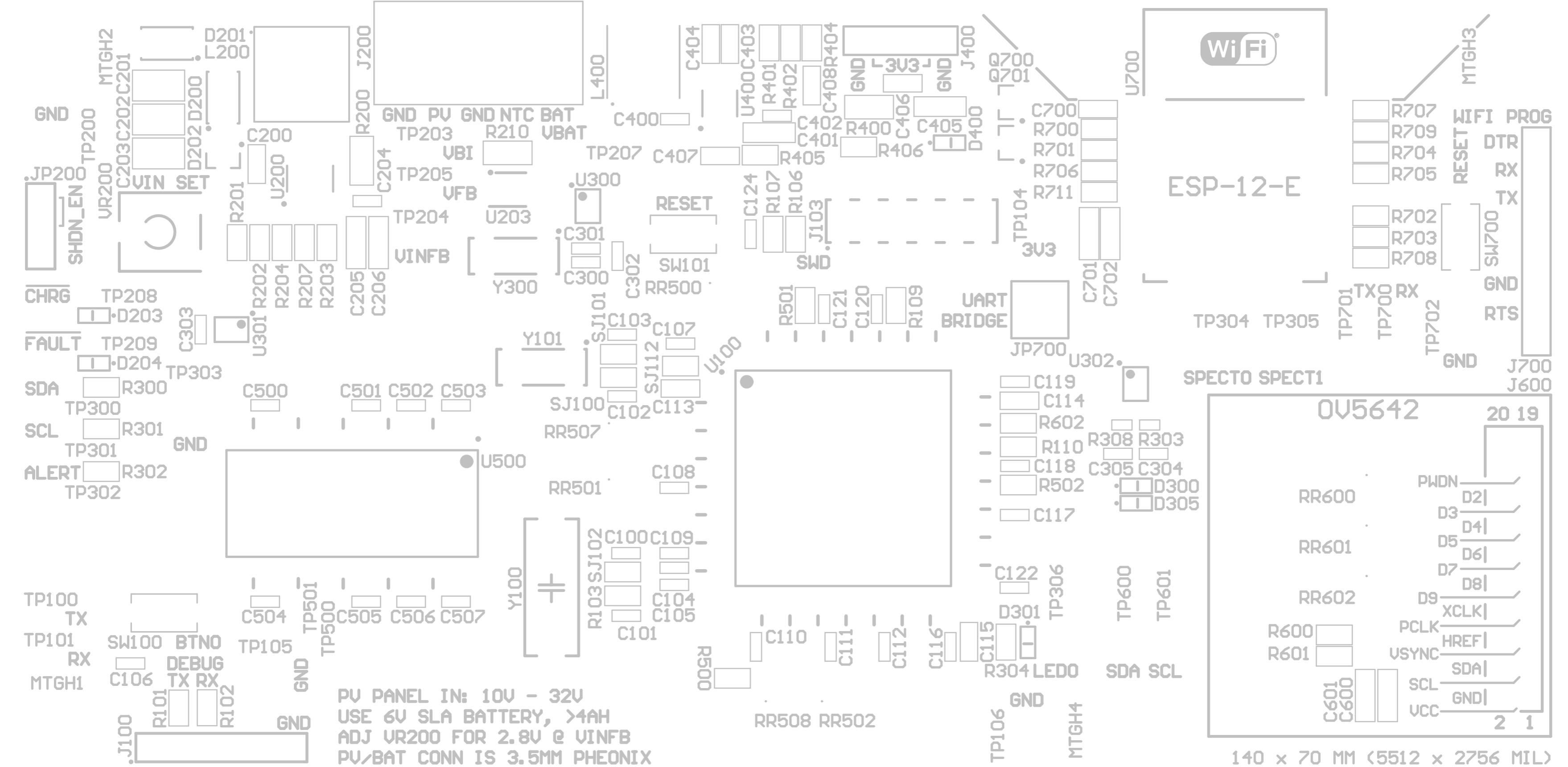


Board Stack Report

Stack Up		Layer Stack			
Layer	Board Layer Stack	Name	Material	Thickness	Constant
1		Top Paste			
2		Top Overlay			
3		Top Solder	Solder Resist	0.40mil	3.5
4		Top Layer	Copper	1.37mil	
5		Dielectric 1	2x2116	9.80mil	4.2
6		Signal Layer 2	Copper	1.37mil	
7		Dielectric 4		40.00mil	4.2
8		Signal Layer 1	Copper	1.37mil	
9		Dielectric 3	2x2116	9.80mil	4.2
10		Bottom Layer	Copper	1.37mil	
11		Bottom Solder	Solder Resist	0.40mil	3.5
12		Bottom Overlay			
13		Bottom Paste			
Height : 65.88mil					



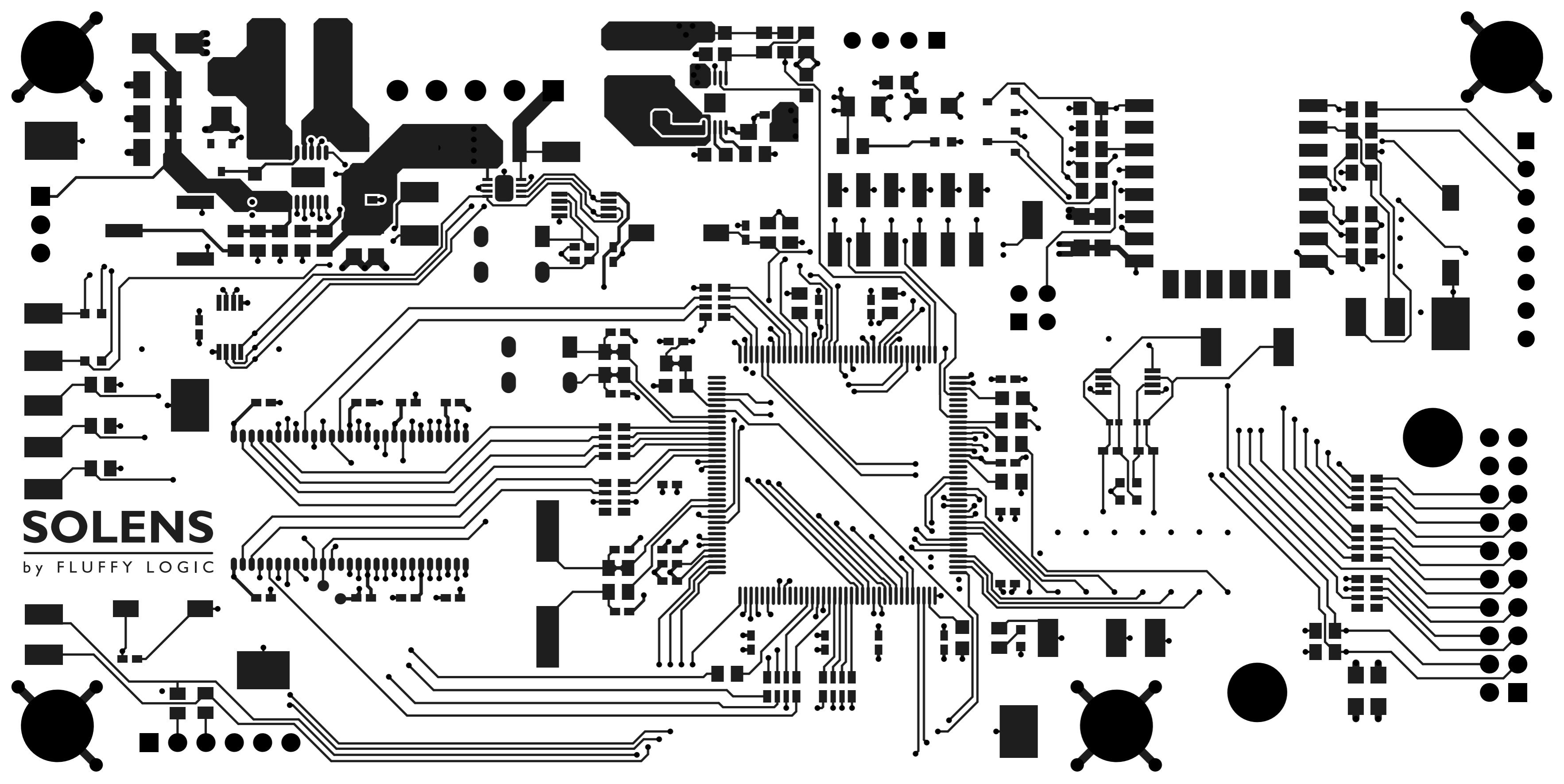
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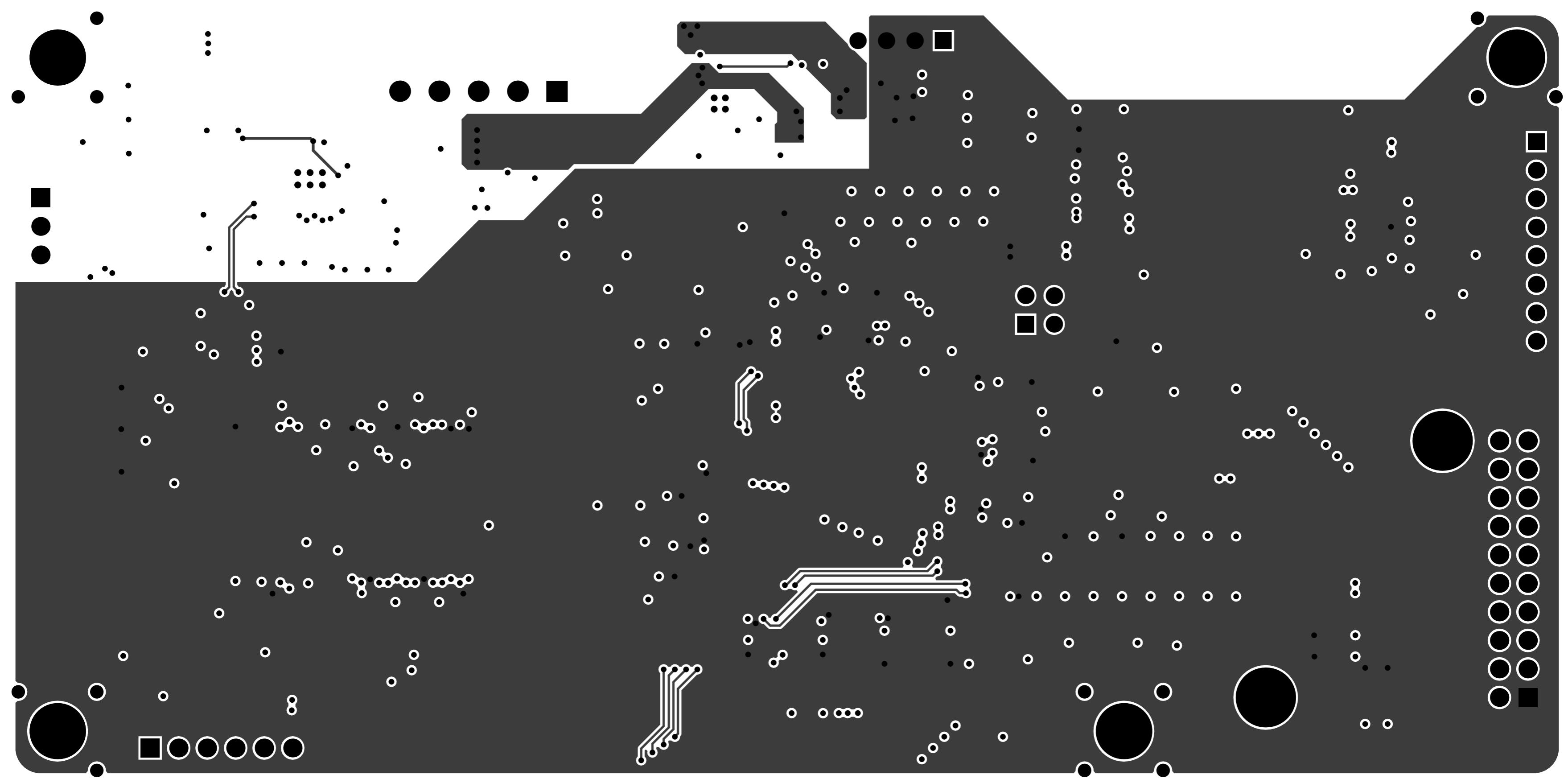
SOLENS

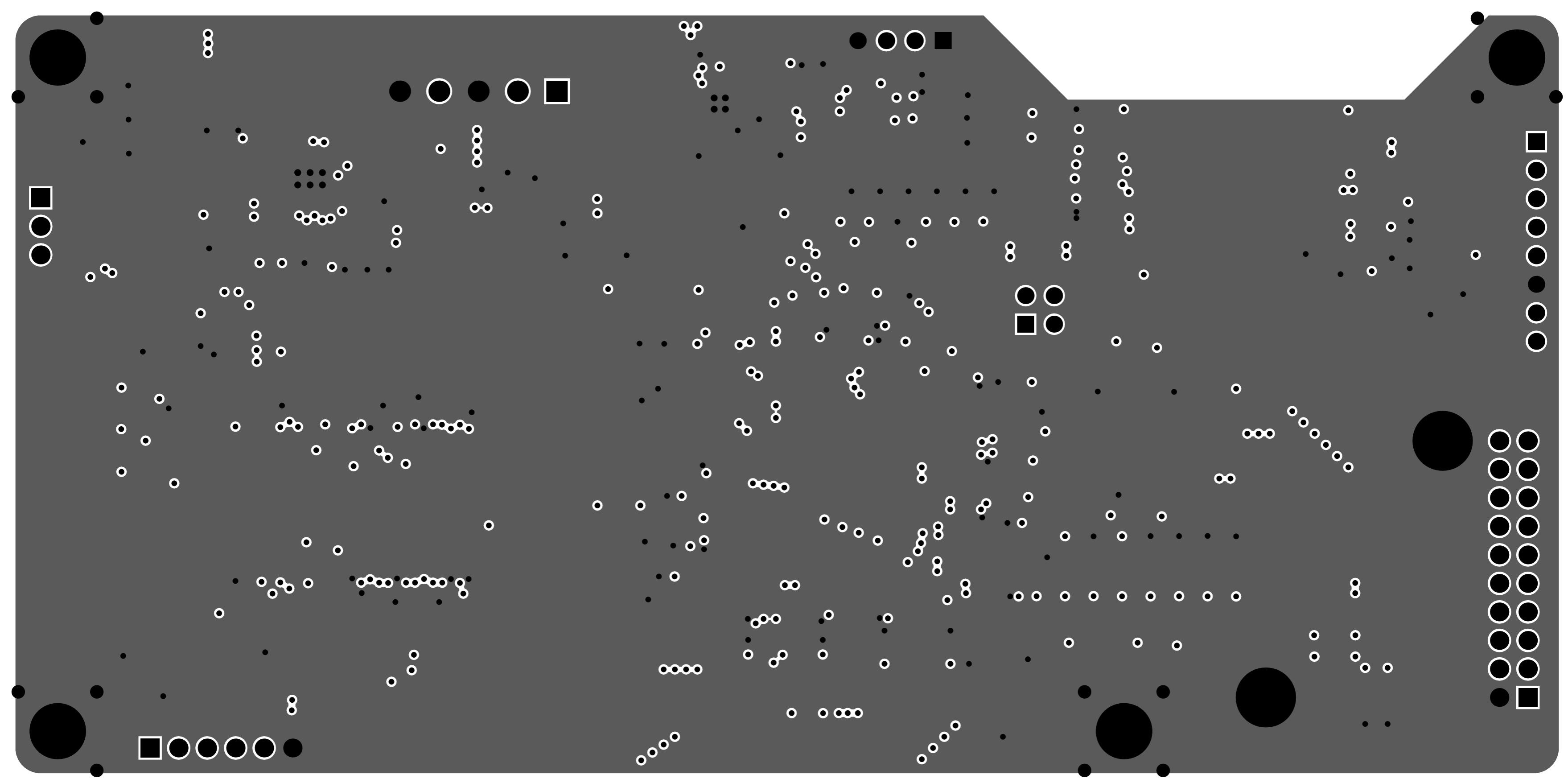
by FLUFFY LOGIC

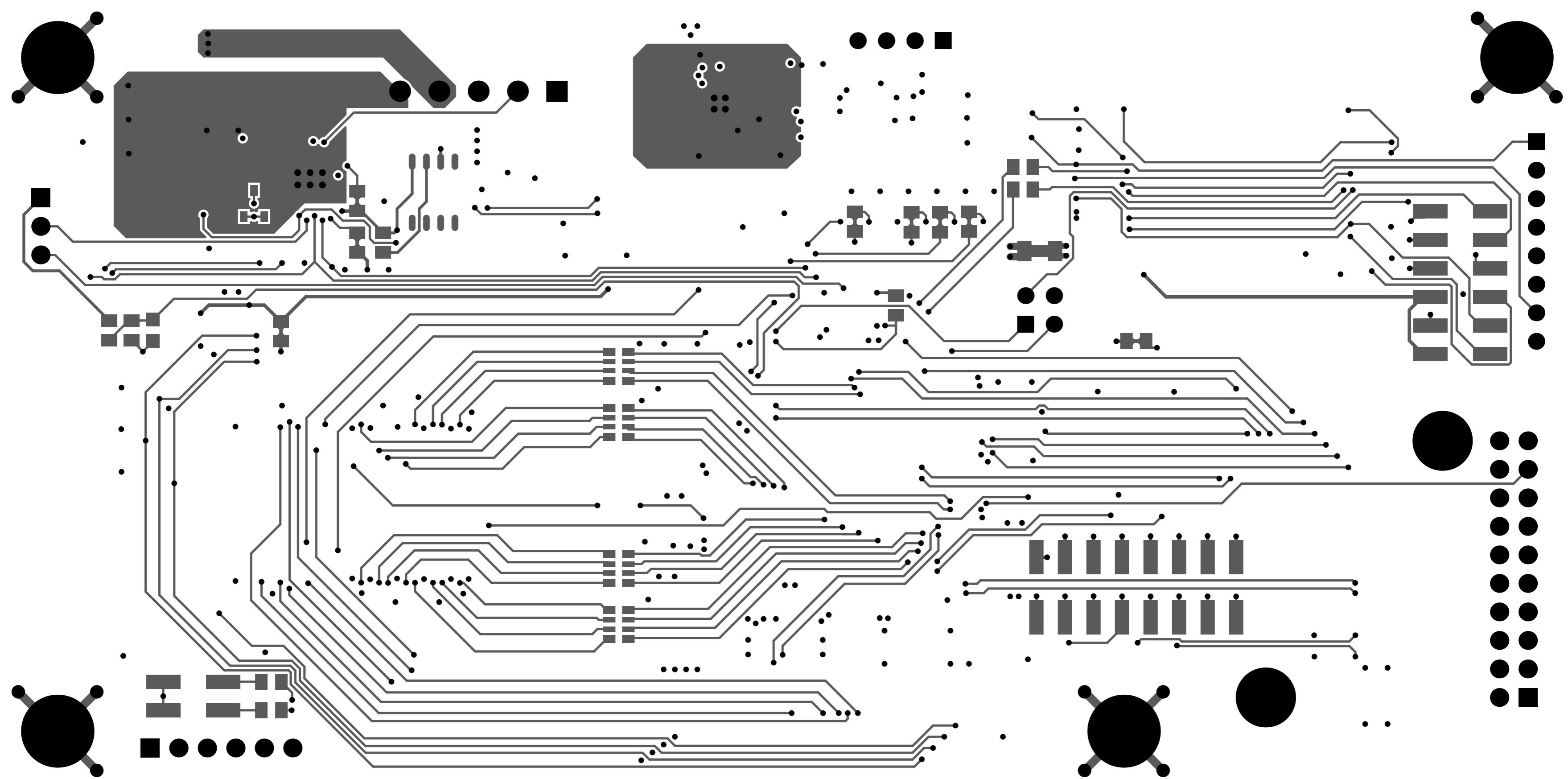
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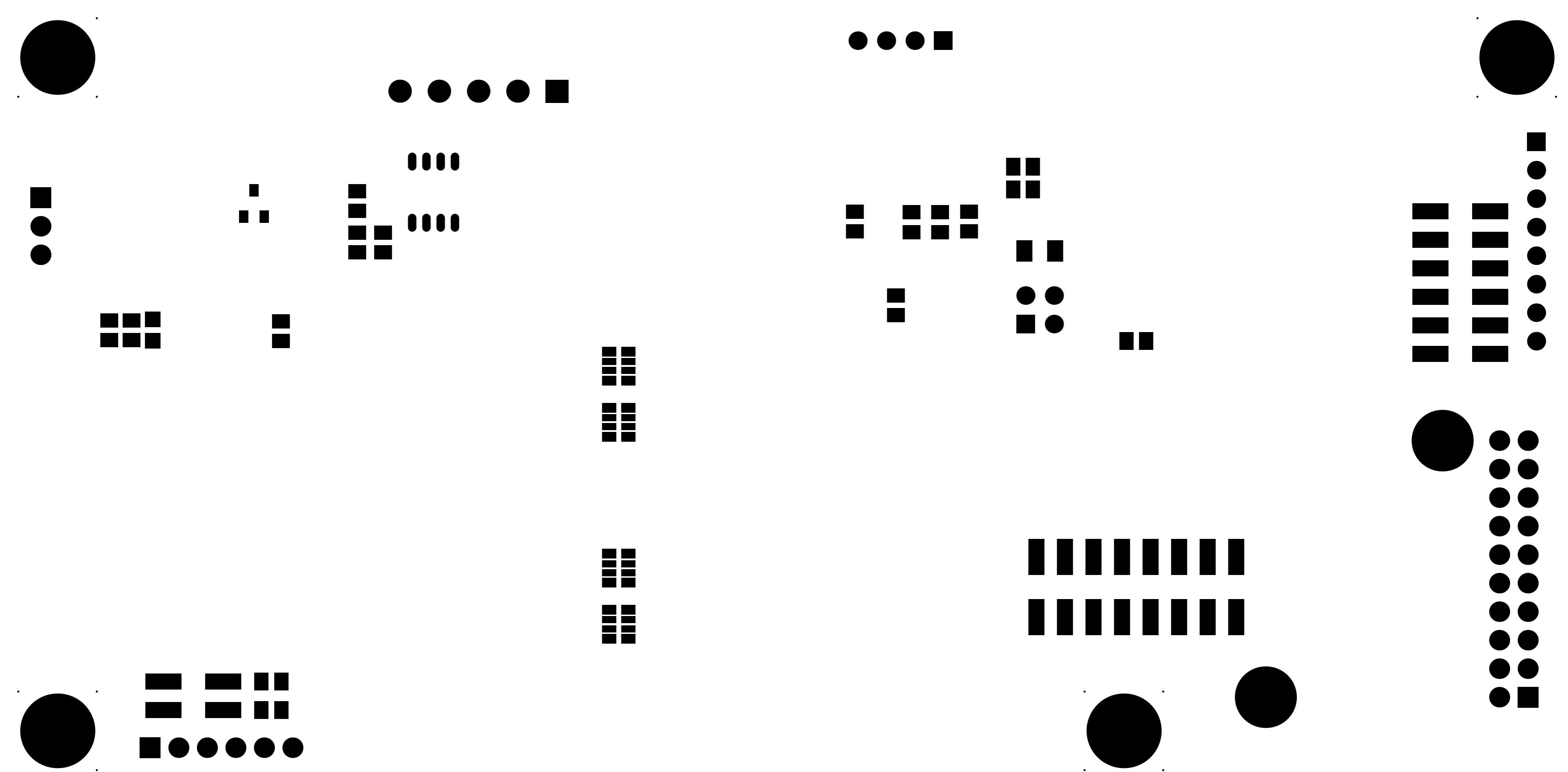
by FLUFFY LOGIC











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graph TD; UART[UART] --- EXTRA[EXTRA]; EXTRA --- END1[END]; EXTRA --- rx[rx.]; rx --- xt[xt]; xt --- END2[END]; xt --- RT05[RT05]; RT05 --- RT04[RT04]
```

Alkira Youngblood
Leremy Raab
Talyor Nagyau
Jeffery Liu
Ben Hepnerlein

A collection of various electronic component labels and symbols, including:

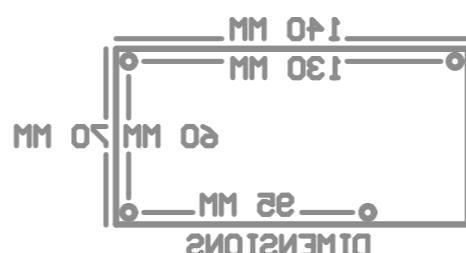
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305RR

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USE M3x0.5 SCREWS

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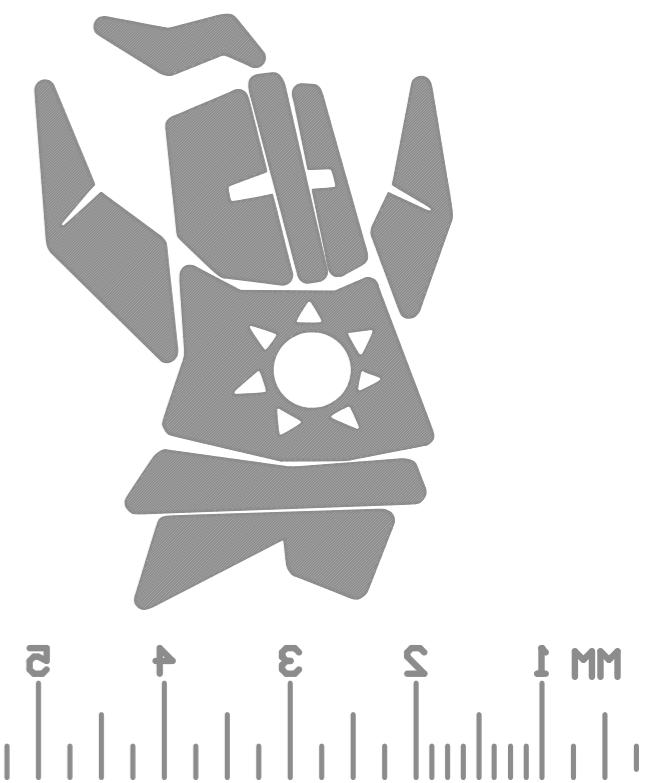
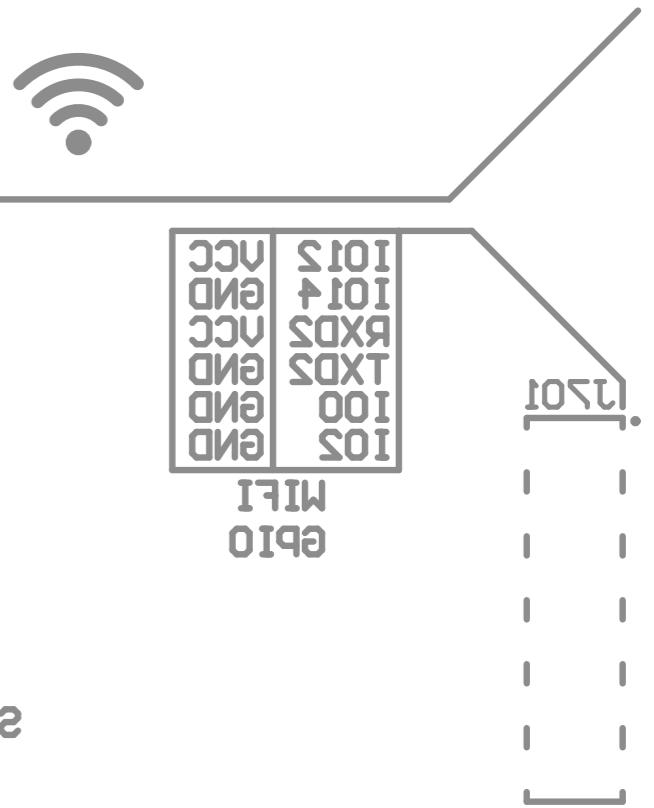
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    GED3_1 -- "MCU" --> GED0_1[GED0]
    GED3_2[GED3] -- "GEOPI" --> GED3_2
    GED3_2 -- "MCU" --> GED0_2[GED0]
    GED3_3[GED3] -- "GEOPI" --> GED3_3
    GED3_3 -- "MCU" --> GED0_3[GED0]

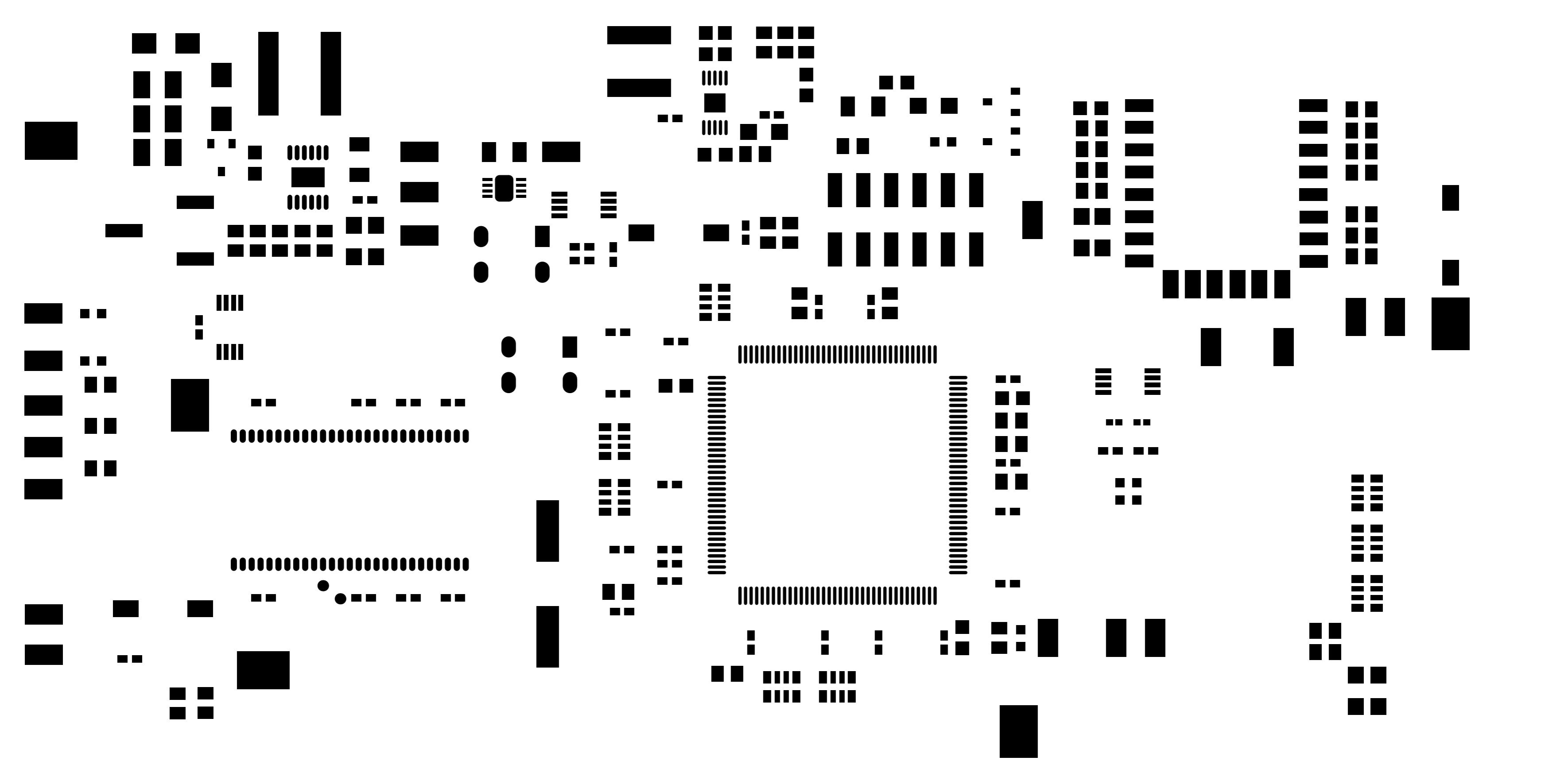
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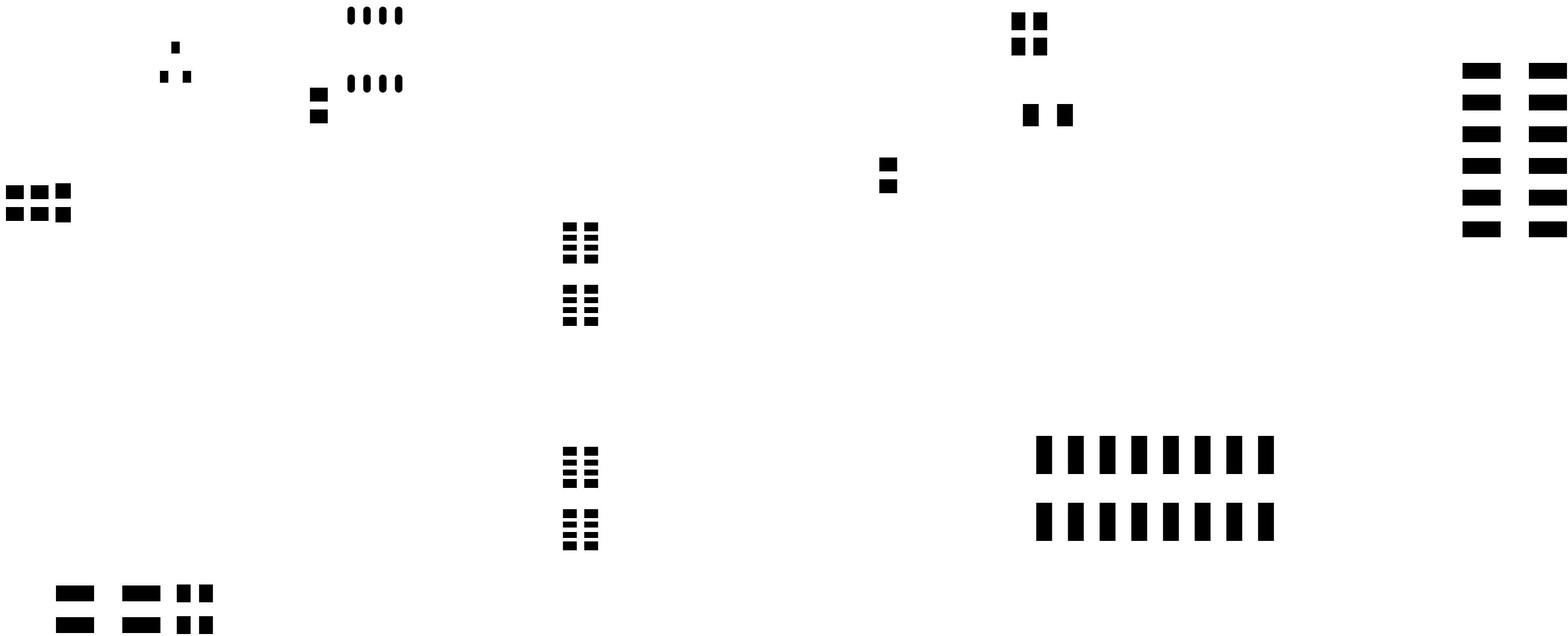
A large 3D-style crossword puzzle grid. The visible letters form the following words:

- Across:
 - Row 1: R108
 - Row 2: SMD10
 - Row 3: MAST
 - Row 4: S1108
 - Row 5: S1108
 - Row 6: S1108
 - Row 7: S1108
 - Row 8: S1108
- Down:
 - Column 1: G710
 - Column 2: R710
 - Column 3: R710
 - Column 4: R710
 - Column 5: R710
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 - Column 9: R710
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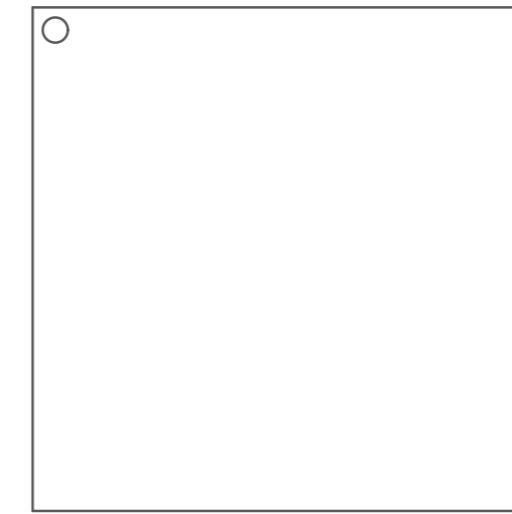
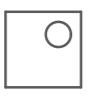
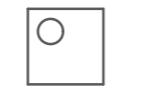
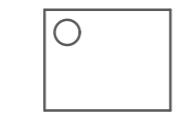
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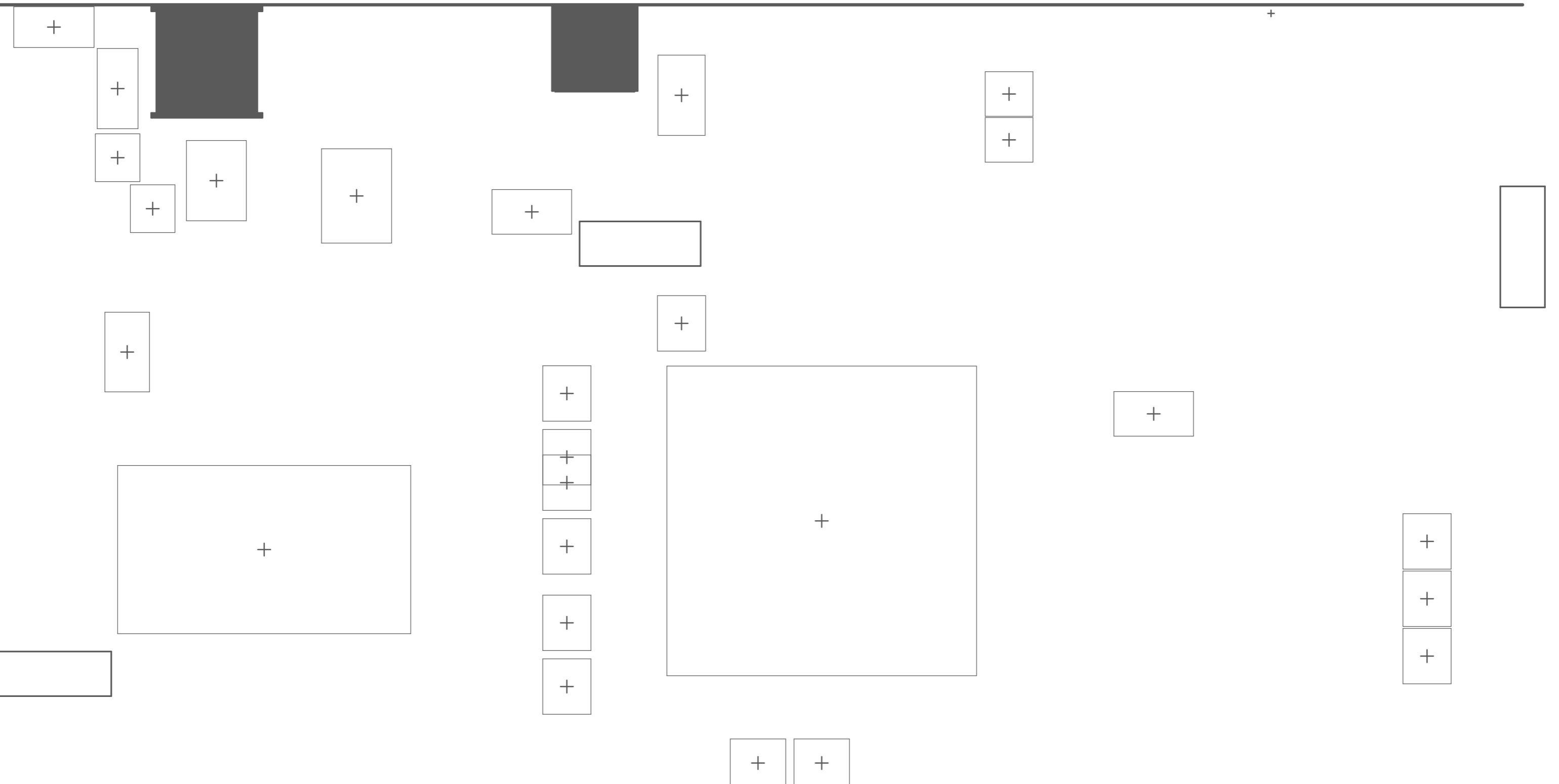


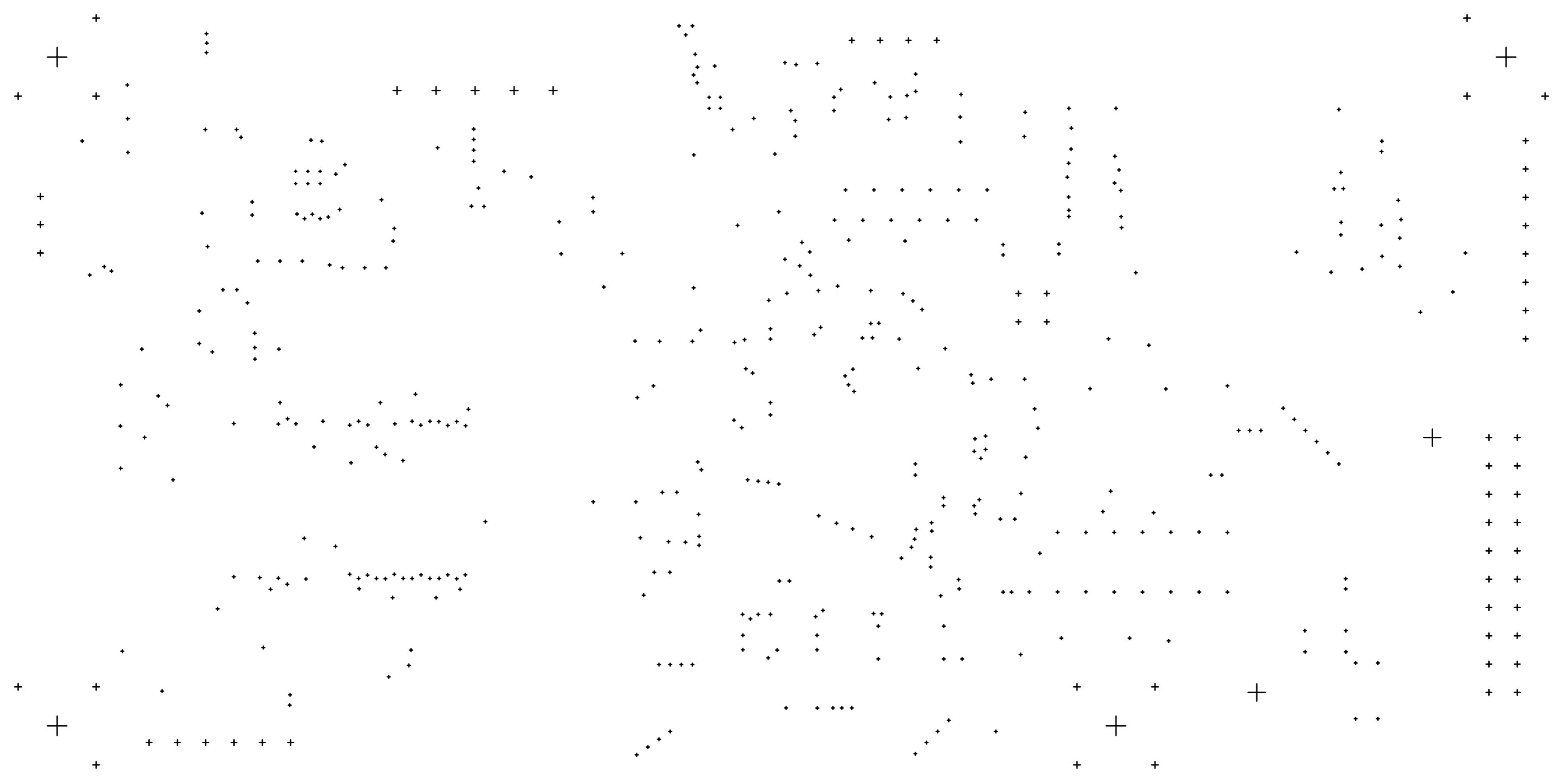














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