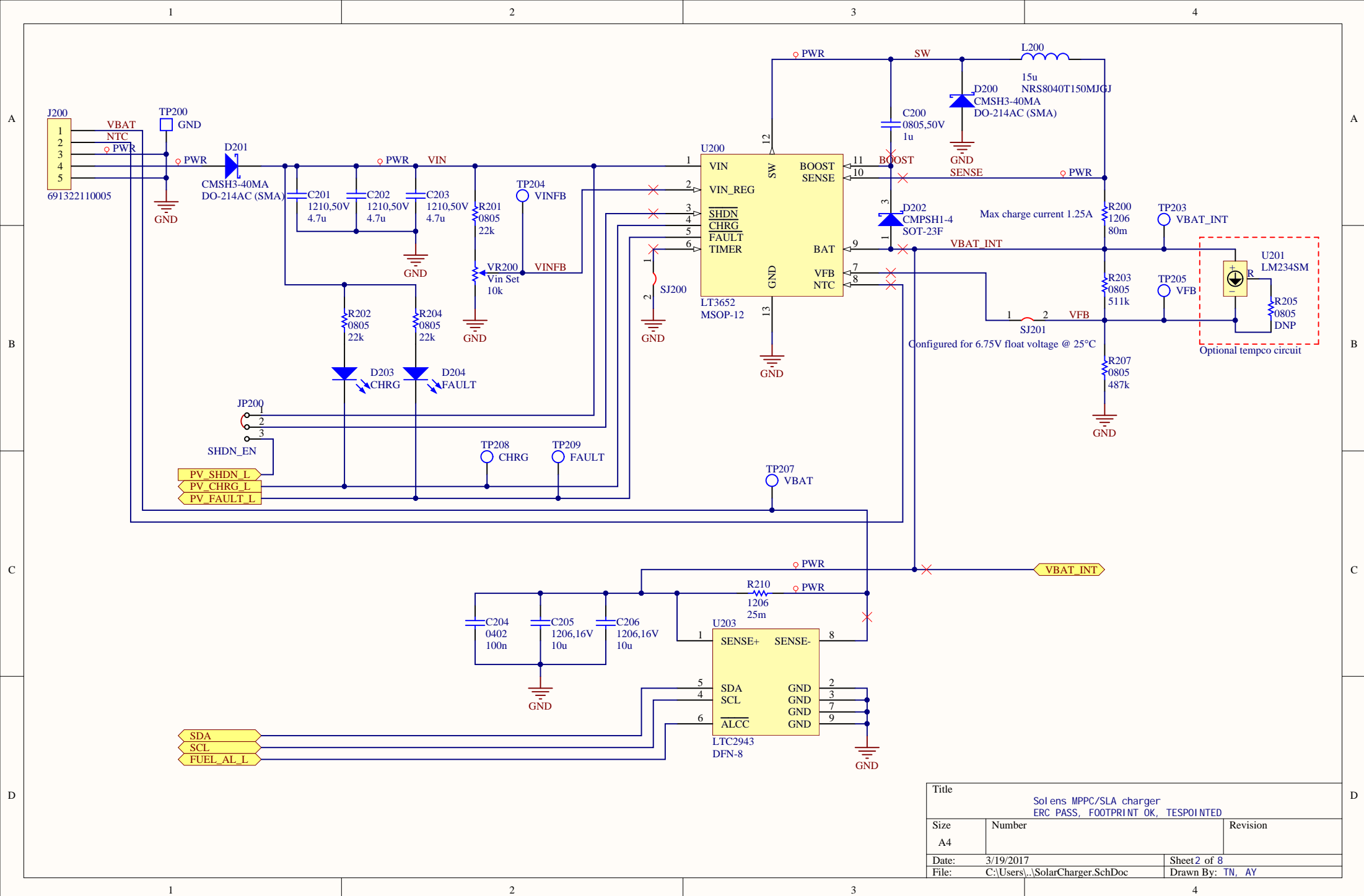
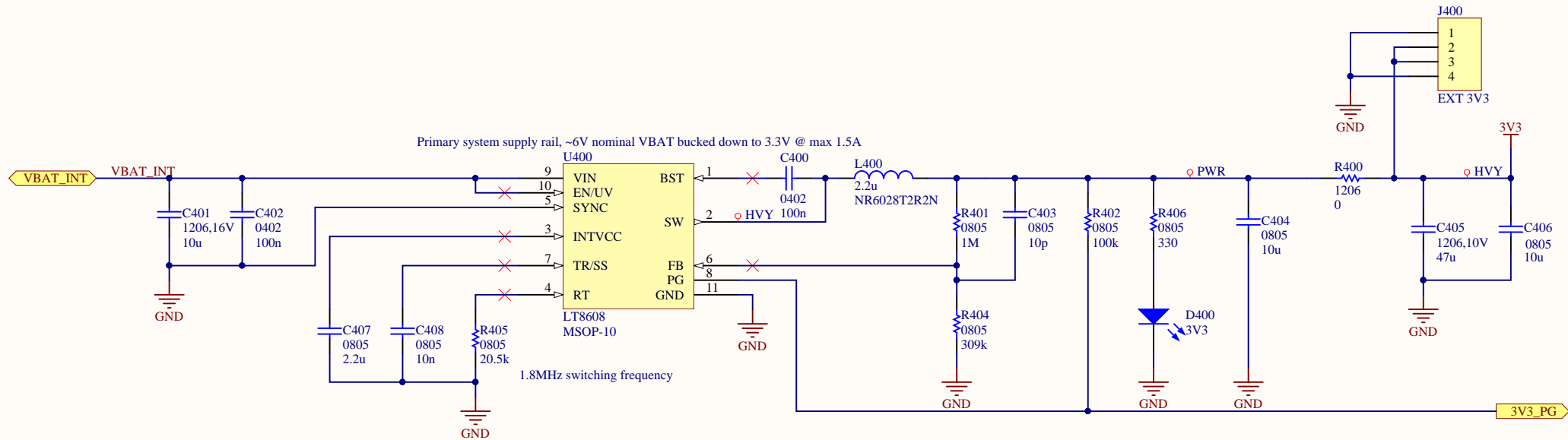


Title		
Solens Camera Subsystem		
ERC WARN, FOOTPRINT N/A		
Size	Number	Revision
A4		
Date:	3/19/2017	Sheet 1 of 8
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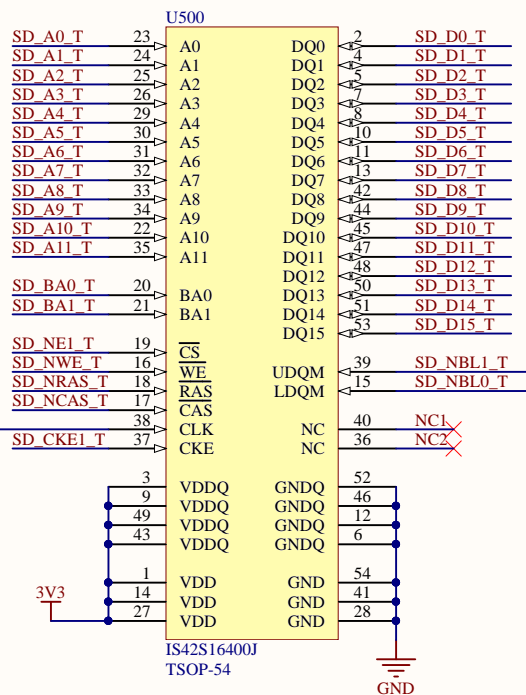
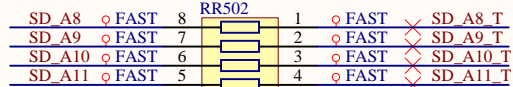
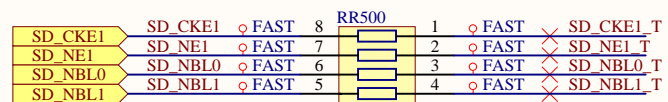
Title		
Solens Voltage Regulators		
ERC PASS, FOOTPRINT OK, TESPOINTED		
Size	Number	Revision
A4		
Date:	3/19/2017	Sheet 3 of 8
File:	C:\Users\...\Regulators.SchDoc	Drawn By: TN, AY

A

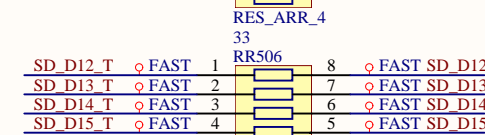
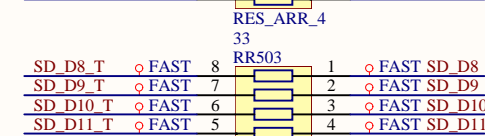
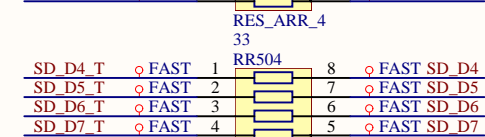
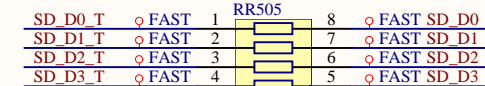


SD\_D[0..15] SD\_D[0..15]

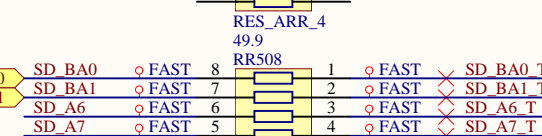
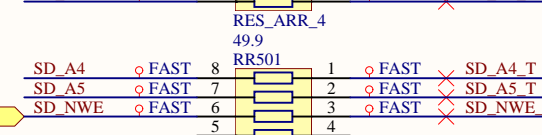
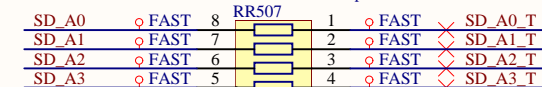
SD\_A[0..11] SD\_A[0..11]



DATA termination to be placed halfway between MCU and RAM



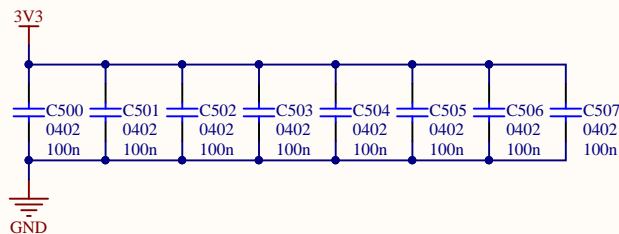
ADDR and CLK termination to be placed at MCU



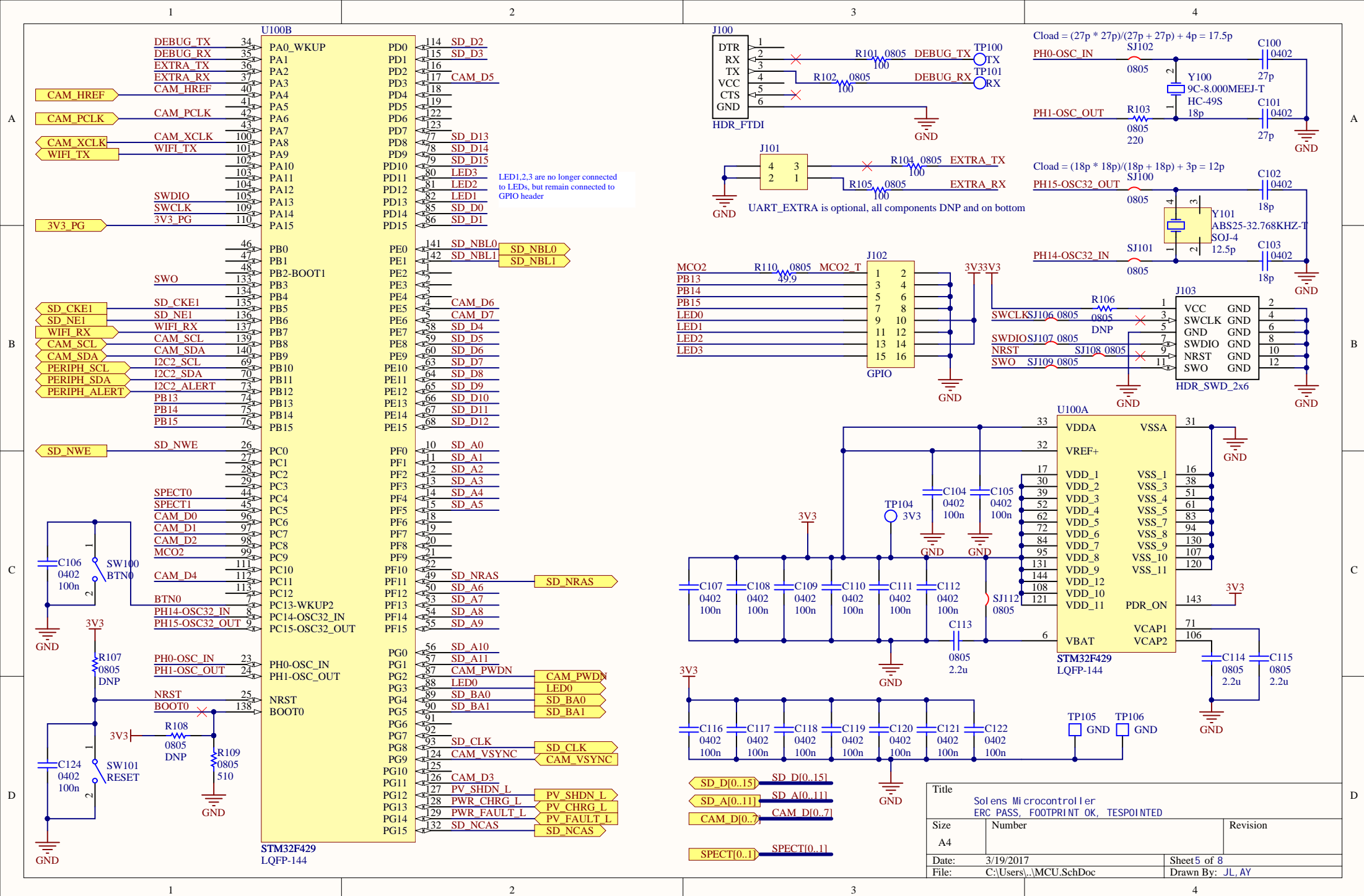
B

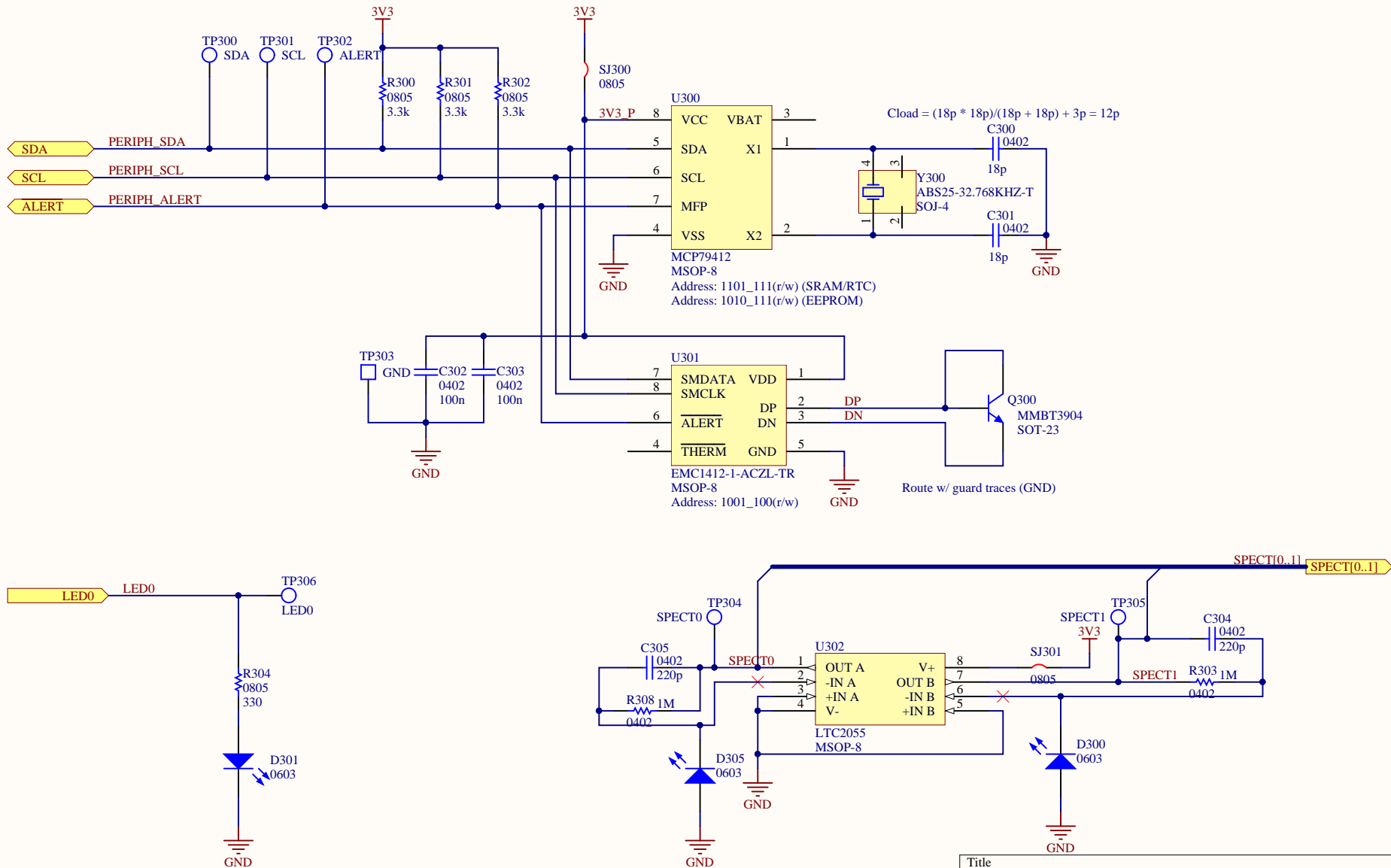
C

D

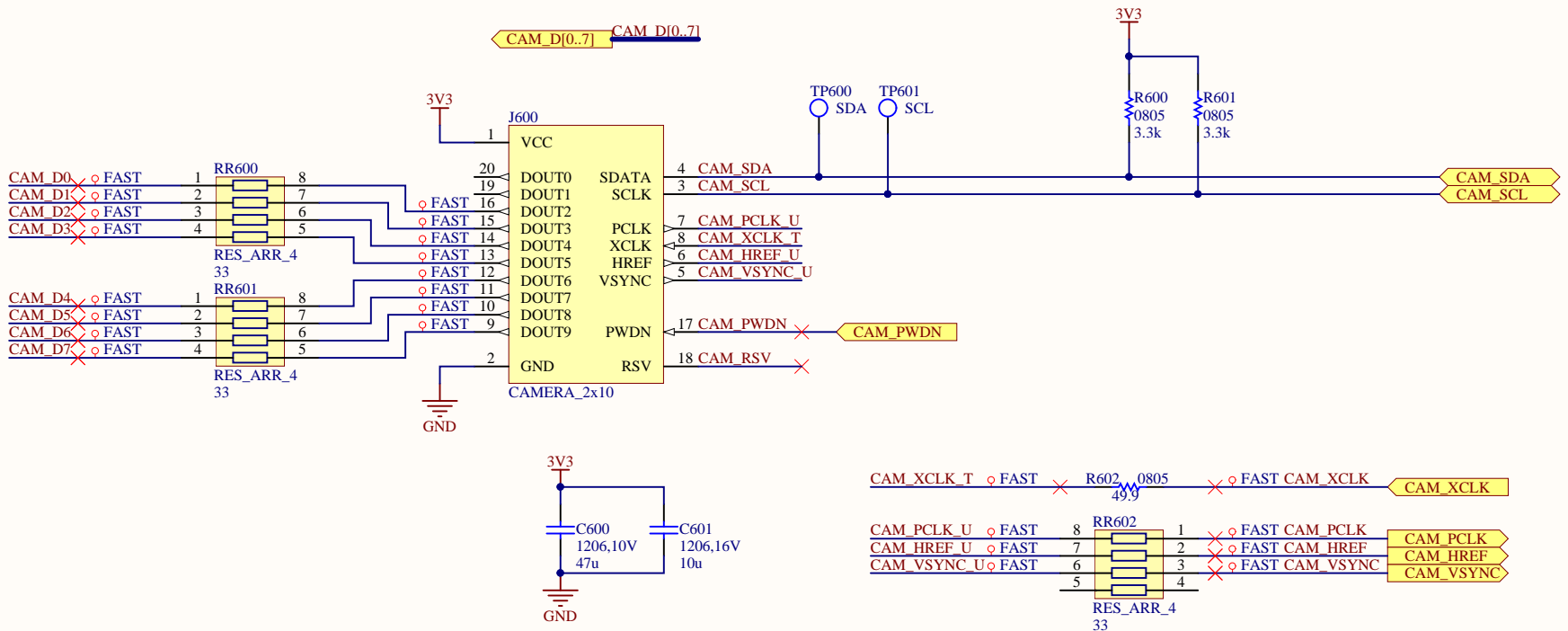


Title		
Solens SDRAM		
ERC PASS, FOOTPRINT OK, TSPPOINTED		
Size	Number	Revision
A4		
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File:	C:\Users\...\SDRAM.SchDoc	Drawn By: JL, AV





Title		
Solens Peripherals		
ERC PASS, FOOTPRINT OK, TESTPOINTED		
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A4		
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File:	C:\Users\...\Peripherals.SchDoc	Drawn By: AY



Title		
Solens Camera Subsystem		
ERC PASS, FOOTPRINT OK, TESTPOINTED		
Size	Number	Revision
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