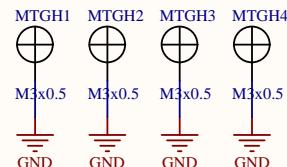
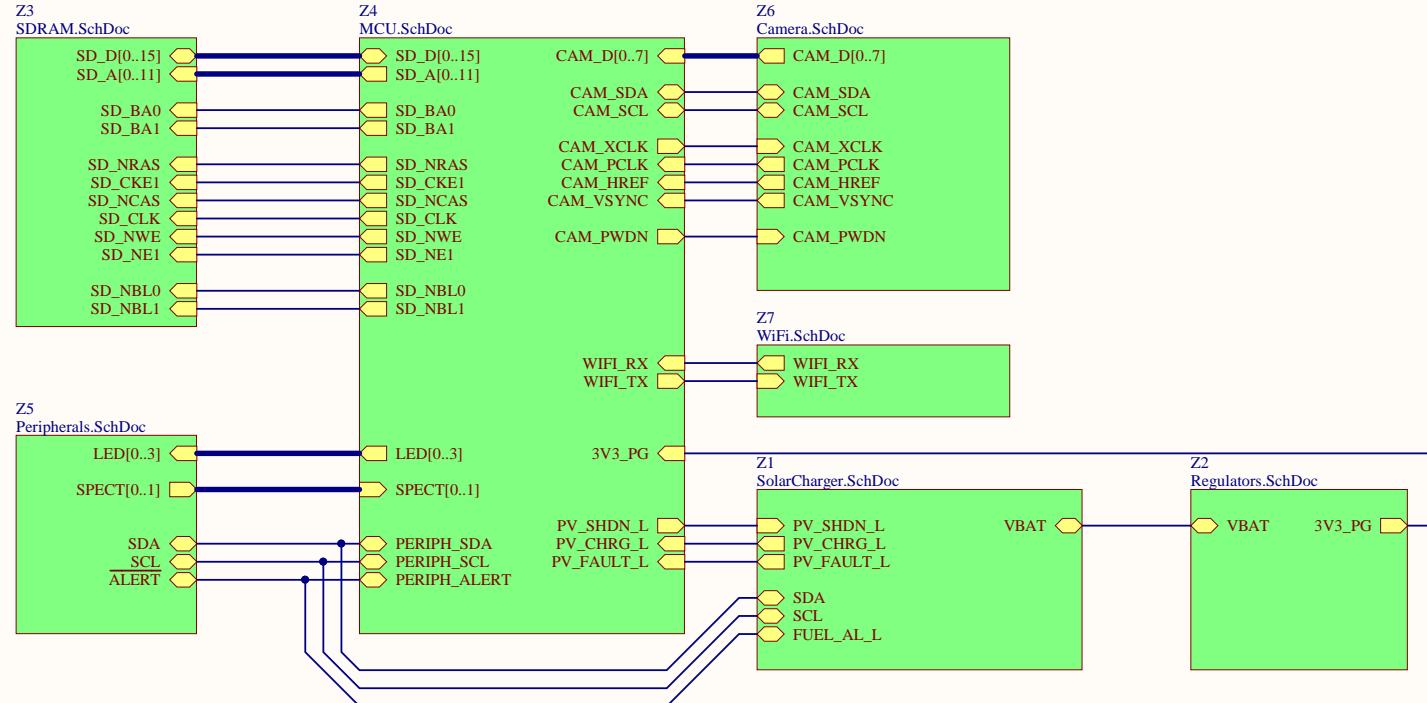
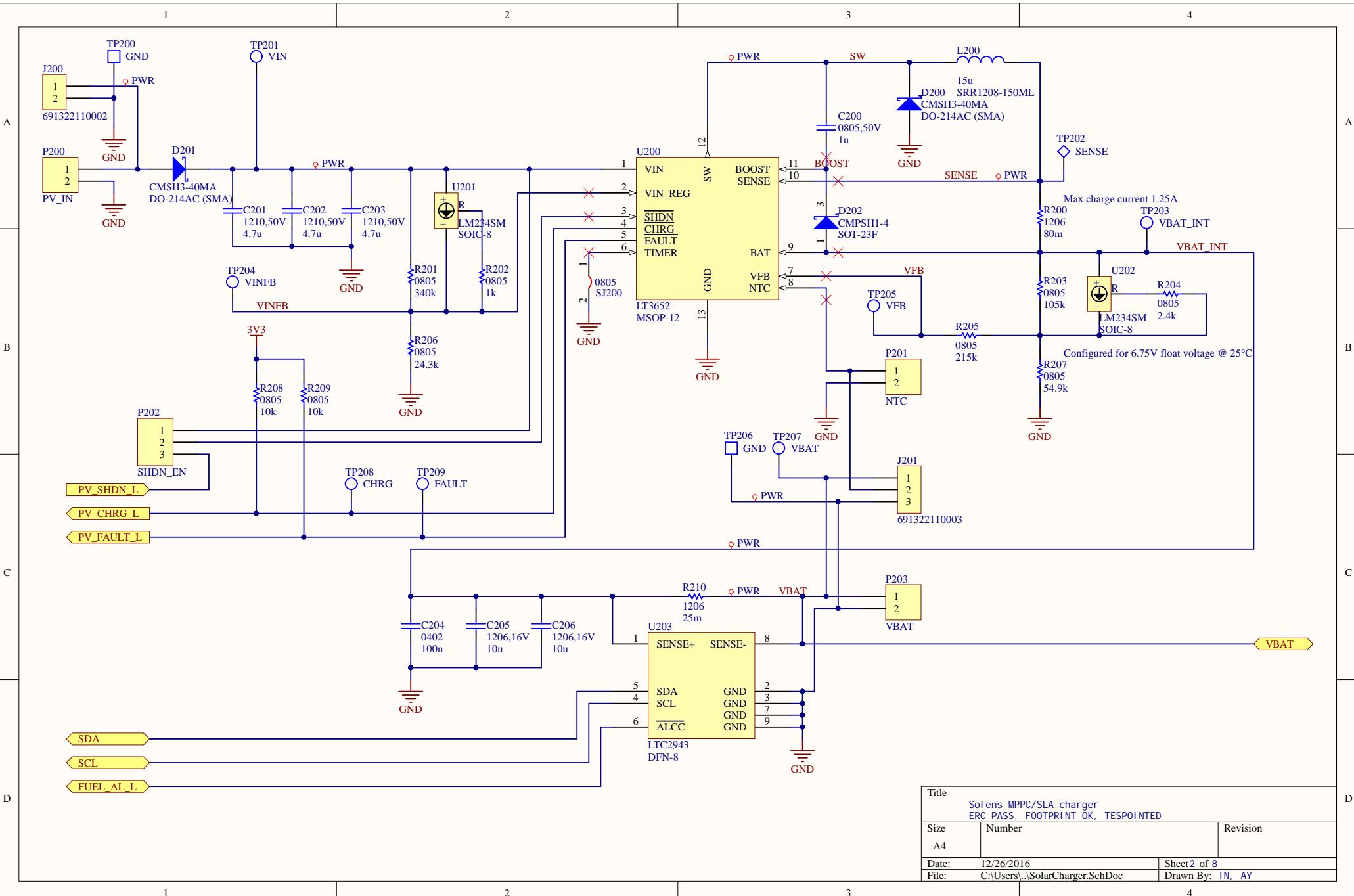


A

A



Title		
Solens Camera Subsystem ERC WARN, FOOTPRINT N/A		
Size	Number	Revision
A4		
Date:	12/26/2016	Sheet 1 of 8
File:	C:\Users..\Solens.SchDoc	Drawn By: JL, AY



A

A

B

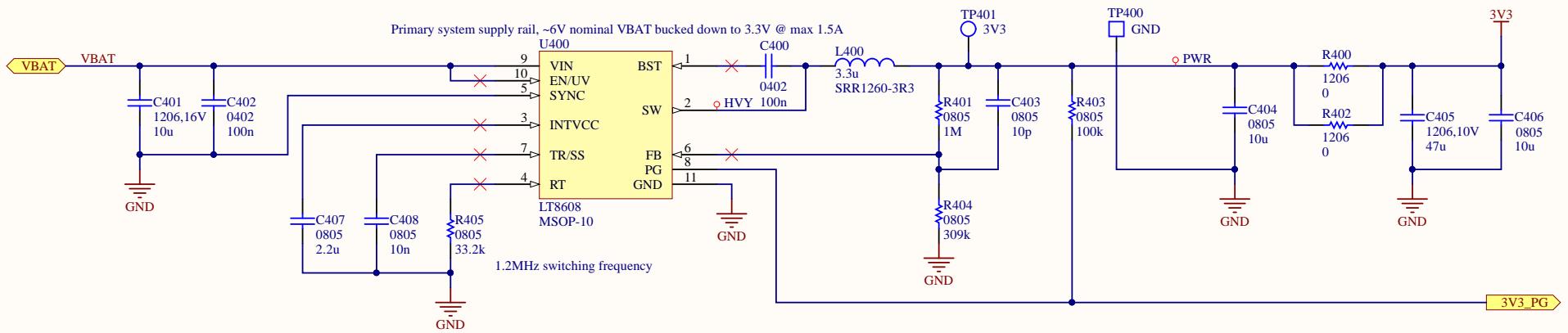
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C

C

D

D



Title		
Solens Voltage Regulators ERC PASS, FOOTPRINT OK, TESPOINTED		
Size	Number	Revision
A4		
Date:	12/26/2016	Sheet 3 of 8
File:	C:\Users..\Regulators.SchDoc	Drawn By: TN, AY

1

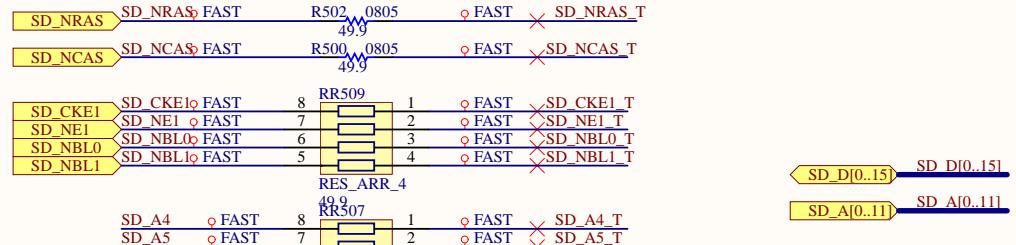
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3

4

A

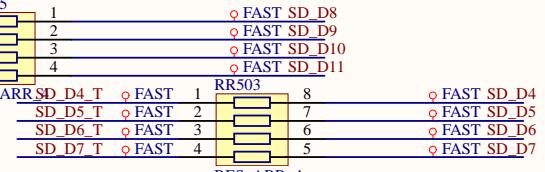
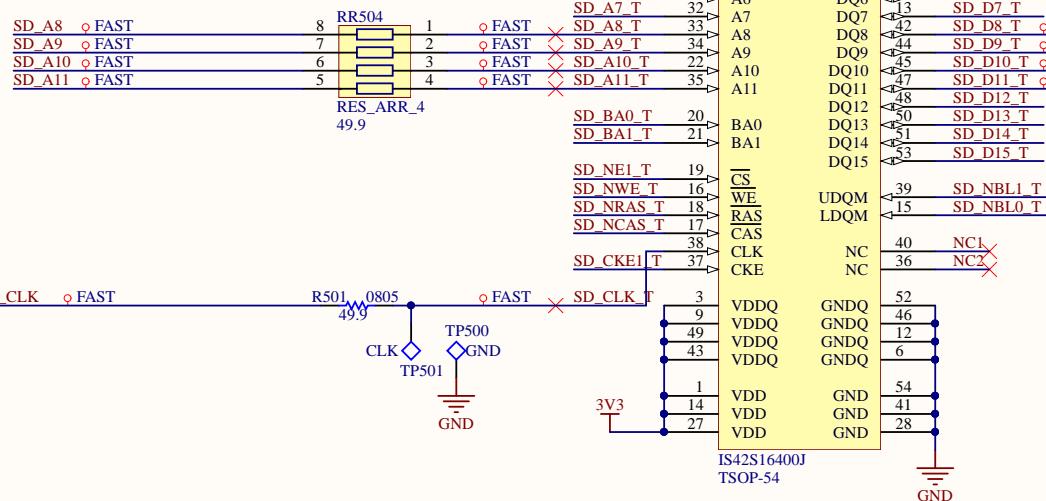
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DATA termination to be placed halfway between MCU and RAM

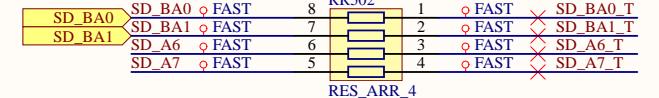
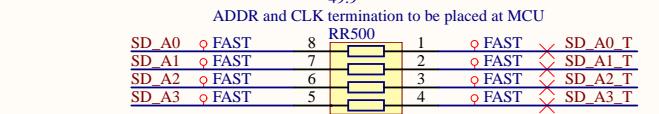
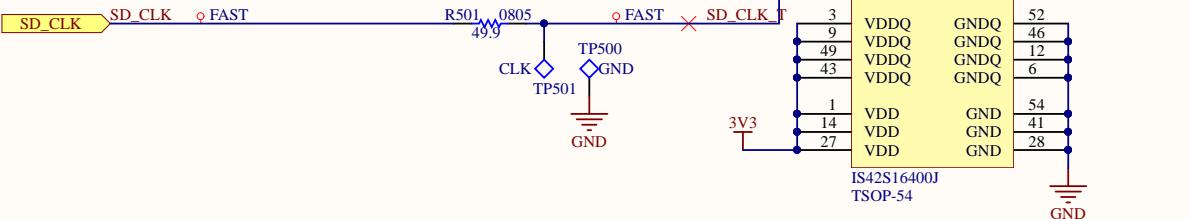
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B



C

C



Title		
Solens SDRAM ERC PASS, FOOTPRINT OK, TESPOINTED		
Size	Number	Revision

Date:	12/26/2016	Sheet 4 of 8
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File:	C:\Users...\SDRAM.SchDoc	Drawn By: JL, AY
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D

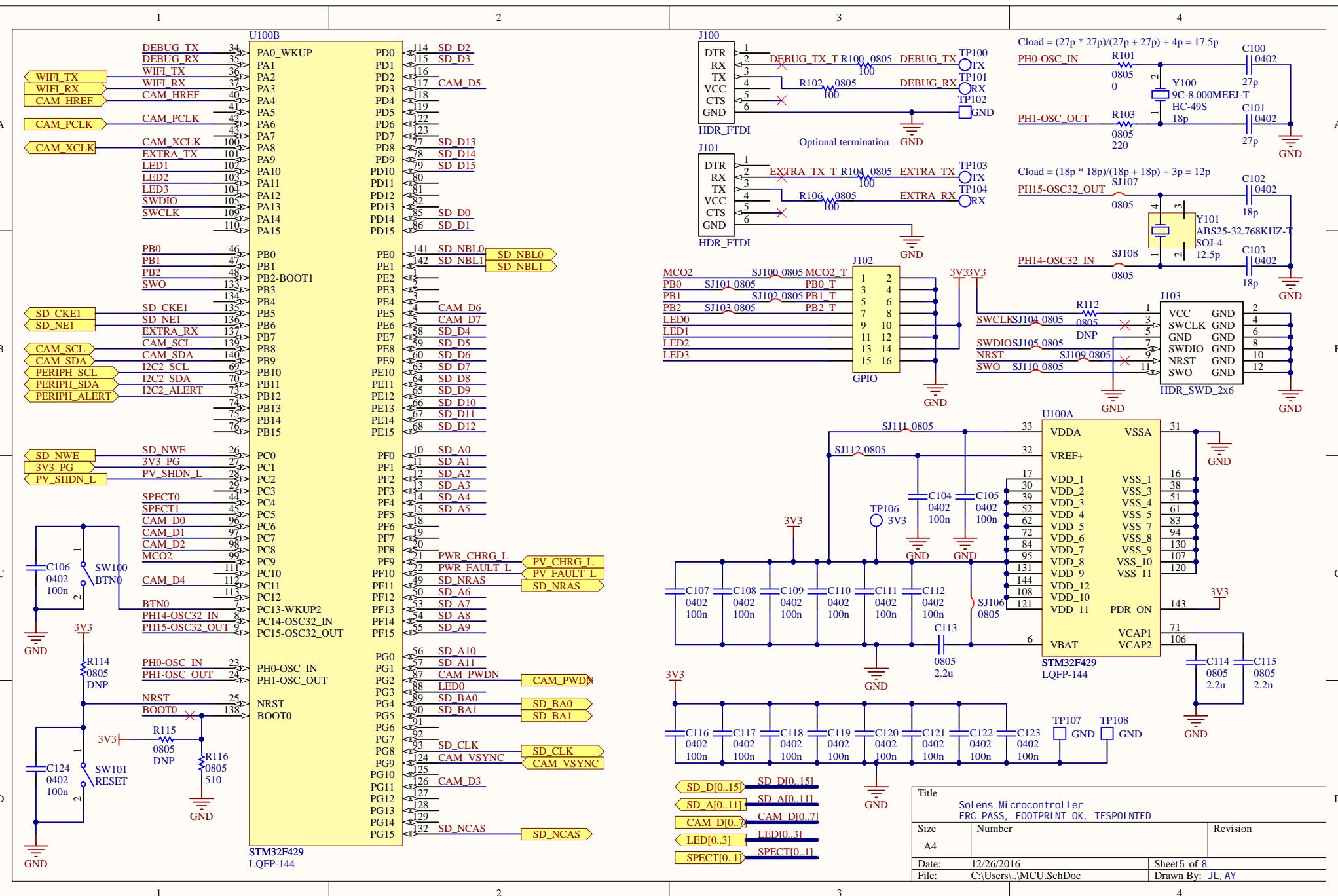
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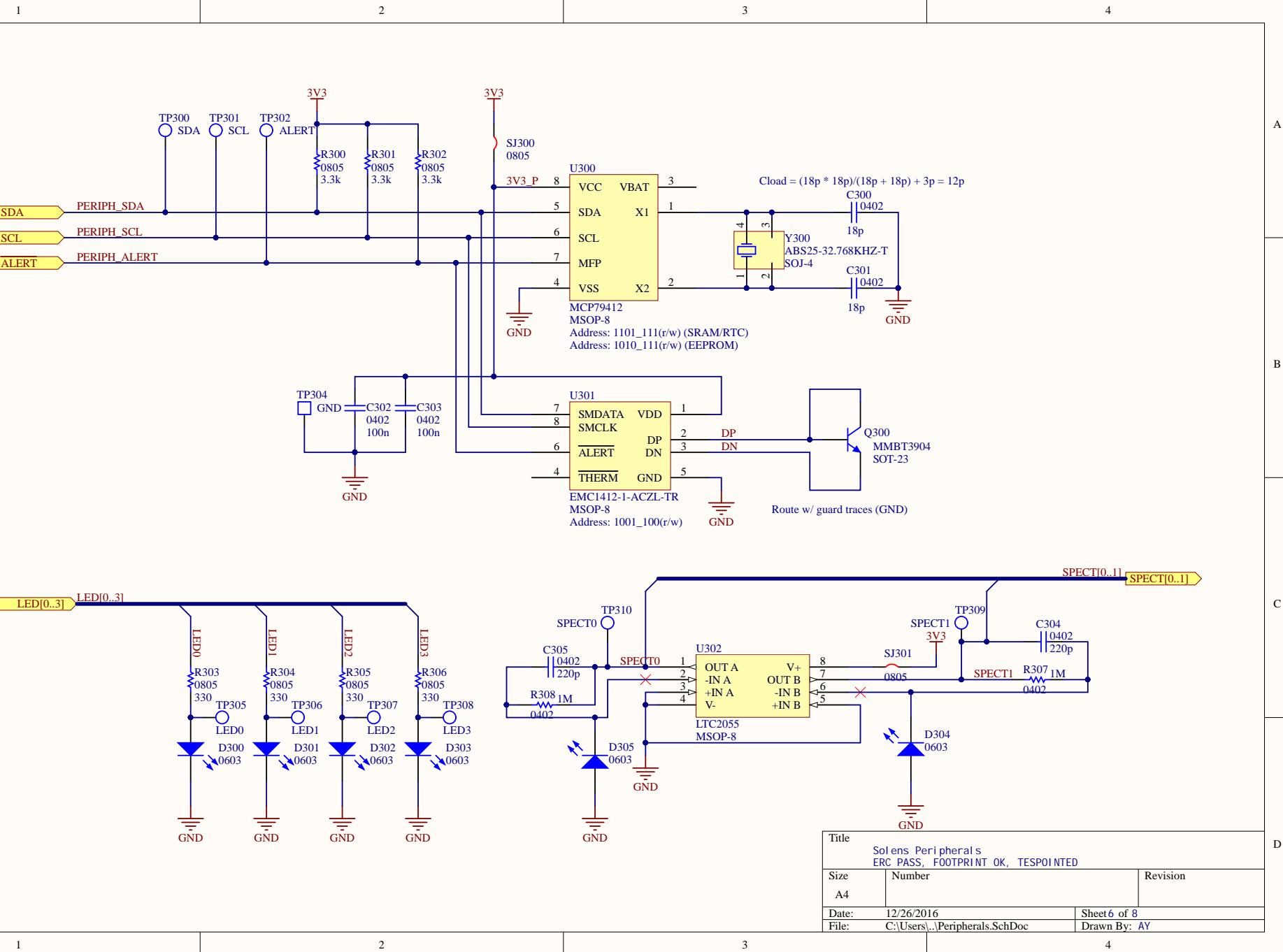
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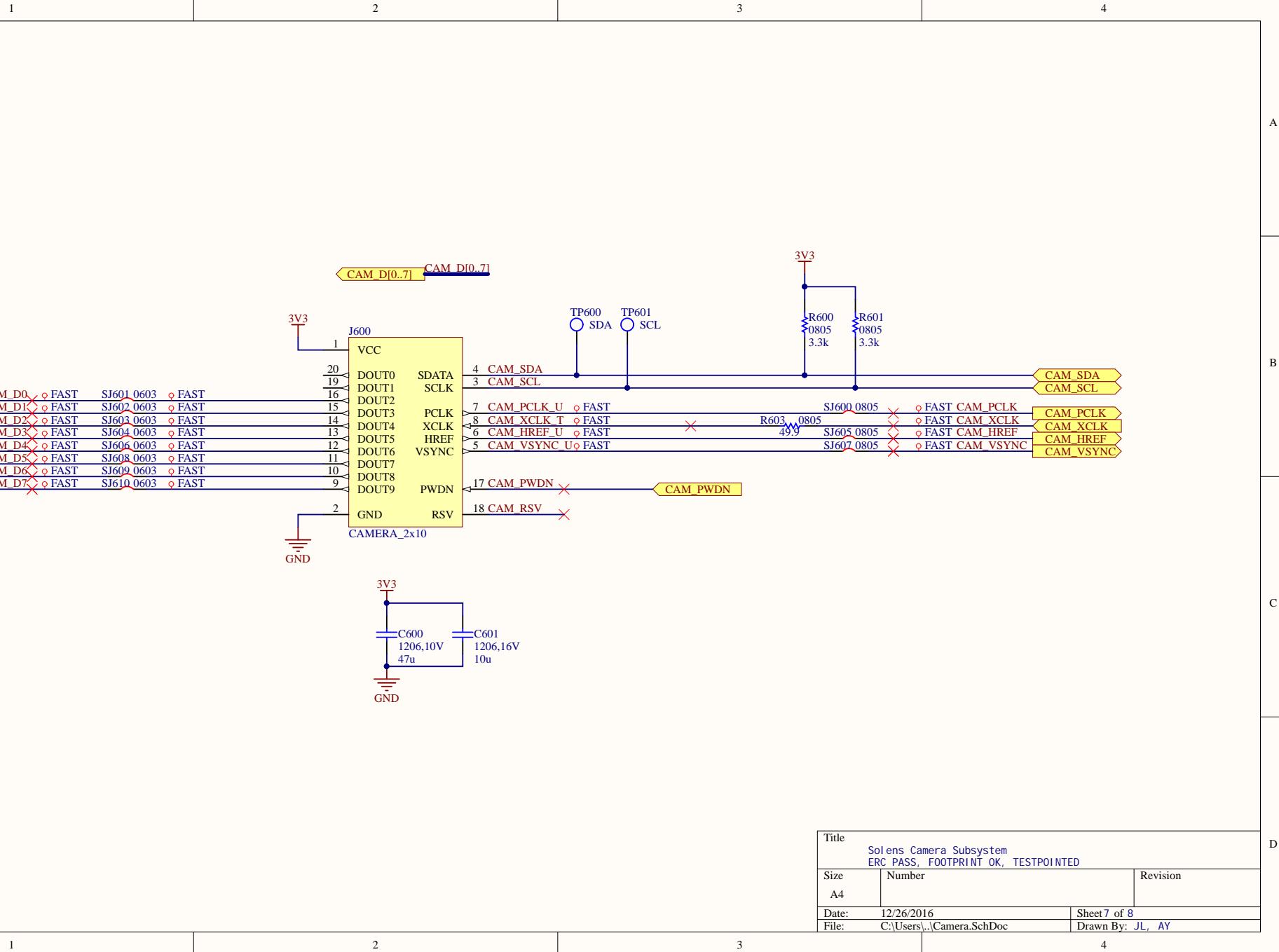
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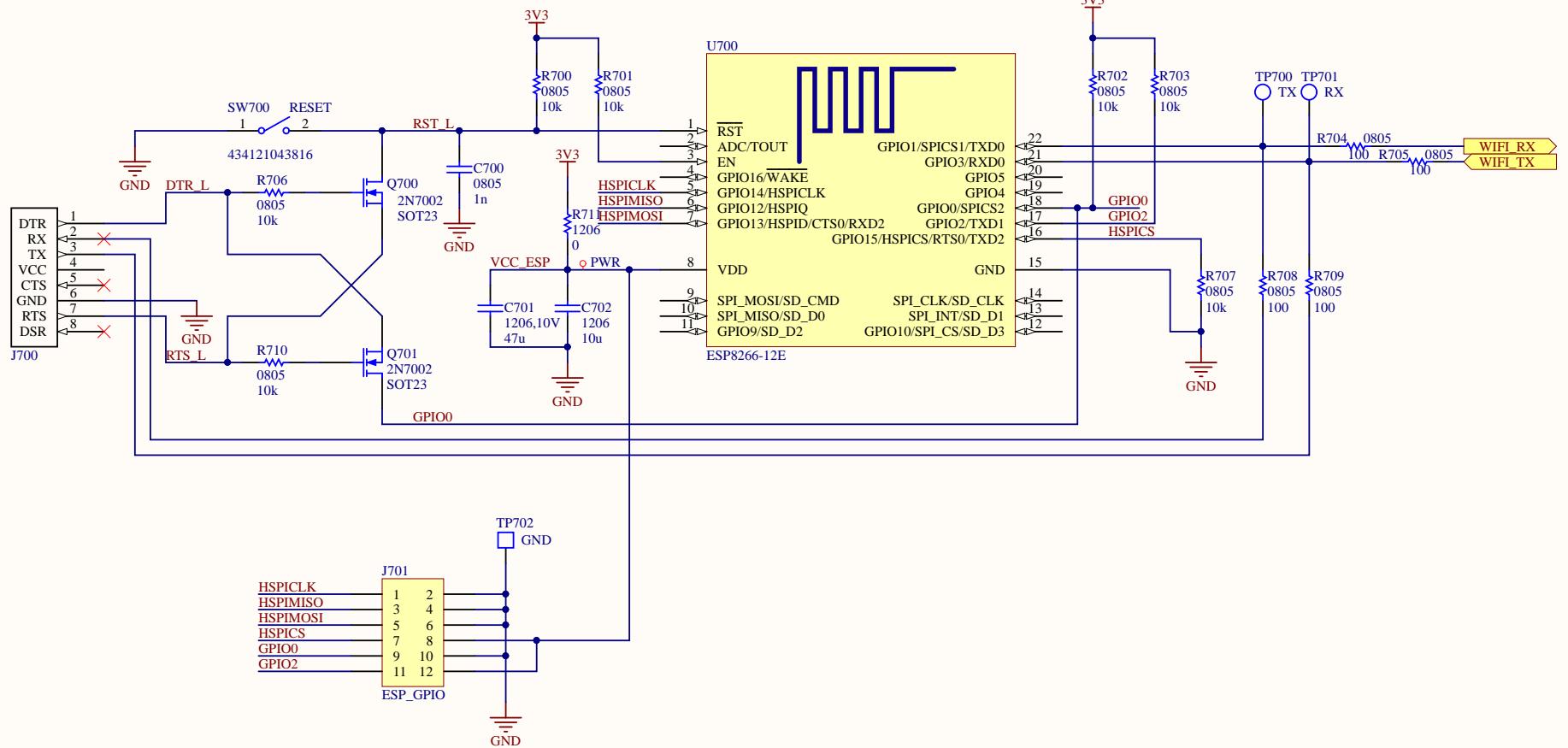
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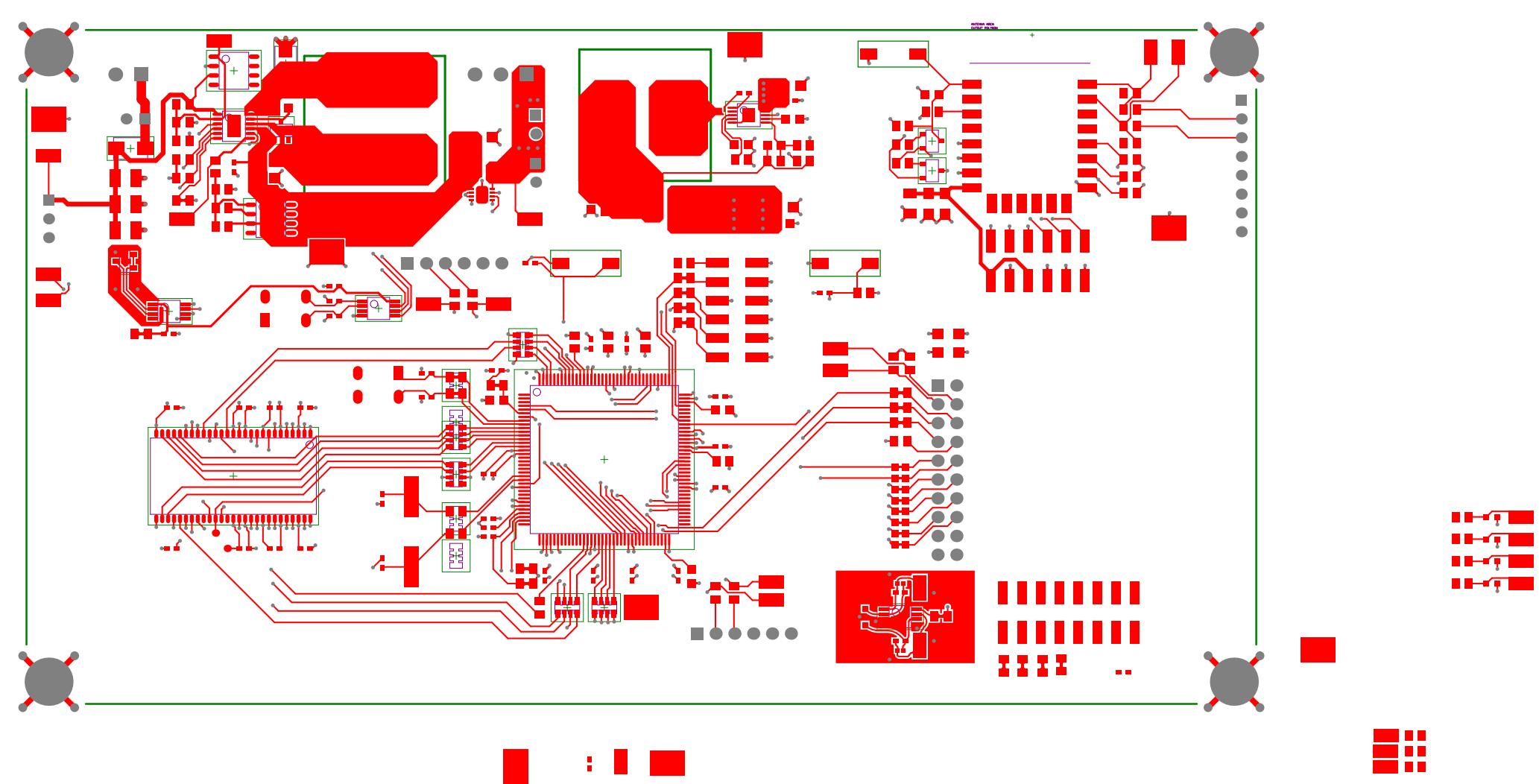


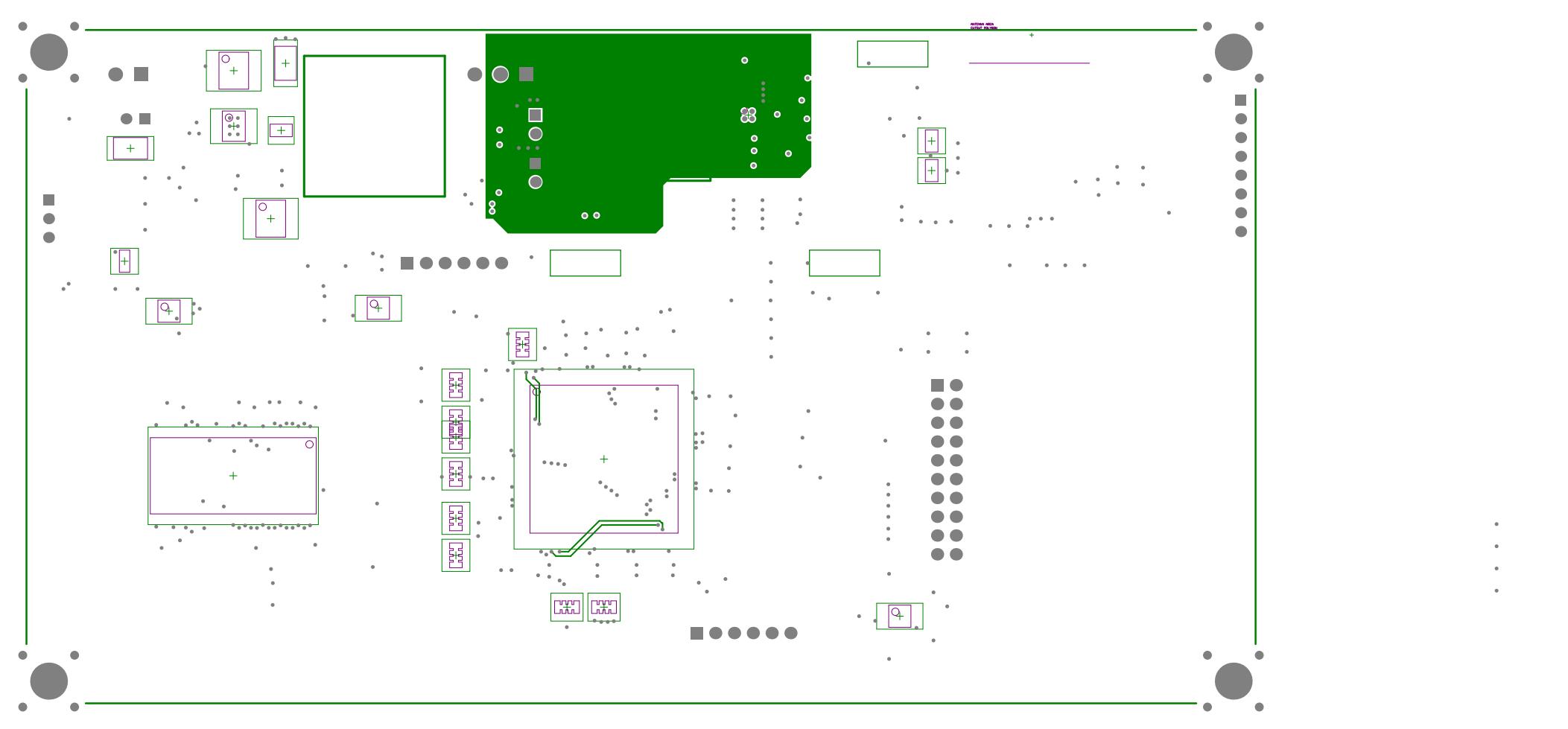


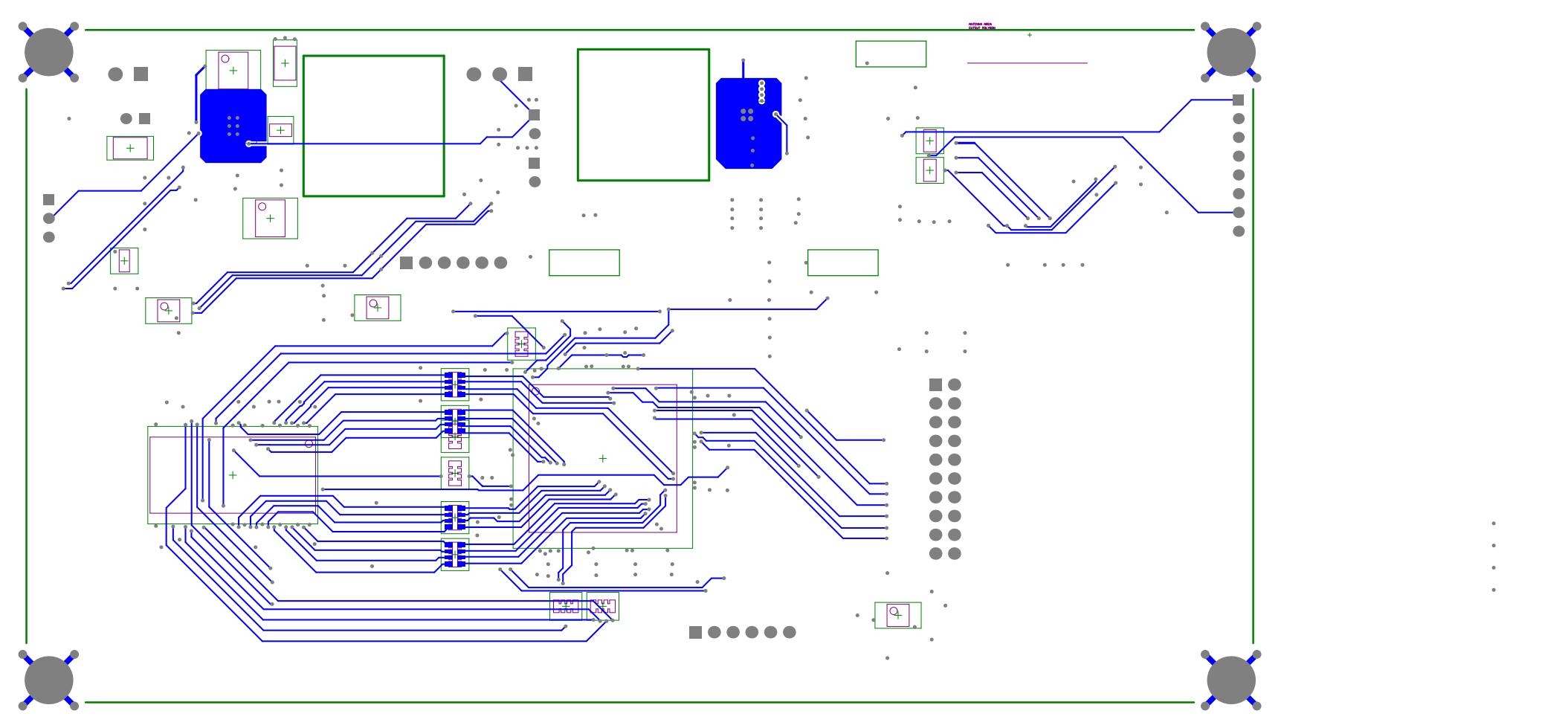


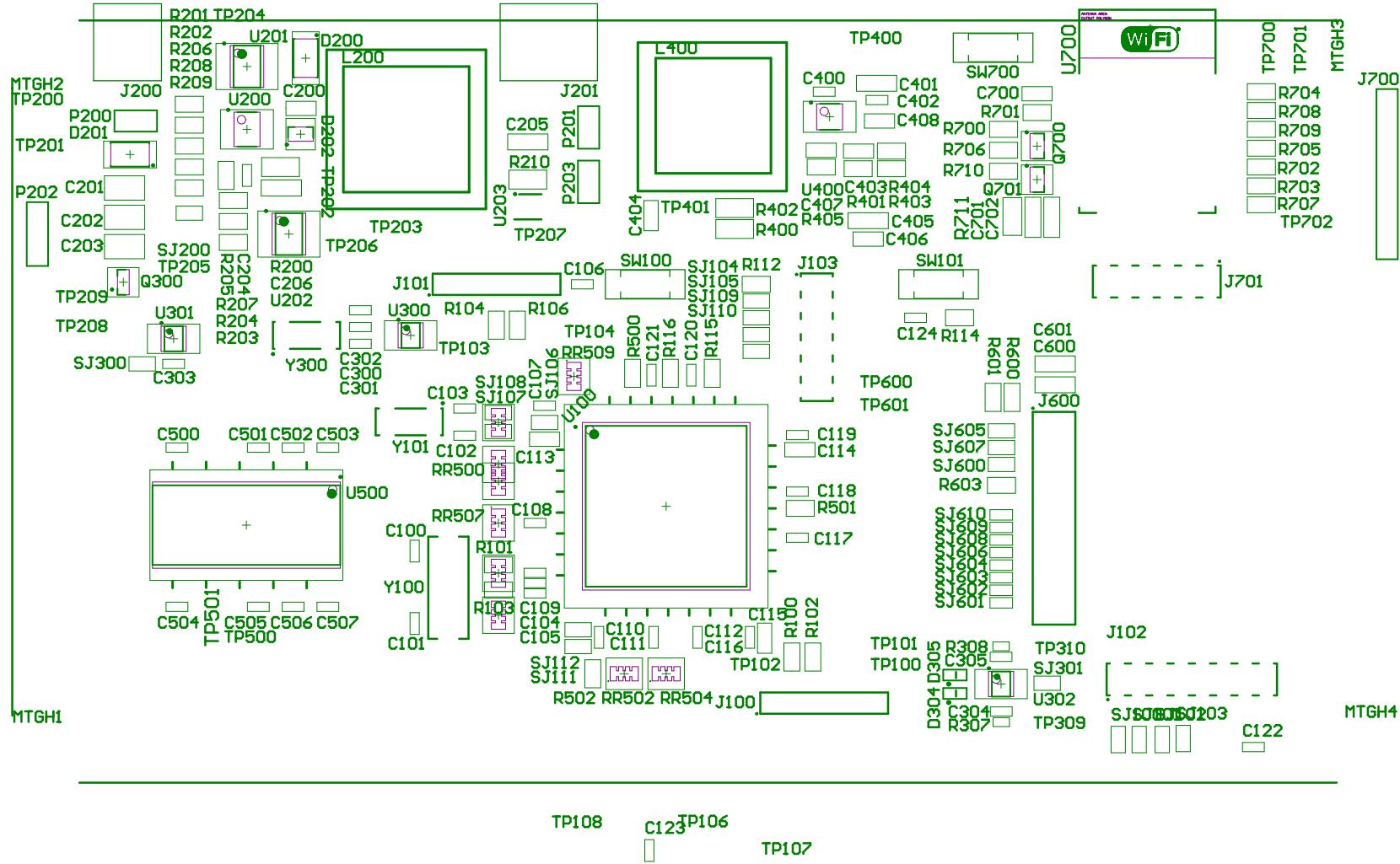


Title Solens Wi Fi Subsystem ERC PASS, FOOTPRINT OK, TESTPOINTED		
Size A4	Number	Revision
Date: 12/26/2016	Sheet 8 of 8	
File: C:\Users..\WiFi.SchDoc		Drawn By: JL, AY









R303D300 TP305  
 R304D301 TP306  
 R305D302 TP307  
 R306D303 TP308  
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TP30000  
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 TP30002  
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