

A

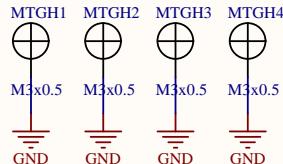
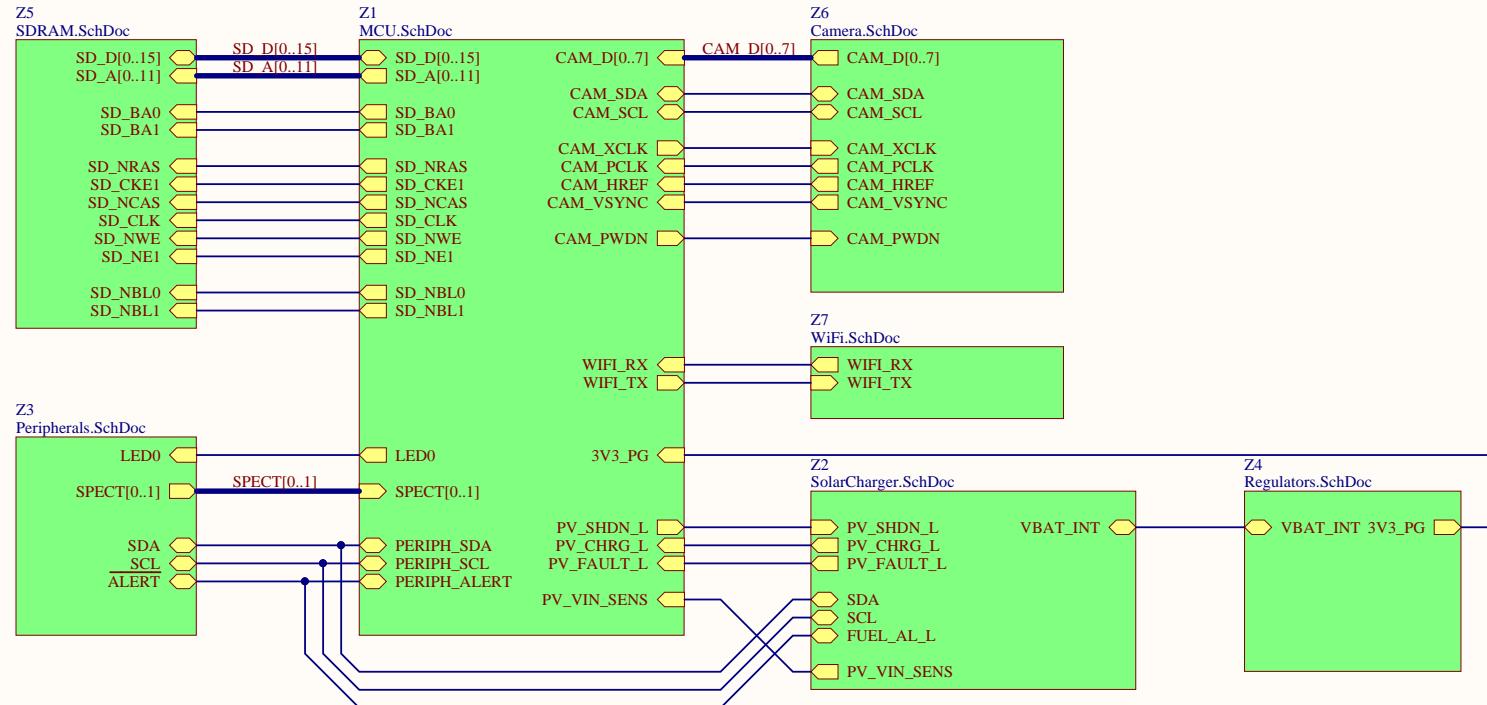
10

B

C

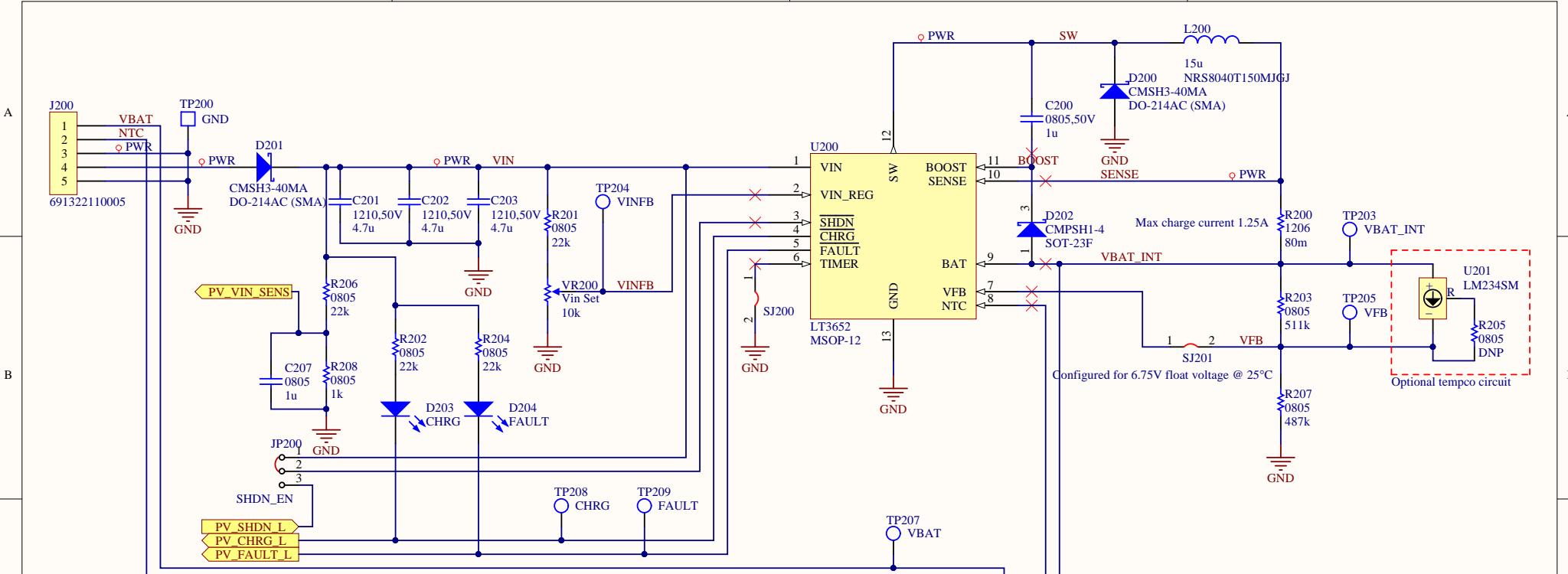
D

10



Title Sol ens Camera Subsystem ERC WARN, FOOTPRINT N/A		
Size A4	Number	Revision
Date:	3/21/2017	Sheet 1 of 8
File:	C:\Users\...\Solens.SchDoc	Drawn By: JL, AY

1 2 3 4



Title		
Solens MPPC/SLA charger		
ERC PASS, FOOTPRINT OK, TESPOINTED		
Size	Number	Revision
A4		
Date: 3/21/2017	Sheet 2 of 8	
File: C:\Users..\SolarCharger.SchDoc		Drawn By: TN, AY

1 2 3 4

A

A

B

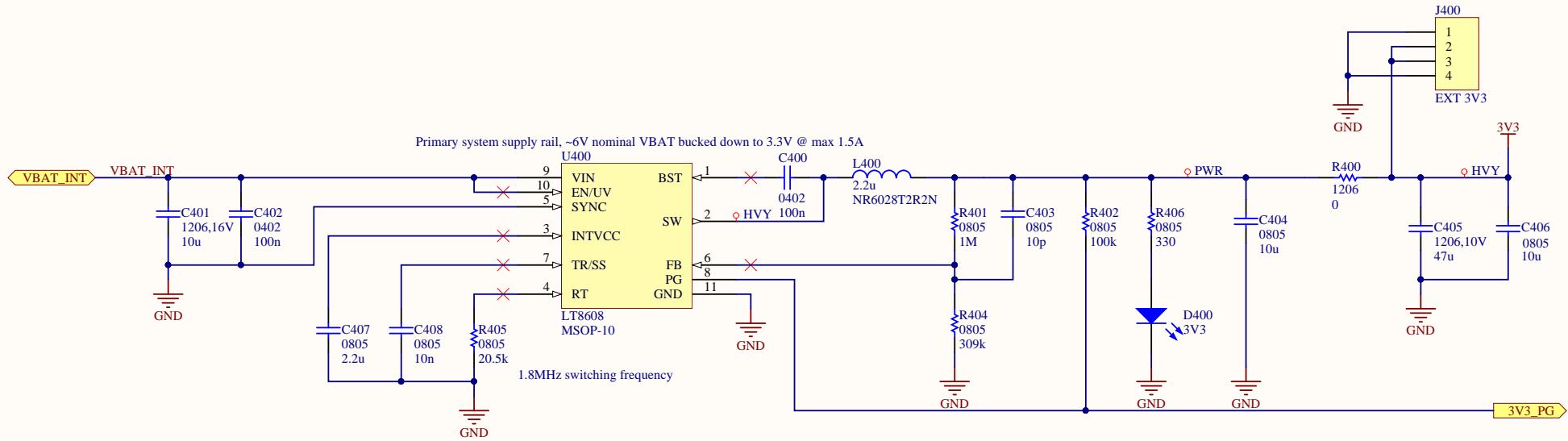
B

C

C

D

D



Title		
Solens Voltage Regulators		ERC PASS, FOOTPRINT OK, TESPOINTED
Size		Revision
A4		
Date:	3/21/2017	Sheet 3 of 8
File:	C:\Users...\Regulators.SchDoc	Drawn By: TN, AY

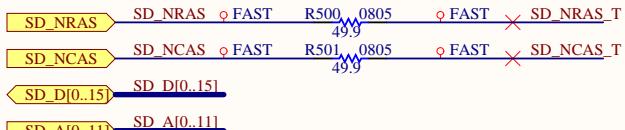
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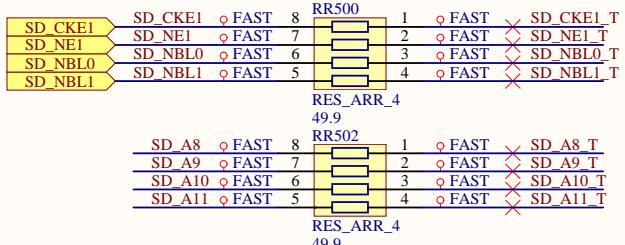
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4

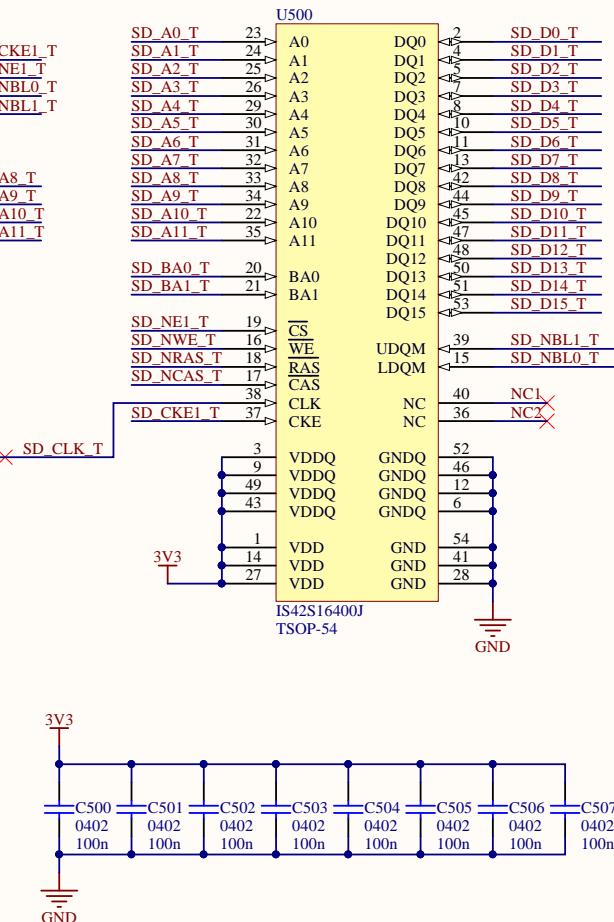
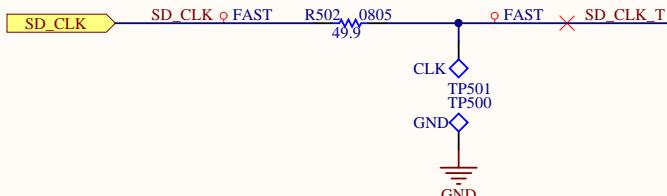
A



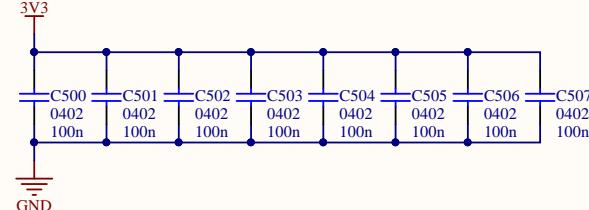
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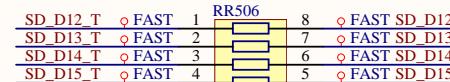
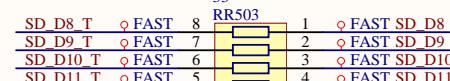
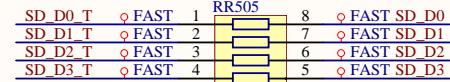
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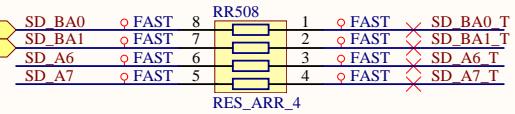
D



DATA termination to be placed halfway between MCU and RAM



ADDR and CLK termination to be placed at MCU



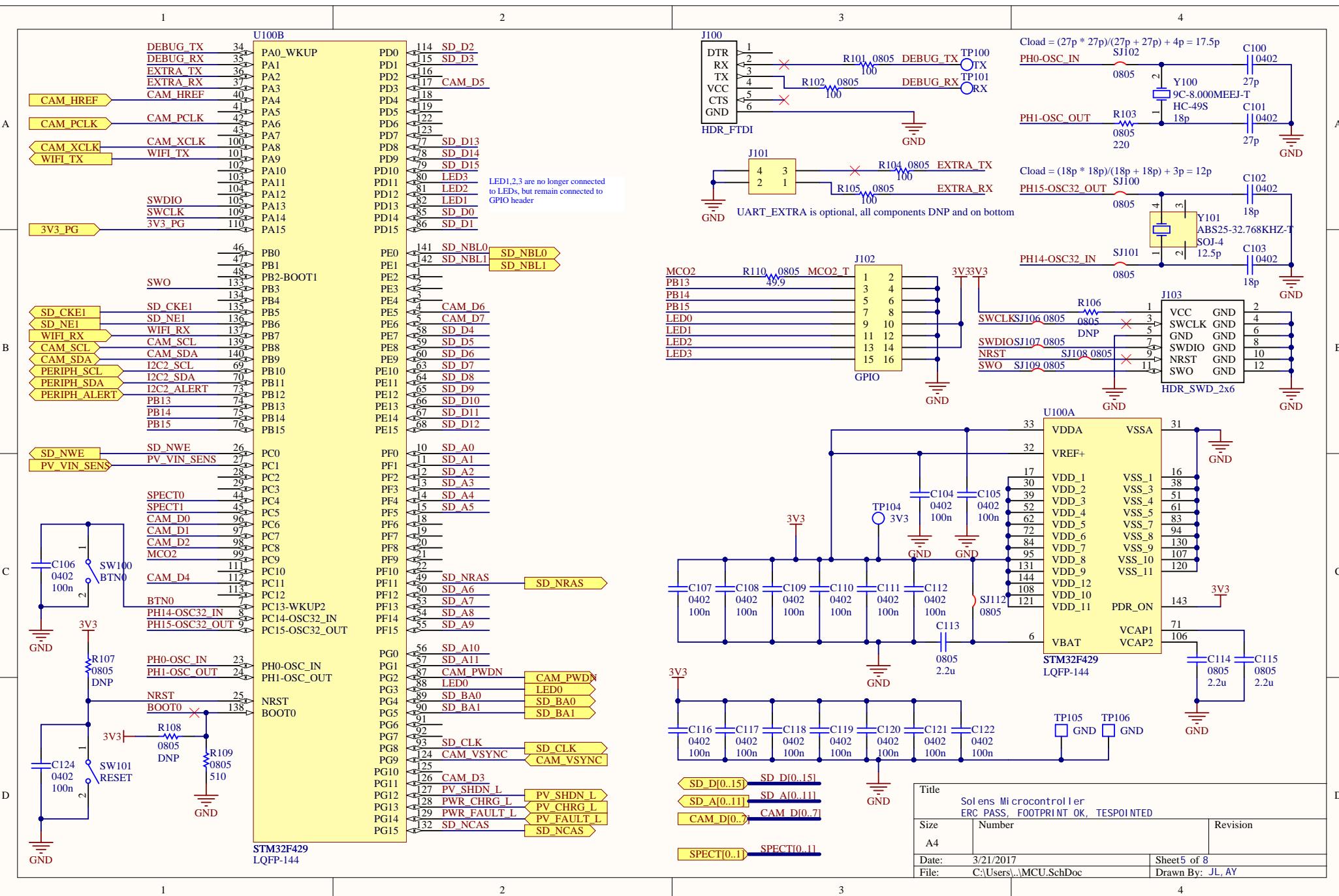
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Solens_SDRAM		
Size	Number	Revision
A4		
Date:	3/21/2017	Sheet 4 of 8
File:	C:\Users\...\SDRAM.SchDoc	Drawn By: JL, AY

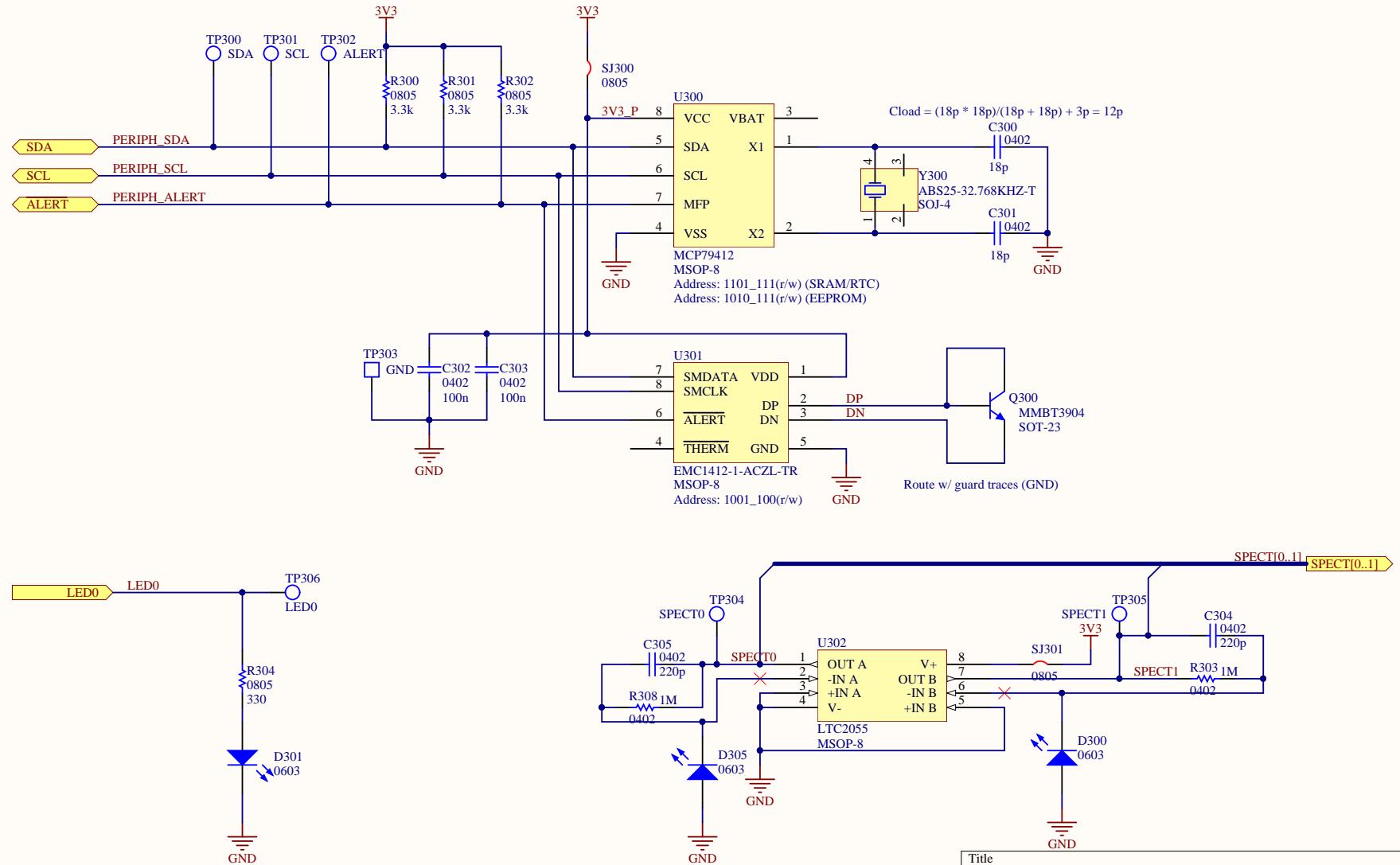
1

2

3

4





Title	Solens Peripherals ERC PASS, FOOTPRINT OK, TESPOINTED	
Size	Number	Revision
A4		
Date:	3/21/2017	Sheet 6 of 8
File:	C:\Users\...\Peripherals.SchDoc	Drawn By: AY

A

A

B

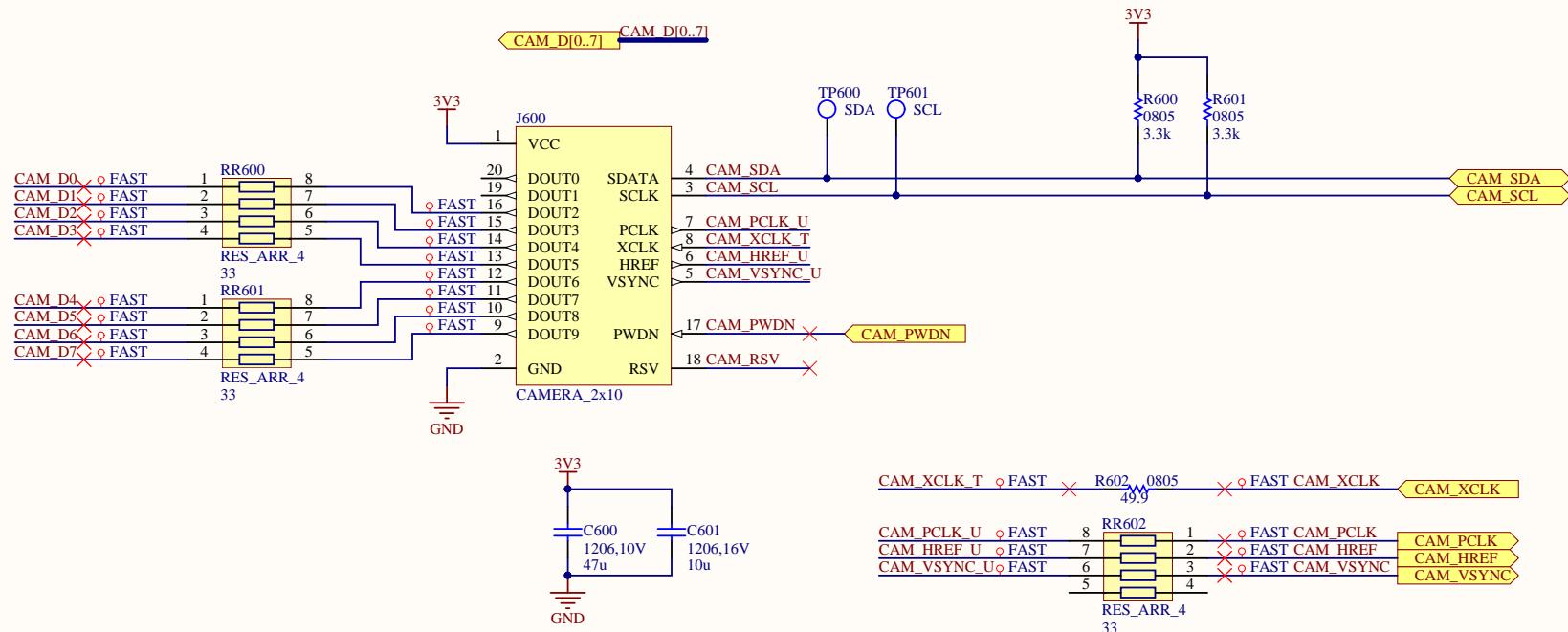
B

C

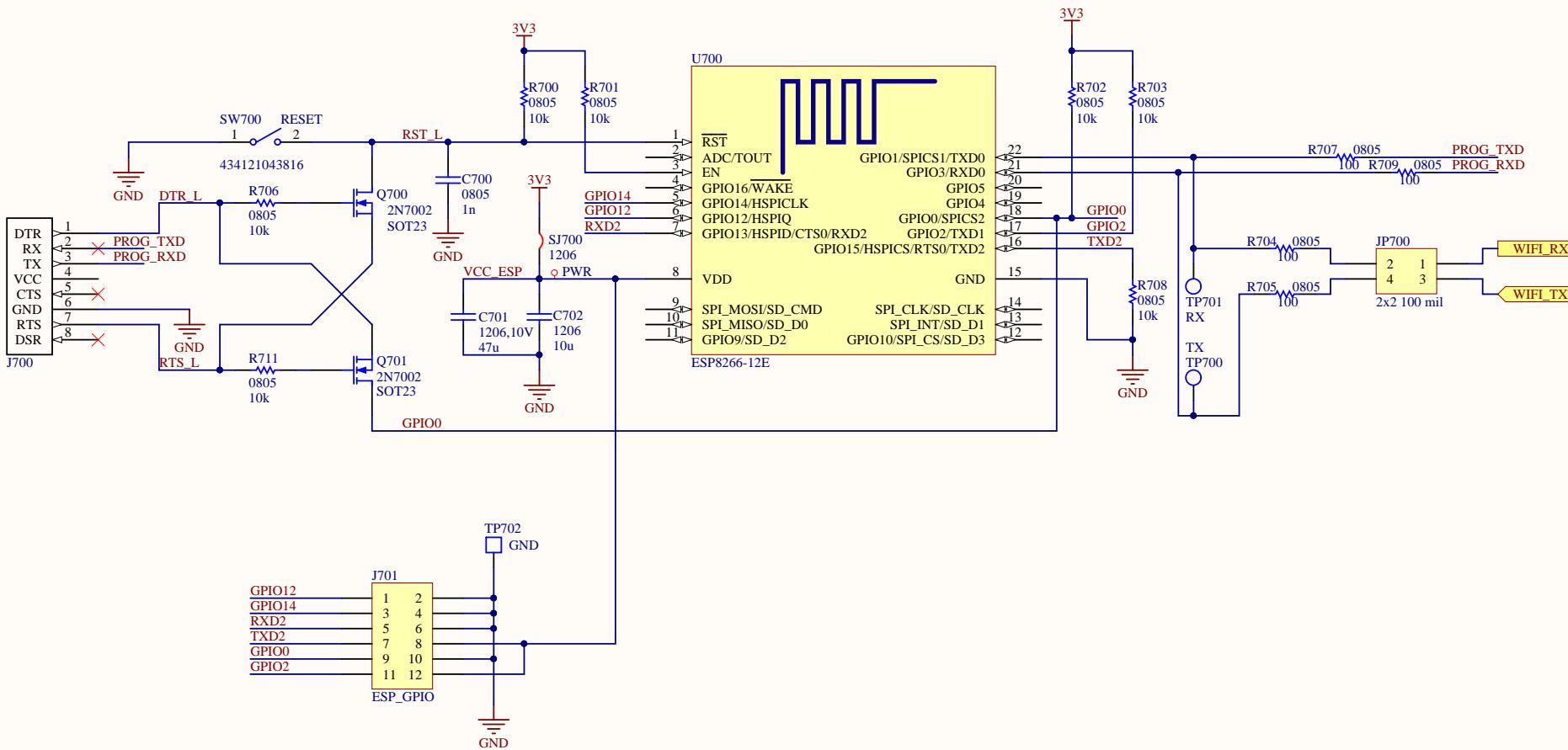
C

D

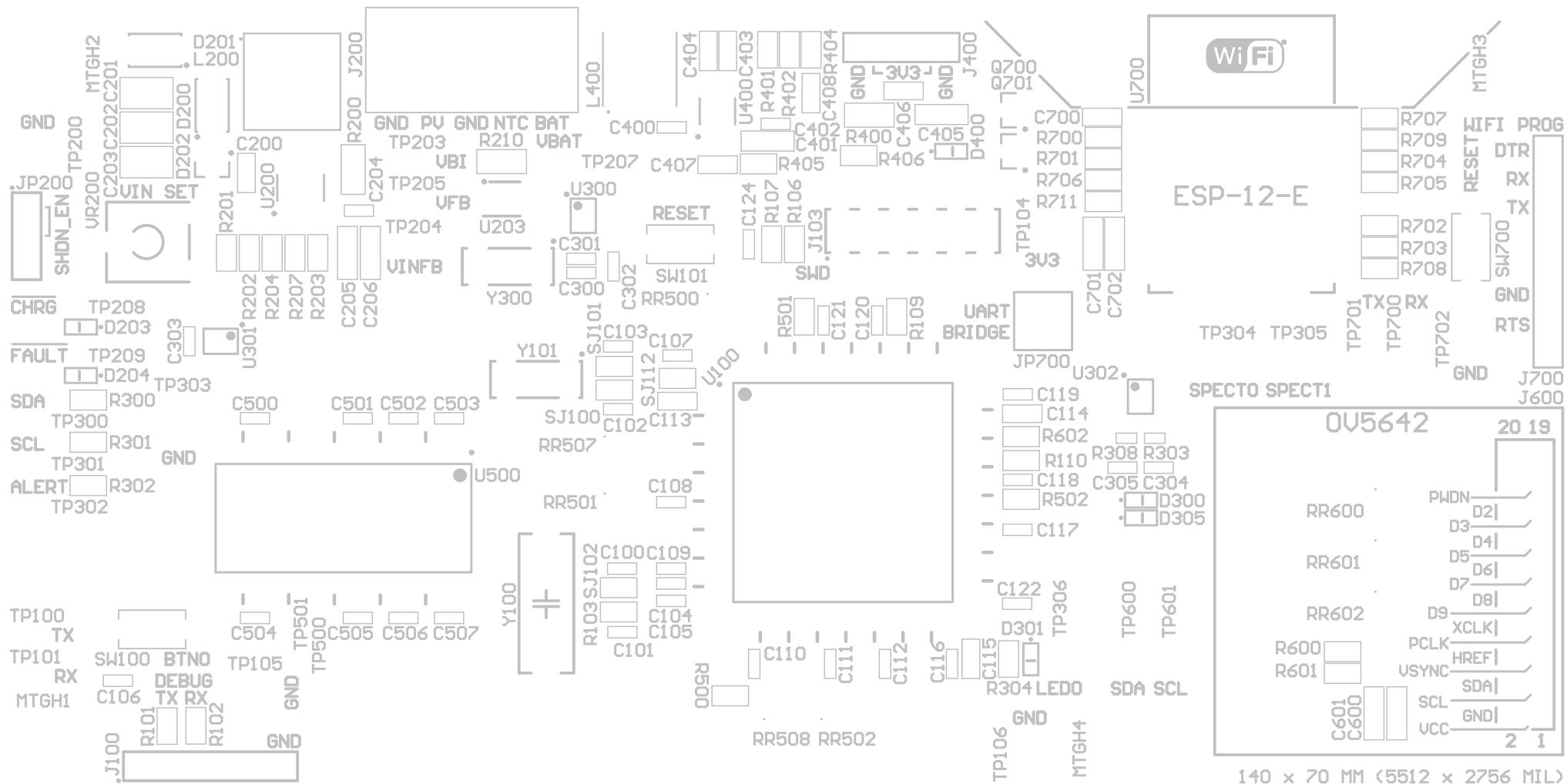
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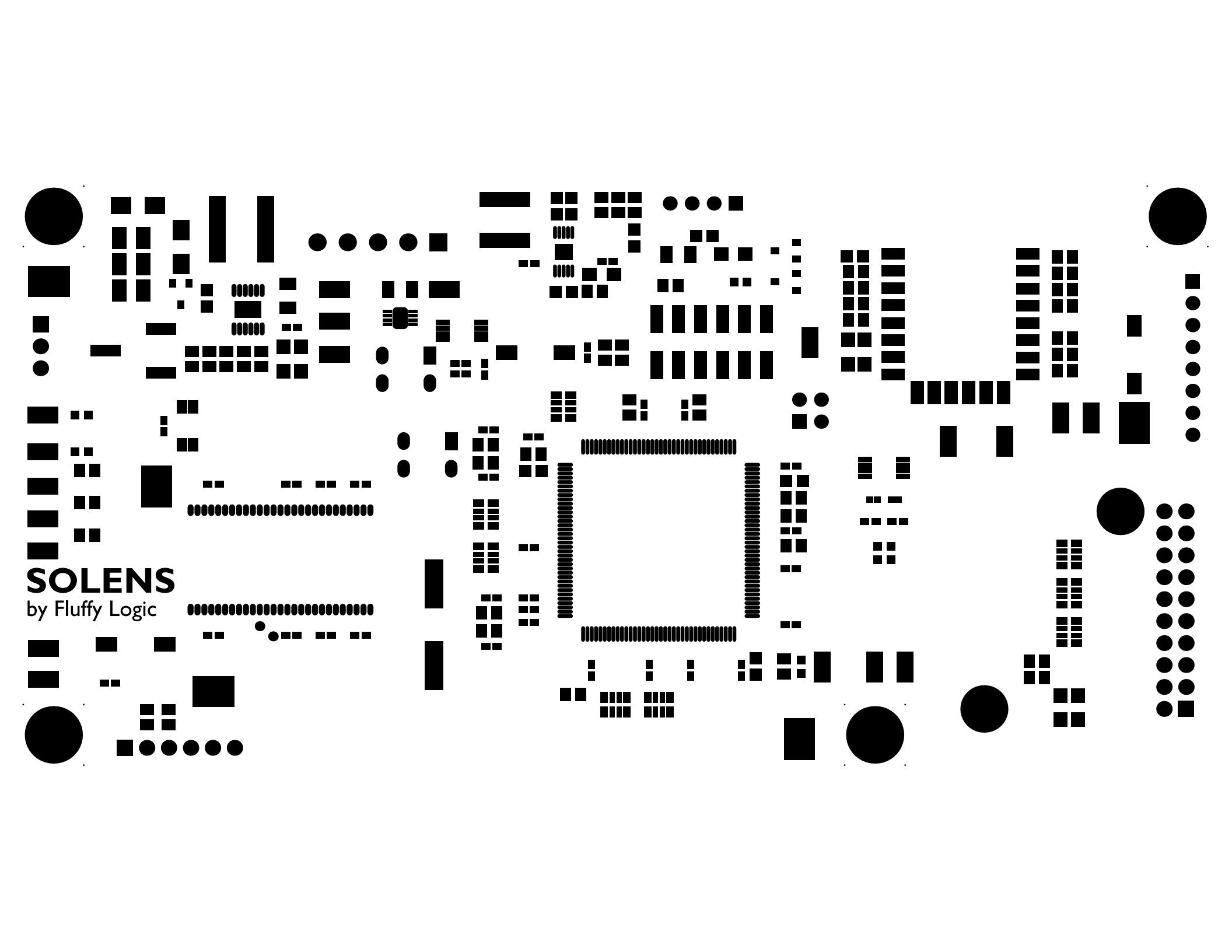
Title Solens Camera Subsystem ERC PASS, FOOTPRINT OK, TESTPOINTED		
Size A4	Number	Revision
Date: 3/21/2017	Sheet 7 of 8	
File: C:\Users\...\Camera.SchDoc		Drawn By: JL, AY



Title		
Sol ens Wi Fi Subsystem ERC PASS, FOOTPRINT OK, TESTPOINTED		
Size	Number	Revision
A4		
Date:	3/21/2017	Sheet 8 of 8
File:	C:\Users\...\WiFi.SchDoc	Drawn By: JL, AY

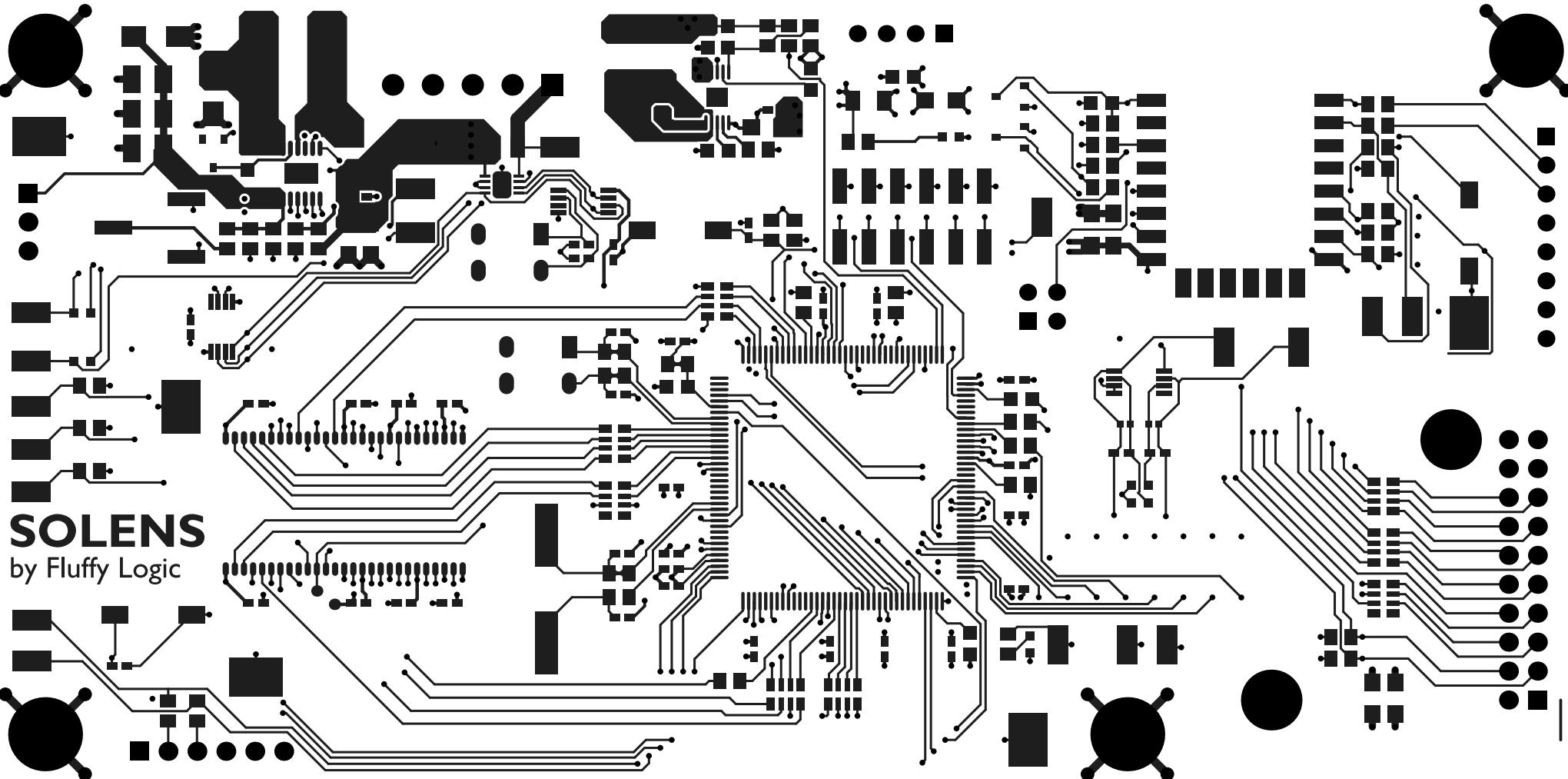


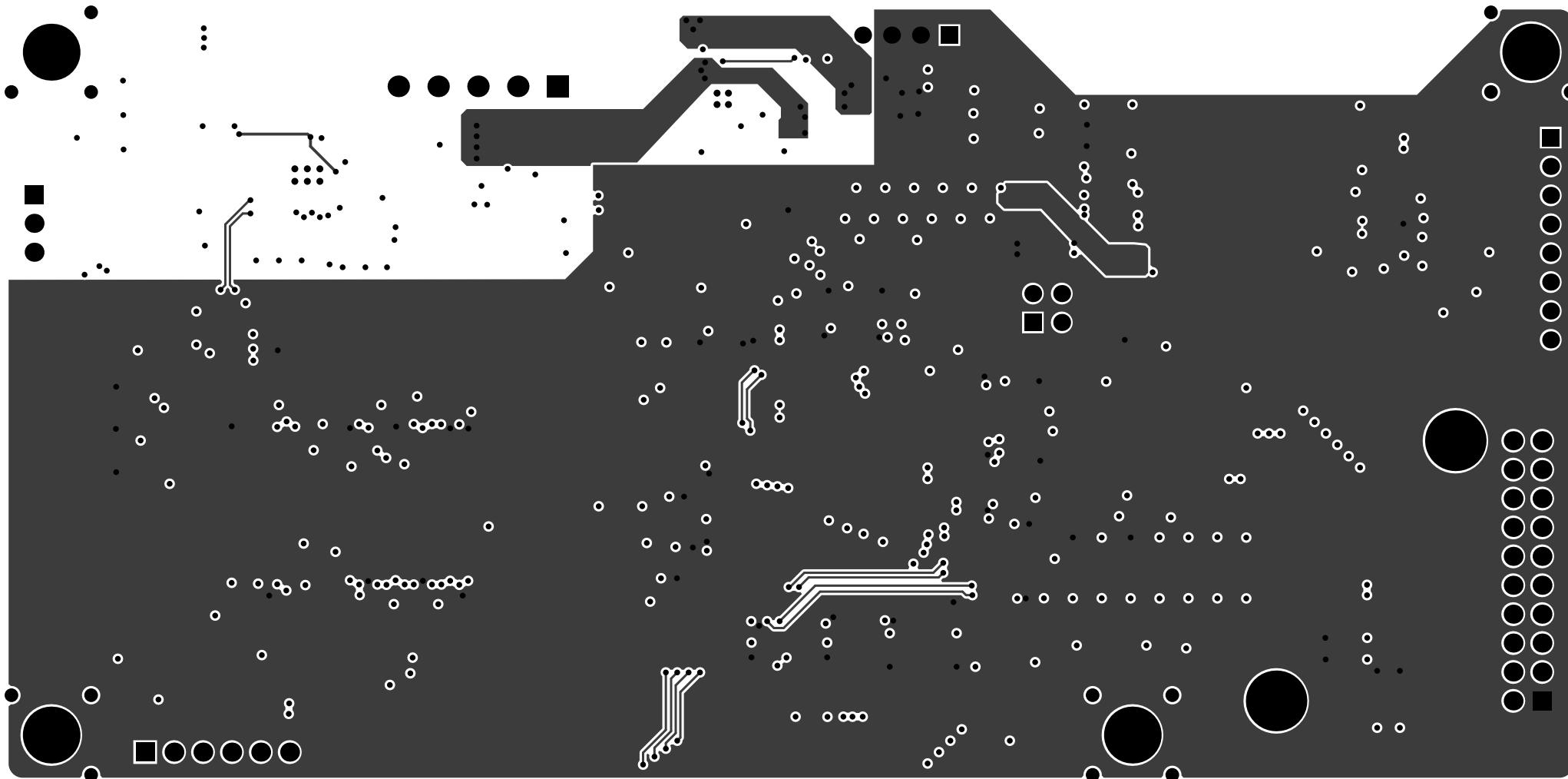
140 x 70 MM (5512 x 2756 MIL)

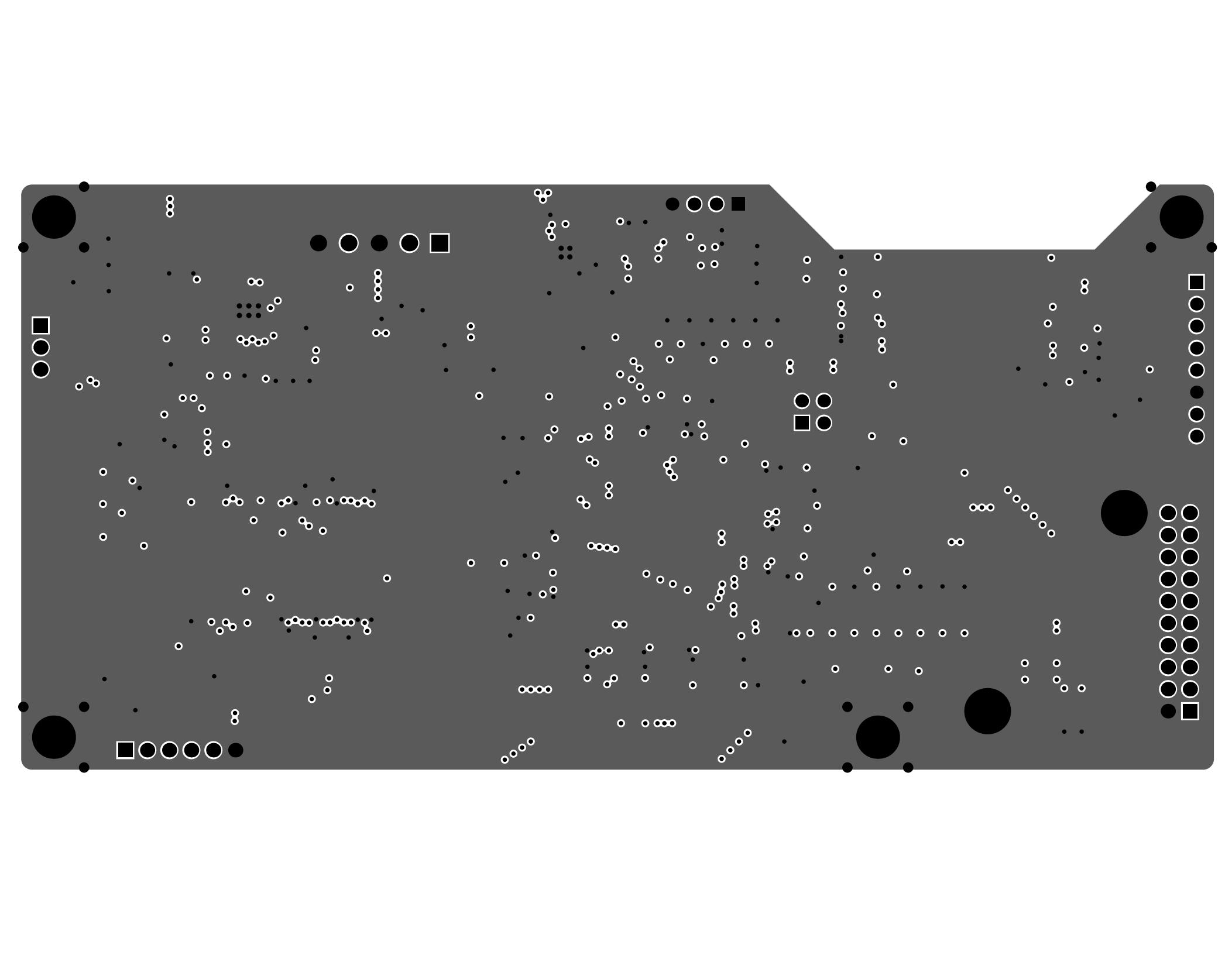


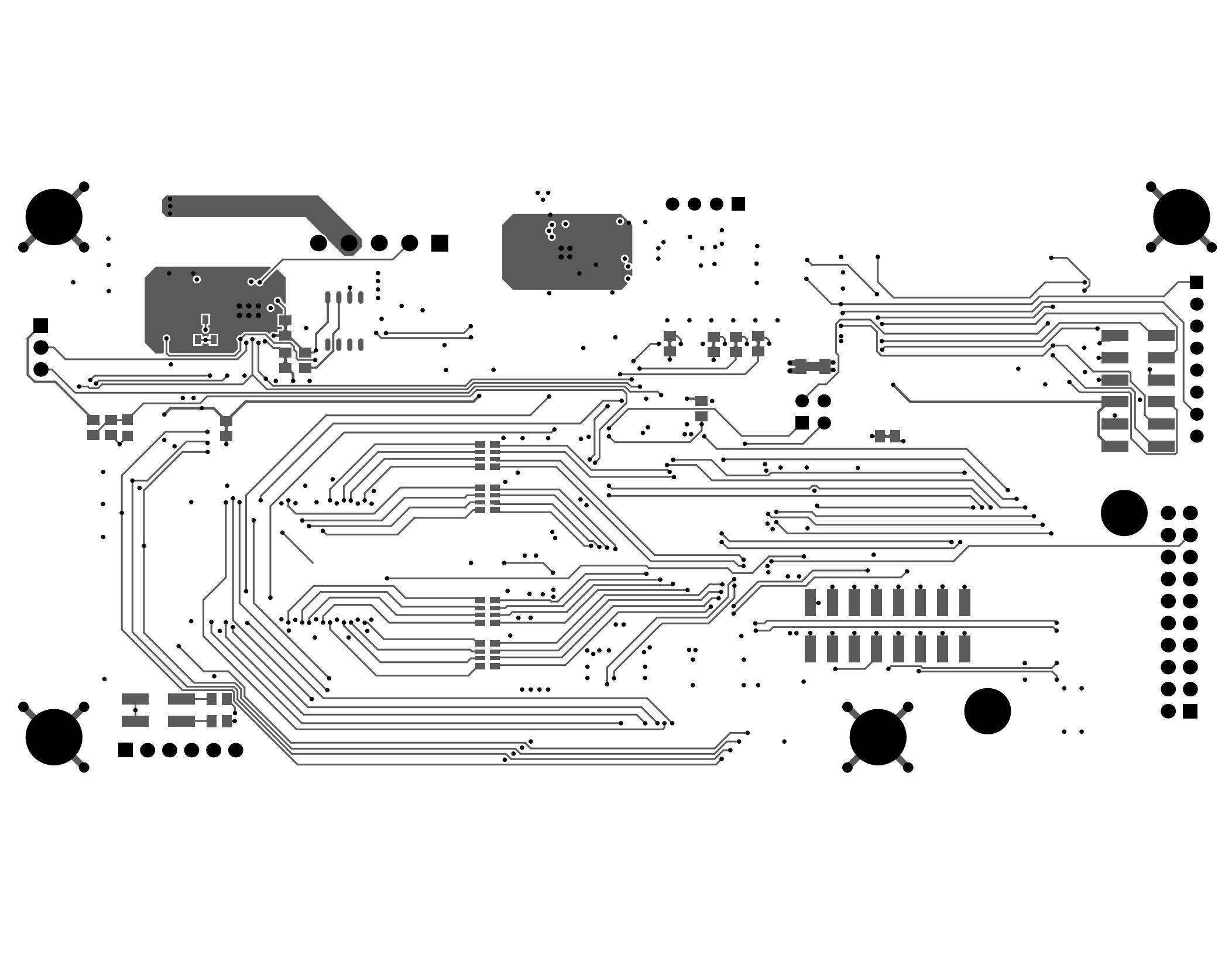
SOLENS

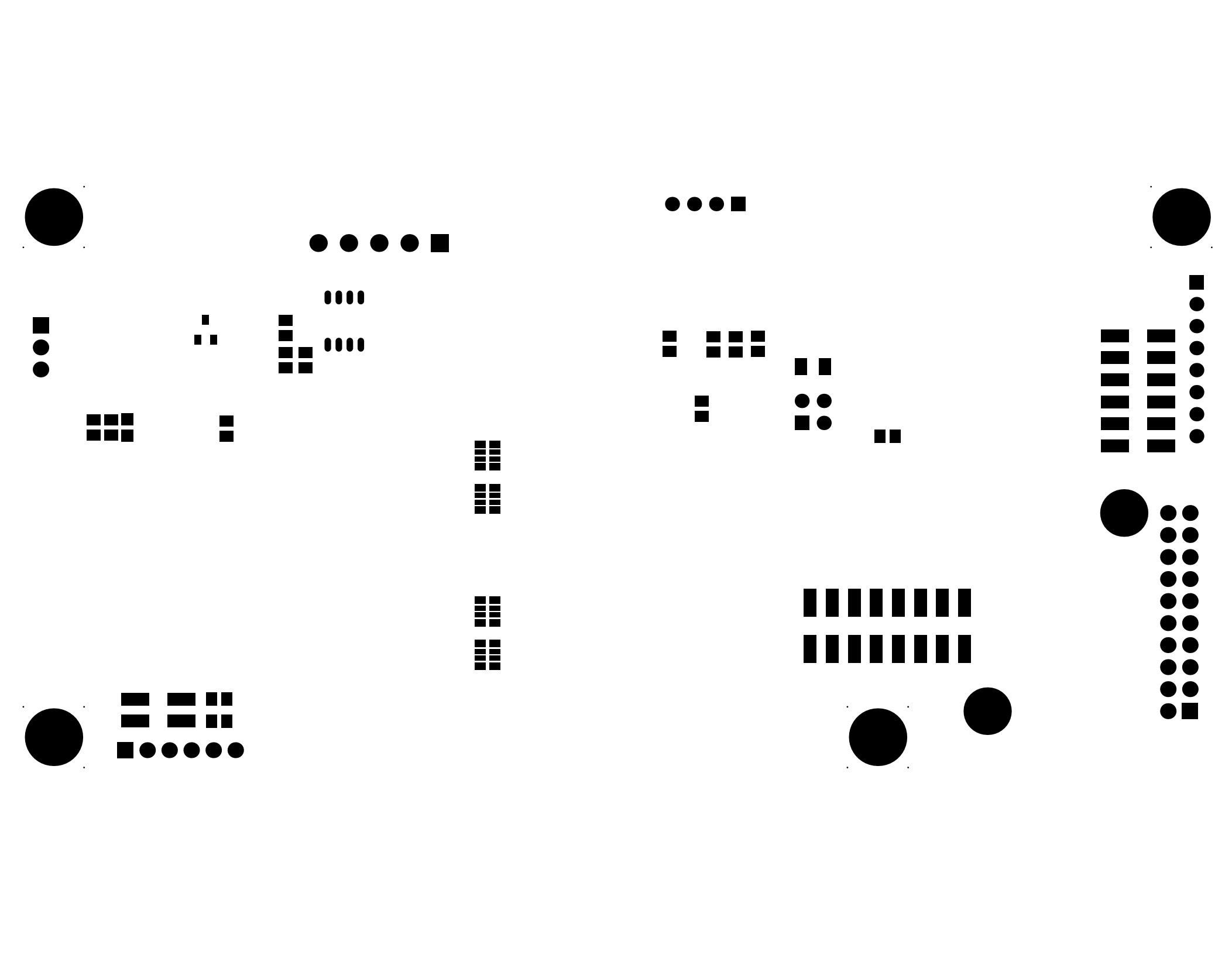
by Fluffy Logic



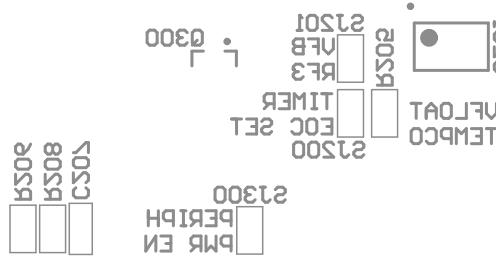




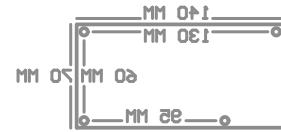




Ben Hepreliu
Jeffery Lin
Taylor Nuykuu
Jeremiah Rabbe
Akira Younghood



RR504 RR505 RR506 RR503



USE SCREENS

