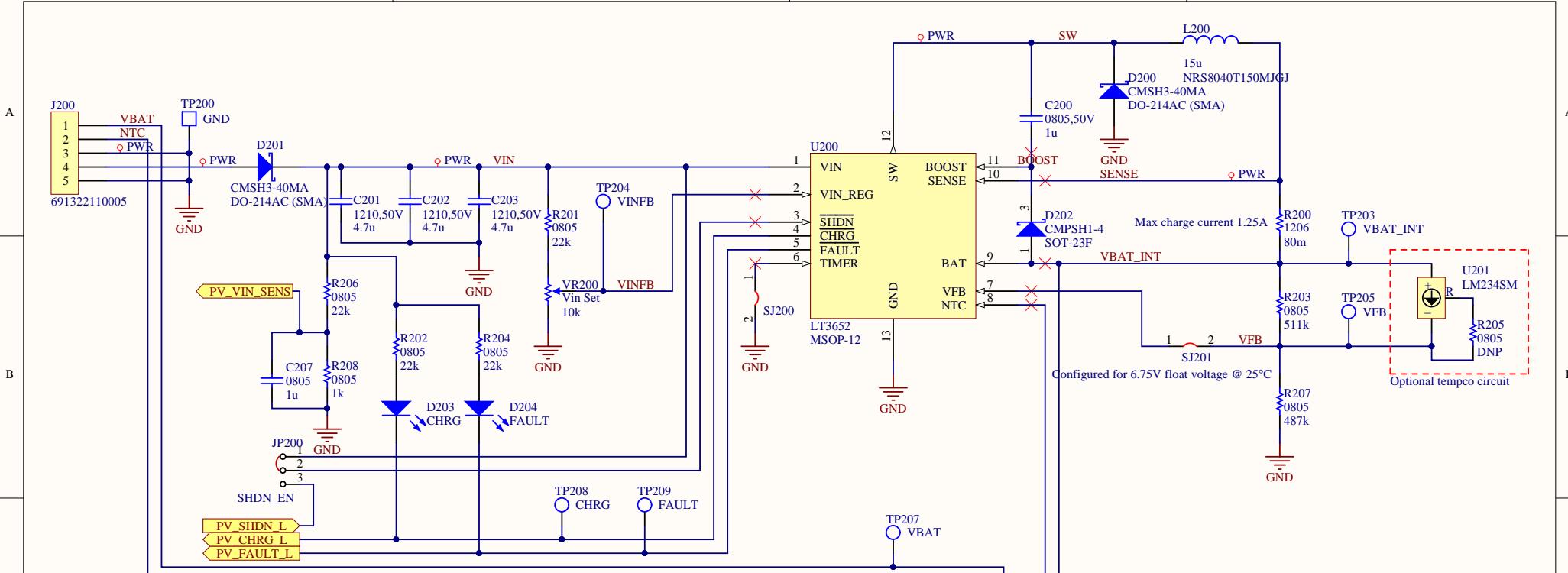


1 2 3 4



Title		
Solens MPPC/SLA charger ERC PASS, FOOTPRINT OK, TESPOINTED		
Size	Number	Revision
A4		
Date:	3/26/2017	Sheet 2 of 8
File:	C:\Users..\SolarCharger.SchDoc	Drawn By: TN, AY

1 2 3 4

A

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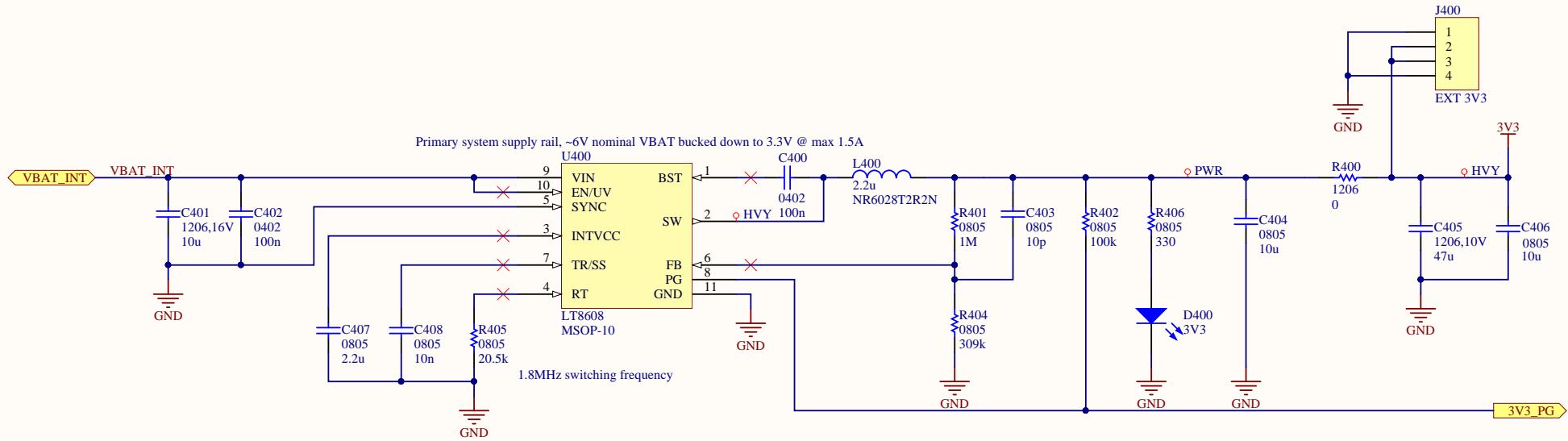
B

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D



Title		
Solens Voltage Regulators		ERC PASS, FOOTPRINT OK, TESPOINTED
Size		Revision
A4		
Date:	3/26/2017	Sheet 3 of 8
File:	C:\Users...\Regulators.SchDoc	Drawn By: TN, AY

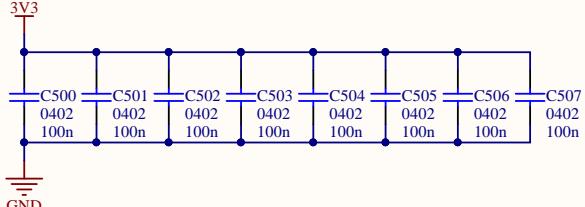
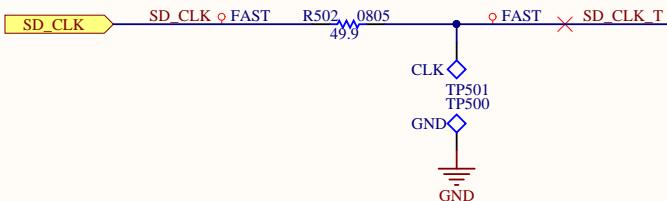
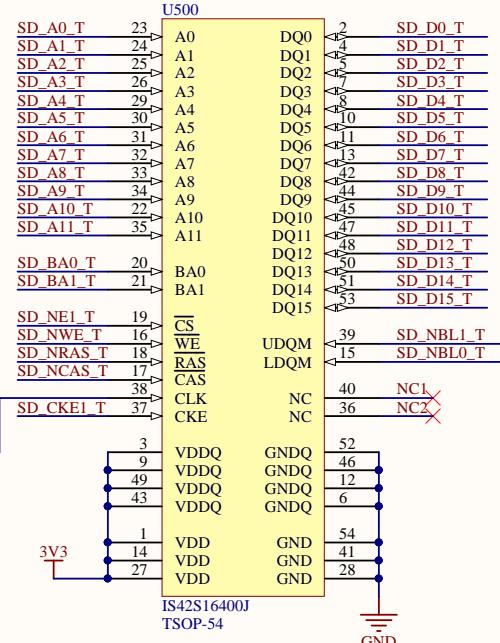
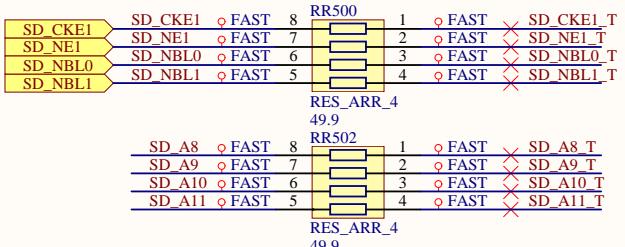
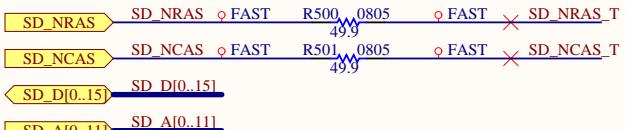
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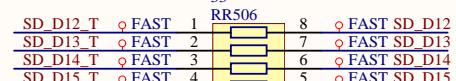
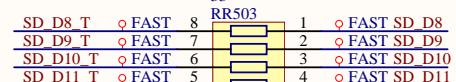
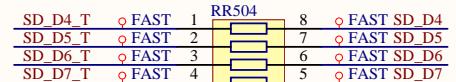
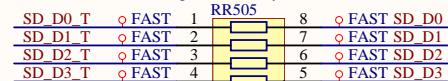
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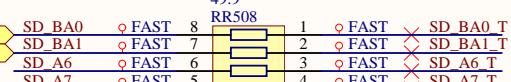
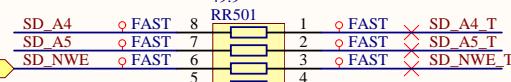
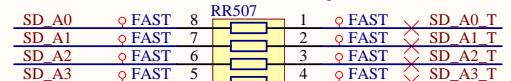
A



DATA termination to be placed halfway between MCU and RAM



ADDR and CLK termination to be placed at MCU



Title: Solens_SDRAM
ERC PASS, FOOTPRINT OK, TESPOINTED

Size	Number	Revision
A4		

Date:	Sheet 4 of 8
3/26/2017	

File:	Drawn By:
C:\Users\...\SDRAM.SchDoc	JL, AY

1

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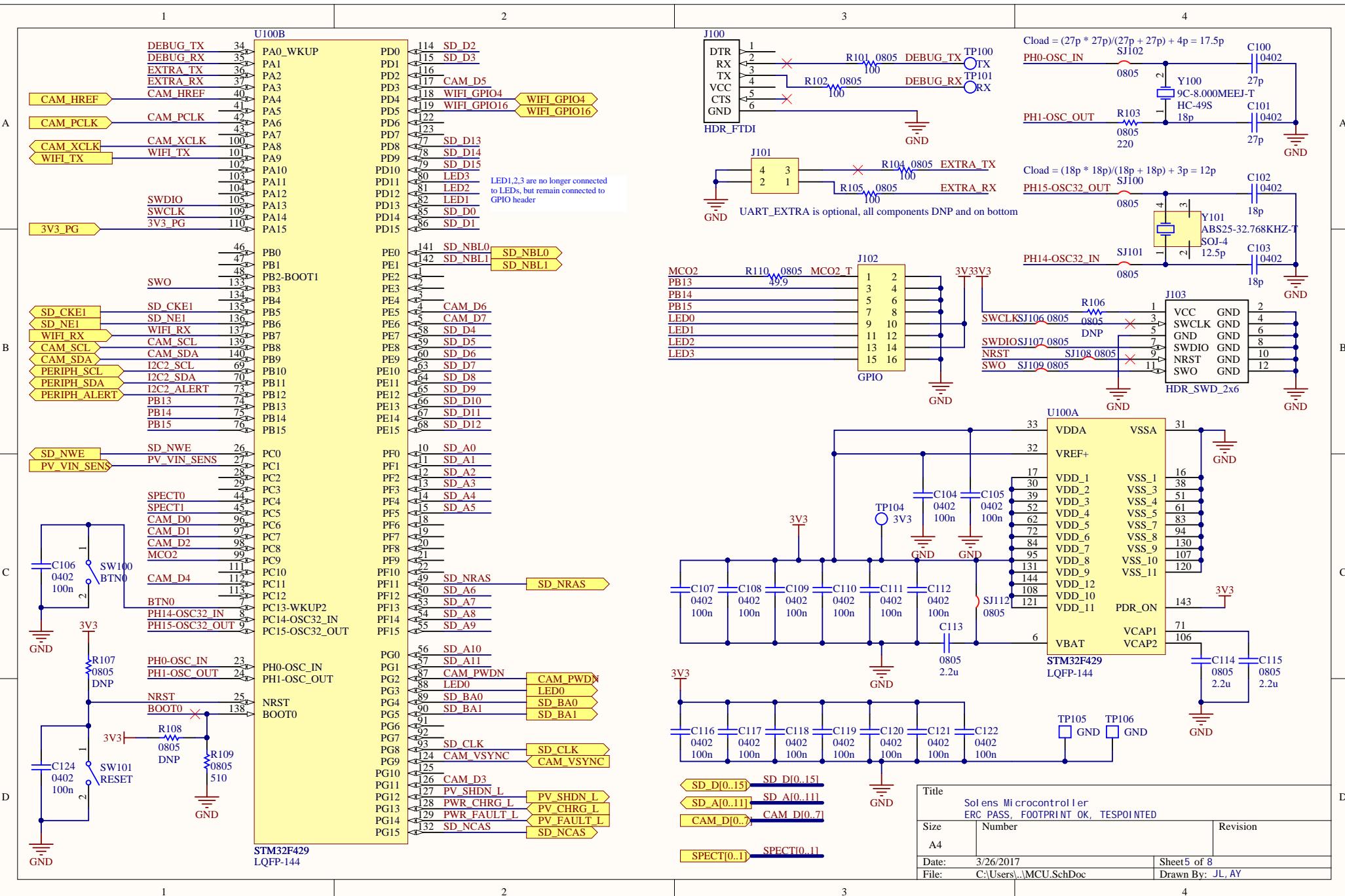
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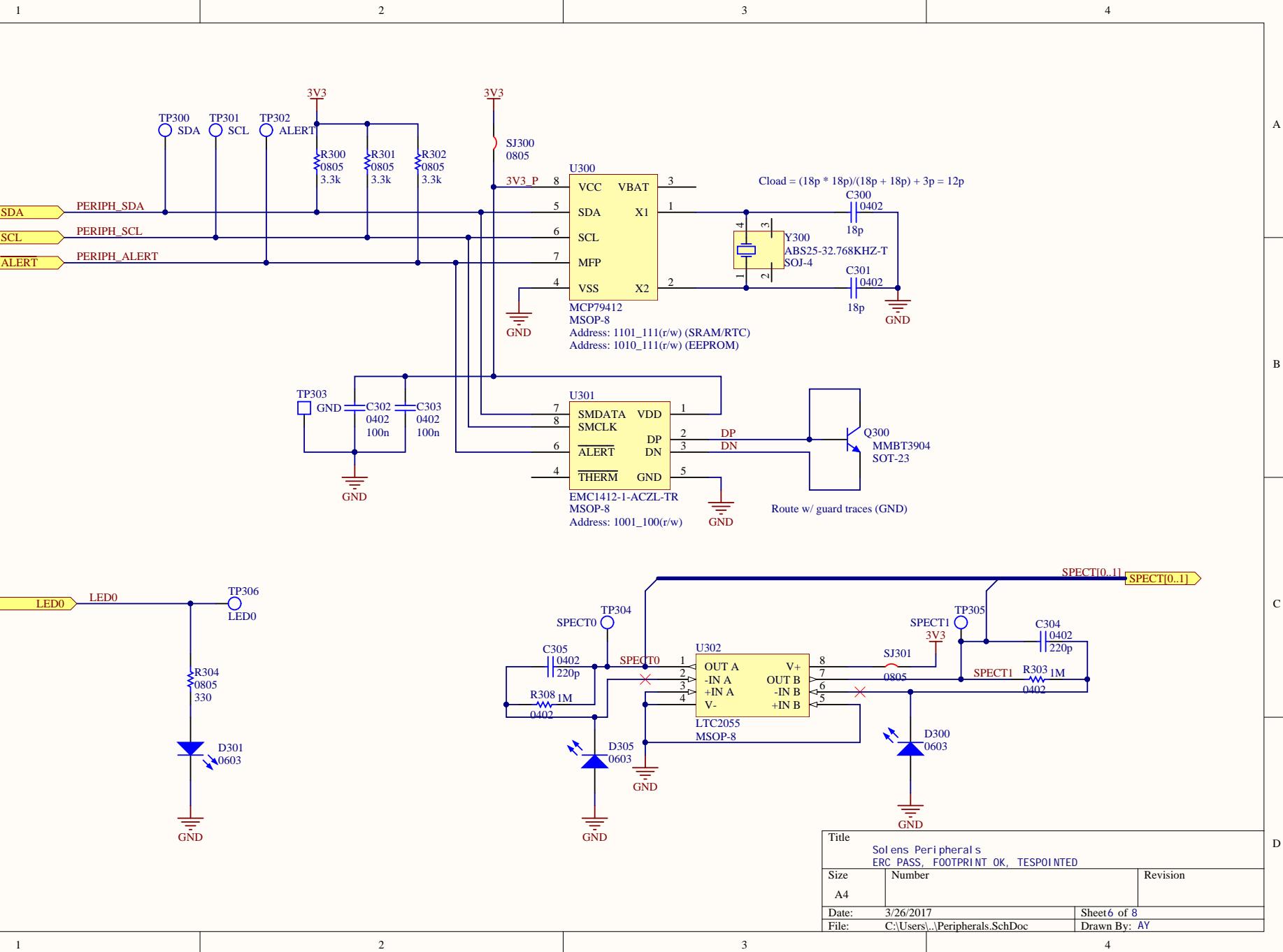
A

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A

A

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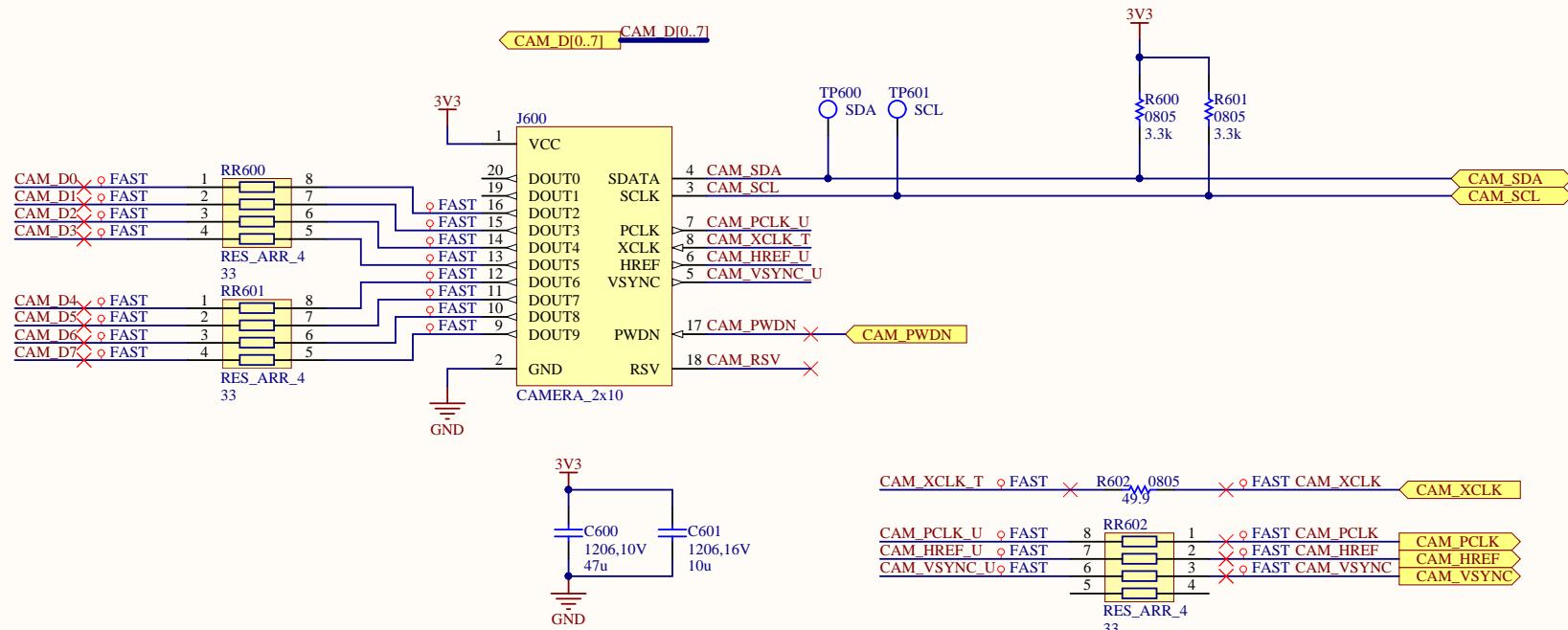
B

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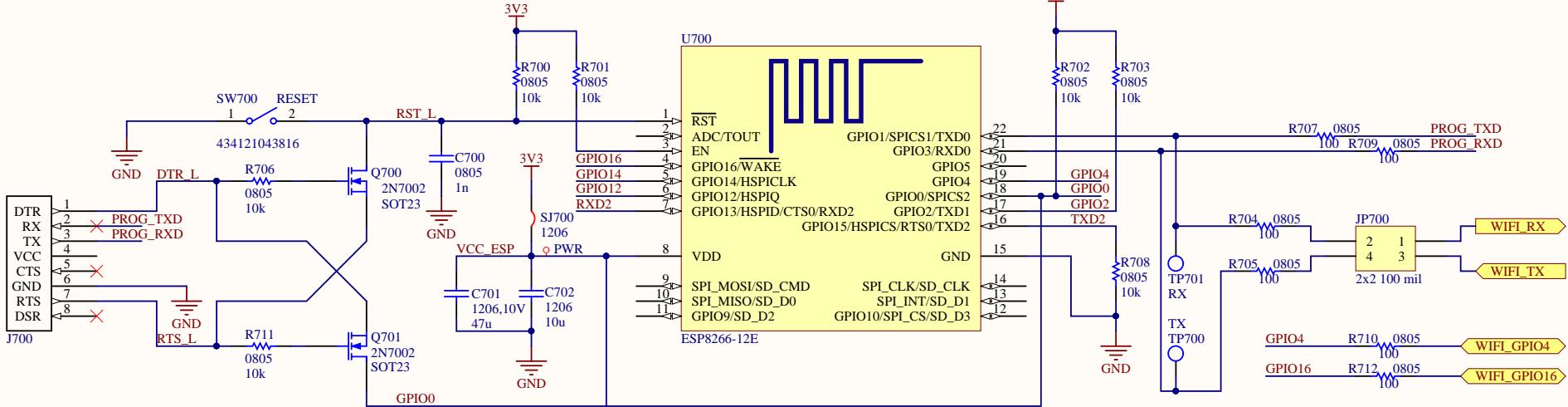
D

D



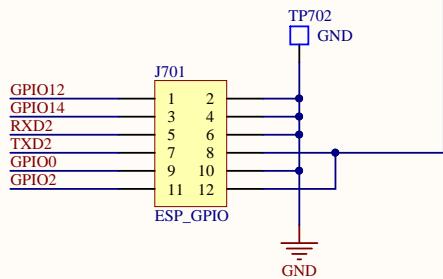
Title Solens Camera Subsystem ERC PASS, FOOTPRINT OK, TESTPOINTED		
Size A4	Number	Revision
Date: 3/26/2017	Sheet 7 of 8	
File: C:\Users\...\Camera.SchDoc		Drawn By: JL, AY

A



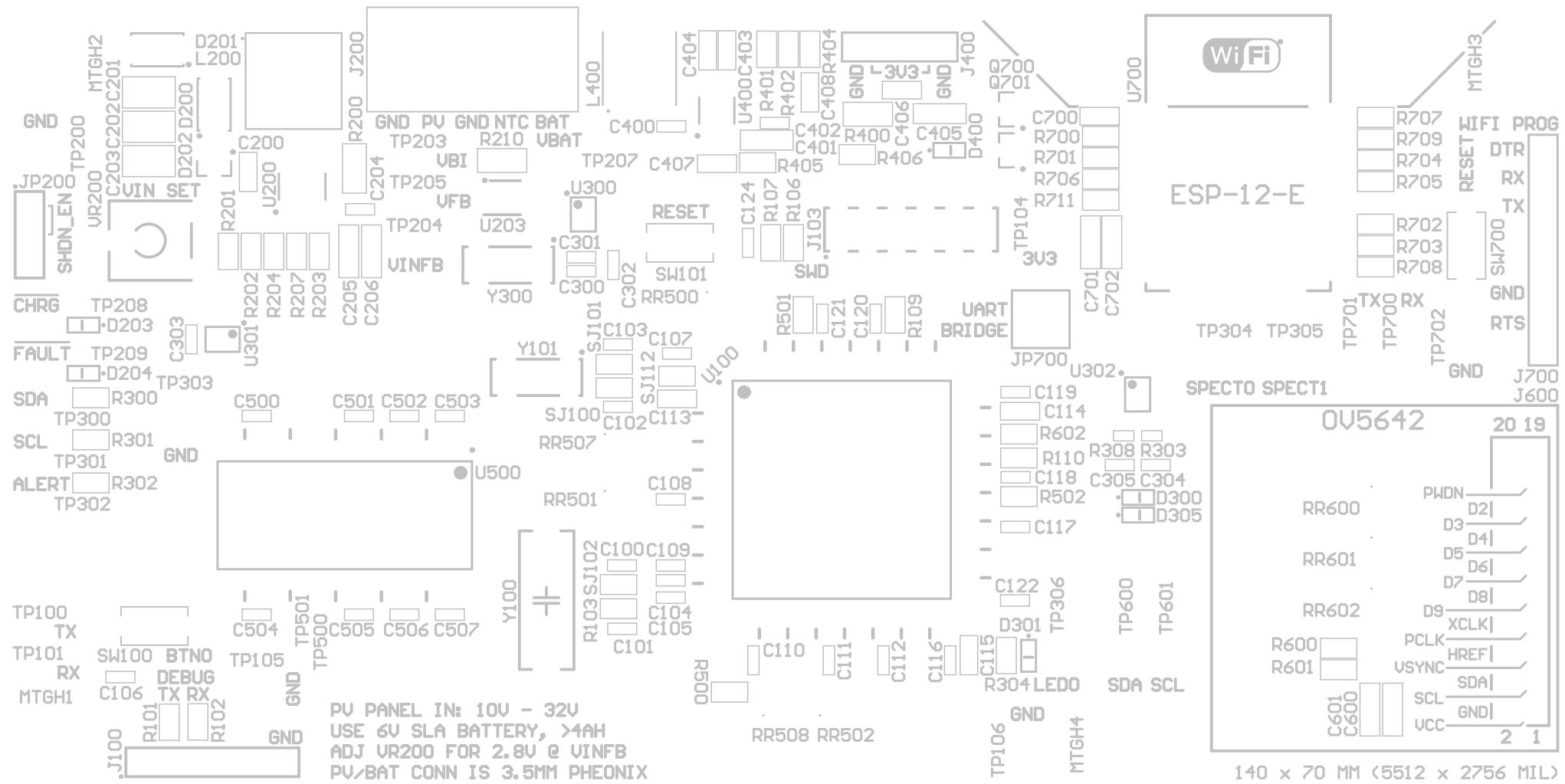
Optional MCU-WIFI GPIO bridging

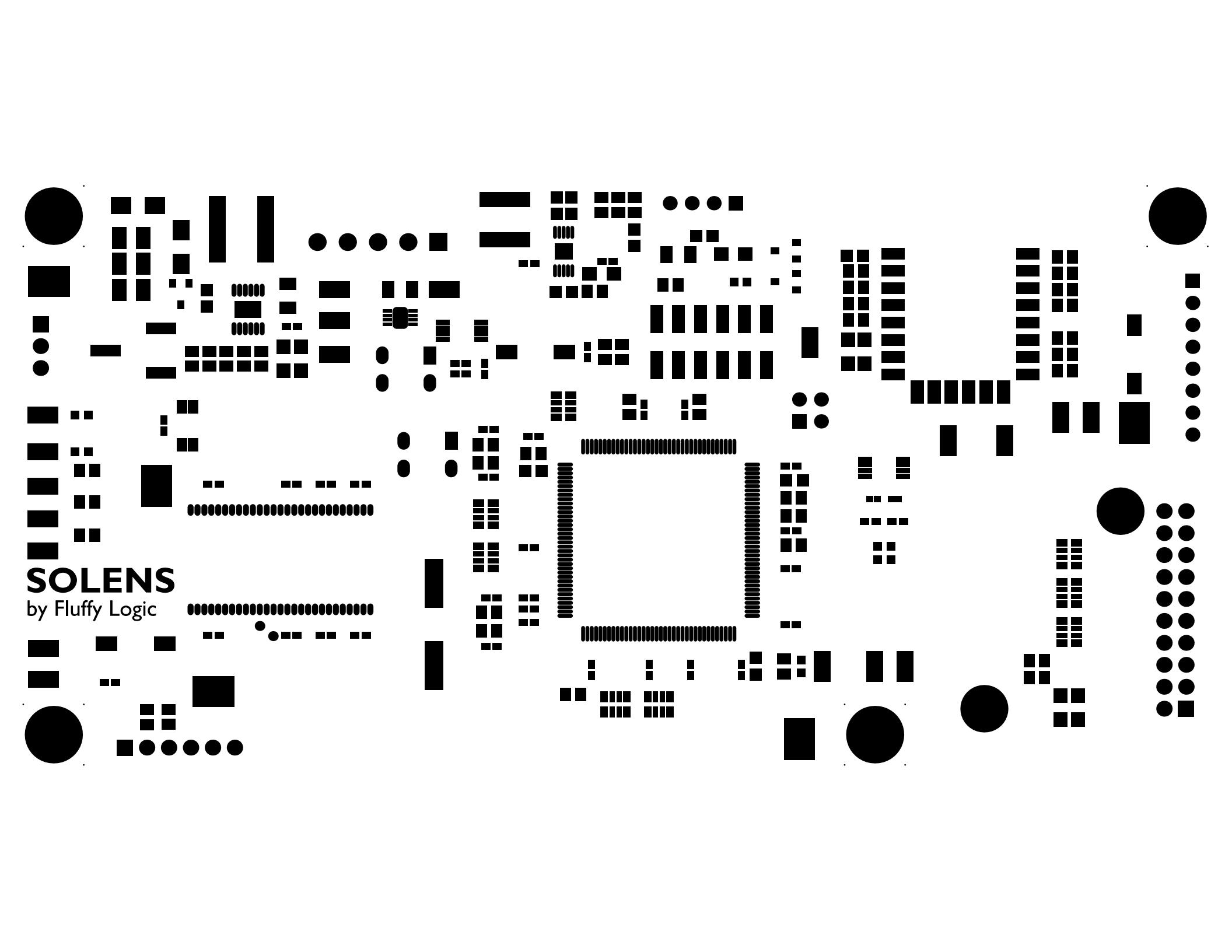
B



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Title		
Solens Wi Fi Subsystem ERC PASS, FOOTPRINT OK, TESTPOINTED		
Size	Number	Revision
A4		
Date:	3/26/2017	Sheet 8 of 8
File:	C:\Users\..\WiFi.SchDoc	Drawn By: JL, AY





SOLENS

by Fluffy Logic

