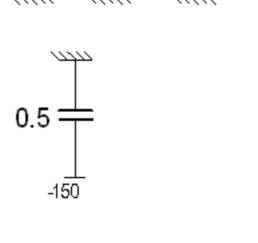


Circuit based on Dai's drawing of Control Gate and Adder, dated 18/12/48, but split into separate gate and adder drawings as suggested by Alec's drawing of 20/8/48. Dai indicates four inputs to V4, Alec has three, but I assume there were only two in June. WHP, 15/6/96

Added 4.7K resistor in series with DASH input. Caused problems when testing without it - current through diodes when DASH positive could be catastrophic!

V4 G3 input amended to be ACTION (see IEE schematic)



 $\pm 0.5 \pm 0.5$

Computer Conservation Society				
Small-Scale Experimental Machine				
Drawn:	WHP+	GR	Scale:	NA
Title: Control Gate Position 6.3				
File: ctrlgate.ecw				
Issue: 4		Date: 1/12/12		