

Getting Started with $eCos^{TM}$

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Foreword

Welcome to the latest release of Red Hat eCos(TM) - the Embedded Configurable Operating System.

What's New?

In the fourth major public release of eCos, starting with version 1.4, we have added a wealth of new features, enhancements, and have further extended the target platform coverage.

Major new elements include:

- ⁿ Package management that supports the extension of eCos functionality via third party add-on packages.
- A standardized configuration save file format that is human readable and editable, and compatible between both GUI and command line configuration tools.
- ⁿ Enhanced web based help and component documentation system integrated into the GUI configuration tool.
- The Component Definition Language (CDL) has been radically revised and has now been implemented as a TCL extension for maximum flexibility. CDL is now fully documented in the Component Writers Guide.
- Template support for straightforward control of multiple configuration elements, which can be used to provide easy access to standard eCos configurations such as a debug stub boot ROM.
- Best of all, the source of the new configuration tools and underlying libCDL technology has been open sourced under the GNU Public License (GPL).

A companion beta version of the eCos TCP/IP stack has been released in conjunction with this release. The stack is derived from the OpenBSD source base and provides UDP, TCP, ICMP, BOOTP and DHCP protocol support on an IPv4 standards base. Device driver support for Cirrus Logic EP72xx evaluation boards, Motorola MBX, and StrongARM EBSA285 with Intel 82559 is included. The stack and ethernet core support are distributed as configurable eCos packages. Platform ethernet device drivers are distributed with the associated platform HAL, but are naturally not useful without the ethernet core support package.

A PCI bus support library has also been added that provides generic PCI bus based device initialization, discovery, and configuration. The library has been ported to both the VR4300 DDB-VRC4373 and StrongARM EBSA285 development boards.

eCos also contains support for the POSIX Specification (ISO/IEC 9945-1)[POSIX]. This support follows EL/IX level 1 in the functionality supplied [ELIX].

POSIX support is divided between the POSIX and the FILEIO packages. The POSIX package provides support for threads, signals, synchronization, timers and message queues. The FILEIO package provides support for file and device I/O. The two packages may be used together or separately, depending on configuration.

Also supplied with eCos is RedBoot; the standard bootstrap and debugging environment for embedded systems from Red Hat. It provides a wide set of tools for downloading and executing programs on embedded target systems, as well as tools for manipulating the target system's environment.

RedBoot's capabilities include:

- Serial and network (Ethernet) based debugging
- n FLASH management
- ⁿ Simple command line interface, available via serial or Ethernet
- n Configurable and extensible, specifically adapted to the target environment

New architectures and platforms added in this release include:

- n ARM Thumb
- n ARM9
- n Cirrus Logic CL-PS7111 and EP72xx
- n Cogent CMA222 and CMA230 ARM boards
- n Hitachi SH3
- n Intel StrongARM
- n Intel x86 PC
- n Matsushita AM33
- n Motorola MBX evaluation board
- n NEC MIPS VR4300
- n NEC CEB-V850SA1

For further details of all the changes see the NEWS file in the eCos sources.

Red Hat is dedicated to continued enhancement and maintenance of the eCos system. Developers can look forward to upcoming releases that further expand the architectural and board coverage, extend the functionality of the TCP/IP stack, add a Linux version of the GUI configuration tool, and add major new features such as a Linux/Posix compatibility layer based on the upcoming EL/IX standard - see

http://sources.redhat.com/elix/

for more details.

eCos in a Nutshell

eCos is an open source, configurable, portable, and royalty-free embedded real-time operating system. The following text expands on these core aspects that define eCos.

eCos is provided as an open source runtime system supported by the Red Hat GNUPro and GNU open source development tools. Developers have full and unfettered access to all aspects of the runtime system. No parts of it are proprietary or hidden, and you are at liberty to examine, add to, and modify the code as you deem necessary. These rights are granted to you and protected by the Red Hat eCos Public License (RHEPL). It also grants you the right to freely develop and distribute applications based on eCos. You are not expected or required to make your embedded applications or any additional components that you develop freely available, although we do require that you make publicly available any modifications to the eCos code itself. Red Hat of course welcomes all contributions back to eCos such as board ports, device drivers and other components, as this helps the growth and development of eCos, and is of benefit to the entire eCos community.

One of the key technological innovations in eCos is our configuration system. The configuration system allows the application writer to impose their requirements on the run-time components, both in terms of their functionality and implementation, whereas traditionally the operating system has constrained the application's own implementation. Essentially, this enables eCos developers to create their own application-specific operating system and makes eCos suitable for a wide range of embedded uses. Configuration also ensures that the resource footprint of eCos is minimized as all unnecessary functionality and features are removed. The configuration system also presents eCos as a component architecture. This provides a standardized mechanism for component suppliers to extend the functionality of eCos and allows applications to be built from a wide set of optional configurable run-time components. Components can be provided from a variety of sources including: the standard eCos release; commercial third party developers; open source contributors; or additional optional components from Red Hat.

The royalty-free nature of eCos means that you can develop and deploy your application using the standard eCos release without incurring any royalty charges. In addition, there are no up-front license charges for the eCos runtime source code and associated tools. We provide, without charge, everything necessary for basic embedded applications development.

eCos is designed to be portable to a wide range of target architectures and target platforms including 16, 32, and 64 bit architectures, MPUs, MCUs and DSPs. The eCos kernel, libraries and runtime components are layered on the Hardware Abstraction Layer (HAL), and thus will run on any target once the HAL and relevant device drivers have been ported to the target's processor architecture and board. Currently eCos supports a large range of different target architectures (ARM, Hitachi SH3, Intel x86, MIPS, Matsushita AM3x, Intel StrongARM, NEC V850, Motorola PowerPC, and SPARC) including many of the popular variants of these architectures and evaluation boards. Many new ports are in development and will be released as they become available.

eCos has been designed to support applications with real-time requirements, providing features such as full preemptability, minimal interrupt latencies, and all the necessary synchronization primitives, scheduling policies, and interrupt handling mechanisms needed for these type of applications. eCos also provides all the functionality required for general embedded application support including device drivers, memory management, exception handling, C, math libraries, etc. In addition to runtime support, the eCos system includes all the tools necessary to develop embedded applications, including eCos software configuration and build tools, and GNU based compilers, assemblers, linkers, debuggers, and simulators.

To get the most out of eCos you should visit the eCos open source developers site:

http://sources.redhat.com/ecos/

The site is dedicated to the eCos developer community and contains a rich set of resources including news, FAQ, online documentation, installation guide, discussion and announcement mailing lists, online problem report form, and runtime and development tools downloads. We also support anonymous CVS and WEBCVS access to provide you with direct access to the very latest eCos source base. Complementing the open source developers site is an eCos product site, featuring news, press releases, details of our commercial engineering and support services, products, and third party partner offerings. This is located at

http://www.redhat.com/embedded/technologies/ecos/

We have released eCos as open source software because we believe that this is the most effective software development model, and that it provides the greatest benefit to the embedded developer community as a whole. As part of this endeavor, we seek the input and participation of eCos developers in its continuing evolution. Participation can take many forms including:

- providing us with feedback on how eCos might be made more useful to you by taking part in the ongoing mailing list discussions and by submitting problem reports covering bugs, documentation issues, and missing features
- n contributing bug fixes and enhancement patches
- n contributing new code including device drivers, board ports, libraries, and other runtime components

Our long term aim is to make eCos a rich and ubiquitous standard infrastructure for the development of deeply embedded applications. This will be achieved in part by Red Hat's own efforts, but also with the assistance of the eCos developer community cooperating to improve eCos for all. I would like to take this opportunity to extend our thanks to the many eCos developers who have already contributed feedback, ideas, patches, and code that have augmented and improved this release.

On behalf of the eCos team, welcome to the eCos developer community.

Paul Beskeen, Director of Engineering, eCos November 2000

Documentation Roadmap

Getting Started with eCos

Release Notes

Description of this release.

Installation Guide

Hardware and software installation instructions, including instructions on how to execute some prebuilt tests to verify the installation.

Programming Tutorial

A tutorial that gets you started running programs with eCos.

Appendixes

Extra information about the licensing terms for **eCos**.

eCos User's Guide

The eCos Configuration Tool

A description of all features of the Configuration Tool.

Programming concepts and techniques

An explanation of the **eCos** programming cycle, and a description of some debugging facilities that **eCos** offers.

Configuration and the Package Repository

Information on how to configure **eCos** manually, including a reference on the ecosconfig command, memory layouts, and information on how to manage a package repository using the **eCos Package Administration Tool**.

eCos Reference Manual

Preliminaries

An overview of the **eCos** kernel and configurability system.

Kernel APIs

In-depth description of **eCos**'s native C kernel API, the μ ITRON API, the ISO standard C library, and the **eCos** Hardware Abstraction Layer (HAL). Important considerations are given for programming the **eCos** kernel. The semantics for each kernel function are described, including how they are affected by configuration.

eCos Device Drivers

A description of the philosophy behind **eCos** device drivers, as well as a presentation of the C language API for using the current device drivers.

The ISO Standard C and Math Libraries

eCos comes with an implementation of the ISO C library specification. This section gives details about the implementation, lists the few functions that are not yet implemented, and gives a complete reference for configuring the C library.

Part I: Release Notes

This release of eCos supports the following architectures:

- n Toshiba TX39 (MIPS R3900 derivative)
- n Toshiba TX49 (MIPS R4900 derivative)
- n PMC-Sierra RM7000A (MIPS IV ISA)
- n NEC V_R4300
- n Linux i386—synthetic Linux target

and supports the following target platforms:

- n Toshiba JMR3904 (TX39)
- n Toshiba REF4955 (TX49)
- n Momentum Computer Inc. Ocelot (PMC-Sierra RM7000A)
- _n NEC DDB-VRC4373 (V_R4300)
- n Linux (i386) synthetic Linux target (i386 and compatibles)

This release also supports the following host operating systems:

- $_{n}$ UNIX TM . Redhat Linux TM and Solaris TM are the only tested UNIX variants.
- Microsoft® Windows NT®, Windows 95®, Windows 98®, Windows 2000®. Note that support for Windows 95, 98 and 2000 is still "beta".

Notation and Conventions

Since there are many supported target architectures, notation conventions are used in this manual to avoid repeating instructions that are very similar.

GDB and GCC Command Notation

Cross-development commands like gcc and gdb will be shown without prefixed information about the platform for which you are cross-compiling. You need to add the necessary prefix before you execute the commands, so instead of simply typing gcc and gdb, as illustrated in the various example in this manual, use:

```
mips64vr4300-elf-gcc and mips64vr4300-elf-gdb for MIPS vr4300 mips-tx39-elf-gcc and mips-tx39-elf-gdb for MIPS tx39 mips-tx49-elf-gcc and mips-tx49-elf-gdb for MIPS tx49 mipsisa32-elf-gcc and mipsisa32-elf-gdb for PMC-Sierra RM7000A i686-pc-linux-gnu-gcc and i686-pc-linux-gnu-gdb for Synthetic Linux.
```

Note that the GCC cross compiler generates executable files with the .exe suffix on Windows, but not on UNIX. The suffix .exe will be omitted from executable file names, so you will see hello instead of hello.exe.

Directory and File System Conventions

The default directory for installing eCos on Windows (usually C:/Program Files/Red Hat/eCos) is different from that on UNIX (usually /usr/local/ecos-1.4.x). Since many command line examples in the tutorials use these paths, this default (base) directory will be cited as *BASE DIR*.

Windows and UNIX have a similar file system syntax, but the MS-DOS command interpreter on Windows uses the backslash character (\) as a path separator, while UNIX and POSIX shells (including the Cygwin bash shell for windows) use the forward slash (/).

This document will use the POSIX shell convention of forward slashes throughout.

Version Conventions

This manual outlines the features of eCos version 1.4.x. The initial release version was 1.4, and additional 1.4 releases will incorporate one or more additional numbers, represented in this manual by "x". Please note the exact version number of the version that you are using, because it is incorporated in certain file paths.

Release Overview

The Embedded Configurable Operating System (eCos) software consists of a set of tools and a run-time environment for developing embedded applications. It is a configurable, open source framework that allows you to build a run-time system that closely matching the needs of your application.

eCos is aimed at embedded software developers who use architectures with tight memory constraints, who want a portable framework for developing their applications.

If you want to start programming eCos immediately, see "Part II: Installation Guide" on page 32 and "Part III: Programming Tutorial" on page 49.

Hardware Abstraction

eCos includes a Hardware Abstraction Layer (HAL) that hides the specific features of each supported CPU and platform, so that the kernel and other run-time components can be implemented in a portable fashion.

The eCos HAL has now been ported to numerous architectures, and to one synthetic target, Linux i386. Notes on porting the HAL to new platforms are provided in the eCos Reference Manual under Kernel porting notes in The eCos Hardware Abstraction Layer section.

Embedded Kernel

The core of eCos is a full-featured, flexible, and configurable embedded kernel.

The kernel provides, among other features, multi-threading, a choice of schedulers, a full set of synchronization primitives, memory allocation primitives, and thread manipulation functions (see the *eCos Reference Manual* for the full kernel API).

The kernel is designed such that some parts of it can be changed or replaced without affecting other kernel components.

The following is a partial list of kernel features:

- n choice of memory allocation algorithm
- n choice of scheduling algorithm
- n a rich set of synchronization primitives
- n timers, counters, and alarms
- n interrupt handling
- n exception handling
- n cache control
- n thread support
- n kernel support for multi-threaded debugging with GDB
- n trace buffers
- ⁿ infrastructure and instrumentation

The kernel API and configuration are described in the eCos Reference Manual.

Configurability

The eCos kernel and other components can be configured in great detail at compile time, which avoids the need to add unwanted code to the library to be linked with your application code. There is no performance penalty for configuration.

Configuration is fine-grained, so that very small details of eCos' behavior can be tuned by selecting different configuration options.

eCos is organized as a component architecture, with a language to describe the constraints between the components and individual configuration options. These constraints are necessary to resolve inconsistent configurations, such as disabling the code which handles the real-time clock, while enabling per-thread timers.

The designer of a component or general-purpose library should write configurable code using a component definition language (CDL). Once that has been done there is no additional burden on the end user (i.e. an embedded systems developer), who will be able to use eCos' graphical **Configuration Tool** to configure the kernel and basic libraries without needing to understand how the configuration infrastructure works.

A tutorial explaining how to configure eCos is located in "Configuring and Building eCos from Source" on page 53. The eCos *User's Guide* has more detailed information on running the **Configuration Tool** and CDL.

μITRON and Other Operating Systems

eCos' configurability is the key to simulating different operating systems by using compatibility layers on top of eCos' kernel, because the semantics of basic kernel functions can be configured to match the semantics of other operating systems.

The specification for the μ ITRON operating system has been implemented on top of eCos. μ ITRON is configured by selecting appropriate options in the kernel (a real-time clock, the mlqueue scheduler, and no timeslicing); and writing a thin layer to map the μ ITRON system calls.

The μ ITRON port implements the complete μ ITRON 3.02 "Standard functionality" (level S) specification, as well as some of the "Extended" (level E) functions. The μ ITRON implementation is described in more detail in the *eCos Reference Manual*.

ISO C Library

The ISO C and math library shipped with eCos was written to be configurable and tightly integrated with the kernel and the HAL.

By carefully selecting configuration options in the C library, you can significantly reduce the size of the final executable image.

Serial Device Drivers

eCos provides serial device drivers for all supported eCos platforms, with the exception of the i386 Linux synthetic target and most simulator platforms. The serial drivers provide an API (documented in the *eCos Reference Manual*) to control serial ports directly. The standard I/O library can be configured to use them as a transport layer.

Monitor Image

RedBoot

The new standard bootstrap and debugging environment for Red Hat embedded systems is RedBoot, a configurable and extensible command line application which provides serial and network debugging and FLASH management. Based on the eCos HAL, RedBoot supports eCos, GNUPro applications and embedded Linux systems on a wide range of platforms, including ARM, MIPS, MN10300, PowerPC, SHx, v850 and x86.

RedBoot is recommended for the i386 PC, ARM PID/EPI Dev7/EPI Dev9, ARM Evaluator-7T, Cirrus Logic EP72xx, Intel StrongARM, Intel XScale, PMC-Sierra Ocelot, and Motorola PPC MBX860 platforms.

Redboot provides a GDB stub allowing debugging with the GDB debugger, and is supplied as a standalone application with the eCos distribution. See also the *RedBoot User's Guide*.

CygMon

eCos ships with a CygMon ROM monitor for the MN10300, TX39, SPARClite, EP7209, EP7211 and EP7212 Development Boards. This includes a GDB stub, thus allowing GDB to be used to debug eCos applications on these evaluation boards. For the Brutus board, two stubs are provided: one with the high FLASH portion and the other with the low FLASH portion. In addition to shipping the actual ROM, the image of that ROM is provided in case you need to burn identical copies for additional boards (see "Target Setup" on page 38).

For the MN10300, TX39 and SPARClite port of CygMon, the source code to it is included as part of the GNUPro package, so that you may recompile it as described in the GNUPro documentation.

Please note that releases of CygMon previous to the one currently supplied with eCos are incompatible with eCos. **GDB Stubs**

For the VRC4373, and TX39 jmr3904 targets, the ROM images include a GDB stub. This allows GDB to connect to the board and download eCos programs.

For the TX49 REF4955, eCos ships with a GDB stub image in SREC format which must be programmed into the board's FLASH memory.

No monitor image is required for the synthetic Linux target.

Tests and Examples

Test suites are included for every portion of eCos shipped in this release. These are brief programs that test the behavior of most system calls and libraries in eCos. "Test Suites" on page 70 describes how to build and run these test suites.

The last chapters of "Part III: Programming Tutorial" on page 49 provide examples that guide you the steps required for running eCos applications, starting from a "Hello world" program and then moving on to more complex programs that use additional kernel features.

GNU Tools and their Documentation

Red Hat's GNUPro Toolkit, which includes the GCC and G++ compilers and the GDB debugger, is needed to build eCos applications. It is bundled with the CD-ROM distribution of the eCos Developer's Kit, and is also available on the net at

```
http://sources.redhat.com/ecos/
```

Online HTML versions of the full GNUPro documentation are included with eCos, as well as a specific GNUPro tools reference guide for your hardware architecture, customized for use with eCos. The full GNUPro documentation can also be found on the web at:

```
http://www.redhat.com/support/manuals/gnupro.html
```

NOTE The Linux synthetic i386 target is an exception, as there is (currently) no GNUPro manual. However, the GNUPro source archive contains documentation for the tools. This documentation is usually also included as part of a default Red Hat Linux installation, accessible with the **info** program.

eCos Documentation

The eCos documentation set includes *Getting Started with eCos*, the *eCos User's Guide*, the *eCos Reference Manual*, and a *GNUPro Reference Manual* for your specific architecture.

For users of the eCos Net releases, these are available online in HTML format at http://sources.redhat.com/ecos/

Package Contents

eCos Net Release

The eCos Net Release consists of the archive files for GNUPro and eCos, which are located on the Red Hat eCos web site:

http://sources.redhat.com/ecos/

The eCos Net Release, because it is digitally distributed only, does not provide images for the various development boards. However, the images for the supported hardware platforms are included in the distribution, so you can burn your own Flash ICs to work with eCos.

HTML versions of the GNUPro and eCos manuals are included in the distribution, and are also available online.

TX39 Package

The TX39 package contains eCos-specific monitor PROMs for the Toshiba JMR3904 evaluation board.

TX49 Package

The TX49 package does not contain anything in addition to the installation CD.

V_R4300 Package

The $\rm V_R4300$ package contains an eCos-specific PROM for the NEC VRC4373 evaluation board.

System Requirements

Required

- Standard Intel™ architecture PC running Linux (tested on Red Hat Linux distributions 5.0-7.0), and English or Japanese versions of Microsoft Windows NT version 4.0 (service pack 3 or above must be installed), Windows 95, Windows 98, or Windows 2000. Other versions of Red Hat distributions, or Linux distributions from other vendors should work as well.
- Windows NT users must install Internet Explorer 4.0 or later, since this will ensure correct operation of the Configuration Tool.
 Sun™ workstation running Solaris 2.5.1 or later for the SPARC™.
 Support for any platform except for Windows NT 4.0, Solaris 2.5.1 and Linux is beta. In particular, rebuilding the GNUPro compiler toolchain is only supported and tested on Windows NT 4.0, Solaris 2.5.1, and Red Hat Linux.
- Enough disk space for the installed distribution. The eCos installation process will detail the various components of eCos and the GNUPro toolkit that can be installed, and their disk space requirements.
- 64MB of RAM and a 350MHz or faster Pentium processor.

 If you are downloading the eCos Net Release distribution from Red Hat's sources.redhat.com site, you will also need space to store that image and to compile GNUPro and eCos from source.

If you will be using the TX39 JMR3904 board, you will also need:

n One 16550-based serial port on the PC

- JMR-TX3904 RISC Processor Reference Board with eCos CygMon Debug EPROMs installed
- ⁿ A null modem cable to connect the serial port on the PC to the evaluation board

If you will be using the TX49 REF4955 board, you will also need:

- n One 16550-based serial port on the PC
- _n TX49 REF4955 board with eCos GDB stubs programmed into the FLASH

If you will be using the NEC VRC4373 evaluation board, you will also need:

- n One (16550 based) serial port on the PC
- _n NEC4373 evaluation board with eCos "GDB stubs" ROM installed.
- Straight-thru cable to connect the serial port on the PC to the serial I/O connector J1 on the evaluation board

If you will be using the Momentum Computer Inc. PMC-Sierra RM7000A based Ocelot board, you will also need:

- n One (16550 based) serial port on the PC
- n Ocelot board with RedBoot image installed in FLASH ROM.
- Null modem cable to connect the serial port on the PC to the serial I/O connector on the evaluation board. A gender changer may also be required.

If you will be using the Linux synthetic target, you will also need:

ⁿ An x86 PC with an installed Linux distribution (tested with Red Hat Linux distributions 5.0 - 7.0).

Recommended

A Pentium II computer and 64MB or more of RAM are recommended for best build performance.

The system has been tested only in the recommended configuration above, although other configurations are expected to work.

Reporting Problems

Reporting bugs and other problems is very important: it allows Red Hat to solve your problem quickly, and improves the eCos product. The effort you make in reporting problems is appreciated.

To submit a problem report, please use the web interface. If you have a CD distribution of the eCos Developer's Kit, you should use the address:

http://support.cygnus.com

You will need a login name and an ID, provided by your administrator.

If you are using the eCos Net release you should use the address http://sources.redhat.com/ecos/problemreport.html

Known Bugs in eCos and GNUPro

Before filing bug reports, however, please read the README provided with this release. It describes known problems and possible workarounds in eCos or with the GNUPro Toolkit. The file is at the base of the distribution.

How to Report Problems

For documentation discussing methods of reporting on, editing and querying, see the following *Accessing Red Hat Web Support to Report Problems*, or *Additional Options* in this chapter.

This documentation serves only as a guide and it is not meant to supercede the Help documentation on the Web Support site. We have tried to make our software as trouble-free as possible. If you do encounter problems, we'd like to diagnose and fix the problem as quickly as possible.

Accessing Red Hat Web Support to Report Problems

If you have a CD distribution, use the following instructions to access the Red Hat Support website.

- Use the following URL in your web browser's address or location dialog box. http://support.cygnus.com
- Click on the Case Management System icon, enter your ID and password, and the Welcome page will be displayed.
 - Access the Welcome page at any time by using the Welcome link (in the navigation bar on the left side of each Web Support page).
 - If you have the CD distribution, your details will have been entered in the database, and will be displayed on the Welcome page. If you wish to alter these details, select the Profile link in the navigation bar on the left side of the page.
- Use the links included in the navigation bar on the left side of the page to perform any of the following Red Hat Web Support activities.
 - New Case (see Submitting a support request, and the Red Hat Support website)
 - guery Case (see Additional options, and the Red Hat Support website)
 - Add Notes (see Additional options, and the Red Hat Support website)
 - Find Solutions (see Additional options, and the Red Hat Support website)
 - r Profile (see Additional options, and the Red Hat Support website)
 - Fig. Help documentation see Additional options, and the Red Hat Support website)
 - r Close Case (see Additional options, and the Red Hat Support website)

Submitting a Support Request

Use the following instructions to submit a support request, once you have a valid ID established.

- $_{\scriptscriptstyle \rm n}$ Click on New Case to create a new reported problem case.
 - The New Case page allows you to complete the creation of a new case. If there is more than one site, select the site relating to your problem.
 - r Click on Use This Site ID button to display a list of the relevant products.

- For Select a product from the list and then click on the Create Case for Selected Product button.
 - Each customer has a valid list of parts of Red Hat products for which they can submit problem reports. These components are part of the Web Support database.
- Type a brief description of the case in the Case Title field. You can enter up to 80 characters in this field.
- Select a case type from the Type drop-down menu that best describes the case.
- Select a customer severity level from the Severity drop-down menu that best describes how severe you view this problem.
- Select a case priority level from the Priority drop-down menu that best describes the priority of this case to Red Hat.
- Type a complete description of your case in the Problem Description field.
- ^r Use the scrollbars to scroll text in this field. You can add up to 30 kilobytes of text in this field.
- Click on the Create Case button at the bottom of the page to create the case in the Red Hat Web Support database. Alternatively, clear the input fields on the New Case page, using the Clear button.

After you create your case, the Case Details page displays, which includes the Case ID number that the support database assigns to your case.

To create a new case for a different site and/or part, click the New Case link in the navigation bar; then use the previous instructions.

Additional Options

The following documentation discusses the other features for the Red Hat Web Support site. Red Hat has a database to help in determining when problems developed, tracking the problems case from their first report through analysis and resolution. The database can also be used for correlation with other products as well as to other related problems.

- n Click on Query Case to find an existing problem case in our database.
 - You may examine problem cases in the Red Hat Web Support database, searching by solution ID or by entering keywords and/or a key phrase. There are options on this page enabling you to control how your search works.
 - At this point, view a problem case's details, check its status, add notes or close a problem.
- ⁿ Click on Add Notes to add additional data to an existing case in our database.

- ⁿ Click on Find Solutions to search for problem solutions in the database. The search will provide a list of the current problem cases in the Red Hat Web Support database.
- Click on Profile to change your profile information and/or your Web Support password in our database. A Profile page will be displayed.
- Click on Help for questions about using the Web Support page. The online help documentation for the Web Support site supercedes this guide; it is not meant to supercede the more updated Help documentation for the Web Support site.
- Click on Close Case link to close a case. Closing a case brings the problem to its resolution.

Updating your profile

Clicking on Profile allows you to enter the following details.

- n Your contact name
- The primary phone number where Red Hat Support can contact you
- _n FAX number Red Hat Support can use to send you information
- n Your e-mail address
- Your site ID, used to identify your primary site in the Web Support database (a Red Hat representative will provide this information)
- n Your site name

Part II: Installation Guide

Software Installation

Software Installation on Windows

If you have a CD distribution of the eCos Developer's Kit, you have received the eCos software and its supporting utilities on a single CD-ROM for installation on a PC-compatible computer running Windows NT 4.0, Windows 95, Windows 98 or Windows 2000. If you use NT you must apply the NT 4.0 Service Pack 3 or above before installing eCos. Support is only for Windows NT 4.0. Installations on other Windows platforms are beta.

The following components are provided on the eCos CD-ROM:

- n eCos source code
- n Prebuilt eCos libraries and tests
- n eCos documentation
- Red Hat GNUPro compiler toolchain for eCos source code compilation
- Red Hat Cygwin environment: this product provides a POSIX compatibility layer on top of Windows NT, and supports the GNUPro tools on Windows NT.
- Documentation for the GNUPro tools, including a Reference Manual for the particular evaluation board being used to run eCos.

If you have obtained the Net release of eCos for Windows, you will have the distribution in a self-extracting archive. Apart from the difference in medium, the installation procedure for eCos itself will be the same as for the CD-ROM-based distribution.

The software installation process involves a number of installation utilities. Some familiarity with Windows is assumed.

- 1. Invoke the file Setup.exe on the CD-ROM. This will start the installation procedure. If you have the autorun feature enabled, Windows will run Setup.exe automatically when the CD-ROM is inserted into the drive.
- 2. The setup program will offer to install the GNU user tools. Click **OK**.
- 3. You will be prompted for a file path in which to install the GNU user tools. The default will be in the /cygnus/gnupro/i686-cygwin32/i686-cygwin32 hierarchy (usually on drive C). It will then offer to install the source code and documentation for the GNU user tools. It is recommended that you install the documentation, but not the source code, unless you are interested in modifying or recompiling the GNU user tools.
- **4.** At this point the setup program will begin installing eCos. Click **OK**.
- 5. The default path offered for eCos installation will be in the /Program Files/Red Hat hierarchy (usually on drive C). You may change this path, and indeed you will need to change it if that partition does not have sufficient free disk space available. It is recommended that you accept the default selection of software components for installation.
- **6.** You will be asked to select the program folder under which the eCos menu items will be placed. The default folder name is Red Hat eCos.
- 7. The installation should finish normally, offering to show you the README file that contains any last minute information and a list of known problems detected after this document was printed. Once the installation is finished, you can start eCos or view the online documentation by selecting Start -> Programs -> Red Hat eCos, and then choosing an option within this folder, e.g Configuration Tool, Package Administration Tool, etc.

At this point you are ready to configure and build a customized eCos kernel as described in "Configuring and Building eCos from Source" on page 53.

NOTE The order of directories in the PATH is very important, and build failures may result if the PATH is not set correctly. If you are having difficulties in building eCos, please make sure you have set the PATH exactly as above.

Software Installation on UNIX

Installation and build instructions for the eCos Net release are available on the Red Hat eCos web site

http://sources.redhat.com/ecos/

Installing the eCos Developer's Kit under Linux

Users of the eCos Developer's Kit under Red Hat Linux should use the following instructions, for most of which you will normally need to be the root user.

1. The CD-ROM must be "mounted" before installation can proceed. Execute the command:

```
# mount /dev/cdrom/ /mnt/cdrom
```

Install the eCos repository from the RPM file ecos14x.rpm (where x or xx are final digits of the current version number), located in the root directory of the CD-ROM using the following command:

```
# rpm -i /mnt/cdrom/ecos14x.rpm
```

Note that root privileges are required to perform this installation. On completion, the eCos repository may be found in the directory /opt/ecos/ecos-1.4.x.

2. Extract the eCos development tools from the compressed tar archive tool-bin.tgz, located in the root directory of the CD-ROM, using the following commands:

```
# mkdir /usr/cygnus
# cd /usr/cygnus
# qunzip -c < /mnt/cdrom/tool-bin.tqz | tar xvf -</pre>
```

3. On completion, the eCos development tools may be found in the directory /usr/cygnus/ecos-DEVTOOLSVERSION. The source code for the development tools may optionally be installed in the same way:

```
# gunzip -c < /mnt/cdrom/tool-src.tgz | tar xvf -</pre>
```

4. Add the eCos host tools and development tools to the front of your path. Under Linux, you should modify the PATH environment variable as follows. Using sh, ksh, or bash:

```
$ PATH=/opt/ecos/ecos-1.4.x/tools/bin:/usr/cygnus/\
DEVTOOLSVERSION/H-i686-pc-linux-gnu/bin:$PATH
$ export PATH
```

Using csh or tcsh: Note that csh also requires the shell command "rehash" after modifying the path for the path change to take effect.

```
$ setenv PATH /opt/ecos/ecos-1.4.x/tools/bin:/usr/cygnus/\
DEVTOOLSVERSION/H-i686-pc-linux-qnu/bin:$PATH
```

Set the ECOS_REPOSITORY environment variable as follows. Using sh, ksh or bash:

```
$ ECOS_REPOSITORY=/opt/ecos/ecos-1.4.x/packages
$ export ECOS_REPOSITORY
```

Using csh or tcsh:

```
$ setenv ECOS REPOSITORY /opt/ecos/ecos-1.4.x/packages
```

At this point you are ready to configure and build a customized eCos kernel as shown in "Configuring and Building eCos from Source" on page 53.

NOTE The order of directories in the PATH is very important, and build failures may result if the PATH is not set correctly. If you are having difficulties in building eCos, please make sure you have set the PATH exactly as above.

Installing the eCos Developer's Kit under Solaris

Users of the eCos Developer's Kit under Solaris should use the following instructions, which assume that the CD-ROM is available at /cdrom/cdrom0.

1. Extract the eCos repository from the compressed tar archive ecos14x.taz (where x or xx are the final digits of the version number), located in the root directory of the CD-ROM using the following commands:

```
# mkdir /usr/local
# cd /usr/local
# zcat < /cdrom/cdrom0/ecos14x.taz | tar xvf -</pre>
```

On completion, the eCos repository may be found in the directory /usr/local/\ecos-1.4.x.

2. Extract the eCos development tools from the compressed tar archive toolbin.taz, located in the root directory of the CD-ROM, using the following commands:

```
# mkdir /usr/cygnus
# cd /usr/cygnus
# zcat < /cdrom/cdrom0/tool-bin.taz | tar xvf -</pre>
```

On completion, the executable files of the eCos development tools may be found in the directory /usr/cygnus/ecos-DEVTOOLSVERSION/H-host-triplet/bin. The source code for the development tools may optionally be installed in the same way:

```
# zcat < /cdrom/cdrom0/tool-src.taz | tar xvf -</pre>
```

3. Add the eCos host tools, development tools and any native tools supporting the eCos build process to the front of your path. Under Solaris you should modify the PATH environment variable as follows.

Using sh, ksh, or bash:

```
$ PATH=/usr/local/ecos-1.4.x/tools/bin:/usr/xpg4/bin:\
/usr/ucb:$PATH
$ export PATH
```

Using csh or tcsh:

Note that csh also requires the shell command "rehash" after modifying the path for the path change to take effect.

```
% setenv PATH /usr/local/ecos-1.4.x/tools/bin:/usr/xpg4/bin:\
/usr/ucb:$PATH
```

4. Set the ECOS REPOSITORY environment variable as follows:

Using sh, ksh or bash:

```
$ ECOS_REPOSITORY=/usr/local/ecos-1.4.x/packages
$ export ECOS_REPOSITORY
Using csh or tcsh:
% setenv ECOS REPOSITORY /usr/local/ecos-1.4.x/packages
```

At this point you are ready to configure and build a customized eCos kernel as shown in "Configuring and Building eCos from Source" on page 53.

NOTE The order of directories in the PATH is very important, and build failures may result if the PATH is not set correctly. If you are having difficulties in building eCos, please make sure you have set the PATH exactly as above.

8

Target Setup

Connecting To A Target Via Serial

While eCos supports a variety of targets, communication with all the targets happens in one of four ways. These are descibed in general below.

The descriptions are followed by descriptions of each target, providing specific details of how to set up the target (if hardware) and the necessary communication information (such as baud rate for hardware targets, or special connection options for simulator targets).

Most targets will have eCos GDB stubs or CygMon installed. These normally wait for GDB to connect at 38400 baud, using 8 data bit, no parity bit and 1 stop-bit (no hardware flow control). Check the section for your target to ensure it uses this speed. If not, adjust the following instructions accordingly.

The following instructions depend on your having selected the appropriate serial port on the host. That is, the serial port which connects to the target's (primary) serial port. On Linux this could be /dev/ttyso, while the same port on Windows would be named COM1, or /dev/ttya on Solaris. Substitute the proper serial port name in the below.

Connect to the target by issuing the following commands in GDB console mode:

```
(gdb) set remotebaud 38400
(gdb) set mips saved-gpreg-size 32 (for VR4300)
(gdb) target remote /dev/ttyS0
```

In Insight, connect by opening the **File->Target Settings** window and enter:

```
Target: Remote/Serial Baud Rate: 38400
```

Port: /dev/ttyS0

Set other options according to preference, close the window and select **Run->Connect to target**.

Connecting To A Target Via Ethernet

Some targets allow GDB to connect via Ethernet - if so, it will be mentioned in the section describing the target. Substitute the target's assigned IP address or hostname for <hostname> in the following. The <port> is the TCP port which the eCos GDB stub or CygWin is listening on. It is also listed in the section describing the target.

Connect to the target by issuing the following command in GDB console mode:

```
(gdb) target remote <hostname>:<port>
```

In Insight, connect by opening the **File->Target Settings** window and enter:

Target: Remote/TCP
Hostname: <hostname>
Port: <port>

You will also need to open the GDB console window with **View->Console** and enter "set mips saved-gpreg-size 32" at the prompt

Set other options according to preference, close the window and select **Run->Connect to target**.

Connecting To A Simulator Target

GDB connects to all simulator targets using the same basic command, although each simulator may require additional options. These are listed in the section describing the target, and should be used when connecting.

Connect to the target by issuing the following command in GDB console mode:

```
(qdb) target sim [target specific options]
```

In Insight, connect by opening the **File->Target Settings** window and enter:

```
Target: Simulator
Options: [target specific options]
```

Set other options according to preference, close the window and select **Run->Connect to target**.

Connecting To A Synthetic Target

Synthetic targets are special in that the built tests and applications actually run as native applications on the host. This means that there is no target to connect to. The test or application can be run directly from the GDB console using:

(gdb) run

or from Insight by pressing the **Run** icon. There is therefore no need to connect to the target or download the application, so you should ignore GDB "target" and "load" commands in any instructions found in other places in the documentation.

TX39 Hardware Setup

The eCos Developer's Kit package comes with a pair of ROMs that provide GDB support for the Toshiba JMR-TX3904 RISC processor reference board by way of CygMon.

Images of these ROMs are also provided at BASE_DIR/loaders/tx39-jmr3904/cygmon50.bin and BASE_DIR/loaders/tx39-jmr3904/cygmon66.bin for 50 MHz and 66 MHz boards respectively. The ROMs are installed to sockets IC6 and IC7 on the memory daughterboard according to their labels. Attention should be paid to the correct orientation of these ROMs during installation.

The GDB stub allows communication with GDB using the serial port (channel C) at connector PJ1. The communication parameters are fixed at 38400 baud, 8 data bits, no parity bit, and 1 stop bit (8-N-1). No handshaking is employed. Connection to the host computer should be made using an RS232C null modem cable.

CygMon and eCos currently provide support for a 16Mbyte 60ns 72pin DRAM SIMM fitted to the PJ21 connector. Different size DRAMs may require changes in the value stored in the DCCR0 register. This value may be found near line 211 in hal/mips/arch/v1_4_x/src/vectors.s in eCos, and near line 99 in libstub/mips/tx39jmr-power.s in Cygmon. eCos does not currently use the DRAM for any purpose itself, so it is entirely available for application use.

TX39 Architectural Simulator Setup

The TX39 simulator is an architectural simulator which implements all the features of the Toshiba TX39 needed to run eCos. The current implementation provides accurate simulation of the instruction set, interrupt controller, and timers, as well as having generic support for diagnostic output, serial I/O, and exceptions.

In this release, you can run the same eCos binaries in the simulator that can run on target hardware, if it is built for ROM start-up.

To simplify connection to the simulator, you are advised to create a GDB macro by putting the following code in your personal GDB start-up file (gdb.ini on Windows and .gdbinit on UNIX).

You can then connect to the simulator by invoking the command tsim on the command line:

```
(gdb) tsim
```

You can achieve the same effect by typing out the macro's content on the command line if necessary.

TX49 Hardware Setup

The eCos installation CD contains a copy of the eCos GDB stubs in SREC format which must be programmed into the board's FLASH memory.

Preparing the GDB stubs

These stub preparation steps are not strictly necessary as the eCos distribution ships with precompiled stubs in the directory loaders/tx49-ref4955 relative to the installation root.

Building the GDB stub image with the eCos Configuration Tool

- 1. Start with a new document selecting the **File->New** menu item if necessary to do this.
- 2. Choose the **Build->Templates** menu item, and then select the TX49 REF4955 hardware.
- **3.** While still displaying the **Build->Templates** dialog box, select the stubs package template to build a GDB stub. Click **OK**.
- **4.** Build eCos stubs using **Build->Library**.
- 5. When the build completes, the image files can be found in the bin/ subdirectory of the install tree. GDB stub images have the prefix gdb_module.

Building the GDB stub image with ecosconfig

- 1. Make an empty directory to contain the build tree, and cd into it.
- **2.** To build a GDB stub ROM image, enter the command:

```
$ ecosconfig new ref4955 stubs
```

3. Enter the commands:

```
$ ecosconfig tree
```

\$ make

4. When the build completes, the image files can be found in the bin/ subdirectory of the install tree. GDB stub images have the prefix gdb_module.

Installing GDB stubs into FLASH

Boot into the board's firmware in little-endian mode:

Set the switches like this:

SW1: 10000000 (first lever up, the rest down)

SW2: 10000010

Connect serial cable on the lower connector, configure terminal emulator for 38400, 8-N-1.

When booting the board, you should get this prompt:

```
HCP5 rev 0.9B . HCP5?
```

Select o (option), a (FLASH) and b (boot write). You should see this:

Now send the stub SREC data down to the board using the terminal emulator's 'send ASCII' (or similar) functionality.

Red Hat has experienced some sensitivity to how fast the data is written to the board. Under Windows you should configure Minicom to use a line delay of 100 millisecs. Under Linux, use the slow_cat.tcl script:

```
% cd BASE_DIR/packages/hal/mips/ref4955/v1_4_x/misc
% slow cat.tcl < [path]/gdb module.srec > /dev/ttyS0
```

Power off the board, and change it to boot the GDB stubs in big-endian mode by setting the switches like this:

SW1: 00000000 (all levers down)

SW2: 10001010

The GDB stubs allow communication with GDB using the serial port at connector PJ7A (lower connector). The communication parameters are fixed at 38400 baud, 8 data bits, no parity bit and 1 stop bit (8-N-1). No flow control is employed. Connection to the host computer should be made using a straight through serial cable.

V_R4300 Hardware Setup

The eCos Developer's Kit package comes with an EPROM which provides GDB support for the NEC VRC4373 evaluation board. An image of this EPROM is also provided at loaders/vr4300-vrc4373/gdbload.bin under the root of your eCos installation.

The EPROM is installed to socket U12 on the board. Attention should be paid to the correct orientation of the EPROM during installation. Only replace the board's existing ROM using a proper PLCC extraction tool, as the socket would otherwise risk getting damaged.

The GDB stub in the EPROM allows communication with GDB using the serial port at connector J1. The communication parameters are fixed at 38400 baud, 8 data bits, no parity bit and 1 stop bit (8-N-1). No flow control is employed. Connection to the host computer should be made using a straight-through serial cable.

```
/misc
% slow cat.tcl < [path]/gdb module.srec > /dev/ttyS0
```

Power off the board, and change it to boot the GDB stubs in big-endian mode by setting the switches like this:

SW1: 00000000 (all levers down)

SW2: 10001010

The GDB stubs allow communication with GDB using the serial port at connector PJ7A (lower connector). The communication parameters are fixed at 38400 baud, 8 data bits, no parity bit and 1 stop bit (8-N-1). No flow control is employed. Connection to the host computer should be made using a straight through serial cable.

i386/Linux Synthetic Target Setup

When building for the synthetic Linux target, the resulting binaries are native Linux applications with the HAL providing suitable bindings between the eCos kernel and the Linux kernel.

NOTE: Please be aware that the current implementation of the Linux synthetic target does not allow thread-aware debugging.

These Linux applications cannot be run on a Windows system. However, it is possible to write a similar HAL emulation for the Windows kernel if such a testing target is desired.

Tools

For the synthetic target, eCos relies on features not available in native compilers earlier than gcc-2.95.1. It also requires version 2.9.5 or later of the GNU linker. If you have gcc-2.95.1 or later and ld version 2.9.5 or later, then you do not need to build new tools. eCos does not support earlier versions. You can check the compiler version using gcc -v or egcs -v, and the linker version using 1d -v.

If you have native tools that are sufficiently recent for use with eCos, you should be aware that by default eCos assumes that the tools i686-pc-linux-gnu-gcc, i686-pc-linux-gnu-ar, i686-pc-linux-gnu-ld, and i686-pc-linux-gnu-objcopy are on your system and are the correct versions for use with eCos. But instead, you can tell eCos to use your native tools by editing the configuration value "Global command prefix" (CYGBLD_GLOBAL_COMMAND_PREFIX) in your eCos configuration. If left empty (i.e. set to the empty string) eCos will use your native tools when building.

If you have any difficulties, it is almost certainly easiest overall to rebuild the tools as described on:

http://sources.redhat.com/ecos/getstart.html

9

Running Applications on the Target

At this point you should have installed the eCos software on your system (see "Software Installation" on page 33), and connected to a hardware target (see "Target Setup" on page 38).

To verify both that a hardware target is properly set up, and that the GDB commands used to connect to the target (hardware, simulator or synthetic) work properly on your system, you will now be guided through downloading and executing a prebuilt eCos test. The procedure is exactly the same when you want to download and run applications or tests that you have built yourself.

On Windows you must have the bash command line interpreter running with some environment variables that are useful for eCos work. If you have purchased the eCos Developer's Kit, you can select this by selecting Start->Programs->Red Hat eCos>eCos Development Environment. If you are using the eCos Net Release, you should set the environment variables as shown in the GNUPro Toolkit Reference Manual. On Linux, simply open a new shell window.

You will need to change directory to the prebuilt tests that are provided in the eCos installation, as follows:

```
for the TX3904-based Toshiba JMR-TX3904 board:
   $ cd BASE_DIR/prebuilt/jmr3904/tests/kernel/v1_4_x/tests
for the TX39 simulator target:
   $ cd BASE_DIR/prebuilt/tx39_sim/tests/kernel/v1_4_x/tests
for the TX49based Toshiba REF4995 board:
   $ cd BASE_DIR/prebuilt/ref4955/tests/kernel/v1_4_x/tests
for the VR4300-based NEC VRC4373 board:
```

```
$ cd BASE_DIR/prebuilt/vrc4373/tests/kernel/v1_4_x/tests
for the i386-based Linux synthetic target:
   $ cd BASE DIR/prebuilt/linux/tests/kernel/v1 4 x/tests
```

To execute the thread gdb test case on the desired target,

1. Run GDB in command line mode using the following command, remembering to substitute the appropriate name for the architecture's gdb, eg. <target>-gdb:

```
$ qdb -nw thread qdb
```

GDB will display a copyright banner and then display a prompt (gdb).

2. Connect to the target according to the instructions given earlier (in "Target Setup" on page 38) - via serial or ethernet to hardware targets, or directly, for simulator and synthetic targets.

Depending on the target type, you will be notified about a successful connection, and possibly see some output informing you of the current program counter of the target.

3. Download the test - effectively loading the test case executable into the memory of the target - by typing this command:

```
(qdb) load
```

Again, depending on the target, you may see some output describing how much data was downloaded, and at what speed.

4. Start the test case running. For hardware targets this is done with the continue command, while run must be used on simulators and synthetic targets:

```
(gdb) continue
or
(gdb) run
```

You should now see a number of text messages appear, such as:

```
PASS:<GDB Thread test OK> EXIT:<done>
```

eCos has no concept of the application exiting. All eCos test cases complete and then run in a continuous tight loop. To return control to GDB you must stop the application.

The usual method of stopping an application is with **Ctrl+C**, but **Ctrl+C** may not work on your platform for the prebuilts. First, make default tests and check that they work the same way as prebuilts, then modify your config to enable GDB stubs (if applicable) and break support, so that a **Ctrl+C** character will interrupt the application.

Another way to stop the application is by means of a breakpoint. Before running the application, breakpoint <code>cyg_test_exit()</code> to stop an eCos test case at its end.

The usual method of stopping an application is with Ctrl+C, but Ctrl+C may not work on your platform for the prebuilts. First, make default tests and check that they work the same way as prebuilts, then modify your config to enable GDB stubs (if applicable) and break support, so that a Ctrl+C character will interrupt the application.

Another way to stop the application is by means of a breakpoint. Before running the application, breakpoint <code>cyg_test_exit()</code> to stop an eCos test case at its end.

The full functionality of GDB is now available to you, including breakpoints and watchpoints. Please consult the GNUPro GDB documentation for further information.

Part III: Programming Tutorial

10

Programming with eCos

The remaining chapters of this manual comprise a simple tutorial for configuring and building eCos, building and running eCos tests, and finally building three stand-alone example programs which use the eCos API to perform some simple tasks.

You will need a properly installed eCos system, with the accompanying versions of the GNUPro tools. On Windows you will be using the bash command line interpreter that comes with Cygwin, with the environment variables set as described in the GNUPro documentation.

The Development Process

Most development projects using eCos would contain some (or most) of the following:

eCos Configuration

eCos is configured to provide the desired API (the inclusion of libc, uitron, and the disabling of certain undesired funtions, etc.), and semantics (selecting scheduler, mutex behavior, etc.). See "Configuring and Building eCos from Source" on page 53.

It would normally make sense to enable eCos assertion checking at this time as well, to catch as many programming errors during the development phase as possible.

Note that it should not be necessary to spend much time on eCos configuration initially. It may be important to perform fine tuning to reduce the memory footprint and to improve performance later when the product reaches a testable state.

Integrity check of the eCos configuration

While Red Hat strive to thoroughly test eCos, the vast number of configuration permutations mean that the particular configuration parameters used for your project may not have been tested. Therefore, we advise running all the eCos tests after the project's eCos configuration has been determined. See "Test Suites" on page 70.

Obviously, this should be repeated if the configuration changes later on in the development process.

Application Development - Target Neutral Part

While your project is probably targeting a specific architecture and platform, possibly custom hardware, part of the application development may be possible to perform using simulated or synthetic targets.

There are two primary reasons for doing this:

- It may be possible by this means to perform application development in parallel with the design/implementation of the target hardware, thus providing more time for developing and testing functionality, and reducing time-to-market.
- The build-run-debug-cycle may be faster when the application does not have to be downloaded to a target via a serial interface. Debugging is also likely to be more responsive when you do not have to to communicate with a stub via serial. It also removes the need for manually or automatically resetting the target hardware.

This is approach is possible because all targets (including simulators and synthetic ones) provide the same basic API: that is, kernel, libc, libm, uitron, infra, and to some extent, HAL and IO.

Synthetic targets are especially suitable as they allow you to jury-rig simulations of elaborate devices by interaction with the host system, where an IO device API can hide the details from the application. When switching to hardware later in the development cycle, the IO driver is properly implemented. While this is possible to do, and has been done, it is not specifically documented or supported by Red Hat. It may become so later.

Therefore, select a simulator or synthetic target and use it for as long as possible doing application development. That is, configure for the selected target, build eCos, build the application and link with eCos, run and debug. Repeat the latter two steps.

Obviously, at some time you will have to switch to the intended target hardware, for example when adding target specific feature support, for memory footprint/performance characterization, and for final tuning of eCos and the application.

Application Development - Target Specific Part

Repeat the build-run-debug-cycle while performing final tuning and debugging of application. Remember to disable eCos assertion checking, as it reduces performance.

It may be useful to switch between this and the previous step repeatedly through the development process; use the simulator/synthetic target for actual development, and use the target hardware to continually check memory footprint and performance. There should be little cost in switching between the two targets when using two separate build trees.

11

Configuring and Building eCos from Source

This chapter documents the configuration of eCos, using the ARM PID board as an example. The process is the same for any of the other supported targets: you may select a hardware target (if you have a board available), any one of the simulators, or a synthetic target (if your host platform has synthetic target support).

At the end of the chapter is a section describing special issues for this architecture which may affect the way you should configure eCos for your target.

eCos Start-up Configurations

There are various ways to download an executable image to a target board, and these involve different ways of preparing the executable image. In the eCos Hardware Abstraction Layer (HAL package) there are configuration options to support the different download methods. The following table summarizes the ways in which an eCos image can be prepared for different types of download.

Table 1: Configuration for various download methods

Download method	HAL configuration
Burn hardware ROM	ROM start-up
Download to ROM emulator	ROM start-up
Download to board with CygMon or GDB stub ROM	RAM start-up
Download to simulator without CygMon or GDB stub ROM	ROM start-up
Download to simulator with CygMon	RAM start-up
Download to simulator ignoring devices	SIM configuration
Run synthetic target	RAM start-up

CAUTION You cannot run an application configured for RAM start-up on the simulator directly: it will fail during start-up. You can only download it to the simulator if:

you are already running CygMon (or a GDB stub) in the simulator, as described in the GNUPro documentation

Configuring eCos' HAL package for simulation should rarely be needed for real development; binaries built with such a kernel will not run on target boards at all, and the MN10300 and TX39 simulators can run binaries built for stdeval1 and jmr3904 target boards. The main use for a "simulation" configuration is if you are trying to work around problems with the device drivers or with the simulator. Also note that when using a TX39 system configured for simulator start-up you should then invoke the simulator with the --board=jmr3904pal option instead of --board=jmr3904.

If your chosen architecture does not have simulator support, then the combinations above that refer to the simulator do not apply. Similarly, if your chosen platform does not have CygMon or GDB stub ROM support, the combinations listed above that use CygMon or GDB stub ROMs do not apply.

The debugging environment for most developers will be either a hardware board or the simulator, in which case they will be able to select a single HAL configuration.

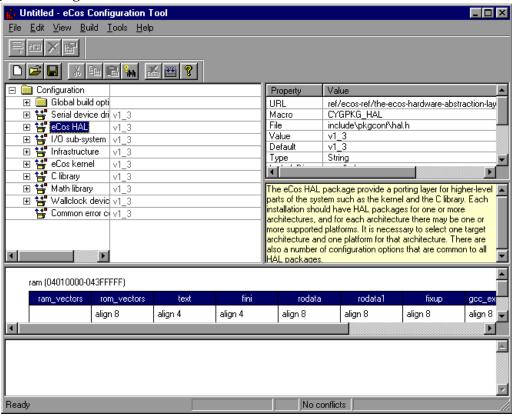
More information on the interactions between CygMon, the simulators, and GDB's thread-aware debugging features is available in the GNUPro Reference Manual for your specific architecture.

Using the Configuration Tool on Windows

Note that the use of the **Configuration Tool** is described in detail in the *eCos User's Guide*.

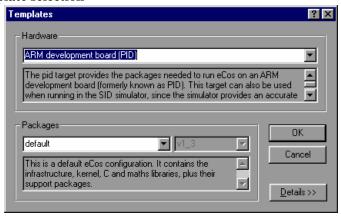
The **Configuration Tool** (see Figure 1) has five main elements: the *configuration window*, the *properties window*, the *short description window*, the *memory layout window*, and the *output window*.

Figure 1: Configuration Tool



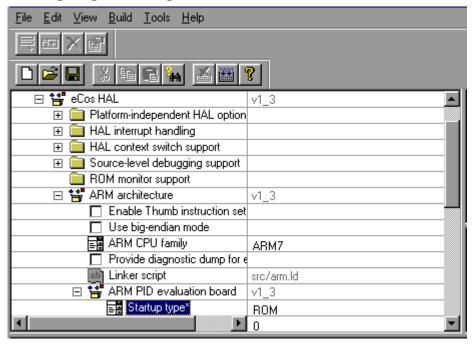
Start by opening the templates window via **Build->Templates**. Select the desired target (see Figure 2).

Figure 2: Template selection



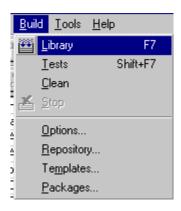
Make sure that the configuration is correct for the target in terms of endianness, CPU model, Startup type, etc. (see Figure 3).

Figure 3: Configuring for the target



Next, select the **Build->Library** menu item to start building eCos (see Figure 4).

Figure 4: Selecting the build Library menu item



The *Save As* dialog box will appear, asking you to specify a directory in which to place your save file. You can use the default, but it is a good idea to make a subdirectory, called *ecos-work* for example.

Figure 5: Build dialog



The first time you build an eCos library for a specific architecture, the **Configuration Tool** may prompt you for the location of the appropriate build tools (including make and gcc) using a **Build Tools** dialog box (as shown in Figure 6, page 58). You can select a location from the drop down list, browse to the directory using the **Browse** button, or type in the location of the build tools manually.

Figure 6: Build tools dialog



The **Configuration Tool** may also prompt you for the location of the user tools (such as cat and ls) using a User Tools dialog box (as shown in Figure 7, page 59). As with the **Build Tools** dialog, you can select a location from the drop down list, browse to the directory using the **Browse** button, or type in the location of the user tools manually.

Figure 7: User tools dialog



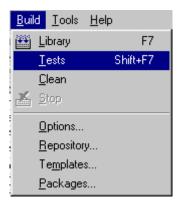
When the tool locations have been entered, the **Configuration Tool** will configure the sources, prepare a build tree, and build the *libtarget.a* library, which contains the eCos kernel and other packages.

The output from the configuration process and the building of *libtarget.a* will be shown in the output window.

Once the build process has finished you will have a kernel with other packages in *libtarget.a*. You should now build the eCos tests for your particular configuration.

You can do this by selecting **Build** -> **Tests**. Notice that you could have selected **Tests** instead of **Library** in the earlier step and it would have built *both* the library and the tests, but this would increase the build time substantially, and if you do not need to build the tests it is unnecessary.

Figure 8: Selecting the build tests menu item



"Test Suites" on page 70 will guide you through running one of the test cases you just built on the selected target, using GDB.

Using ecosconfig on UNIX

On UNIX systems the **Configuration Tool** is not yet available, but it is still possible to configure and build a kernel by editing a configuration file manually and using the *ecosconfig* command.

The following instructions assume that the PATH and ECOS_REPOSITORY environment variables have been setup correctly as described in the section "Software Installation on UNIX" on page 34.

Before invoking *ecosconfig* you need to choose a directory in which to work. For the purposes of this tutorial, the default path will be BASE_DIR/ecos-work. Create this directory and change to it by typing:

```
$ mkdir BASE_DIR/ecos-work
$ cd BASE DIR/ecos-work
```

To see what options can be used with *ecosconfig*, type:

```
$ ecosconfig --help
```

The available packages, targets and templates may be listed as follows:

```
$ ecosconfig list
```

Here is sample output from *ecosconfig* showing the usage message.

Table 2: Getting help from ecosconfig

```
$ ecosconfig --help
Usage: ecosconfig [ qualifier ... ] [ command ]
  commands are:
                                               : list repository contents
   new TARGET [ TEMPLATE [ VERSION ] ]
                                               : create a configuration
    target TARGET
                                               : change the target hardware
                                              : change the template
    template TEMPLATE [ VERSION ]
    add PACKAGE [ PACKAGE ... ]
                                              : add package(s)
   remove PACKAGE [ PACKAGE ... ]
                                              : remove package(s)
   version VERSION PACKAGE [ PACKAGE ... ]
                                              : change version of package(s)
    export FILE
                                               : export minimal config info
    import FILE
                                               : import additional config info
    check
                                               : check the configuration
                                               : resolve conflicts
   resolve
                                               : create a build tree
   tree
  qualifiers are:
                                               : the configuration file
    --config=FILE
    --prefix=DIRECTORY
                                               : the install prefix
    --srcdir=DIRECTORY
                                               : the source repository
                                               : disable conflict resolution
    --no-resolve
    --version
                                               : show version and copyright
```

Table 3: ecosconfig output — list of available packages, targets and templates

```
$ ecosconfig list
```

```
Package CYGPKG CYGMON (CygMon support via eCos):
aliases: cygmon
versions: v1 3 x
Package CYGPKG DEVICES WALLCLOCK DALLAS DS1742 (Wallclock driver
for Dallas 1742):
aliases: devices wallclock ds1742 device wallclock ds1742
versions: v1 3 x
Package CYGPKG DEVICES WALLCLOCK SH3 (Wallclock driver for SH3 RTC
module):
aliases: devices wallclock sh3 device wallclock sh3
versions: v1 3 x
Package CYGPKG DEVICES WATCHDOG ARM AEB (Watchdog driver for ARM/
AEB board):
aliases: devices watchdog aeb device watchdog aeb
versions: v1 3 x
Package CYGPKG DEVICES WATCHDOG ARM EBSA285 (Watchdog driver for
ARM/EBSA285 board):
aliases: devices watchdog ebsa285 device watchdog ebsa285
versions: v1 3 x
Package CYGPKG DEVICES WATCHDOG MN10300 MN10300 (Watchdog driver
for MN10300 chip):
aliases: devices watchdog mn10300 device watchdog mn10300
versions: v1_3_x
Package CYGPKG DEVICES WATCHDOG SH SH3 (Watchdog driver for the
Hitachi SH3 chip):
aliases: devices watchdog sh3 device watchdog sh3
versions: v1 3 x
Package CYGPKG DEVS ETH ARM EBSA285 (Intel EBSA285 with PRO/100+
ethernet driver):
aliases: devs eth arm ebsa285 ebsa285 eth driver
versions: v1 \overline{3} x
Package CYGPKG DEVS ETH ARM EDB7XXX (Cirrus Logic ethernet driver):
aliases: edb7xxx eth driver
versions: v1 3 x
Package CYGPKG DEVS ETH CF (PCMCIA (Compact Flash) ethernet
drivers):
aliases: cf eth drivers
versions: v1 3 x
Package CYGPKG DEVS ETH POWERPC QUICC (QUICC ethernet driver):
aliases: quicc eth driver
versions: v1 3 x
Package CYGPKG DEVS FLASH ASSABET (FLASH memory support for Intel
SA1110 (Assabe\overline{t}):
aliases: flash assabet
versions: v1 3 x
Package CYGPKG DEVS FLASH EBSA285 (FLASH memory support for
StrongARM EBSA-285):
aliases: flash ebsa285
versions: v1 3 x
Package CYGPKG DEVS FLASH EDB7XXX (FLASH memory support for Cirrus
Logic EDB7xxx):
aliases: flash edb7xxx
versions: v1 3 x
Package CYGPKG DEVS PCMCIA ASSABET (SA11x0/Assabet PCMCIA & Compact
Flash support):
aliases: pcmcia assabet assabet pcmcia assabet cf io assabet pcmcia
versions: v1 3 x
Package CYGPKG ERROR (Common error code support):
aliases: error errors
```

```
versions: v1 3 x
Package CYGPKG FS RAM (RAM Filesystem):
aliases: ramfs ram fs fs ram
versions: v1 3 x
Package CYGPKG HAL (eCos common HAL):
aliases: hal hal common
versions: v1 3 x
Package CYGPKG HAL ARM (ARM common HAL):
aliases: hal arm arm hal arm arch hal
versions: v1 3 x
Package CYGPKG HAL ARM AEB (ARM evaluation board (AEB-1)):
aliases: hal arm aeb arm aeb hal
versions: v1 3 x
Package CYGPKG HAL ARM CMA230 (Cogent CMA230/222 board):
aliases: hal arm cma230 arm cma230 hal
versions: v1 3 x
Package CYGPKG HAL ARM EBSA285 (Intel EBSA285 StrongARM board):
aliases: hal arm ebsa285 arm ebsa285 hal
versions: v1 3 x
Package CYGPKG HAL ARM EDB7XXX (Cirrus Logic development board):
aliases: hal arm edb7xxx arm edb7xxx hal
versions: v1 3 x
Package CYGPKG HAL ARM PID (ARM development board (PID)):
aliases: hal arm pid arm pid hal
versions: v1 3 x
Package CYGPKG HAL ARM SA11X0 (Intel SA11X0 Chipset):
aliases: hal arm sallx0
versions: v1 3 x
Package CYGPKG HAL ARM SA11X0 ASSABET (Intel SA1110 Assabet eval
board):
aliases: hal arm sal1x0 assabet
versions: v1 3 x
Package CYGPKG HAL ARM SA11X0 BRUTUS (Intel SA1100 Brutus eval
board):
aliases: hal arm sallx0 brutus
versions: v1 3 x
Package CYGPKG HAL I386 (i386 common HAL):
aliases: hal i386 i386 hal i386 arch hal
versions: v1 3 x
Package CYGPKG HAL I386 LINUX (Linux synthetic target):
aliases: hal i386 linux
versions: v1 3 x
Package CYGPKG HAL I386 PC (i386 PC target):
aliases: hal \overline{1386} pc
versions: v1 3 x
Package CYGPKG HAL MIPS (MIPS common HAL):
aliases: hal mips mips hal mips arch hal
versions: v1 3 x
Package CYGPKG HAL MIPS SIM (MIPS simulator):
aliases: hal mips sim mips sim hal
versions: v1 3 x
Package CYGPKG HAL MIPS TX39 (TX39 chip HAL):
aliases: hal tx39 tx39 hal tx39 arch hal
versions: v1 3 x
Package CYGPKG HAL MIPS TX39 JMR3904 (Toshiba JMR-TX3904 board):
aliases: hal t\bar{x}39 \ \bar{j}mr3904 \ tx\bar{3}9 \ jmr3904 \ hal
versions: v1 3 x
Package CYGPKG HAL MIPS VR4300 (VR4300 chip HAL):
aliases: hal vr4300 vr4300 hal vr4300 arch hal
```

```
versions: v1 3 x
Package CYGPKG HAL MIPS VR4300 VRC4373 (NEC VRC4373 board):
aliases: hal vrc4373 vrc4373 hal
versions: v1 3 x
Package CYGPKG HAL MN10300 (MN10300 common HAL):
aliases: hal mn10300 mn10300 hal mn10300 arch hal
versions: v1 3 x
Package CYGPKG HAL MN10300 AM31 (MN10300 AM31 variant HAL):
aliases: hal mn10300 am31 mn10300 am31 hal
versions: v1 3 x
Package CYGPKG HAL MN10300 AM31 SIM (MN10300 simulator):
aliases: hal mn10300 sim mn10300 sim hal
versions: v1 3 x
Package CYGPKG HAL MN10300 AM31 STDEVAL1 (Matsushita stdeval1
board):
aliases: hal mn10300 stdeval1 mn10300 stdeval1 hal
versions: v1 3 x
Package CYGPKG HAL MN10300 AM33 (MN10300 AM33 variant HAL):
aliases: hal mn10300 am33 mn10300 am33 hal
versions: v1 3 x
Package CYGPKG HAL MN10300 AM33 STB (Matsushita STB board):
aliases: hal mn10300 am33 stb mn10300 am33 stb hal
versions: v1_3_x
Package CYGPKG HAL POWERPC (PowerPC common HAL):
aliases: hal powerpc powerpc hal powerpc arch hal
versions: v1 3 x
Package CYGPKG HAL POWERPC COGENT (Cogent CMA286/287 board):
aliases: hal powerpc cogent powerpc cogent hal
versions: v1 3 x
Package CYGPKG HAL POWERPC FADS (Motorola MPC8xxFADS board):
aliases: hal powerpc fads powerpc fads hal
versions: v1 3 x
Package CYGPKG HAL POWERPC MBX (Motorola MBX860/821 board):
aliases: hal powerpc mbx powerpc mbx hal
versions: v1_3_x
Package CYGPKG HAL POWERPC MPC8xx (PowerPC 8xx variant HAL):
aliases: hal mpc8xx mpc8xx hal mpc8xx arch hal
versions: v1 3 x
Package CYGPKG HAL POWERPC PPC60x (PowerPC 60x variant HAL):
aliases: hal ppc60x ppc60x hal ppc60x arch hal
versions: v1 3 x
Package CYGPKG HAL POWERPC SIM (PowerPC simulator):
aliases: hal powerpc sim powerpc sim hal
versions: v1 3 x
Package CYGPKG HAL QUICC (Motorola MBX860/821 QUICC support):
aliases: hal_quicc quicc_hal quicc
versions: v1_3_x
Package CYGPKG HAL SH (SH common HAL):
aliases: hal sh sh hal sh arch hal
versions: v1 3 x
Package CYGPKG HAL SH EDK7708 (Hitachi SH7708 board):
aliases: hal sh edk sh edk hal
versions: v1 3 x
Package CYGPKG HAL SH SH7708 CQ7708 (CqREEK SH7708 board):
aliases: hal sh cq sh cq hal
versions: v1 3 x
Package CYGPKG HAL SPARCLITE (SPARClite common HAL):
aliases: hal sparclite sparclite hal sparclite arch hal
versions: v1 3 x
```

```
Package CYGPKG HAL SPARCLITE SIM (SPARClite simulator):
aliases: hal sparclite sim sparclite sim hal
versions: v1 3 x
Package CYGPKG HAL SPARCLITE SLEB (Fujitsu MB86800-MA01 board):
aliases: hal sparclite sleb sparclite sleb hal
versions: v1 3 x
Package CYGPKG HAL V85X (NEC V85x common HAL):
aliases: hal v85x nec arch hal
versions: v1 3 x
Package CYGPKG HAL V85X V850 (NEC V850 variant HAL):
aliases: hal_v85x_v850 nec_v850_hal
versions: v1 3 x
Package CYGPKG HAL V85X V850 CEB (Cosmo CEB-V850/SA1 board):
aliases: hal v85x v850 ceb nec v850 ceb hal
versions: v1 3 x
Package CYGPKG INFRA (Infrastructure):
aliases: infra
versions: v1 3 x
Package CYGPKG IO (I/O sub-system):
aliases: io
versions: v1 3 x
Package CYGPKG IO DSP BLOCK (DSP (block) device drivers):
aliases: dsp_block io_dsp_block
versions: v1 3 x
Package CYGPKG IO ETH DRIVERS (Common ethernet support):
aliases: net drivers eth drivers
versions: v1 3 x
Package CYGPKG IO FILEIO (File IO):
aliases: fileio io file
versions: v1 3 x
Package CYGPKG IO FLASH (Generic FLASH memory support):
aliases: flash
versions: v1 3 x
Package CYGPKG IO PCI (PCI configuration library):
aliases: io pci
versions: v\overline{1} 3 x
Package CYGPKG IO PCMCIA (PCMCIA & Compact Flash support):
aliases: pcmcia io pcmcia cf
versions: v1 3 x
Package CYGPKG IO SERIAL (Serial device drivers):
aliases: serial io serial
versions: v1 3 x
Package CYGPKG IO SERIAL ARM AEB (ARM AEB-1 serial device drivers):
aliases: devs serial arm aeb aeb serial driver
versions: v1 \overline{3} x
Package CYGPKG IO SERIAL ARM CMA230 (Cogent ARM/CMA230 serial
device drivers):
aliases: devs serial arm cma230 cma230 serial driver
versions: v1 \overline{3} x
Package CYGPKG IO SERIAL ARM EBSA285 (Intel EBSA285 serial driver):
aliases: devs serial arm ebsa285 ebsa285 serial driver
versions: v1 3 x
Package CYGPKG IO SERIAL ARM EDB7XXX (ARM EDB7XXX serial device
drivers):
aliases: devs serial arm edb7xxx edb7xxx serial driver
versions: v1 3 x
Package CYGPKG IO SERIAL ARM PID (ARM PID serial device drivers):
aliases: devs serial arm pid pid serial driver
versions: v1 3 x
```

```
Package CYGPKG IO SERIAL ARM SA11X0 (Intel StrongARM SA11x0 serial
driver):
aliases: devs serial arm sallx0 sallx0 serial driver
versions: v1 3 x
Package CYGPKG IO SERIAL GENERIC 16X5X (16x5x compatible serial
device drivers):
aliases: devs serial generic 16x5x 16x5x serial driver
versions: v1 \overline{3} x
Package CYGPKG IO SERIAL I386 PC (PC serial device drivers):
aliases: devs serial i386 pc pc serial driver
versions: v1_{\overline{3}}x
Package CYGPKG IO SERIAL LOOP (Loop serial device drivers):
aliases: devs serial loop loop serial driver
versions: v1 3 x
Package CYGPKG IO SERIAL MIPS VRC4373 (VRC4373 serial device
drivers):
aliases: devs serial mips vrc4373 vrc4373 serial driver
versions: v1 3 x
Package CYGPKG IO SERIAL MN10300 (MN10300 serial device drivers):
aliases: devs serial mn10300 mn10300 devs serial mn10300
mn10300 serial driver
versions: v1_3_x
Package CYGPKG IO SERIAL POWERPC COGENT (Cogent PowerPC serial
device drivers):
aliases: devs serial powerpc cogent cogent serial driver
versions: v1 3 x
Package CYGPKG IO SERIAL POWERPC QUICC SMC (PowerPC QUICC/SMC
serial device drivers):
aliases: devs serial quicc smc quicc smc serial driver
devs serial powerpc quicc smc devs serial powerpc quicc
quicc serial driver
versions: v1 3 x
Package CYGPKG IO SERIAL SH CQ7708 (SH3 cg7708 serial device
drivers):
aliases: devs_serial_sh3_cq7708 devs serial sh cq7708
cq7708 serial driver
versions: v1 3 x
Package CYGPKG IO SERIAL SH EDK7708 (SH3 EDK7708 serial device
drivers):
aliases: devs serial sh3 edk7708 devs serial sh edk7708
edk7708 serial driver
versions: v1 3 x
Package CYGPKG IO SERIAL SH SCI (SH SCI serial device drivers):
aliases: devs serial sh sci sci serial driver
versions: v1 \overline{3} x
Package CYGPKG IO SERIAL SH SCIF (SH SCIF serial device drivers):
aliases: devs serial sh scif scif serial driver
versions: v1 \overline{3} x
Package CYGPKG IO SERIAL SPARCLITE SLEB (SPARClite SLEB serial
device drivers):
aliases: devs serial sparclite sleb sleb serial driver
versions: v1 3 x
Package CYGPKG IO SERIAL TX39 JMR3904 (TX39 JMR3904 serial device
drivers):
aliases: devs serial tx39 jmr3904 jmr3904 serial driver
devs serial mips jmr3904 tx3904 serial driver
versions: v1 3 x
Package CYGPKG IO SERIAL V85X V850 (NEC V850 serial device
drivers):
```

```
aliases: devs serial v85x v850 v850 serial driver
versions: v1 \overline{3} x
Package CYGPKG IO WALLCLOCK (Wallclock device framework):
aliases: wallclock io wallclock devices wallclock device wallclock
versions: v1 3 x
Package CYGPKG IO WATCHDOG (Watchdog IO device):
aliases: watchdog io watchdog
versions: v1 3 x
Package CYGPKG ISOINFRA (ISO C and POSIX infrastructure):
aliases: isoinfra
versions: v1_3_x
Package CYGPKG KERNEL (eCos kernel):
aliases: kernel
versions: v1 3 x
Package CYGPKG LIBC (C library):
aliases: libc clib clibrary
versions: v1 3 x
Package CYGPKG LIBC I18N (ISO C library internationalization):
aliases: libc \overline{1}18n
versions: v1 3 x
Package CYGPKG LIBC SETJMP (ISO C library non-local jumps):
aliases: libc setjmp
versions: v1 3 x
Package CYGPKG LIBC SIGNALS (ISO C library signals):
aliases: libc signals
versions: v1 3 x
Package CYGPKG LIBC STARTUP (ISO environment startup/termination):
aliases: libc startup
versions: v1 3 x
Package CYGPKG LIBC STDIO (ISO C library standard input/output
functions):
aliases: libc stdio
versions: v1 \overline{3} x
Package CYGPKG LIBC STDLIB (ISO C library general utility
functions):
aliases: libc stdlib
versions: v1 \overline{3} x
Package CYGPKG LIBC STRING (ISO C library string functions):
aliases: libc string
versions: v1 3 x
Package CYGPKG LIBC TIME (ISO C library date/time functions):
aliases: libc \overline{t}ime
versions: v1 \overline{3} x
Package CYGPKG LIBM (Math library):
aliases: libm mathlib mathlibrary
versions: v1 3 x
Package CYGPKG MEMALLOC (Dynamic memory allocation):
aliases: memalloc malloc
versions: v1 3 x
Package CYGPKG POSIX (POSIX compatibility):
aliases: posix
versions: v1 3 x
Package CYGPKG REDBOOT (RedBoot, the Red Hat bootstrap):
aliases: cygmon
versions: v1 3 x
Package CYGPKG UITRON (uITRON compatibility):
aliases: uitron
versions: v1 3 x
Target aeb (ARM evaluation board (AEB-1)):
```

```
aliases: aeb1
Target am31 sim (MN10300 AM31 minimal simulator):
aliases:
Target assabet (Intel StrongARM SA1110 board):
aliases: assabet
Target brutus (Intel StrongARM SA1100 board):
aliases: brutus
Target ceb v850 (Cosmo CEB-V850/SA1 board):
aliases: CEB
Target cma230 (Cogent CMA230/222 board):
aliases: cma222
Target cma28x (Cogent CMA286/287 board):
aliases: cma286 cma287
Target cq7708 (CqREEK SH7708 board):
aliases: cq7708
Target ebsa285 (Intel EBSA285 StrongARM board):
aliases: ebsa
Target edb7xxx (Cirrus Logic development board):
aliases: edb7211 eb7xxx eb7211
Target fads (Motorola MPC8xxFADS board):
aliases:
Target jmr3904 (Toshiba JMR-TX3904 board):
aliases: jmr tx39
Target linux (Linux synthetic target):
aliases:
Target mbx (Motorola MBX860/821 board):
aliases: mbx860 mbx821
Target pc (i386 PC target):
aliases:
Target pid (ARM development board (PID)):
aliases: PID
Target psim (PowerPC simulator):
aliases: ppc sim powerpc sim
Target sh7708 (Hitachi EDK/SH7708 board):
aliases: edk7708
Target sleb (Fujitsu MB86800-MA01 board):
aliases:
Target sparclite sim (SPARClite simulator):
aliases: sl sim sparcl sim
Target stb (Matsushita STB board):
aliases:
Target stdeval1 (Matsushita stdeval1 board):
aliases:
Target tx39 sim (TX39 minimal simulator):
aliases:
Target vrc4373 (NEC VRC4373 board):
aliases:
Template all:
versions: v1 3 x
Template cygmon:
versions: v1 3 x
Template cygmon no kernel:
versions: v1 3 x
Template default:
versions: v1 3 x
Template elix:
versions: v1 3 x
Template kernel:
versions: v1 3 x
```

```
Template minimal:
versions: v1_3_x
Template posix:
versions: v1_3_x
Template redboot:
versions: v1_3_x
Template stubs:
versions: v1_3_x
Template uitron:
versions: v1_3 x
```

For detailed information about how to edit the ecos.ecc file, see the *CDL Writer's Guide* and *Editing an eCos Savefile* in the *eCos User's Guide*.

Selecting a Target

To configure for a listed target, type:

```
$ ecosconfig new <target>
```

For example, to configure for the ARM PID development board, type:

```
$ ecosconfig new pid
```

Then edit the generated file, ecos.ecc, setting the options as required for the target (endianess, CPU model, Startup type, etc.)

Create a build tree for the configured target by typing:

```
$ ecosconfig tree
```

You can now run the command *make* or *make tests*, after which you will be at the same point you would be after running the **Configuration Tool** on Windows— you can start developing your own applications, following the steps in "Building and Running Sample Applications" on page 73.

The procedure shown above allows you to do very coarse-grained configuration of the eCos kernel: you can select which packages to include in your kernel, and give target and start-up options. But you cannot select components within a package, or set the very fine-grained options.

To select fine-grained configuration options you will need to edit the configuration file ecos.ecc in the current directory and regenerate the build tree.

CAUTION

You should follow the manual configuration process described above very carefully, and you should read the comments in each file to see when one option depends on other options or packages being enabled or disabled. If you do not, you might end up with an inconsistently configured kernel which could fail to build or might execute incorrectly.

Architectural Notes

There are no notes for this architecture.

12 Test Suites

The eCos kernel and other packages have test suites that rigorously exercise the available features and confirm correct execution. The tests are run on many different possible configurations, but the high number of configuration permutations makes it impossible to test them all. The use of test suites is particularly important for embedded systems, where software robustness is a priority. All eCos software is tested prior to shipping, but if you define your own configuration, you will probably want to verify that the test cases work for it.

This release includes test suites for the eCos kernel, kernel C API, C library, µITRON compatibility, and device driver packages. The use of the test suites is similar for all packages. The tests are supplied as source code for building with your specific eCos configurations. The test case source code is located under the base source directory BASE_DIR/packages/:

- n compat/uitron/v1_4_x/tests
- $_{n}$ hal/common/v1_4_x/tests
- n io/serial/v1_4_x/tests
- n io/wallclock/v1_4_x/tests
- n devs/watchdog/v1_4_x/tests
- n kernel/v1_4_x/tests
- n language/c/libc/v1_4_x/tests
- $_{n} \quad language/c/libm/v1_4_x/tests$

There may be additional tests found in other packages.

Each test suite consists of a number of test cases which can be executed individually. A test case may involve one or more individual tests of the package's features. Successful completion of each test within the test case is reported as a line of text that is sent to the diagnostic channel (usually the serial port) for display on a terminal or terminal emulator.

Each test case runs only once and usually requires target hardware to be reset on completion. Note that certain test cases may not terminate immediately, especially if they involve delays and run on a target simulator.

Using the Configuration Tool

Using the eCos Configuration Tool it is possible to automate the downloading and execution of tests with the appropriately configured eCos packages. To do so, compile and link the test cases by using the **Build->Tests** menu item, after which the tests can be downloaded and executed by selecting **Tools->Run Tests**.

When a test run is invoked, a resizable property sheet is displayed, comprising three tabs: **Executables**, **Output** and **Summary**.

Three buttons appear on the property sheet itself: **Run/Stop**, **Close** and **Properties**.

The **Run** button is used to initiate a test run. Those tests selected on the Executables tab are run, and the output recorded on the **Output** and **Summary** tabs. During the course of a run, the **Run** button changes to **Stop**. This button may be used to interrupt a test run at any point.

See the *eCos User's Guide* for further details.

Using the command line

At the moment, there is no tool for automating testing on Linux, so you will have to run the tests manually.

It may also be necessary to run tests by hand if the automated tool finds any failing tests: it may be necessary to diagnose the problem by debugging the test.

Build the tests by typing 'make tests' in the root of the build directory. This will cause the tests to be built and installed under <install-path>/tests/.

Running the test manually is done simply by invoking GDB, connecting to the target, downloading the test, optionally setting some breakpoints, and then running the test. All this was covered in "Target Setup" on page 38.

Testing Filters

While most test cases today run solely in the target environment, some packages may require external testing infrastructure and/or feedback from the external environment to do complete testing.

The serial package is an example of this. It is the first package to require external testing infrastructure, but it will certainly not be the last.

Since the serial line is also used for communication with GDB, a filter is inserted in the communication pathway between GDB and the serial device which is connected to the hardware target. The filter forwards all communication between the two, but also listens for special commands embedded in the data stream from the target.

When such a command is seen, the filter stops forwarding data to GDB from the target and enters a special mode. In this mode the test case running on the target is able to control the filter, commanding it to run various tests. While these tests run, GDB is isolated from the target.

As the test completes (or if the filter detects a target crash) the communication path between GDB and the hardware target is re-established, allowing GDB to resume control.

In theory, it is possible to extend the filter to provide a generic framework for other target-external testing components, thus decoupling the testing infrastructure from the (possibly limited) communication means provided by the target (serial, JTAG, Ethernet, etc).

Another advantage is that the host tools will not need to know about the various testing environments required by the eCos packages, since all contact with the target will continue to happen via GDB.

It remains to be seen if it will be possible, or sensible, to implement all target-external testing infrastructure via filters.

13

Building and Running Sample Applications

The example programs in this tutorial are included, along with a *Makefile*, in the *examples* directory of the eCos distribution. The first program you will run is a *hello world*-style application, then you will run a more complex application that demonstrates the creation of threads and the use of cyg_thread_delay(), and finally you will run one that uses clocks and alarm handlers.

The *Makefile* has two variables you will need to adjust: *PKG_INSTALL_DIR* and *XCC*.

Edit the Makefile, setting *PKG_INSTALL_DIR* to the install tree previously created by ecosconfig and uncommenting the relevant *XCC* line for your architecture.

eCos Hello World

The following code is found in the file hello.c in the *examples* directory:

eCos hello world program listing

```
/* this is a simple hello world program */
#include <stdio.h>
int main(void)
{
  printf("Hello, eCos world!\n");
  return 0;
}
```

To compile this or any other program that is not part of the eCos distribution, you can follow the procedures described below. Type this explicit compilation instruction (assuming your current working directory is also where you built the eCos kernel):

```
$ gcc -g -IBASE_DIR/ecos-work/install/include hello.c -LBASE_DIR/
ecos-work/install/lib -Ttarqet.ld -nostdlib
```

The compilation instruction above contains some standard GCC options (for example, -g enables debugging), as well as some mention of paths (-IBASE_DIR/ecos-work/install/include allows files like cyg/kernel/kapi.h to be found, and - LBASE DIR/ecos-work/install/lib allows the linker to find -Ttarget.ld).

The executable program will be called a .out.

NOTE Some target systems require special options to be passed to gcc to compile correctly for that system. Please examine the Makefile in the examples directory to see if this applies to your target.

You can now run the resulting program in the simulator using GDB the way you ran the test case. The procedure will be the same, but this time run "gdb" specifying "-nw a.out" on the command line:

```
$ gdb -nw a.out
```

For targets other than the synthetic linux target, you should now run the usual GDB commands described earlier. Once this is done, typing the command "run" at the (gdb) prompt ("continue" for real hardware) will allow the program to execute and print the string "Hello, eCos world!" on your screen.

On the synthetic linux target, you may use the "run" command immediately - you do not need to invoke simulator macros, nor the "load" command.

A Sample Program with Two Threads

Below is a program that uses some of eCos' system calls. It creates two threads, each of which goes into an infinite loop in which it sleeps for a while (using cyg_thread_delay()). This code is found in the file twothreads.c in the examples directory.

eCos two-threaded program listing

```
#include <cyg/kernel/kapi.h>
#include <stdio.h>
#include <math.h>
#include <stdlib.h>

/* now declare (and allocate space for) some kernel objects,
like the two threads we will use */
cyg_thread thread_s[2];/* space for two thread objects */
```

```
char stack[2][4096];/* space for two 4K stacks */
/* now the handles for the threads */
cyg handle t simple threadA, simple threadB;
/* and now variables for the procedure which is the thread */
cyg thread entry t simple program;
/* and now a mutex to protect calls to the C library */
cyg mutex t cliblock;
/* we install our own startup routine which sets up threads */
void cyg user start(void)
printf("Entering twothreads' cyg user start() function\n");
cyg mutex init(&cliblock);
cyg thread create(4, simple program, (cyg addrword t) 0,
 "Thread A", (void *) stack[0], 4096,
 &simple threadA, &thread s[0]);
cyg_thread_create(4, simple_program, (cyg_addrword_t) 1,
 "Thread B", (void *) stack[1], 4096,
 &simple threadB, &thread s[1]);
cyg thread resume (simple threadA);
cyg thread resume (simple threadB);
/* this is a simple program which runs in a thread */
void simple program(cyg addrword t data)
int message = (int) data;
int delay;
printf("Beginning execution; thread data is %d\n", message);
cyg thread delay(200);
for (;;) {
delay = 200 + (rand() % 50);
/* note: printf() must be protected by a
call to cyg mutex lock() */
cyg mutex lock(&cliblock); {
printf("Thread %d: and now a delay of %d clock ticks\n",
 message, delay);
cyg mutex unlock(&cliblock);
cyg thread delay(delay);
```

When you run the program (by typing run at the (**gdb**) prompt) the output should look like this:

```
Starting program: BASE_DIR/examples/twothreads.exe Entering twothreads' cyg user start() function
```

```
Beginning execution; thread data is 0
Beginning execution; thread data is 1
Thread 0: and now a delay of 240 clock ticks
Thread 1: and now a delay of 225 clock ticks
Thread 0: and now a delay of 234 clock ticks
Thread 0: and now a delay of 231 clock ticks
Thread 1: and now a delay of 24 clock ticks
Thread 0: and now a delay of 249 clock ticks
Thread 0: and now a delay of 202 clock ticks
Thread 0: and now a delay of 202 clock ticks
Thread 0: and now a delay of 235 clock ticks
```

NOTE When running in a simulator the delays might be quite long. On a hardware board (where the clock speed is 100 ticks/second) the delays should average to about 2.25 seconds. In simulation, the delay will depend on the speed of the processor and will almost always be much slower than the actual board. You might want to reduce the delay parameter when running in simulation.

Figure 9, page 77 shows how this multitasking program executes. Note that apart from the thread creation system calls, this program also creates and uses a *mutex* for synchronization between the <code>printf()</code> calls in the two threads. This is because the C library standard I/O (by default) is configured not to be thread-safe, which means that if more than one thread is using standard I/O they might corrupt each other. This is fixed by a mutual exclusion (or *mutex*) lockout mechanism: the threads do not call <code>printf()</code> until <code>cyg_mutex_lock()</code> has returned, which only happens when the other thread calls <code>cyg_mutex_unlock()</code>.

You could avoid using the mutex by configuring the C library to be thread-safe (by selecting the component CYGSEM_LIBC_STDIO_THREAD_SAFE_STREAMS). Keep in mind that if the C library is thread-safe, you can no longer use printf() in cyg user start().

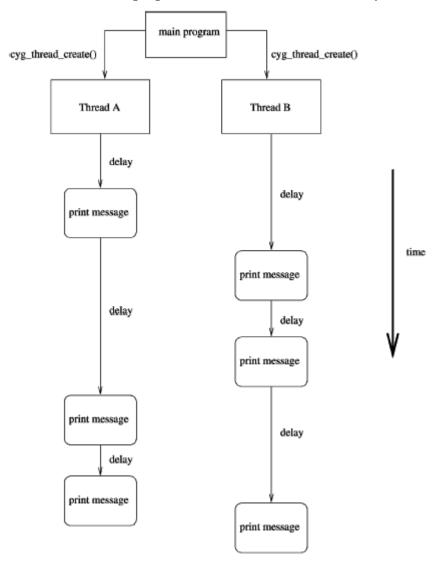


Figure 9: Two threads with simple print statements after random delays

14

More Features — Clocks and Alarm Handlers

If a program wanted to execute a task at a given time, or periodically, it could do it in an inefficient way by sitting in an infinite loop and checking the real-time clock to see if the proper amount of time has elapsed. But operating systems usually provide system calls which allow the program to be interrupted at the desired time.

eCos provides a rich timekeeping formalism, involving *counters*, *clocks*, *alarms*, and *timers*. The precise definition, relationship, and motivation of these features is beyond the scope of this tutorial, but these examples illustrate how to set up basic periodic tasks.

Alarms are events that happen at a given time, either once or periodically. A thread associates an alarm handling function with the alarm, so that the function will be invoked every time the alarm "goes off".

A Sample Program with Alarms

simple-alarm.c (in the examples directory) is a short program that creates a thread that creates an alarm. The alarm is handled by the function test_alarm_func(), which sets a global variable. When the main thread of execution sees that the variable has changed, it prints a message.

Table 4: A sample program that creates an alarm

```
/* this is a very simple program meant to demonstrate
a basic use of time, alarms and alarm-handling functions
in eCos */
#include <cyg/kernel/kapi.h>
#include <stdio.h>
#define NTHREADS 1
#define STACKSIZE 4096
static cyg handle t thread[NTHREADS];
static cyg thread thread obj [NTHREADS];
static char stack[NTHREADS][STACKSIZE];
static void alarm prog( cyg addrword t data );
/* we install our own startup routine which sets up
threads and starts the scheduler */
void cyg_user_start(void)
cyg thread create(4, alarm prog, (cyg addrword t) 0,
 "alarm thread", (void *) stack[0],
 STACKSIZE, &thread[0], &thread obj[0]);
cyg_thread_resume(thread[0]);
/* we need to declare the alarm handling function (which is
defined below), so that we can pass it to
cyg_alarm_initialize() */
cyg alarm t test alarm func;
/* alarm prog() is a thread which sets up an alarm which is then
handled by test alarm func() */
static void alarm_prog(cyg_addrword_t data)
cyg handle t test counterH, system clockH, test alarmH;
cyg tick count t ticks;
cyg alarm test alarm;
unsigned how many alarms = 0, prev alarms = 0, tmp how many;
system_clockH = cyg_real_time_clock();
cyg_clock_to_counter(system_clockH, &test_counterH);
cyg_alarm_create(test_counterH, test_alarm_func,
 (cyg_addrword_t) &how_many_alarms,
 &test alarmH, &test alarm);
cyg_alarm_initialize(test_alarmH, cyg_current_time()+200, 200);
/* get in a loop in which we read the current time and
print it out, just to have something scrolling by */
for (;;) {
ticks = cyg current time();
printf("Time is %llu\n", ticks);
/* note that we must lock access to how_many_alarms, since the
alarm handler might change it. this involves using the
annoying temporary variable tmp how many so that I can keep the
```

```
critical region short */
cyg_scheduler_lock();
tmp_how_many = how_many_alarms;
cyg_scheduler_unlock();
if (prev_alarms != tmp_how_many) {
  printf(" --- alarm calls so far: %u\n", tmp_how_many);
  prev_alarms = tmp_how_many;
}
  cyg_thread_delay(30);
}

/* test_alarm_func() is invoked as an alarm handler, so
  it should be quick and simple. in this case it increments
  the data that is passed to it. */
void test_alarm_func(cyg_handle_t alarmH, cyg_addrword_t data)
{
  ++*((unsigned *) data);
}
```

When you run this program (by typing run at the (**gdb**) prompt) the output should look like this:

```
Starting program: BASE DIR/examples/simple-alarm.exe
Time is 0
Time is 30
Time is 60
Time is 90
Time is 120
Time is 150
Time is 180
Time is 210
 --- alarm calls so far: 1
Time is 240
Time is 270
Time is 300
Time is 330
Time is 360
Time is 390
Time is 420
  --- alarm calls so far: 2
Time is 450
Time is 480
```

When running in a simulator the delays might be quite long. On a hardware board (where the clock speed is 100 ticks/second) the delays should average to about 0.3 seconds (and 2 seconds between alarms). In simulation, the delay will depend on the speed of the processor and will almost always be much slower than the actual board. You might want to reduce the delay parameter when running in simulation.

Here are a few things you might notice about this program:

It used the cyg_real_time_clock(); this always returns a handle to the default system real-time clock.

- Alarms are based on counters, so the function cyg_alarm_create() uses a counter handle. The program used the function cyg_clock_to_counter() to strip the clock handle to the underlying counter handle.
- Once the alarm is created it is initialized with cyg_alarm_initialize(), which sets the time at which the alarm should go off, as well as the period for repeating alarms. It is set to go off at the current time and then to repeat every 200 ticks.
- The alarm handler function test_alarm_func() conforms to the guidelines for writing alarm handlers and other *delayed service routines*: it does not invoke any functions which might lock the scheduler. This is discussed in detail in the *eCos Reference Manual*, in the chapter *Requirements for programs*.
- There is a *critical region* in this program: the variable *how_many_alarms* is accessed in the main thread of control and is also modified in the alarm handler. To prevent a possible (though unlikely) race condition on this variable, access to *how_many_alarms* in the principal thread is protected by calls to cyg_scheduler_lock() and cyg_scheduler_unlock(). When the scheduler is locked, the alarm handler will not be invoked, so the problem is averted.

Appendixes

Appendix 1: Real-time characterization

For a discussion of real-time performance measurement for eCos, see the eCos Users' Guide.

Sample numbers:

Board: Toshiba JMR3904 Evaluation Board

CPU: TMPR3904F 50MHz

```
eCOS Kernel Timings
Note: all times are in microseconds (.000001) unless otherwise stated
Reading the hardware clock takes 0 'ticks' overhead
... this value will be factored out of all other measurements
Clock interrupt took 29.68 microseconds (45 raw clock ticks)
Testing parameters:
   Clock samples:
                             32
   Threads:
   Thread switches:
                            128
   Mutexes:
                             32
   Mailboxes:
   Semaphores:
                             32
   Scheduler operations: 128
   Counters:
   Alarms:
                                    Confidence
     Ave Min Max Var Ave Min Function
  13.62 11.72 27.99 1.51 79% 54% Create thread
          2.60 3.91 0.26 79% 79% Yield thread [all suspended]
2.60 6.51 0.27 83% 12% Suspend [suspended] thread
1.95 7.81 0.47 58% 37% Resume thread
4.56 11.07 0.60 95% 79% Set priority
0.65 1.95 0.10 95% 95% Get priority
    2.77
    3.31
```

14.97 14.32 25.39 0.87 95% 95% Kill [suspended] thread

2.58 4.94 0.71

13.43	1.95 6.51 1.95 3.26 1.95 1.95 12.37 20.83 4.56	9.11 12.37 7.16 12.37 7.16 6.51 22.79 37.76 11.07	0.57 0.42 0.51 0.75 0.42 0.51 0.80 1.76 0.13	95% 79% 95% 95% 95% 91% 918%	79% Resume [runnable low prio] thread 79% Suspend [runnable] thread 95% Yield [only low prio] thread 58% Suspend [runnable->not runnable] 91% Kill [runnable] thread 91% Resume [high priority] thread
1.51 2.36 2.39 2.38 2.38	1.30 1.95 1.95 1.95	2.60 3.26 5.21 4.56 5.21	0.29 0.31 0.32 0.32 0.32	62% 61%	37% Scheduler unlock [0 threads] 36% Scheduler unlock [1 suspended] 37% Scheduler unlock [many suspended]
0.90 2.48 2.83 2.30 1.99 0.04 42.40	0.65 1.95 2.60 1.95 1.30 0.00 42.32	3.26 8.46 9.11 6.51 5.86 1.30 44.92	0.35 0.50 0.42 0.45 0.24 0.08 0.16	93% 96% 84% 96%	93% Unlock [locked] mutex 65% Trylock [unlocked] mutex 12% Trylock [locked] mutex 96% Destroy mutex
2.75 3.15 2.22 2.40 0.47 0.59 4.01	1.30 0.00 2.60 0.00 3.91 0.00 2.60 1.95 2.60 1.95 0.00 0.00 3.26 26.04	5.86 1.30 9.11 1.30 5.21 0.65 10.42 9.77 9.11 7.81 9.11 6.51 5.86 0.65 1.30 10.42 30.60	0.45 0.40 0.40 0.32 0.48 0.41 0.42 0.26	96% 71% 59%% 65% 78% 93%% 93%% 50% 71% 84%	78% Put [first] mbox 25% Peek [1 msg] mbox 59% Put [second] mbox 31% Peek [2 msgs] mbox 31% Get [first] mbox 18% Get [second] mbox 18% Tryput [first] mbox 3% Peek item [non-empty] mbox 4% Tryget [non-empty] mbox 78% Peek item [empty] mbox 46% Tryget [empty] mbox 28% Waiting to get mbox 12% Waiting to put mbox 15% Delete mbox
0.92 2.24 2.32 2.03 1.91 0.77 0.61 22.62	0.65 1.95 1.95 1.30 1.30 0.00 0.00	3.91 6.51 7.16 5.86 4.56 1.95 1.95 30.60	0.38 0.43 0.48 0.24 0.23 0.30 0.15 0.61	78% 65% 84%	65% Wait [1] semaphore 6% Trywait [0] semaphore 18% Trywait [1] semaphore 9% Peek semaphore 12% Destroy semaphore
0.92 0.69 0.41 3.21 0.65	0.65 0.65 0.00 2.60 0.00	3.91 1.95 1.30 5.86 3.26	0.38 0.08 0.33 0.27 0.16		
1.57 4.52 0.61 4.43 0.87 2.93 14.83 4.88 83.25	1.30 3.91 0.00 3.91 0.65 2.60 14.32 4.56 82.03	4.56 13.02 1.95 9.11 2.60 6.51 22.79 11.07	0.38 0.57 0.15 0.43 0.32 0.43 0.60 0.51	71% 50% 84% 56% 71% 96% 96% 96%	71% Create alarm 46% Initialize alarm 12% Disable alarm 40% Enable alarm 71% Delete alarm 65% Tick counter [1 alarm] 59% Tick counter [many alarms] 78% Tick & fire counter [1 alarm] 93% Tick & fire counters [>1 together]

```
17.58
        16.93
                27.34
                          0.61
                                  50% 46% Tick & fire counters [>1 separately]
                                      0% Alarm latency [0 threads] 6% Alarm latency [2 threads]
26.18
        24.74
                40.36
                          0.30
                                  97%
        29.30 56.64
33.88
                          1.70
                                  85%
36.37
       29.30
               61.20
                         3.25
                                53% 24% Alarm latency [many threads]
7.85
       6.51 14.97
                          0.00
                                           Clock/interrupt latency
```

Timing complete - 23540 ms total

PASS:<Basic timing OK>

EXIT:<done>

Board: Toshiba REF 4955

CPU: Toshiba TX4955 66MHz

```
Startup, main stack : stack used 960 size 2936
Startup : Interrupt stack used 168 size 4096
Startup : Idlethread stack used 372 size 2048
```

eCos Kernel Timings

Notes: all times are in microseconds (.000001) unless otherwise stated

Reading the hardware clock takes 3 'ticks' overhead ... this value will be factored out of all other measurements Clock interrupt took 4.00 microseconds (264 raw clock ticks)

Testing parameters: Clock samples: 32 Threads: 64 Thread switches: 128 Mutexes: 32 Mailboxes: 32 Semaphores: 32 Scheduler operations: 128 Counters: 32 Alarms:

Ave Min Max Var Ave Min Function ====== =============================	Confidence							
0.66	Ave	Min	Max	Var	Ave	Min	Function	
0.66	=====	=====	=====	=====	=====	====	======	
0.63	11.21	9.58	14.11	0.95	48%	34%	Create thread	
0.54 0.53 1.06 0.02 98% 98% Resume thread 0.78 0.74 1.39 0.05 93% 93% Set priority 0.05 0.05 0.36 0.01 98% 98% Get priority 2.06 1.89 6.65 0.25 95% 79% Kill [suspended] thread 0.65 0.65 0.68 0.00 98% 98% Yield [no other] thread	0.66	0.65	1.29	0.02	98%	98%	Yield thread [all suspended]	
0.78 0.74 1.39 0.05 93% 93% Set priority 0.05 0.05 0.36 0.01 98% 98% Get priority 2.06 1.89 6.65 0.25 95% 79% Kill [suspended] thread 0.65 0.65 0.68 0.00 98% 98% Yield [no other] thread	0.63	0.53	3.06	0.17	82%	82%	Suspend [suspended] thread	
0.05 0.05 0.36 0.01 98% 98% Get priority 2.06 1.89 6.65 0.25 95% 79% Kill [suspended] thread 0.65 0.65 0.68 0.00 98% 98% Yield [no other] thread								
2.06 1.89 6.65 0.25 95% 79% Kill [suspended] thread 0.65 0.65 0.68 0.00 98% 98% Yield [no other] thread	0.78	0.74	1.39	0.05	93%	93%	Set priority	
0.65 0.65 0.68 0.00 98% 98% Yield [no other] thread	0.05	0.05	0.36	0.01	98%	98%	Get priority	
	2.06	1.89	6.65	0.25	95%	79%	Kill [suspended] thread	
	0.65	0.65	0.68	0.00	98%	98%	Yield [no other] thread	
1.15 1.02 3.03 0.20 81% 81% Resume [suspended low prio] threa	1.15	1.02	3.03	0.20	81%			
0.54 0.52 1.18 0.03 96% 96% Resume [runnable low prio] thread	0.54	0.52	1.18	0.03	96%	96%	Resume [runnable low prio] thread	
0.94	0.94	0.88	1.27	0.01	95%	1%	Suspend [runnable] thread	
0.65 0.65 0.68 0.00 98% 98% Yield [only low prio] thread	0.65	0.65	0.68	0.00	98%	98%	Yield [only low prio] thread	
0.54 0.53 0.86 0.01 98% 96% Suspend [runnable->not runnable]	0.54	0.53	0.86	0.01	98%	96%	Suspend [runnable->not runnable]	
1.97 1.89 2.98 0.12 84% 84% Kill [runnable] thread	1.97	1.89	2.98	0.12	84%	84%	Kill [runnable] thread	
1.03 0.92 4.94 0.17 89% 89% Destroy [dead] thread	1.03	0.92	4.94	0.17	89%	89%	Destroy [dead] thread	
2.55 2.33 4.38 0.24 89% 70% Destroy [runnable] thread	2.55	2.33	4.38	0.24	89%	70%	Destroy [runnable] thread	
5.62 4.11 13.23 0.99 65% 40% Resume [high priority] thread	5.62	4.11	13.23	0.99	65%	40%	Resume [high priority] thread	
1.84 1.83 2.79 0.02 98% 98% Thread switch	1.84	1.83	2.79	0.02	98%	98%	Thread switch	
0.12 0.02 0.65 0.15 74% 74% Scheduler lock	0 12	0 02	0 65	0 15	74%	74%	Scheduler lock	
0.35 0.35 0.35 0.00 100% Scheduler unlock [0 threads]								

```
0.00 100% 100% Scheduler unlock [1 suspended]
    0.35
            0.35
                    0.35
    0.43
            0.35
                    1.17
                            0.13
                                   78% 78% Scheduler unlock [many suspended]
                                   75% 75% Scheduler unlock [many low prio]
    0.45
            0.35
                            0.15
                    1.17
    0.46
            0.15
                    3.38
                            0.30
                                   62% 50% Init mutex
    0.73
            0.64
                    3.27
                            0.16
                                   96% 96% Lock [unlocked] mutex
    0.77
            0.65
                   4.50
                            0.23
                                   96% 96% Unlock [locked] mutex
                                        96% Trylock [unlocked] mutex
96% Trylock [locked] mutex
                                   96%
    0.58
            0.55
                    1.42
                            0.05
   0.51
            0.50
                    0.83
                            0.02
                                   96%
    0.12
           0.11
                   0.41
                            0.02
                                   96%
                                        96% Destroy mutex
    4.72
           4.70
                    5.58
                            0.05
                                   96%
                                        96% Unlock/Lock mutex
   1.01
           0.67
                   3.48
                            0.40
                                   71% 71% Create mbox
    0.02
           0.00
                   0.53
                            0.03
                                   96%
                                        96% Peek [empty] mbox
    0.89
            0.68
                   4.20
                            0.29
                                   96%
                                        71% Put [first] mbox
    0.02
                                   96% 96% Peek [1 msg] mbox
           0.00
                   0.33
                            0.02
                                   50% 46% Put [second] mbox
    0.69
           0.68
                   0.76
                            0.01
    0.02
           0.00
                  0.30
                            0.02
                                   96% 96% Peek [2 msgs] mbox
    0.81
           0.71
                  3.83
                            0.19
                                   96% Get [first] mbox
    0.72
           0.71
                   1.02
                            0.02
                                   96% 96% Get [second] mbox
    0.81
           0.65
                   2.74
                            0.22
                                   96%
                                        71% Tryput [first] mbox
    0.67
            0.62
                    2.27
                            0.10
                                   96%
                                        96% Peek item [non-empty] mbox
   0.77
            0.71
                                        96% Tryget [non-empty] mbox
                    2.41
                            0.10
                                   96%
    0.59
           0.58
                            0.02
                                   96%
                                        96% Peek item [empty] mbox
                   0.88
    0.62
           0.62
                   0.67
                            0.00
                                   96% 96% Tryget [empty] mbox
   0.03
           0.02
                 0.32
                            0.02
                                   96% 96% Waiting to get mbox
    0.02
           0.02 0.06
                                   50% 46% Waiting to put mbox
                            0.01
           0.65
    0.75
                   3.59
                            0.18
                                   96%
                                        96% Delete mbox
    2.80
           2.77
                   3.59
                            0.05
                                   96%
                                        96% Put/Get mbox
    0.37
          0.18
                 0.88
                            0.28
                                   71% 71% Init semaphore
    0.48
           0.47
                    0.80
                            0.02
                                   96% 96% Post [0] semaphore
    0.60
           0.59
                   0.67
                            0.01
                                   50% 46% Wait [1] semaphore
                                   96% 96% Trywait [0] semaphore
96% 50% Trywait [1] semaphore
    0.53
           0.50
                    1.41
                            0.06
            0.50
                    0.71
    0.51
                            0.01
    0.09
            0.09
                    0.15
                            0.00
                                   96%
                                        96% Peek semaphore
    0.12
            0.11
                            0.02
                                   96% 96% Destroy semaphore
                    0.41
   3.05
           3.05
                   3.05
                            0.00
                                  100% 100% Post/Wait semaphore
    0.57
           0.17
                   2.76
                            0.24
                                   59% 25% Create counter
    0.06
            0.05
                   0.58
                            0.03
                                   96%
                                        96% Get counter value
    0.06
            0.03
                   0.64
                            0.04
                                   96%
                                        96% Set counter value
    0.73
                   1.02
           0.71
                            0.02
                                   96%
                                        96% Tick counter
                 0.15
                                   50% 46% Delete counter
    0.12
           0.11
                            0.01
    0.89
          0.64
                 3.15
                            0.34
                                   84% 71% Create alarm
    1.00
           0.95
                   2.41
                            0.09
                                   96% 96% Initialize alarm
   0.09
           0.06
                    0.68
                            0.04
                                   96%
                                        96% Disable alarm
    1.05
            1.00
                    2.48
                            0.09
                                   96%
                                        96% Enable alarm
                                   96% Delete alarm
    0.18
            0.17
                    0.50
                            0.02
    0.90
           0.89
                            0.01
                                   96% 96% Tick counter [1 alarm]
                   1.11
           5.59
                                   96% 96% Tick counter [many alarms]
   5.60
                   5.88
                            0.02
   1.53
           1.52
                   2.11
                            0.04
                                   96% 96% Tick & fire counter [1 alarm]
                                   96% 96% Tick & fire counters [>1 together]
   25.48
           25.47
                   25.76
                            0.02
                  6.44
    6.22
           6.21
                            0.01
                                   96%
                                        96% Tick & fire counters [>1 separately]
                                        98% Alarm latency [0 threads] 57% Alarm latency [2 threads]
    2.59
            2.56
                   6.17
                            0.07
                                   98%
    4.06
           3.95
                   6.24
                            0.08
                                   78%
                                   59% 10% Alarm latency [many threads]
    5.03
           2.56
                   9.03
                            0.89
    5.68
           5.59
                  15.45
                            0.15
                                   99% 99% Alarm -> thread resume latency
    2.52
           1.41
                   8.12
                            0.00
                                             Clock/interrupt latency
    2.05
            1.17
                    6.00
                            0.00
                                            Clock DSR latency
                 1072 (main stack: 1320) Thread stack used (1912 total)
All done, main stack
                               : stack used 1320 size 2936
```

```
All done : Interrupt stack used 136 size 4096
All done : Idlethread stack used 996 size 2048

Timing complete - 30360 ms total

PASS:<Basic timing OK>
EXIT:<done>
```

Board: NEC VR4373

CPU: NEC VR4300 133MHz

```
Startup, main stack
                                : stack used 1304 size 3576
                     : Interrupt stack used
                                             980 size 4096
Startup
Startup
                     : Idlethread stack used
                                             494 size 2552
eCos Kernel Timings
Notes: all times are in microseconds (.000001) unless otherwise stated
Reading the hardware clock takes 3 'ticks' overhead
... this value will be factored out of all other measurements
Clock interrupt took
                      6.49 microseconds (431 raw clock ticks)
Testing parameters:
   Clock samples:
                           32
   Threads:
  Thread switches:
                          128
  Mutexes:
                          32
  Mailboxes:
  Semaphores:
                          32
  Scheduler operations: 128
   Counters:
                          32
  Alarms:
                          32
```

			_		
Con	f.	iα	aF	n	70

Ave	Min	Max	Var	Ave	Min	Function
=====	=====	=====	=====			======
17.21	16.18	22.14	0.88	75%	68%	Create thread
0.84	0.78	1.29	0.10	81%	81%	Yield thread [all suspended]
0.90	0.62	3.20	0.35	87%	87%	Suspend [suspended] thread
0.74	0.65	1.16	0.12	81%	68%	Resume thread
1.11	0.90	1.70	0.25	75%	68%	Set priority
0.11	0.05	0.35	0.09	75%	75%	Get priority
2.93	2.24	8.27	0.78	93%	75%	Kill [suspended] thread
0.88	0.78	1.92	0.16	93%	81%	Yield [no other] thread
1.82	1.20	4.71	0.62	87%	62%	Resume [suspended low prio] thread
0.70	0.63	0.86	0.09	68%	68%	Resume [runnable low prio] thread
1.21	1.07	1.61	0.13	81%	68%	Suspend [runnable] thread
0.86	0.78	1.58	0.13	81%	81%	Yield [only low prio] thread
0.69	0.62	0.84	0.09	68%	68%	Suspend [runnable->not runnable]
2.64	2.24	4.35	0.43	81%	62%	Kill [runnable] thread
1.50	1.07	5.82	0.56	93%	87%	Destroy [dead] thread
3.66	2.75	7.74	0.82	50%	56%	Destroy [runnable] thread
13.65	8.33	27.88	3.70	50%	43%	Resume [high priority] thread
2.04	1.89	3.32	0.15	46%	49%	Thread switch
0.19	0.05	0.83	0.13	48%	44%	Scheduler lock
0.50	0.41	1.59	0.13	89%	73%	Scheduler unlock [0 threads]
0.52	0.41	1.29	0.14	89%	64%	Scheduler unlock [1 suspended]
0.56	0.41	1.49	0.15	42%	47%	Scheduler unlock [many suspended]
0.56	0.41	1.41	0.15			Scheduler unlock [many low prio]

```
0.57
           0.20
                   2.33
                           0.27
                                   65% 50% Init mutex
96% 75% Lock [unlock
    0.89
            0.75
                    3.35
                           0.20
                                        75% Lock [unlocked] mutex
                                   96% 93% Unlock [locked] mutex
    0.90
           0.74
                           0.25
                   4.38
    0.77
           0.65
                           0.17
                                   96% 75% Trylock [unlocked] mutex
                   2.63
    0.66
           0.59
                            0.10
                                   75% 75% Trylock [locked] mutex
                  1.16
                 0.45
    0.07
           0.00
                           0.09
                                   75% 75% Destroy mutex
    7.95
           7.71
                   9.49
                           0.19
                                   50%
                                       46% Unlock/Lock mutex
    1.04
                           0.27
                                   93% 68% Create mbox
           0.81
                   3.44
    0.10
           0.02
                                   71% 68% Peek [empty] mbox
                   0.57
                           0.11
    1.15
           0.83
                   4.71
                           0.31
                                   53% 71% Put [first] mbox
    0.10
           0.02
                   0.57
                           0.12
                                   68% Feek [1 msq] mbox
   1.01
           0.83
                   3.83
                           0.22
                                   93% 75% Put [second] mbox
                                   71% 71% Peek [2 msgs] mbox
96% 87% Get [first] mbox
                           0.10
    0.09
           0.02
                   0.57
    1.03
           0.81
                           0.27
                   5.02
                   1.61
                                   84% 62% Get [second] mbox
   0.93
           0.81
                           0.14
           0.77
                  4.18
                                   68% 50% Tryput [first] mbox
    1.07
                           0.23
    0.89
           0.72
                  3.49
                           0.21
                                   93% 71% Peek item [non-empty] mbox
   1.04
           0.83
                 4.09
                           0.26
                                   90% 81% Tryget [non-empty] mbox
                  1.97
    0.79
           0.68
                           0.15
                                   87% 68% Peek item [empty] mbox
   0.84
           0.72
                   2.36
                           0.17
                                   93% 68% Tryget [empty] mbox
    0.13
            0.02
                   0.59
                            0.13
                                   87%
                                       62% Waiting to get mbox
                                       62% Waiting to put mbox
    0.13
           0.02
                   0.90
                           0.13
                                   90%
    0.93
           0.77
                   3.23
                           0.21
                                   90%
                                        71% Delete mbox
    4.74
           4.51
                 8.80
                           0.32
                                   93%
                                       78% Put/Get mbox
    0.50
                   1.95
                           0.29
                                   90% 50% Init semaphore
           0.21
    0.86
           0.57
                    2.87
                           0.29
                                   93%
                                       56% Post [0] semaphore
                                   93% 56% Wait [1] semaphore
90% 59% Trywait [0] semaphore
    1.01
            0.74
                    3.62
                           0.28
                   3.17
    0.87
           0.60
                           0.28
                                   93% 56% Trywait [1] semaphore
    0.74
           0.62
                  1.70
                           0.14
    0.36
          0.11
                  1.11
                           0.26
                                   65% 56% Peek semaphore
   0.25
           0.12
                  1.19
                           0.14
                                   93% 56% Destroy semaphore
    7.85
           7.52
                   8.93
                           0.21
                                   62% 43% Post/Wait semaphore
    0.90
                           0.29
                                   65%
            0.44
                   3.08
                                       28% Create counter
    0.07
           0.05
                           0.05
                                   96% 96% Get counter value
                   0.89
    0.06
           0.05
                   0.33
                            0.02
                                   96% 96% Set counter value
    0.88
          0.86
                   1.62
                           0.05
                                   96% 96% Tick counter
                                  96% 96% Delete counter
    0.13
         0.12
                   0.41
                           0.02
    1.37
           0.81
                   2.95
                           0.27
                                   62%
                                       25% Create alarm
    1.35
                                   96% 93% Initialize alarm
           1.17
                   6.03
                           0.31
    0.11
                                   90% 90% Disable alarm
           0.08
                           0.05
                   0.65
                                   93% 87% Enable alarm
   1.23
           1.14
                  3.05
                           0.15
    0.21
           0.18
                 0.47
                          0.04
                                   90% Delete alarm
                           0.07
                                   96% 96% Tick counter [1 alarm]
   1.03
           0.99
                 2.11
   4.96
           4.96
                   4.96
                           0.00 100% 100% Tick counter [many alarms]
    1.70
            1.67
                    2.51
                            0.05
                                   96% 96% Tick & fire counter [1 alarm]
                                   96% 96% Tick & fire counters [>1 together]
   26.39
                  26.71
          26.38
                           0.02
                                   96% 96% Tick & fire counters [>1 separately]
   5.65
           5.64
                   5.91
                           0.02
                  9.86
                                   96% 54% Alarm latency [0 threads]
   2.55
           2.38
                           0.19
    5.37
           3.80
                   9.73
                           0.95
                                   50% 34% Alarm latency [2 threads]
                                   57% 14% Alarm latency [many threads]
    8.79
           5.83
                  16.12
                           1.29
   5.85
            2.26
                  16.24
                            0.00
                                            Clock/interrupt latency
                1544 (main stack: 1664) Thread stack used (2552 total)
        1536
                               : stack used 1664 size 3576
All done, main stack
All done
                     : Interrupt stack used 312 size 4096
All done
                     : Idlethread stack used 1440 size 2552
Timing complete - 23810 ms total
PASS: < Basic timing OK>
EXIT: <done>
```

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Appendix 3: The eCos Copyright Assignment Form, Revision 1.1

Rationale

This preamble describes how to use the standard **eCos** copyright assignment form. The rationale behind this assignment is to avoid any possible confusion over the legal ownership of **eCos**, and to indemnify Red Hat and all **eCos** users against copyright or patent claims on contributed code used within **eCos**. Red Hat would be especially vulnerable, but all users and their **eCos** based applications could be affected.

All contributions to **eCos** for which there are copyright assignments will be covered by the Red Hat **eCos** public license. The license provides a guarantee that the contribution will remain freely available to all.

This agreement gives Red Hat ownership of your changes but promises that you will retain the right to use your contributed changes as you see fit.

Because employers often can claim ownership over things that employees write, you may also have to get your employer to sign a disclaimer that says that they have no claim to the changes you are contributing.

Please read everything, and if you have any questions, email

ecos-assign@redhat.com

for help.

Thanks for your contribution to eCos!

How to assign copyright

The way to assign copyright to Red Hat is to sign an assignment contract. This is what makes Red Hat the legal copyright holder, so that Red Hat can register the copyright on the new version.

If you are employed as a programmer (even at a university), or have made an agreement with your employer or school that gives them ownership of the software you write, then Red Hat needs a signed letter from your employer disclaiming rights to the contributed software.

The disclaimer should be printed on the company's headed paper, and signed by an officer of the company, or someone authorized to license the company's intellectual property. Here is an example of wording that can be used for this purpose:

<INSERT COMPANY NAME> hereby disclaims all copyright interest in the changes and enhancements made by <INSERT YOUR NAME> to eCos, including any future revisions of these changes and enhancements.

<INSERT COMPANY NAME> affirms that it has no other intellectual property interest that would undermine this release, or the use of **eCos**, and will do nothing to undermine it in the future.

<INSERT SIGNATURE OF OFFICER OF COMPANY>,

<INSERT DATE>

<INSERT PRINTED NAME OF OFFICER OF COMPANY>

<INSERT TITLE OF OFFICER>

If your employer says they do have an intellectual property claim that could conflict with the use of the program, then please contact Red Hat to discuss possible next steps.

Below is the usual assignment contract. You need to edit and replace <INSERT NAME OF CONTRIBUTOR> with your full name. Please print a copy, sign, date, and mail it to:

Legal Department (eCos Assignments) Red Hat, Inc. 2600 Meridian Parkway NC 27713 USA

Don't forget to include the original signed copy of the employer's disclaimer.

Please try to print the whole first page of the form on a single piece of paper. If it doesn't fit on one printed page, put it on two sides of a single piece of paper, and attach the second page of the form. Please write the date using letters rather than numbers to avoid any confusion due to international day/month ordering conventions.

Note: This text is also available in the **eCos** software distribution, in the file assign.txt.

----- Cut Here -----

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[Print Name]
 Date: [Please write using letters]
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[Which files have you changed so far, and which new files have you written so

far?]

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