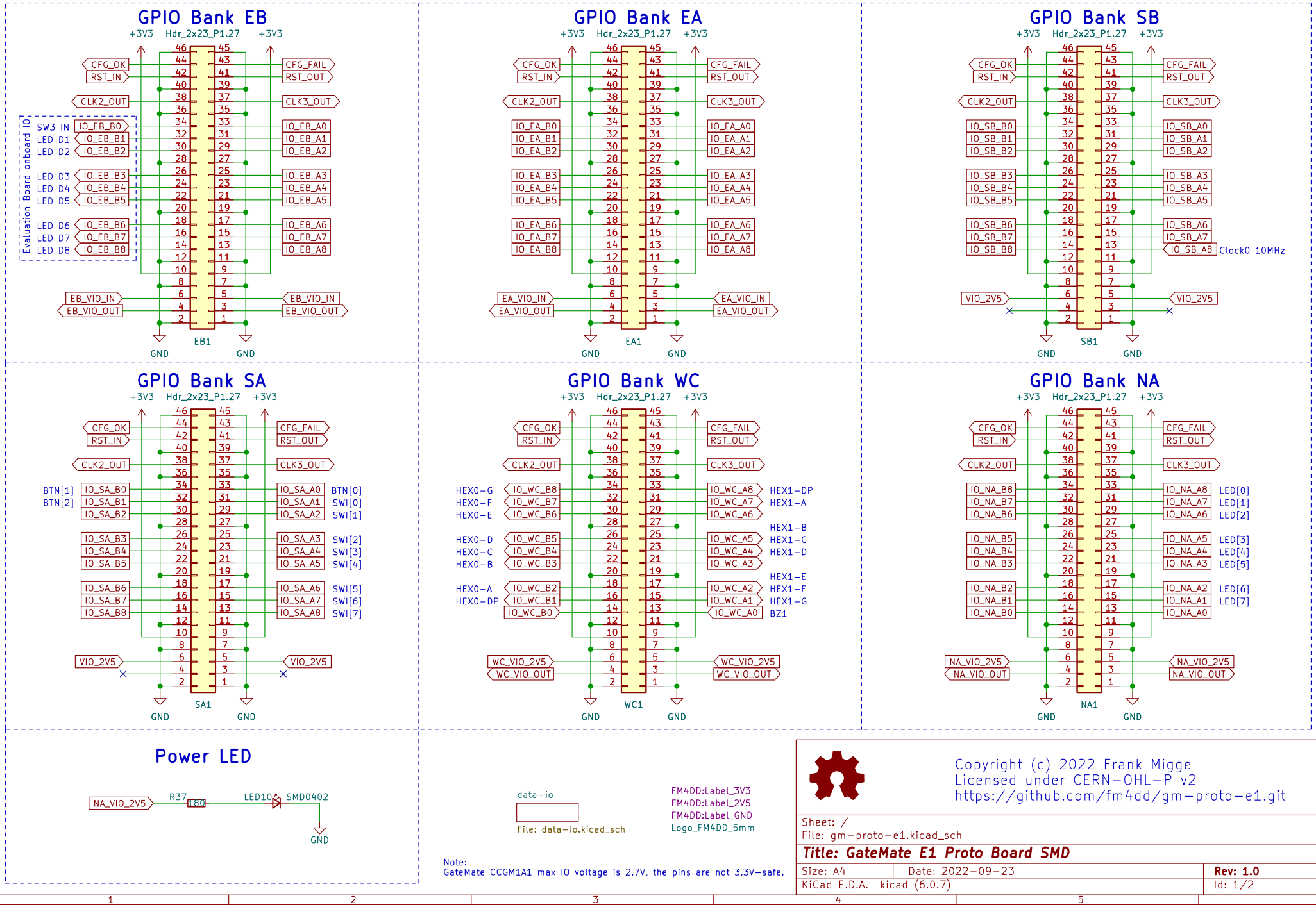
GPIO Bank EB
+3V3 Hdr_2x23_P1.27 +3V3
CFG_OK
RST_IN
CLK2_OUT
SW3 IN IO_EB_B0
LED D1 IO_EB_B1
LED D2 IO_EB_B2
LED D3 IO_EB_B3
LED D4 IO_EB_B4
LED D5 IO_EB_B5
LED D6 IO_EB_B6
LED D7 IO_EB_B7
LED D8 IO_EB_B8
Evaluation Board onboard IO
EB_VIO_IN
EB_VIO_OUT
CFG_FAIL
RST_OUT
CLK3_OUT
IO_EB_A0
IO_EB_A1
IO_EB_A2
IO_EB_A3
IO_EB_A4
IO_EB_A5
IO_EB_A6
IO_EB_A7
IO_EB_A8
EB1
GND
GPIO Bank EA
IO_EA_B0
IO_EA_B1
IO_EA_B2
IO_EA_B3
IO_EA_B4
IO_EA_B5
IO_EA_B6
IO_EA_B7
IO_EA_B8
EA_VIO_IN
EA_VIO_OUT
IO_EA_A0
IO_EA_A1
IO_EA_A2
IO_EA_A3
IO_EA_A4
IO_EA_A5
IO_EA_A6
IO_EA_A7
IO_EA_A8
EA1
GPIO Bank SB
IO_SB_B0
IO_SB_B1
IO_SB_B2
IO_SB_B3
IO_SB_B4
IO_SB_B5
IO_SB_B6
IO_SB_B7
IO_SB_B8
VIO_2V5
IO_SB_A0
IO_SB_A1
IO_SB_A2
IO_SB_A3
IO_SB_A4
IO_SB_A5
IO_SB_A6
IO_SB_A7
IO_SB_A8
Clock0 10MHz
SB1
GPIO Bank SA
BTN[1]
BTN[2]
IO_SA_B0
IO_SA_B1
IO_SA_B2
IO_SA_B3
IO_SA_B4
IO_SA_B5
IO_SA_B6
IO_SA_B7
IO_SA_B8
IO_SA_A0
IO_SA_A1
IO_SA_A2
IO_SA_A3
IO_SA_A4
IO_SA_A5
IO_SA_A6
IO_SA_A7
IO_SA_A8
BTN[0]
SWI[0]
SWI[1]
SWI[2]
SWI[3]
SWI[4]
SWI[5]
SWI[6]
SWI[7]
SA1
GPIO Bank WC
HEX0-G
HEX0-F
HEX0-E
HEX0-D
HEX0-C
HEX0-B
HEX0-A
HEX0-DP
IO_WC_B8
IO_WC_B7
IO_WC_B6
IO_WC_B5
IO_WC_B4
IO_WC_B3
IO_WC_B2
IO_WC_B1
IO_WC_B0
WC_VIO_2V5
WC_VIO_OUT
IO_WC_A8
IO_WC_A7
IO_WC_A6
IO_WC_A5
IO_WC_A4
IO_WC_A3
IO_WC_A2
IO_WC_A1
IO_WC_A0
HEX1-DP
HEX1-A
HEX1-B
HEX1-C
HEX1-D
HEX1-E
HEX1-F
HEX1-G
BZ1
WC1
GPIO Bank NA
IO_NA_B8
IO_NA_B7
IO_NA_B6
IO_NA_B5
IO_NA_B4
IO_NA_B3
IO_NA_B2
IO_NA_B1
IO_NA_B0
NA_VIO_2V5
NA_VIO_OUT
IO_NA_A8
IO_NA_A7
IO_NA_A6
IO_NA_A5
IO_NA_A4
IO_NA_A3
IO_NA_A2
IO_NA_A1
IO_NA_A0
LED[0]
LED[1]
LED[2]
LED[3]
LED[4]
LED[5]
LED[6]
LED[7]
NA1
Power LED
NA_VIO_2V5
R37
180
LED10
SMD0402
GND
data-io
File: data-io.kicad_sch
FM4DD:Label_3V3
FM4DD:Label_2V5
FM4DD:Label_GND
Logo_FM4DD_5mm
Note:
GateMate CCGM1A1 max IO voltage is 2.7V, the pins are not 3.3V-safe.
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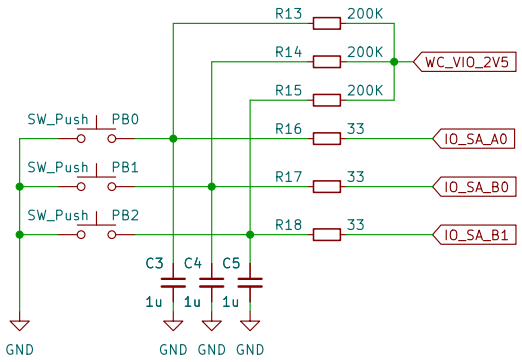
https://github.com/fm4dd/gm-proto-e1.git
Sheet: /
File: gm-proto-e1.kicad_sch
Title: GateMate E1 Proto Board SMD
Size: A4
Date: 2022-09-23
Rev: 1.0
KiCad E.D.A. kicad (6.0.7)
Id: 1/2

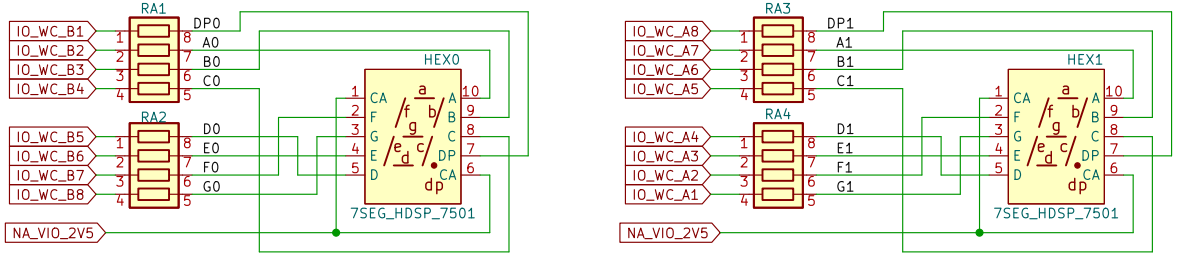
Input: Push Buttons

2x push buttons with pull-up. Negative logic: press = 0.



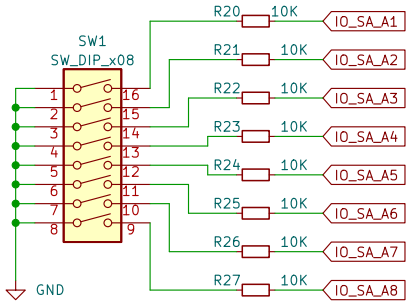
Output: 7-Segment Display

2x Digits, common anode, no mulitplexing. Broadcom HDSP-7501 0.3" module



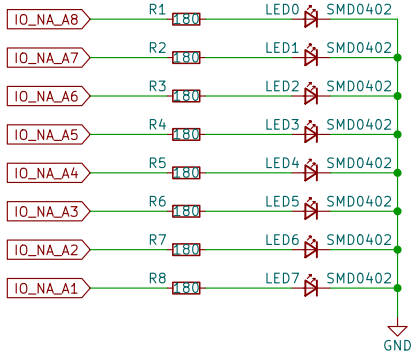
Input: Slide Switches

4x slide switches set GND / +2V5 level signals.

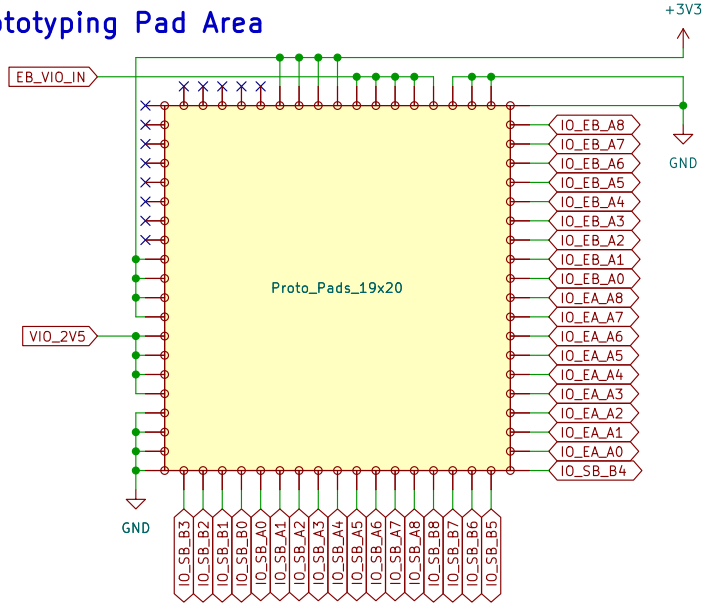


Output: LED

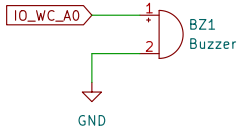
4x 3mm LED for digital output signals.



Prototyping Pad Area



Buzzer Output



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https://github.com/fm4dd/gm-proto-e1.git

Sheet: /data-io/
File: data-io.kicad_sch

Title: GateMate E1 Proto Board SMD – Input Output Signals

Size: A4	Date: 2022-09-23	Rev:
KiCad E.D.A. kicad (6.0.7)		Id: 2/2