grandideastudio / **jtagulator**   Public

<> Code   ⊙ Issues 2   ⅈↅ Pull requests   ▶ Actions   📖 Wiki   ⊘ Security   ∿ Insights

Home

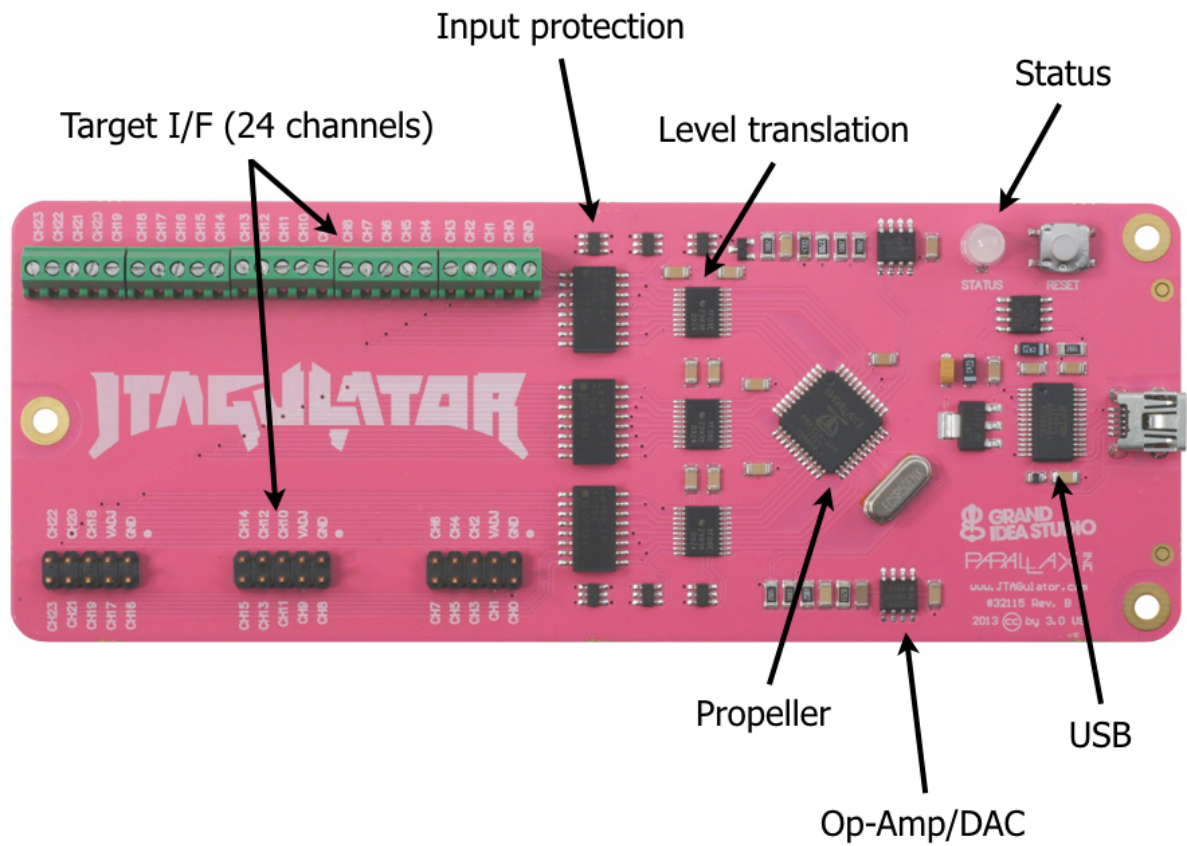Edit   New page   **Jump to bottom**

Joe Grand edited this page on Mar 2 · 9 revisions

On-chip debug (OCD) interfaces can provide chip-level control of a target device and are a primary vector used by engineers, researchers, and hackers to extract program code or data, modify memory contents, or affect device operation on-the-fly. Depending on the complexity of the target device, manually locating available OCD interfaces can be a difficult and time consuming task, sometimes requiring physical destruction or modification of the device.

Designed by [Grand Idea Studio](), the [JTAGulator]() is an open source hardware hacking tool that assists in identifying OCD interfaces from test points, vias, component pads, or connectors of a target device.

Features

- Detection of JTAG/IEEE 1149.1, ARM SWD, and UART/asynchronous serial
- Direct connection to [sigrok]() and [OpenOCD]()
- 24 channels with input protection circuitry
- Adjustable target I/O voltage for level translation: 1.4 to 3.3 V
- USB interface (FTDI FT232) for menu-based control from host computer (Windows, macOS, Linux)

Input protection

Target I/F (24 channels)

Level translation

Status

Propeller

USB

Op-Amp/DAC

+ Add a custom footer

▸ **Pages**   7

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