

mintermo	Α	В	С	D	а	b	С	d	е	f	g	sin	C-2
0	0	0	0	0	1	1	1	1	1	1	0	0	0
1	0	0	0	1	0	0	0	0	1	1	0	0	1
2	0	0	1	0	1	0	1	1	0	1	1	0	2
3	0	0	1	1	1	0	0	1	1	1	1	0	3
4	0	1	0	0	0	1	0	0	1	1	1	0	4
5	0	1	0	1	1	1	0	1	1	0	1	0	5
6	0	1	1	0	1	1	1	1	1	0	1	0	6
7	0	1	1	1	1	0	0	0	1	1	0	0	7
8	1	0	0	0	1	1	1	1	1	1	1	1	-8
9	1	0	0	1	1	0	0	0	1	1	0	1	-7
10	1	0	1	0	1	1	1	1	1	0	1	1	-6
11	1	0	1	1	1	1	0	1	1	0	1	1	-5
12	1	1	0	0	0	1	0	0	1	1	1	1	-4
13	1	1	0	1	1	0	0	1	1	1	1	1	-3
14	1	1	1	0	1	0	1	1	0	1	1	1	-2
15	1	1	1	1	0	0	0	0	1	1	0	1	-1

Figura 2: Tabela-verdade a ser preenchida com o comportamento do circuito.





