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Topics

1. Locality and Arrays
2. Taxonomy
3. Vector Programming (Vector)
4. Atomic Instructions and Locks
5. Open MP
6. Very Long Instruction Word (VLIW)
7. Multithreading Architectures (Multithreaded Architecture)
8. MPI (Distributed Memory Parallel Machine)
9. Dynamic Instruction Scheduling (Out of Order Superscalar)

Processors

- Multicore
- GPUs
- Vector
- VLIW
- Multithreaded Architecture
- Distributed Memory Parallel Machine
- Out of Order Superscalar