

ZynqLab01

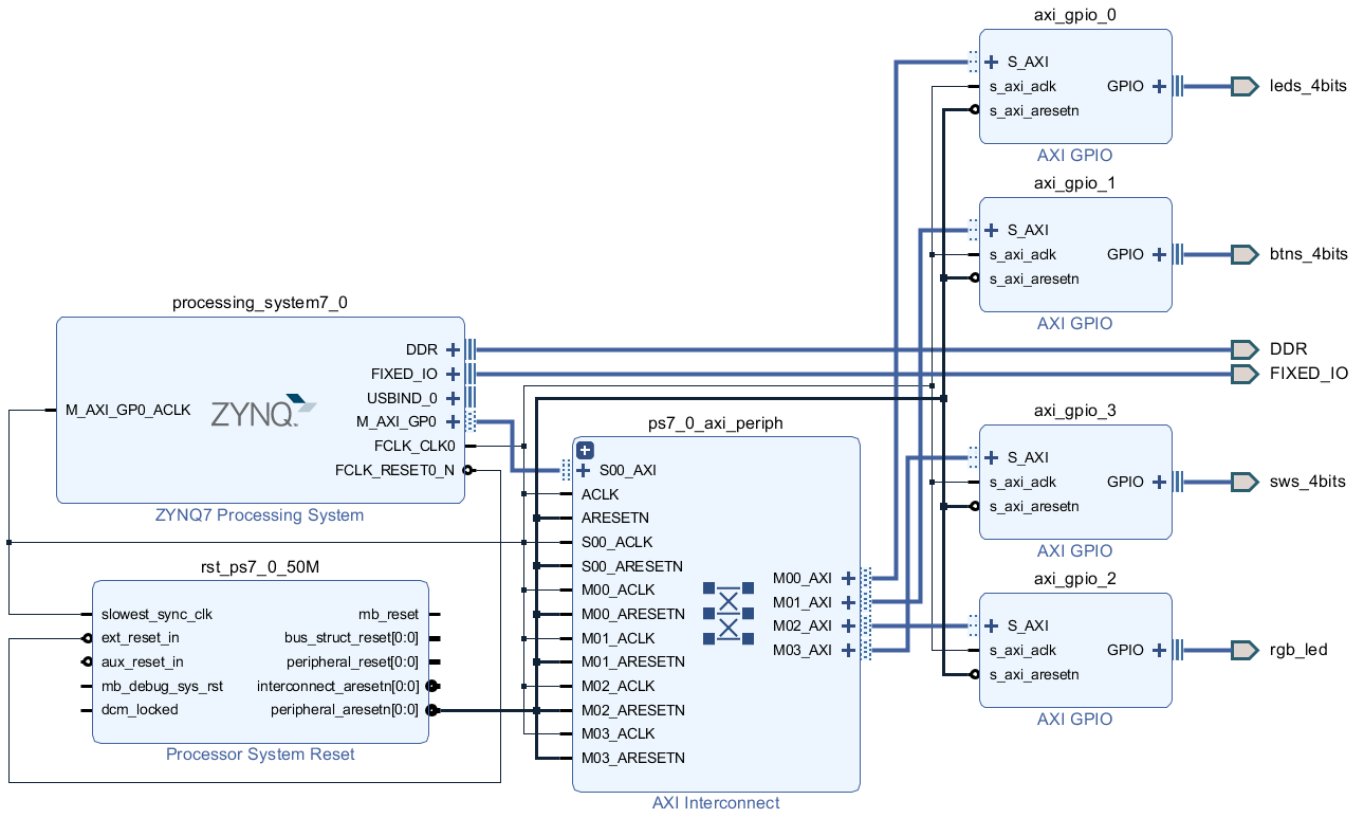


학과	전기정보공학과
학번	20101418
이름	장민진
과목명	SoC 설계 입문
제출일자	2024/10/06

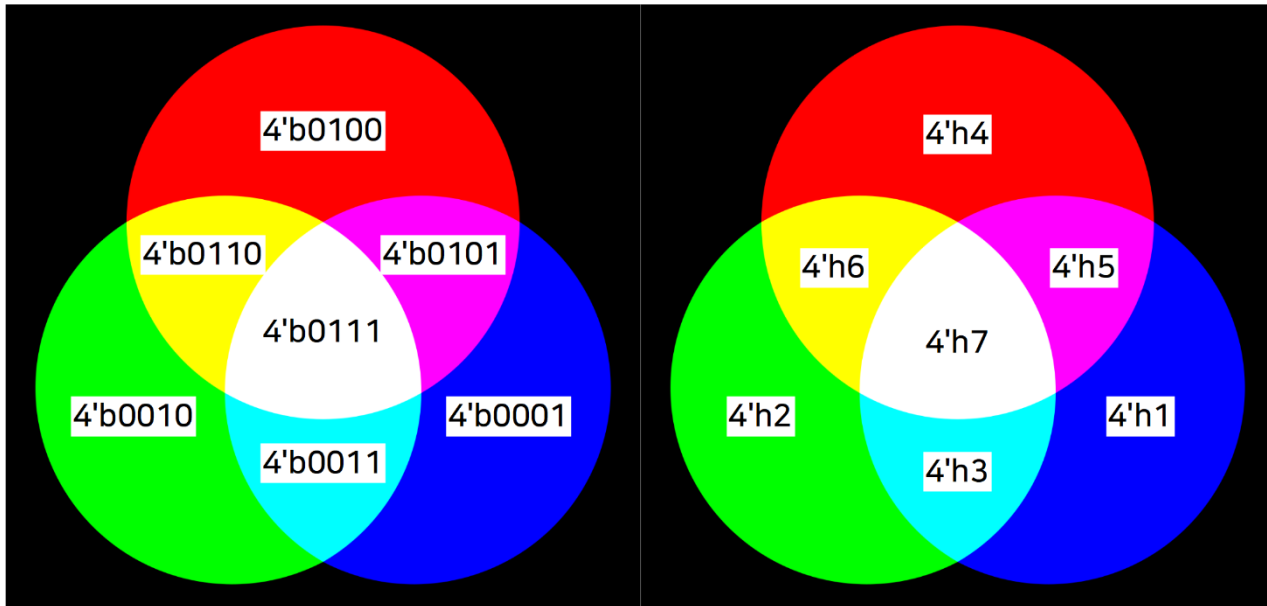
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I. 구조 및 개요

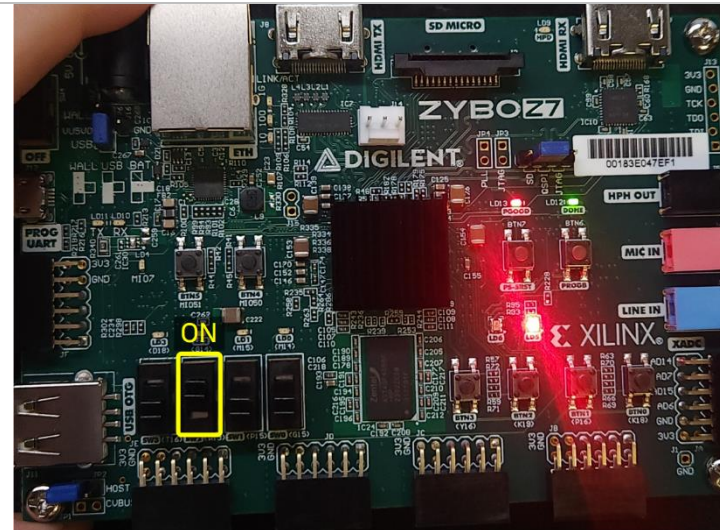


dsdf

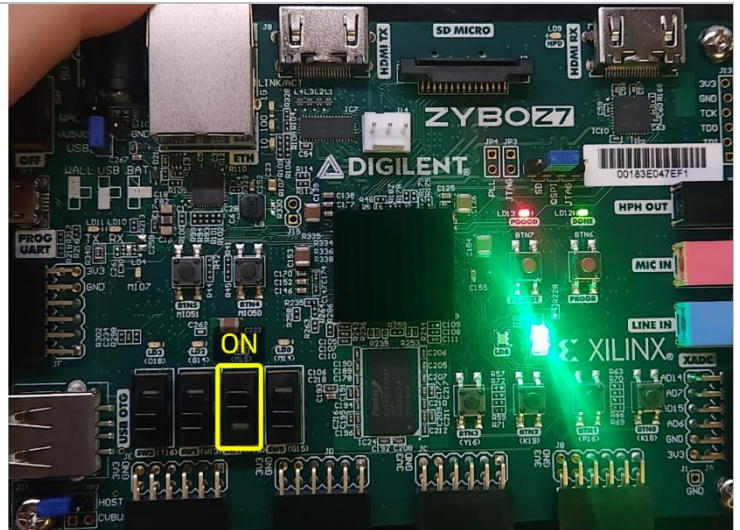


II. Board Test 결과

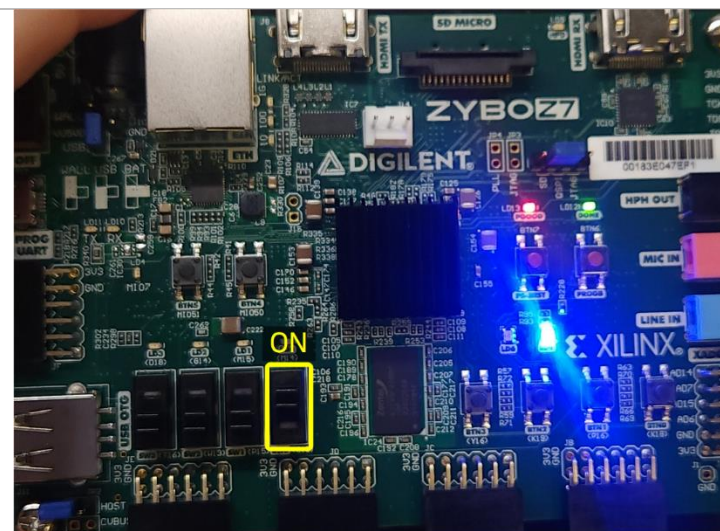
첫 번째 RGB LED Test - Switch Input



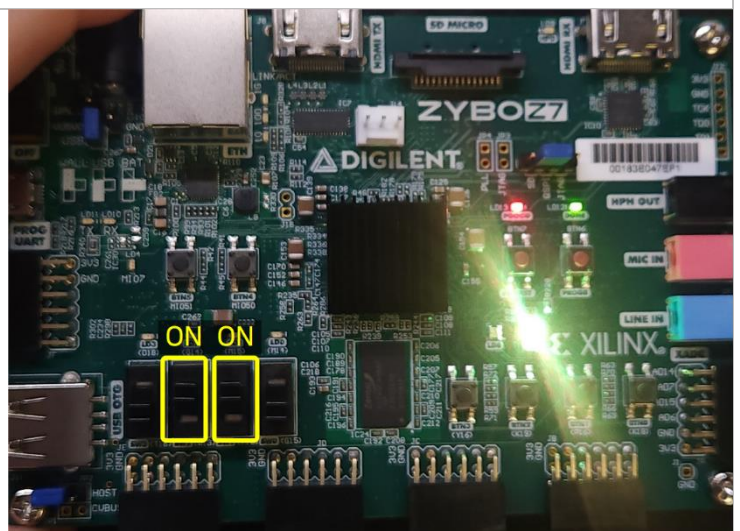
SW State = 0x4, Btn State = 0x0,
RGB1 = 0x4, RGB2 = 0x0



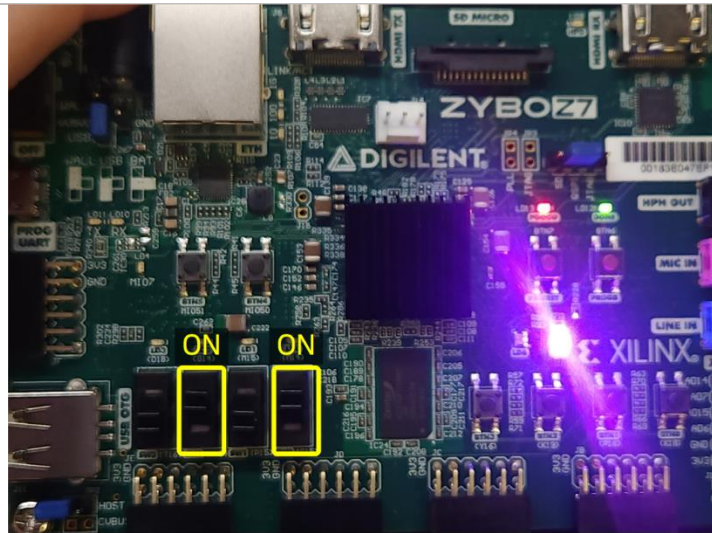
SW State = 0x2, Btn State = 0x0,
RGB1 = 0x2, RGB2 = 0x0



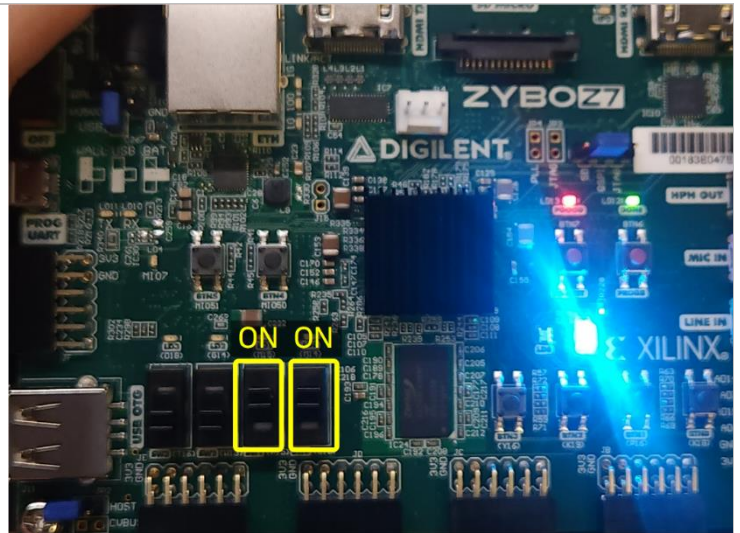
SW State = 0x1, Btn State = 0x0,
RGB1 = 0x1, RGB2 = 0x0



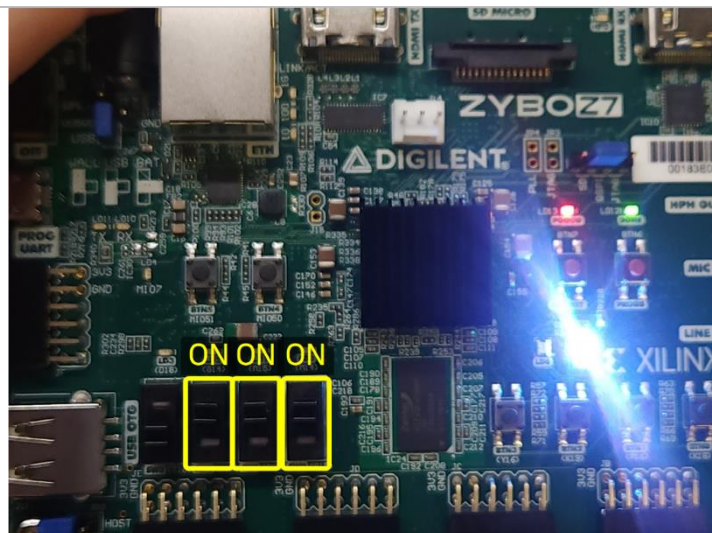
SW State = 0x6, Btn State = 0x0,
RGB1 = 0x6, RGB2 = 0x0



SW State = 0x5, Btn State = 0x0,
RGB1 = 0x5, RGB2 = 0x0

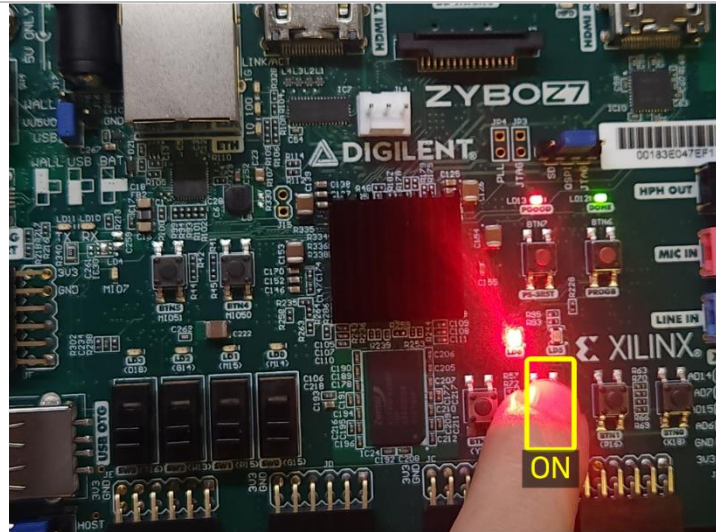


SW State = 0x3, Btn State = 0x0,
RGB1 = 0x3, RGB2 = 0x0

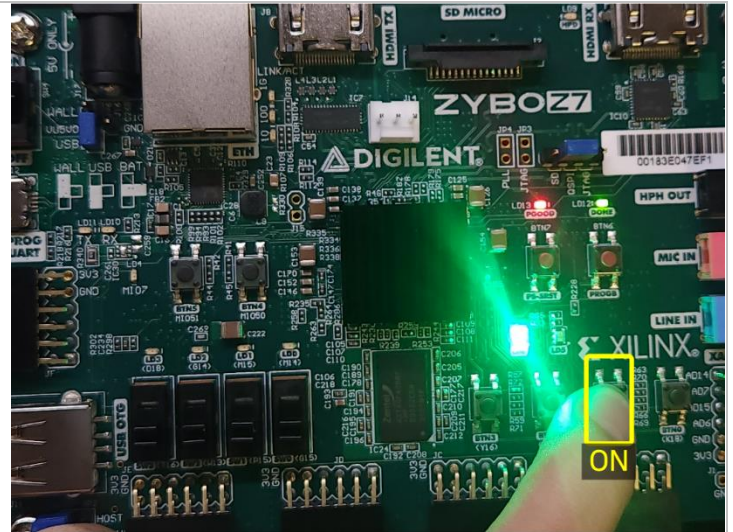


SW State = 0x7, Btn State = 0x0,
RGB1 = 0x7, RGB2 = 0x0

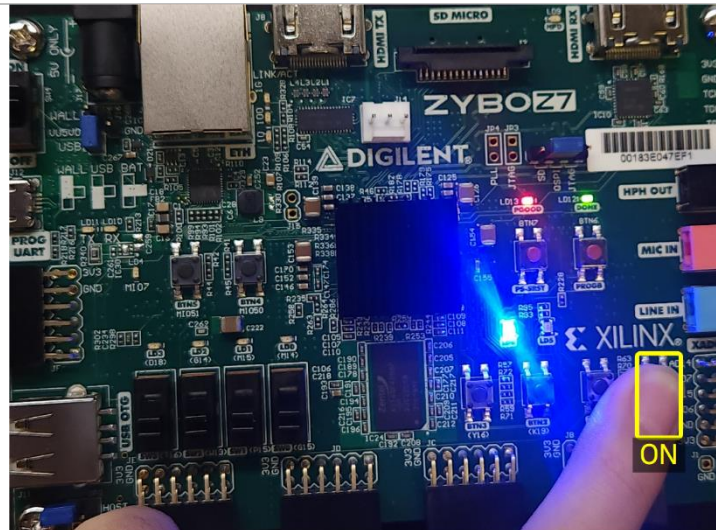
두 번째 RGB LED Test - Button Input



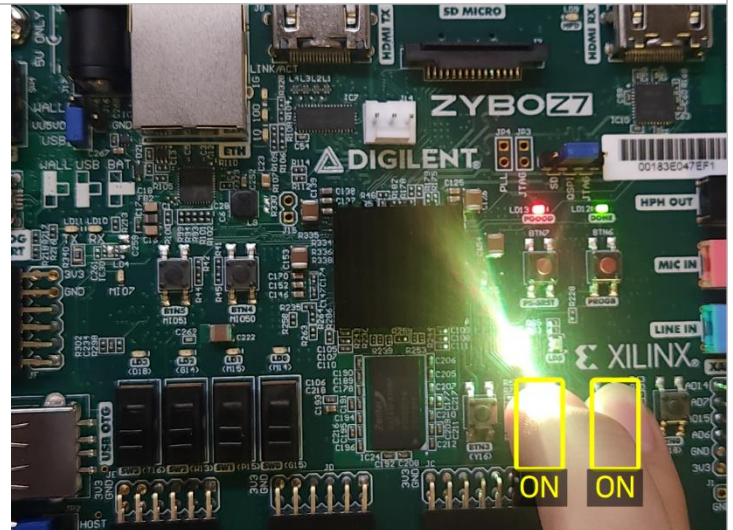
SW State = 0x0, Btn State = 0x4,
RGB1 = 0x0, RGB2 = 0x4



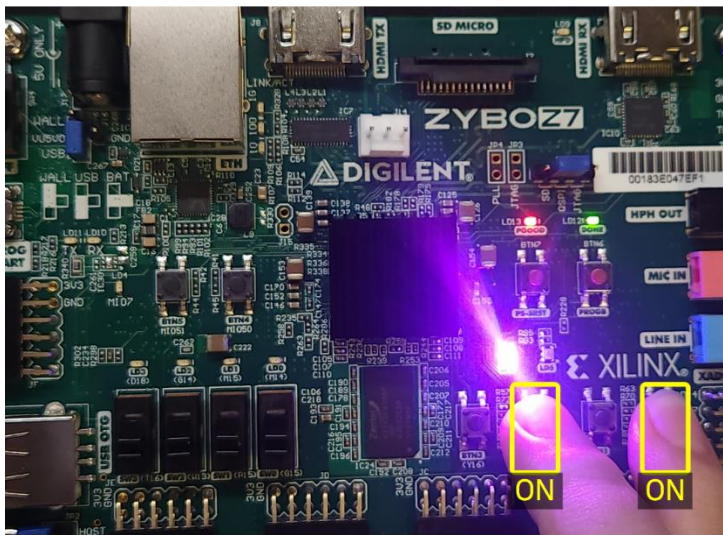
SW State = 0x0, Btn State = 0x2,
RGB1 = 0x0, RGB2 = 0x2



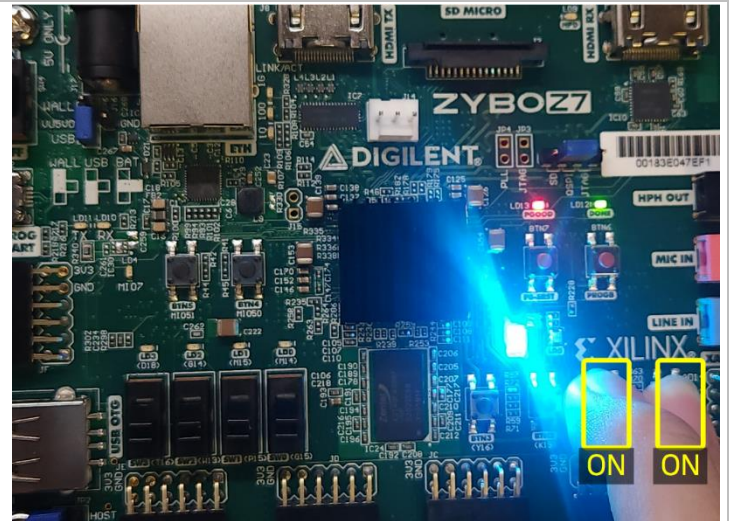
SW State = 0x0, Btn State = 0x1,
RGB1 = 0x0, RGB2 = 0x1



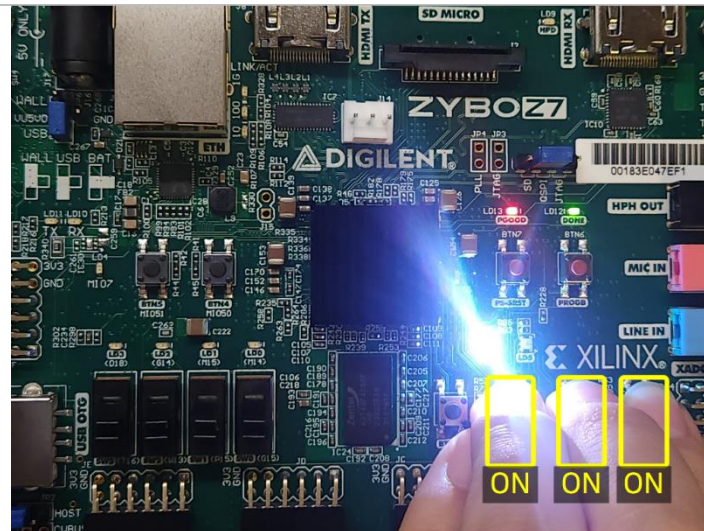
SW State = 0x0, Btn State = 0x6,
RGB1 = 0x0, RGB2 = 0x6



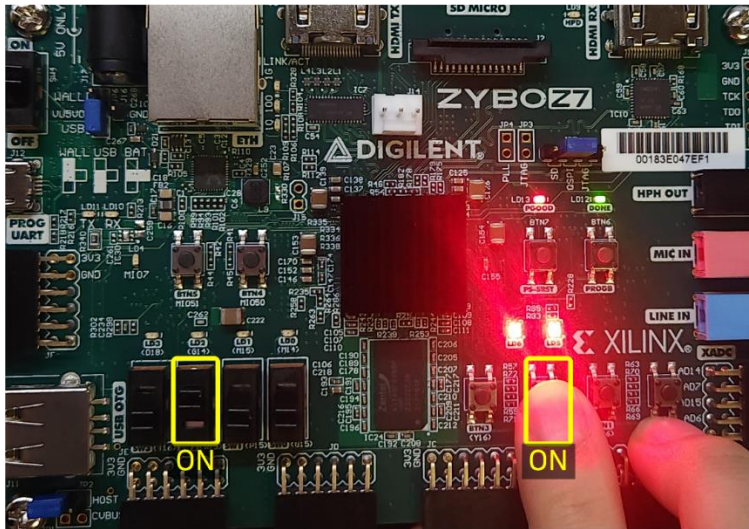
SW State = 0x0, Btn State = 0x5,
RGB1 = 0x0, RGB2 = 0x5



SW State = 0x0, Btn State = 0x3,
RGB1 = 0x0, RGB2 = 0x3



SW State = 0x0, Btn State = 0x7,
RGB1 = 0x0, RGB2 = 0x7



SW State = 0x4, Btn State = 0x4, RGB1 = 0x4, RGB2 = 0x4

III. 결론