# CSF 2019 Programming Assignment #4

## Overview

For this assignment, you will be writing a program in C or C++ that assembles a SCRAM program from an assembler level description. The extended SCRAM instructions that you must decode are:

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$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	Mnemonic				En	codi	ng			updated?	Comments
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	HLT	0	0	0	0	0	0	0	0	no	
LDI 0 0 1 1 0 $b_3$ $b_2$ $b_1$ $b_0$ no $A \leftarrow M[M[b]]$ STA 0 0 1 1 $b_3$ $b_2$ $b_1$ $b_0$ no $M[b] \leftarrow A$ STI 0 1 0 0 $b_3$ $b_2$ $b_1$ $b_0$ no $M[M[b]] \leftarrow A$ ADD 0 1 0 1 $b_3$ $b_2$ $b_1$ $b_0$ yes $A \leftarrow A + M[b]$ SUB 0 1 1 0 $b_3$ $b_2$ $b_1$ $b_0$ yes $A \leftarrow A + M[b] + 1$ JMP 0 1 1 1 $b_3$ $b_2$ $b_1$ $b_0$ no $PC \leftarrow b$ JMZ 1 0 0 0 $b_3$ $b_2$ $b_1$ $b_0$ no $PC \leftarrow b$ if $A = 0$ AND 1 0 0 1 $b_3$ $b_2$ $b_1$ $b_0$ no $PC \leftarrow b$ if $A = 0$ AND 1 0 0 1 $b_3$ $b_2$ $b_1$ $b_0$ no $A \leftarrow A \land M[b]$ IOR 1 0 1 1 $b_3$ $b_2$ $b_1$ $b_0$ no $A \leftarrow A \land M[b]$ XOR 1 0 1 1 $b_3$ $b_2$ $b_1$ $b_0$ no $A \leftarrow A \land M[b]$ ADL 1 1 0 0 $b_3$ $b_2$ $b_1$ $b_0$ no $A \leftarrow A \land M[b]$ ADL 1 1 0 0 $b_3$ $b_2$ $b_1$ $b_0$ no $A \leftarrow A \land M[b]$ ADC 1 1 0 1 $b_3$ $b_2$ $b_1$ $b_0$ yes $A \leftarrow A + b$ (sign extended)  ADC 1 1 0 0 $b_3$ $b_2$ $b_1$ $b_0$ yes $A \leftarrow A + M[b] + C$ SBB 1 1 1 0 0 $b_3$ $b_2$ $b_1$ $b_0$ yes $A \leftarrow A + M[b] + C$ SBB 1 1 1 0 0 0 0 1 0 0 0 0 0 0 0 0 0 0 0	EXT	0	0	0	0	$b_7$	$b_6$	$b_5$	$b_4$	no	$b_i \neq 0$
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	LDA	0	0	0	1	$b_3$	$b_2$	$b_1$	$b_0$	no	$A \leftarrow M[b]$
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	LDI	0	0	1	0	$b_3$	$b_2$	$b_1$	$b_0$	no	$A \leftarrow M[M[b]]$
ADD 0 1 0 1 $b_3$ $b_2$ $b_1$ $b_0$ yes $A \leftarrow A + M[b]$ SUB 0 1 1 1 0 $b_3$ $b_2$ $b_1$ $b_0$ yes $A \leftarrow A + M[b] + 1$ JMP 0 1 1 1 1 $b_3$ $b_2$ $b_1$ $b_0$ no PC $\leftarrow$ $b$ JMZ 1 0 0 0 $b_3$ $b_2$ $b_1$ $b_0$ no PC $\leftarrow$ $b$ AND 1 0 0 1 $b_3$ $b_2$ $b_1$ $b_0$ no $A \leftarrow A \land M[b]$ IOR 1 0 1 0 $b_3$ $b_2$ $b_1$ $b_0$ no $A \leftarrow A \land M[b]$ XOR 1 0 1 1 $b_3$ $b_2$ $b_1$ $b_0$ no $A \leftarrow A \lor M[b]$ ADL 1 1 0 0 $b_3$ $b_2$ $b_1$ $b_0$ no $A \leftarrow A \lor M[b]$ ADL 1 1 0 0 $b_3$ $b_2$ $b_1$ $b_0$ yes $A \leftarrow A + b$ (sign extended) ADC 1 1 0 1 $b_3$ $b_2$ $b_1$ $b_0$ yes $A \leftarrow A + M[b] + C$ SBB 1 1 1 0 $b_3$ $b_2$ $b_1$ $b_0$ yes $A \leftarrow A + M[b] + C$ NEG 1 1 1 1 0 0 0 0 1 no $A \leftarrow \overline{A}$ CLR 1 1 1 1 0 0 0 1 no $A \leftarrow \overline{A}$ CLR 1 1 1 1 0 0 1 1 0 no $A \leftarrow \overline{A}$ SET 1 1 1 1 0 0 1 1 0 no $A \leftarrow \overline{A}$ RTL 1 1 1 0 0 1 0 1 no $A \leftarrow \overline{A}$ RTL 1 1 1 0 0 1 0 0 no $A \leftarrow \overline{A}$ RTR 1 1 1 1 0 1 0 1 no $A \leftarrow \overline{A}$ LSR 1 1 1 1 0 1 1 1 1 0 no $A \leftarrow \overline{A}$ ACS 1 1 1 1 1 0 0 1 1 1 no $A \leftarrow \overline{A}$ SET 1 1 1 1 1 0 1 1 0 1 0 0 no $A \leftarrow \overline{A}$ SET 1 1 1 1 1 0 1 1 1 1 0 0 0 $A \leftarrow \overline{A}$ SET 1 1 1 1 1 0 0 1 0 0 no $A \leftarrow \overline{A}$ RTR 1 1 1 1 1 0 1 1 0 1 no $A \leftarrow \overline{A}$ SET 1 1 1 1 1 0 0 1 0 $A \leftarrow \overline{A}$ SET 1 1 1 1 1 0 0 1 0 $A \leftarrow \overline{A}$ SET 1 1 1 1 1 0 0 1 0 0 no $A \leftarrow \overline{A}$ SET 1 1 1 1 1 0 0 1 0 $A \leftarrow \overline{A}$ SET 1 1 1 1 1 0 0 1 0 0 no $A \leftarrow \overline{A}$ SET 1 1 1 1 1 0 0 1 0 0 no $A \leftarrow \overline{A}$ SET 1 1 1 1 1 0 0 1 0 0 no $A \leftarrow \overline{A}$ SET 1 1 1 1 1 0 0 1 0 0 no $A \leftarrow \overline{A}$ SET 1 1 1 1 1 0 0 1 0 0 no $A \leftarrow \overline{A}$ SET 1 1 1 1 1 0 0 1 1 no $A \leftarrow \overline{A}$ SET 1 1 1 1 1 0 0 1 1 no $A \leftarrow \overline{A}$	STA	0	0	1	1	$b_3$	$b_2$	$b_1$	$b_0$	no	$M[b] \leftarrow A$
SUB 0 1 1 0 b <sub>3</sub> b <sub>2</sub> b <sub>1</sub> b <sub>0</sub> yes A ← A + $\overline{M[b]}$ + 1  JMP 0 1 1 1 1 b <sub>3</sub> b <sub>2</sub> b <sub>1</sub> b <sub>0</sub> no PC ← b  JMZ 1 0 0 0 b <sub>3</sub> b <sub>2</sub> b <sub>1</sub> b <sub>0</sub> no PC ← b  AND 1 0 0 1 b <sub>3</sub> b <sub>2</sub> b <sub>1</sub> b <sub>0</sub> no A ← A ∧ M[b]  IOR 1 0 1 0 b <sub>3</sub> b <sub>2</sub> b <sub>1</sub> b <sub>0</sub> no A ← A ↑ M[b]  XOR 1 0 1 1 b <sub>3</sub> b <sub>2</sub> b <sub>1</sub> b <sub>0</sub> no A ← A ↑ M[b]  ADL 1 1 0 0 b <sub>3</sub> b <sub>2</sub> b <sub>1</sub> b <sub>0</sub> yes A ← A + $\overline{M[b]}$ + C  SBB 1 1 1 0 0 b <sub>3</sub> b <sub>2</sub> b <sub>1</sub> b <sub>0</sub> yes A ← A + $\overline{M[b]}$ + C  NEG 1 1 1 1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	STI	0	1	0	0	$b_3$	$b_2$	$b_1$	$b_0$	no	$M[M[b]] \leftarrow A$
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	ADD	0	1	0	1	$b_3$	$b_2$	$b_1$	$b_0$	yes	$A \leftarrow A + M[b]$
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	SUB	0	1	1	0	$b_3$	$b_2$	$b_1$	$b_0$	yes	$A \leftarrow A + \overline{M[b]} + 1$
AND 1 0 0 1 $b_3$ $b_2$ $b_1$ $b_0$ no $A \leftarrow A \land M[b]$ TOR 1 0 1 0 $b_3$ $b_2$ $b_1$ $b_0$ no $A \leftarrow A \lor M[b]$ XOR 1 0 1 1 $b_3$ $b_2$ $b_1$ $b_0$ no $A \leftarrow A \uplus M[b]$ ADL 1 1 0 0 $b_3$ $b_2$ $b_1$ $b_0$ yes $A \leftarrow A + b$ (sign extended)  ADC 1 1 0 1 $b_3$ $b_2$ $b_1$ $b_0$ yes $A \leftarrow A + M[b] + C$ SBB 1 1 1 0 $b_3$ $b_2$ $b_1$ $b_0$ yes $A \leftarrow A + M[b] + C$ NEG 1 1 1 1 0 0 0 0 0 0 no $A \leftarrow A + A + A + A + A + A + A + A + A + $	JMP	0	1	1	1	$b_3$	$b_2$	$b_1$	$b_0$	no	$PC \leftarrow b$
TOR 1 0 1 0 $b_3$ $b_2$ $b_1$ $b_0$ no $A \leftarrow A \lor M[b]$ XOR 1 0 1 1 $b_3$ $b_2$ $b_1$ $b_0$ no $A \leftarrow A \oplus M[b]$ ADL 1 1 0 0 $b_3$ $b_2$ $b_1$ $b_0$ yes $A \leftarrow A + b$ (sign extended)  ADC 1 1 0 1 $b_3$ $b_2$ $b_1$ $b_0$ yes $A \leftarrow A + M[b] + C$ SBB 1 1 1 0 $b_3$ $b_2$ $b_1$ $b_0$ yes $A \leftarrow A + M[b] + C$ NEG 1 1 1 1 0 0 0 0 0 no $A \leftarrow \overline{A} + 1$ COM 1 1 1 1 0 0 0 1 no $A \leftarrow \overline{A}$ CLR 1 1 1 1 0 0 0 1 no $A \leftarrow \overline{A}$ SET 1 1 1 1 0 0 0 1 1 no $A \leftarrow \overline{A}$ RTL 1 1 1 0 0 1 0 no $A \leftarrow \overline{A}$ RTL 1 1 1 0 0 1 0 $A \leftarrow \overline{A}$ RTR 1 1 1 1 0 0 1 0 $A \leftarrow \overline{A}$ LSL 1 1 1 1 0 0 1 1 0 no $A \leftarrow \overline{A}$ LSR 1 1 1 1 0 1 1 0 $A \leftarrow \overline{A}$ ACR 1 1 1 1 1 0 0 1 1 0 $A \leftarrow \overline{A}$ CLR 1 1 1 1 1 0 0 1 $A \leftarrow \overline{A}$ CLA 1 1 1 1 1 0 0 1 $A \leftarrow \overline{A}$ CLA 1 1 1 1 1 0 0 1 $A \leftarrow \overline{A}$ CLA 1 1 1 1 1 0 0 1 $A \leftarrow \overline{A}$ CLA 1 1 1 1 1 $A \leftarrow \overline{A}$ CLA 1 1 1 1 1 $A \leftarrow \overline{A}$ CLA 1 1 1 1 1 $A \leftarrow \overline{A}$ CLA 1 1 1 1 1 $A \leftarrow \overline{A}$ CLA 1 1 1 1 1 $A \leftarrow \overline{A}$ CLA 1 1 1 1 1 $A \leftarrow \overline{A}$ CLA 1 1 1 1 1 $A \leftarrow \overline{A}$ CLA 1 1 1 1 1 1 0 1 1 0 0 0 0 0 0 0 0 0 0	JMZ	1	0	0	0	$b_3$	$b_2$	$b_1$	$b_0$	no	$PC \leftarrow b \text{ if } A=0$
XOR 1 0 1 1 $b_3$ $b_2$ $b_1$ $b_0$ no $A \leftarrow A \oplus M[b]$ ADL 1 1 0 0 $b_3$ $b_2$ $b_1$ $b_0$ yes $A \leftarrow A + b$ (sign extended)  ADC 1 1 0 1 $b_3$ $b_2$ $b_1$ $b_0$ yes $A \leftarrow A + M[b] + C$ SBB 1 1 1 0 $b_3$ $b_2$ $b_1$ $b_0$ yes $A \leftarrow A + M[b] + C$ NEG 1 1 1 1 0 0 0 0 0 no $A \leftarrow \overline{A} + 1$ COM 1 1 1 1 0 0 0 1 no $A \leftarrow \overline{A}$ CLR 1 1 1 1 0 0 0 1 no $A \leftarrow \overline{A}$ SET 1 1 1 1 0 0 0 1 1 no $A \leftarrow \overline{A}$ RTL 1 1 1 0 0 1 0 no $A \leftarrow \overline{A} \leftarrow \overline{A}$ RTL 1 1 1 1 0 1 0 1 no $A \leftarrow \overline{A} \leftarrow A$	AND	1	0	0	1	$b_3$	$b_2$	$b_1$	$b_0$	no	$A \leftarrow A \land M[b]$
ADL 1 1 0 0 $b_3$ $b_2$ $b_1$ $b_0$ yes $A \leftarrow A + b$ (sign extended) ADC 1 1 0 1 $b_3$ $b_2$ $b_1$ $b_0$ yes $A \leftarrow A + M[b] + C$ SBB 1 1 1 1 0 $b_3$ $b_2$ $b_1$ $b_0$ yes $A \leftarrow A + M[b] + C$ NEG 1 1 1 1 0 0 0 0 0 no $A \leftarrow \overline{A} + 1$ COM 1 1 1 1 1 0 0 0 1 no $A \leftarrow \overline{A}$ CLR 1 1 1 1 0 0 0 1 no $A \leftarrow \overline{A}$ SET 1 1 1 1 0 0 0 1 no $A \leftarrow 0$ SET 1 1 1 1 0 0 0 1 no $A \leftarrow 0$ SET 1 1 1 1 0 0 1 0 $A \leftarrow 0$ SET 1 1 1 1 0 0 1 0 $A \leftarrow 0$ SET 1 1 1 1 1 0 0 $A \leftarrow 0$ SET 1 1 1 1 1 0 0 $A \leftarrow 0$ SET 1 1 1 1 1 0 0 $A \leftarrow 0$ SET 1 1 1 1 1 0 0 0 $A \leftarrow 0$ SET 1 1 1 1 1 0 0 0 0 $A \leftarrow 0$ SET 1 1 1 1 1 0 0 0 0 $A \leftarrow 0$ SET 1 1 1 1 1 0 0 0 0 $A \leftarrow 0$ SET 1 1 1 1 1 0 0 0 0 $A \leftarrow 0$ SET 1 1 1 1 1 0 0 0 0 0 $A \leftarrow 0$ SET 1 1 1 1 1 0 0 0 0 0 NO $A \leftarrow 0$ SET 1 1 1 1 1 0 0 1 0 0 0 NO $A \leftarrow 0$ SET 1 1 1 1 1 1 0 0 1 0 0 NO $A \leftarrow 0$ SET 1 1 1 1 1 1 0 0 1 1 NO $A \leftarrow 0$ SET 1 1 1 1 1 1 0 0 1 1 NO $A \leftarrow 0$ SET 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	IOR	1	0	1	0	$b_3$	$b_2$	$b_1$	$b_0$	no	$A \leftarrow A \vee M[b]$
ADC 1 1 0 1 $b_3$ $b_2$ $b_1$ $b_0$ yes $A \leftarrow A + M[b] + C$ SBB 1 1 1 1 0 $b_3$ $b_2$ $b_1$ $b_0$ yes $A \leftarrow A + M[b] + C$ NEG 1 1 1 1 0 0 0 0 0 no $A \leftarrow \overline{A} + 1$ COM 1 1 1 1 0 0 0 1 0 no $A \leftarrow \overline{A}$ CLR 1 1 1 1 0 0 0 1 1 no $A \leftarrow \overline{A}$ SET 1 1 1 1 0 0 0 1 1 no $A \leftarrow 0$ SET 1 1 1 1 0 0 1 0 no $A \leftarrow 0$ SET 1 1 1 1 1 0 0 1 0 no $A \leftarrow ((A < 1) \lor (A > 7)) \land 0xFF$ RTL 1 1 1 1 0 1 0 1 no $A \leftarrow ((A < 7) \lor (A > 1)) \land 0xFF$ RTR 1 1 1 1 0 1 1 0 no $A \leftarrow ((A < 1) \land 0xFF)$ LSL 1 1 1 1 0 1 1 0 no $A \leftarrow ((A < 1) \land 0xFF)$ LSR 1 1 1 1 1 0 1 1 no $A \leftarrow ((A < 1) \land 0xFF)$ ASR 1 1 1 1 1 0 0 1 1 no $A \leftarrow ((A > 1) \land 0xFF)$ ASR 1 1 1 1 1 0 0 0 no $A \leftarrow ((A > 1) \land 0xFF) \lor (A \land 0x80)$ TST 1 1 1 1 1 0 0 1 no $A \leftarrow ((A > 1) \land 0xFF) \lor ((A \land 0x80))$ TST 1 1 1 1 1 1 0 0 1 no $A \leftarrow ((A > 1) \land 0xFF) \lor ((A \land 0x80))$ TST 1 1 1 1 1 1 0 0 1 no $A \leftarrow ((A > 1) \land 0xFF) \lor ((A \land 0x80))$ TST 1 1 1 1 1 1 0 0 1 no $A \leftarrow ((A > 1) \land 0xFF) \lor ((A \land 0x80))$ TCA 1 1 1 1 1 1 0 0 1 no $A \leftarrow ((A \land 0xFF)) \lor ((A \land 0xFF))$ TCA 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	XOR	1	0	1	1	$b_3$	$b_2$	$b_1$	$b_0$	no	$A \leftarrow A \oplus M[b]$
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	ADL	1	1	0	0	$b_3$	$b_2$	$b_1$	$b_0$	yes	$A \leftarrow A + b$ (sign extended)
NEG 1 1 1 1 1 0 0 0 0 0 no $A \leftarrow \overline{A} + 1$ COM 1 1 1 1 1 0 0 0 1 no $A \leftarrow \overline{A}$ CLR 1 1 1 1 0 0 1 1 no $A \leftarrow 0$ SET 1 1 1 1 1 0 0 1 1 no $A \leftarrow 0$ SET 1 1 1 1 1 0 0 0 1 1 no $A \leftarrow 0$ FRIL 1 1 1 1 0 1 0 1 no $A \leftarrow 0$ RTR 1 1 1 1 0 1 0 1 no $A \leftarrow ((A < 1) \lor (A > 7)) \land 0xFF$ RTR 1 1 1 1 0 1 0 1 no $A \leftarrow ((A < 7) \lor (A > 1)) \land 0xFF$ LSL 1 1 1 1 0 1 1 0 no $A \leftarrow (A < 1) \land 0xFE$ LSR 1 1 1 1 0 1 1 1 no $A \leftarrow (A > 1) \land 0xFE$ LSR 1 1 1 1 1 0 0 0 no $A \leftarrow (A > 1) \land 0xFE$ ASR 1 1 1 1 1 0 0 0 no $A \leftarrow ((A > 1) \land 0xFF) \lor (A \land 0x80)$ TST 1 1 1 1 1 0 0 0 1 no $A \leftarrow 0x01$ if $A \neq 0$ CLC 1 1 1 1 1 0 1 0 yes $C \leftarrow 0$ SEC 1 1 1 1 1 1 0 1 0 $A \leftarrow 0x$ TCA 1 1 1 1 1 1 1 0 0 0 no $A \leftarrow C$ TVA 1 1 1 1 1 1 1 0 1 no $A \leftarrow C$ TVA 1 1 1 1 1 1 1 0 1 no $A \leftarrow C$	ADC	1	1	0	1	$b_3$	$b_2$	$b_1$	$b_0$	yes	$A \leftarrow A + M[b] + C$
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	SBB	1	1	1	0	$b_3$	$b_2$	$b_1$	$b_0$	yes	$A \leftarrow A + \overline{M[b]} + C$
CLR	NEG	1	1	1	1	0	0	0	0	no	$A \leftarrow \overline{A} + 1$
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	COM	1	1	1	1	0	0	0	1	no	$A \leftarrow \overline{A}$
RTL	CLR	1	1	1	1	0	0	1	0	no	$A \leftarrow 0$
RTR	SET	1	1	1	1	0	0	1	1	no	$A \leftarrow 0xFF$
LSL	RTL	1	1	1	1	0	1	0	0	no	$A \leftarrow ((A << 1) \lor (A >> 7)) \land 0xFF$
LSR 1 1 1 1 0 1 1 1 no $A \leftarrow (A >> 1) \land 0x7F$ ASR 1 1 1 1 1 0 0 0 no $A \leftarrow ((A >> 1) \land 0x7F) \lor (A \land 0x80)$ ) TST 1 1 1 1 1 0 0 1 no $A \leftarrow 0x01$ if $A \neq 0$ CLC 1 1 1 1 1 0 1 0 yes $C \leftarrow 0$ SEC 1 1 1 1 1 1 0 1 yes $C \leftarrow 1$ TCA 1 1 1 1 1 1 0 0 no $C \leftarrow 1$ TVA 1 1 1 1 1 1 0 1 no $C \leftarrow 1$ TVA 1 1 1 1 1 1 1 0 1 no $C \leftarrow 1$ TVA 1 1 1 1 1 1 1 1 0 no $C \leftarrow 1$	RTR	1	1	1	1	0	1	0	1	no	$A \leftarrow ((A << 7) \lor (A >> 1)) \land 0xFF$
ASR 1 1 1 1 1 0 0 0 no $A \leftarrow ((A >> 1) \land 0x7F) \lor (A \land 0x80))$ TST 1 1 1 1 1 0 0 1 no $A \leftarrow 0x01$ if $A \neq 0$ CLC 1 1 1 1 1 0 1 0 yes $C \leftarrow 0$ SEC 1 1 1 1 1 1 0 1 yes $C \leftarrow 1$ TCA 1 1 1 1 1 1 0 0 no $A \leftarrow C$ TVA 1 1 1 1 1 1 0 1 no $A \leftarrow C \oplus N$ JAL 1 1 1 1 1 1 1 0 no $A \leftarrow C \oplus N$	LSL	1	1	1	1	0	1	1	0	no	$A \leftarrow (A << 1) \land 0xFE$
TST 1 1 1 1 1 0 0 1 no $A \leftarrow 0x01 \text{ if } A \neq 0$ CLC 1 1 1 1 1 0 1 0 yes $C \leftarrow 0$ SEC 1 1 1 1 1 1 0 1 yes $C \leftarrow 1$ TCA 1 1 1 1 1 1 0 0 no $A \leftarrow C$ TVA 1 1 1 1 1 1 0 1 no $A \leftarrow C \oplus N$ JAL 1 1 1 1 1 1 0 no $A \leftarrow C \oplus N$	LSR	1	1	1	1	0	1	1	1	no	$A \leftarrow (A >> 1) \land 0x7F$
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	ASR	1	1	1	1	1	0	0	0	no	$A \leftarrow ((A \gt\gt 1) \land 0x7F) \lor (A \land 0x80))$
SEC 1 1 1 1 1 0 1 1 yes $C \leftarrow 1$ TCA 1 1 1 1 1 0 0 no $A \leftarrow C$ TVA 1 1 1 1 1 0 1 no $A \leftarrow C \oplus N$ JAL 1 1 1 1 1 1 0 no $PC \leftarrow A$ and $A \leftarrow PC + 1$	TST	1	1	1	1	1	0	0	1	no	$A \leftarrow 0x01 \text{ if } A \neq 0$
TCA	CLC	1	1	1	1	1	0	1	0	yes	$C \leftarrow 0$
TVA	SEC	1	1	1	1	1	0	1	1	yes	C ← 1
JAL 1 1 1 1 1 1 0 no $PC \leftarrow A$ and $A \leftarrow PC + 1$	TCA	1	1	1	1	1	1	0	0	no	$A \leftarrow C$
	TVA	1	1	1	1	1	1	0	1	no	$A \leftarrow C \oplus N$
NOP	JAL	1	1	1	1	1	1	1	0	no	$PC \leftarrow A \text{ and } A \leftarrow PC + 1$
	NOP	1	1	1	1	1	1	1	1	no	

## Requirements

- 1. Your code must compile with the command make and produce the executable called asm. Among other implications, this means that your source code must include a makefile. (Note that make must return 0, so do not include a compiled version of your code.)
- 2. Your code may not generate errors or warnings when compiled.
- 3. Your code must be able to accept the SCRAM assembler file as either a file argument or directly on stdin (if no argument is supplied).
- 4. Your code must be able to accept a second argument which is the name of a file to output the SCRAM code to. This only applies to the case where the first argument is the SCRAM assembler file. Otherwise, the SCRAM code should be output to stdout.
- 5. Your code may not crash. If you encounter an error, your code must exit cleanly with a non-zero status and an explanatory message. If the SCRAM code is simulated successfully, your code must exit with a zero status.
- 6. If you want partial credit or CA assistance, your code must be readable. **CA are allowed to refuse to review messy and/or unreadable code!**

### **Input format**

The input consists of lines with the following format:

```
label: opcode argument ; comment
```

The label is any string starting with a letter (a-z, A-Z) and composed of letters, numbers, and underscores. The label is optional, but if present, must be followed by a colon.

The opcode is any of the ones listed on the previous page, or an assembler directive. It is optional.

The argument is only present on those opcodes that need it. It can be either a number (decimal, octal or hexadecimal – see strtol()) or a label.

The comment is also optional, but if present, must be preceded by a semicolon.

Note that empty lines, as well as lines with only a label or only a comment, or other such combinations are allowed. Also, there can be any number of spaces or tab characters between the label, opcode, argument and comment.

#### **Assembler directives**

In addition to the opcodes listed on the first page, the assembler must recognize two directives.

**ORG:** This directive requires an argument which is the address of where the subsequent opcodes should be assembled to.

**DAT:** This directive requires an argument which is the data to be placed at the current address.

## **Output format**

The output is a binary file, which does not have a particular format. You can verify that the output is correct by running your disassembler on it and comparing the result to the original SCRAM assembler file supplied to the assembler.

If the input program is too long (requiring more than 256 bytes of SCRAM memory), exit with a non-zero status (see exit status below). If the input program is too short, pad the output to 256 bytes with HLT instructions.

#### **Exit status**

Your code should exit with a zero status unless one of the following errors occurs:

- **return 1:** The wrong number of arguments were given.
- **return 2:** The input file cannot be opened or cannot be read.
- **return 3:** The output file cannot be opened or cannot be written.
- **return 4:** The program is too large.
- return 5: An invalid op-code was encountered (this includes EXT 0).
- **return 6:** An invalid argument was encountered (for instance it's too large or it's not an integer).
- **return 7:** An invalid label was encountered (bad label format or duplicate).
- **return 8:** There was an unresolved reference (missing label).
- return 9: Memory allocation error.

## **Examples**

The supplied example input files (and corresponding output files) are:

- ex1\_in: A file containing the solution to problem 1, homework 5 (shift left 4 bits) in assembler format.
- ex1\_out: A file containing the assembled code.