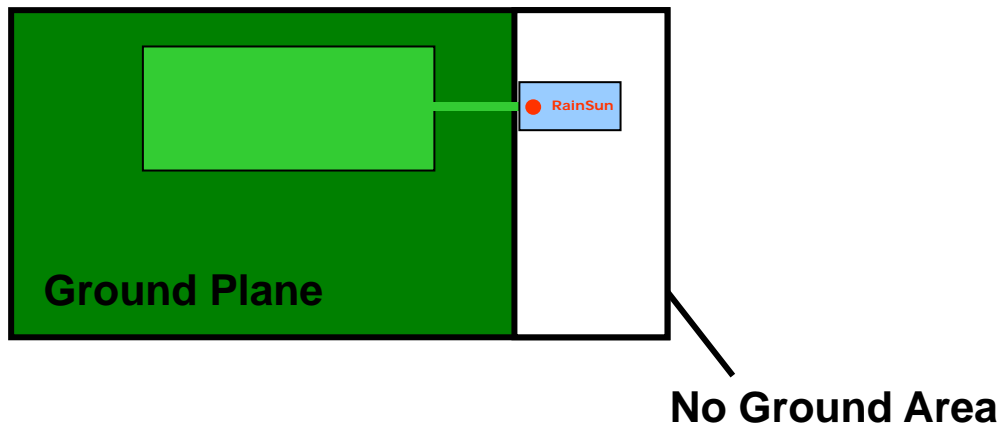
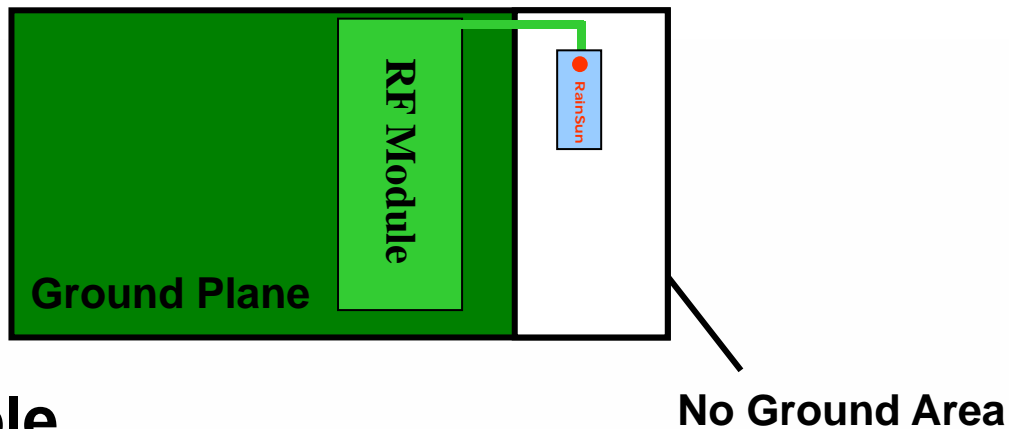


How to Do Chip Antenna Layout

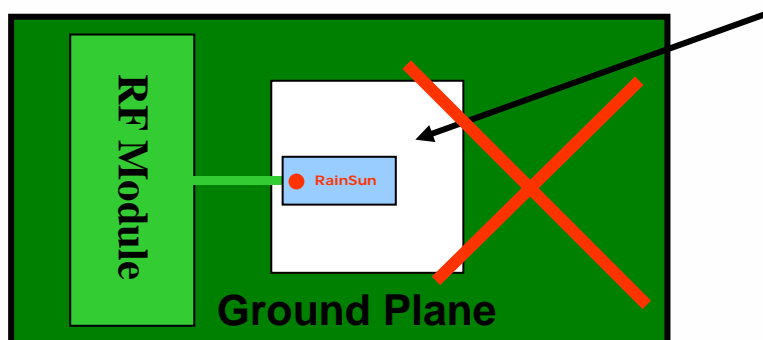
Please consider the position of antenna and PCB ground layer
Recommended Antenna Position – PCB Corner



Suitable

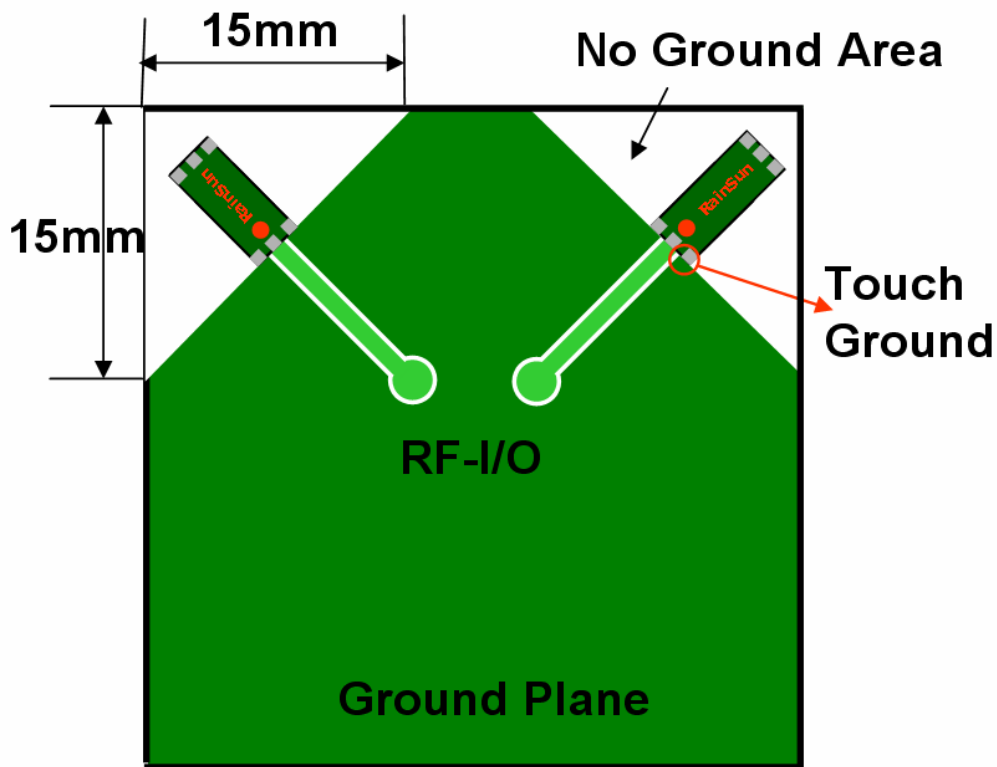
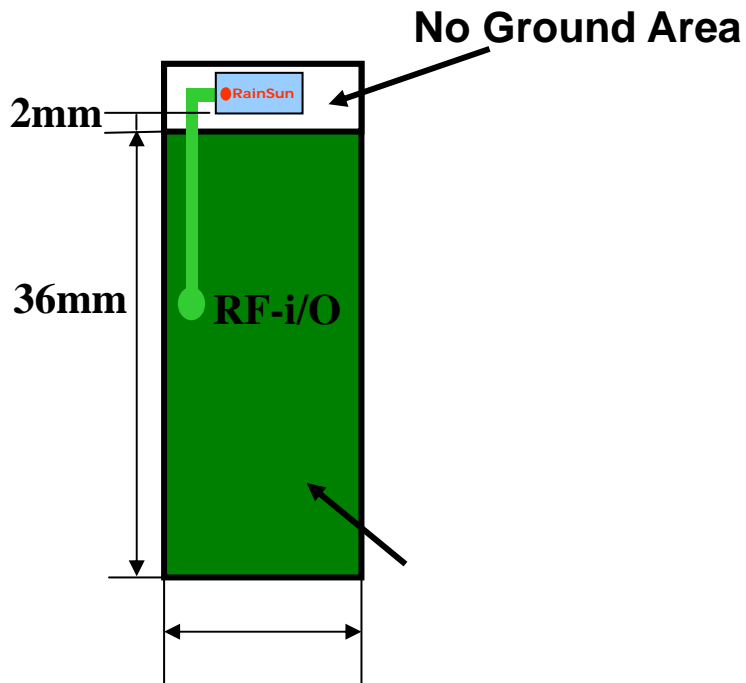


Suitable



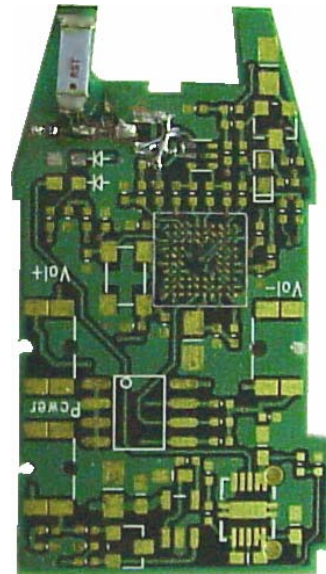
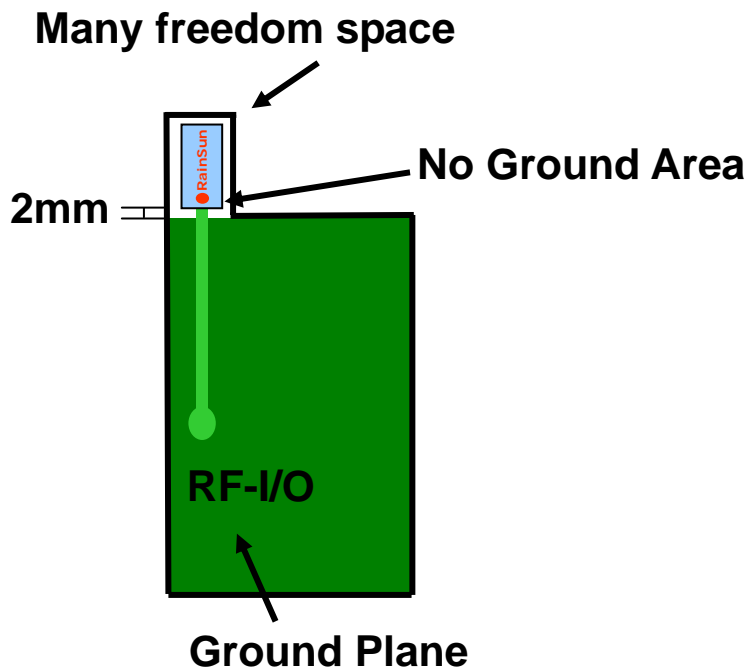
Unsuitable for radiation reason

Antenna Series Layout Reference

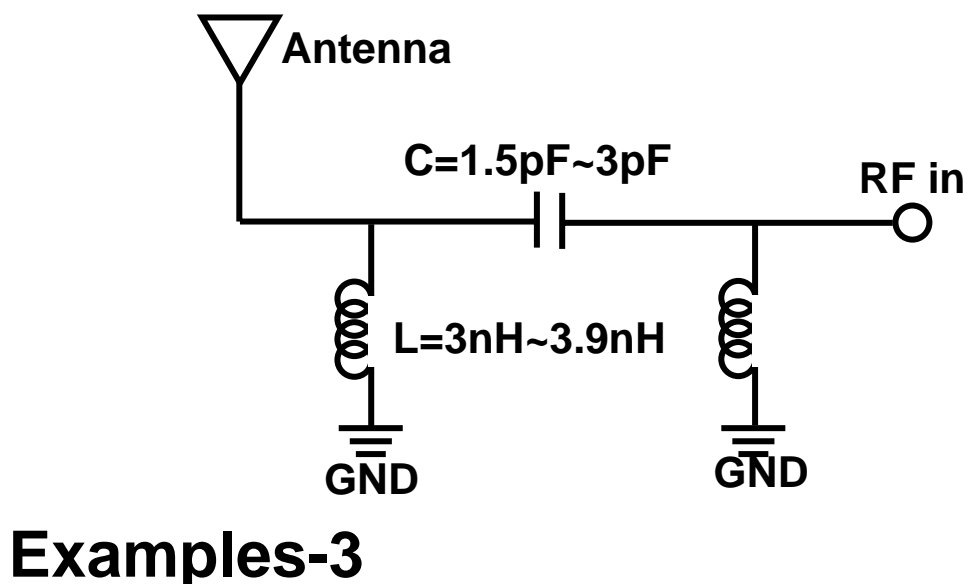
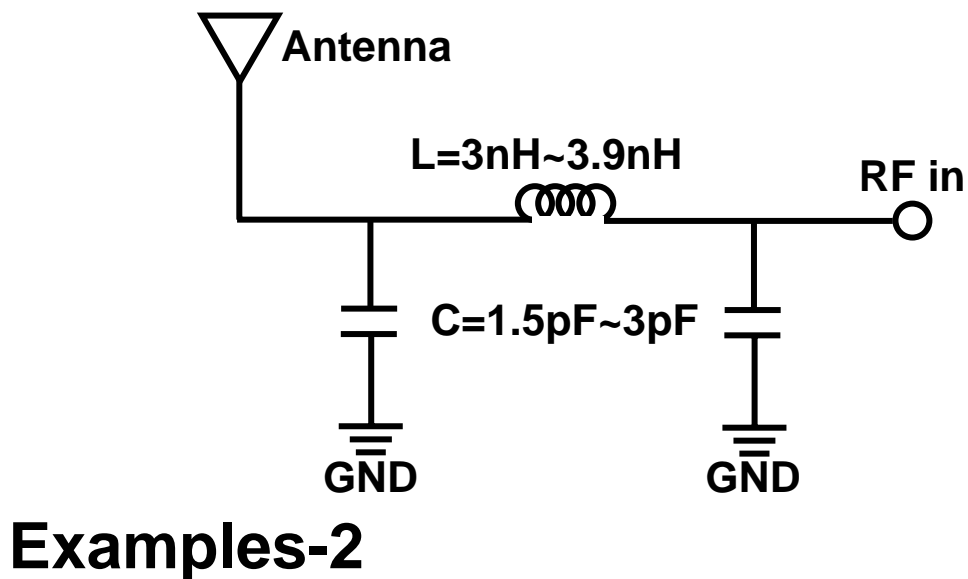
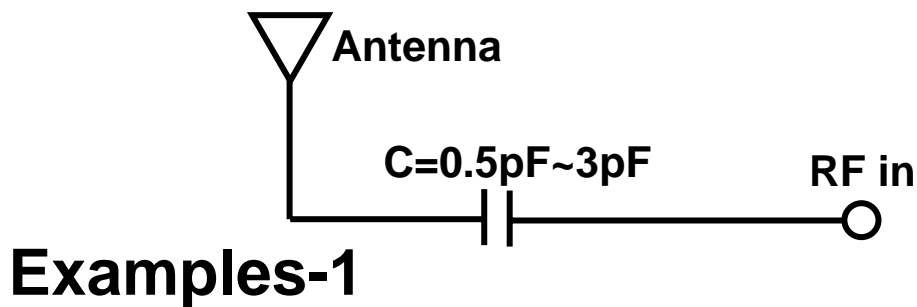


This antenna requires a ground plane

Antenna Series Layout Reference

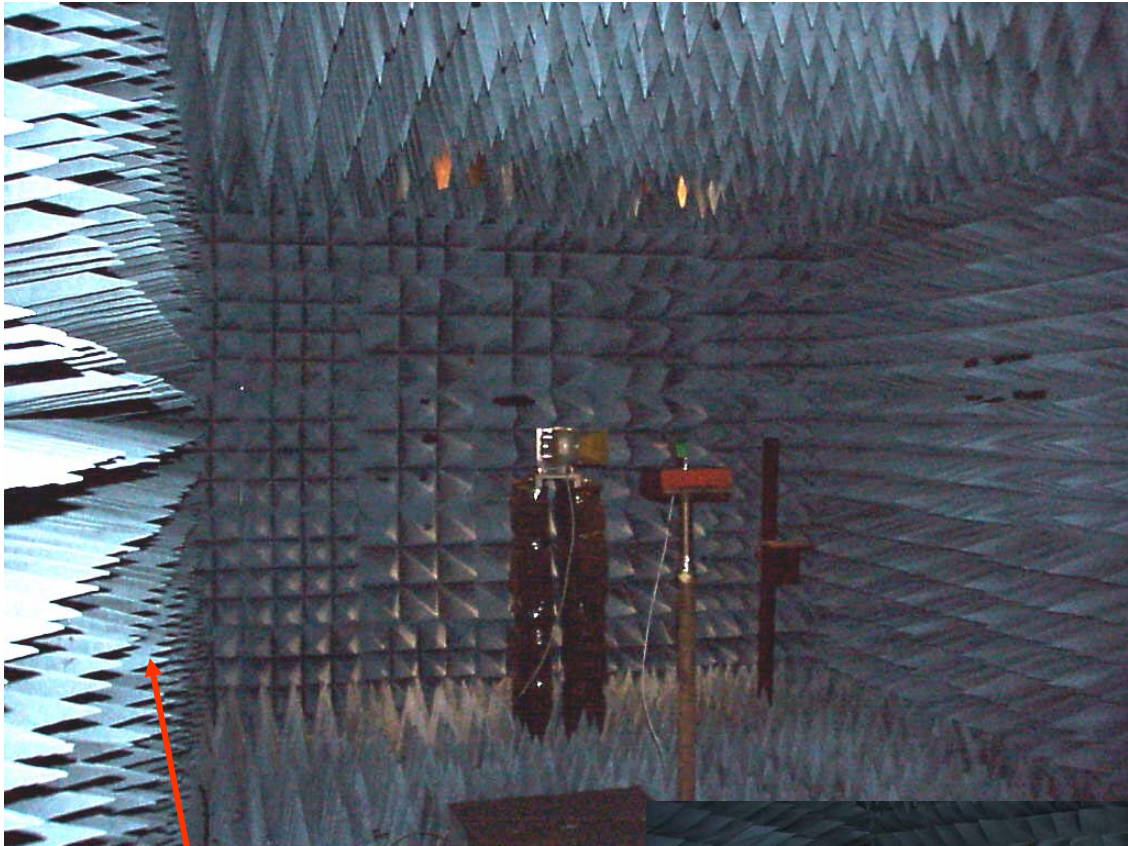


Antenna Matching Network Example

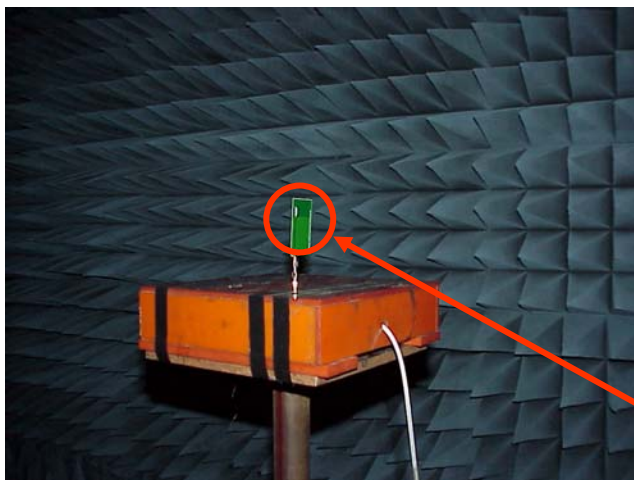


Anechoic Chamber

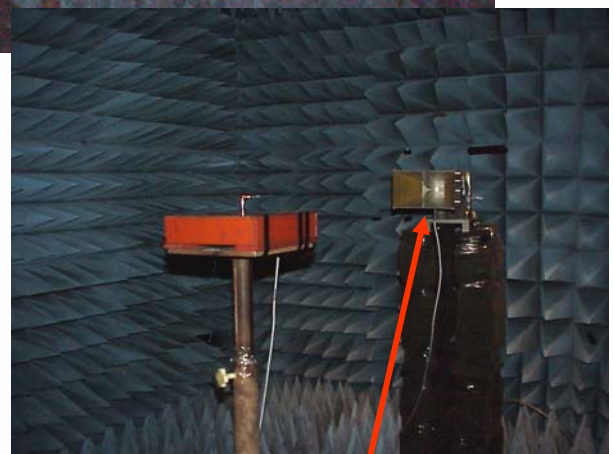
Radiation Pattern Measured Environment



Absorber



Antenna under

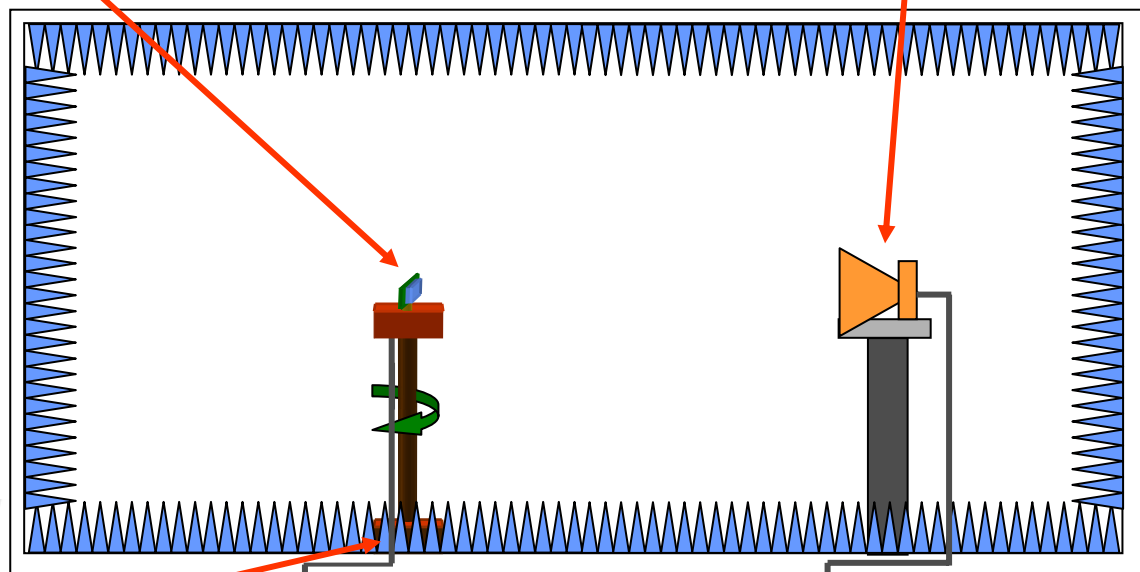


Horn standard Gain antenna

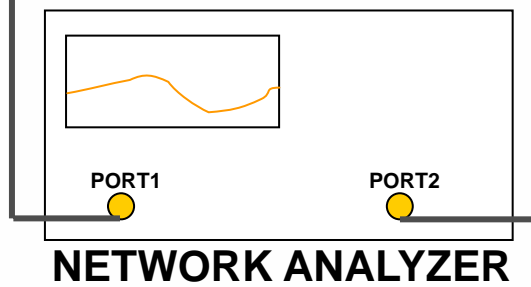
Radiation Pattern Measured

antenna under test

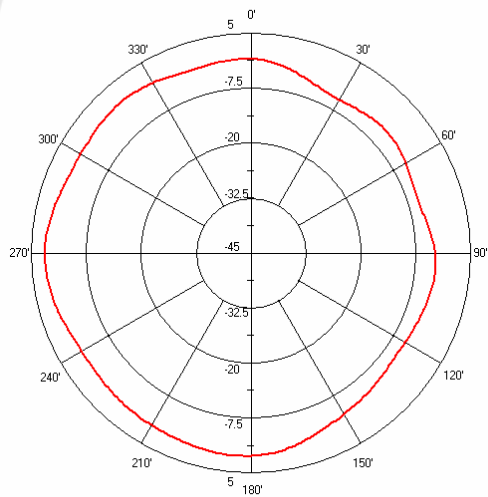
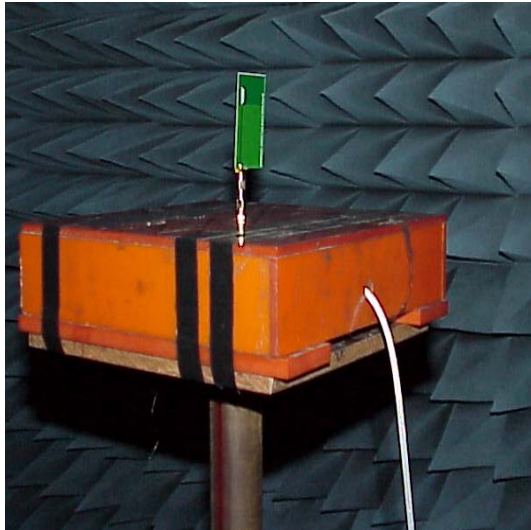
Horn standard Gain antenna



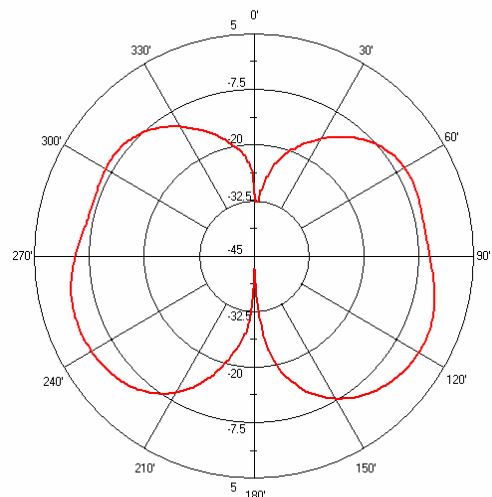
Turn table



Radiation Pattern Measured



H-Plane



E-Plane

Measurement



antenna under test

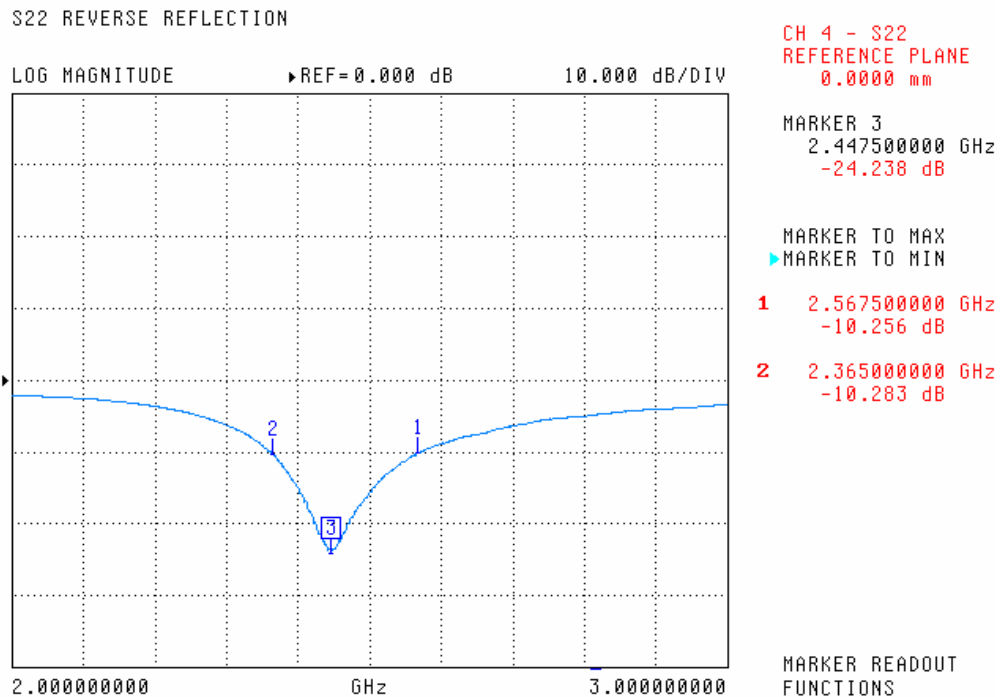


Testing Instrument: Anritsu 37369C VNA(Vector Network Analyzer)

VNA calibrate with 1 path reflection only calibration sequence on test board feed point. The test board layout as recommend dimension.

Measured Antenna patterns

Return loss



Smith Chart

