

## Address Processor

```
rbget s0, rb0    # get request
mfrb r1, s0, 1    # get address
mfrb r2, s0, 0    # get DSid (low-16bit)
andi r2, r2, 0xFFFF
bsrl r2, r2, 2    # offset: DSid*4
# translate base address stored in r5
lwx r3, r5, r2    # r3 <- base[DSid]
add r1, r1, r3    # addr += base[DSid]
mtrb s0, r1, 1    # write address
rbput rb0, s0     # send out request
```

## Data Processor

```
rbget s0, rb0    # get request
rbput rb0, s0     # send out request
```

