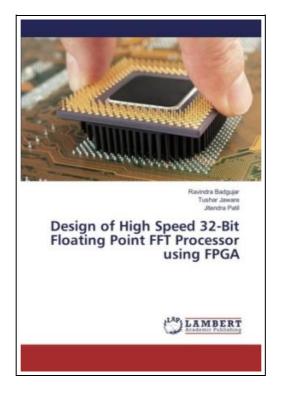
Design of High Speed 32-Bit Floating Point FFT Processor using FPGA



Filesize: 7.13 MB

Reviews

Unquestionably, this is the best operate by any author. It is among the most amazing pdf i actually have read. Its been designed in an remarkably basic way which is just right after i finished reading this pdf by which basically altered me, change the way i believe. (Harold Spencer)

DESIGN OF HIGH SPEED 32-BIT FLOATING POINT FFT PROCESSOR USING FPGA



To download **Design of High Speed 32-Bit Floating Point FFT Processor using FPGA** eBook, you should access the hyperlink listed below and save the ebook or have access to additional information that are highly relevant to DESIGN OF HIGH SPEED 32-BIT FLOATING POINT FFT PROCESSOR USING FPGA book.

LAP Lambert Academic Publishing Mai 2016, 2016. Taschenbuch. Condition: Neu. Neuware - The Discrete Fourier Transform (DFT) is used in a wide variety of Digital Signal Processing applications. The algorithm used to implement this transform requires intensive arithmetic computation as well as complex control and sequence functions. The designer of VLSI components is faced with problem of identifying requirements and architectures for FFT algorithm which directly support the DFT. Design goals of this book includes 32-bit floating point FFT calculation in IEEE 754 single Precision number format with very high speed. This book focuses on design, simulation, synthesis and implementation of 32-bit Floating point FFT algorithm in Radix 2 DIT form implemented on virtex II pro FPGA. FPGAs as programmable hardware devices are particularly suitable to encompass both high processing speeds and flexibility to meet the quickly changing DSP designs. Particularly, this book has revealed that the FFT algorithm for DSP-based systems can be efficiently implemented by using Virtex II Pro FPGA with a high speed. 64 pp. Englisch.



Read Design of High Speed 32-Bit Floating Point FFT Processor using FPGA Online



Download PDF Design of High Speed 32-Bit Floating Point FFT Processor using FPGA

Relevant Kindle Books



[PDF] Design and Development of Low Cost Adsorbents

Click the hyperlink under to download "Design and Development of Low Cost Adsorbents" PDF document.

Save eBook

>>



[PDF] Arsenic Removal Technologies from ground water

Click the hyperlink under to download "Arsenic Removal Technologies from ground water" PDF document.

Save eBook

.



$[PDF] \, Game \, Theory: A \, Very \, Short \, Introduction$

 ${\bf Click\ the\ hyperlink\ under\ to\ download\ "Game\ Theory: A\ Very\ Short\ Introduction"\ PDF\ document.}$

Save eBook

.



[PDF] Modeling of Evanescent wave Optical Fiber Biosensor

Click the hyperlink under to download "Modeling of Evanescent wave Optical Fiber Biosensor" PDF document.

Save eBook

*



[PDF] Intermediate Accounting (IFRS) Volume 1

 ${\it Click the hyperlink under to download "Intermediate Accounting (IFRS) Volume 1" {\it PDF document.} }$

Save eBook

...



[PDF] Business books (Book Guide): Peter Principle, Airlines of North America, The Dilbert principle, The Joy of Work, The 33 Strategies of War, The Law of Success, Identifying and Managing Project Risk, Blue

Click the hyperlink under to download "Business books (Book Guide): Peter Principle, Airlines of North America, The Dilbert principle, The Joy of Work, The 33 Strategies of War, The Law of Success, Identifying and Managing Project Risk, Blue" PDF document.

Save eBook

»