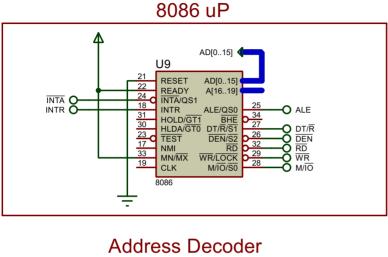
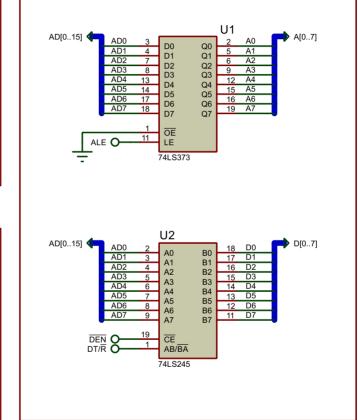
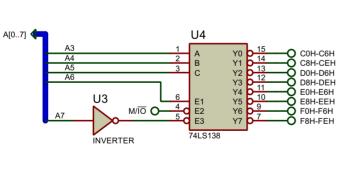
MEMORY SYSTEM



Address Latch and Data Buffer







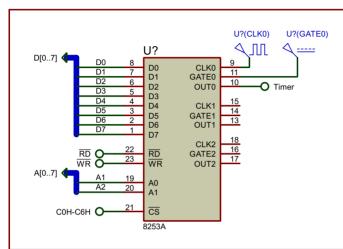
DESIGN TITLE: Building Control System REV: 1

> BY: Aguilar, Sean Karl Tyrese G. Software Alotaya, Isaac Jadon R. Documentation Caños. Bea Belle Therese B.

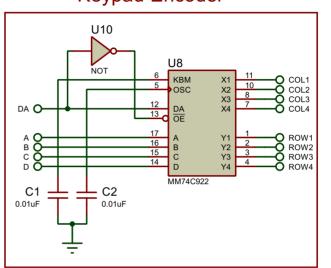
11/27/2024

PERIPHERAL DEVICES

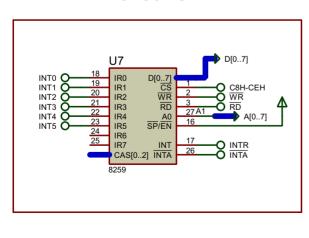
8253A Timer



Keypad Encoder



8259 IC



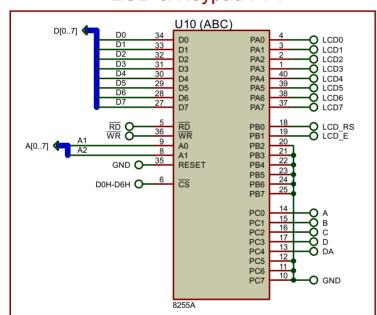
DESIGN TITLE: Building Control System

REV: 1

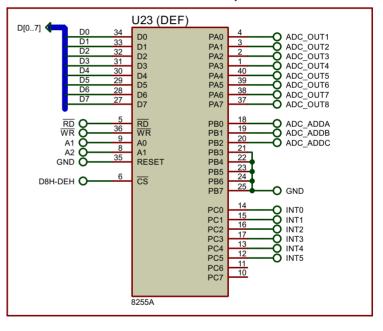
BY: Aguilar, Sean Karl Tyrese G. Alotaya, Isaac Jadon R. Caños, Bea Belle Therese B. Software Documentation Hardware

PERIPHERAL INTERFACES

LCD & Keypad PPI



ADC & Interrupt PPI



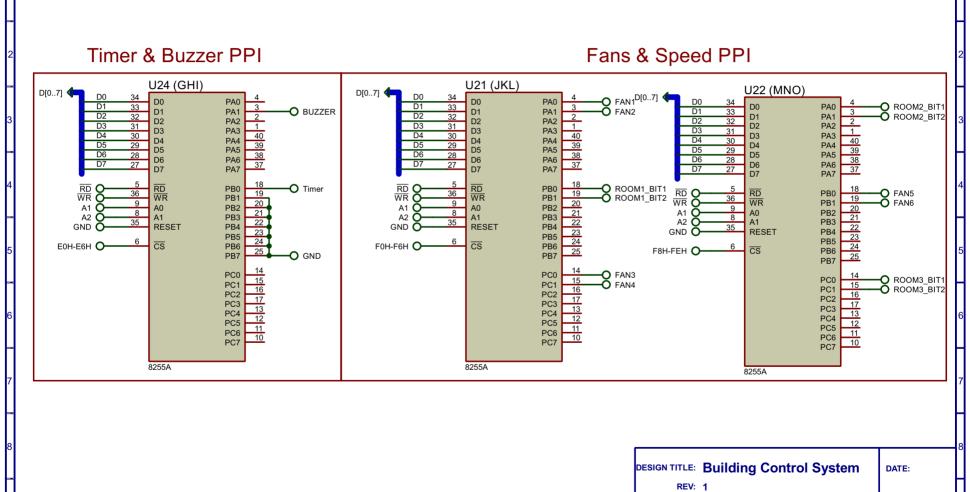
DESIGN TITLE: Building Control System

REV: 1

BY: Aguilar, Sean Karl Tyrese G. Software Alotaya, Isaac Jadon R. **Documentation** Caños. Bea Belle Therese B. Hardware

11/27/2024

PERIPHERAL INTERFACES



11/27/2024

Software

Hardware

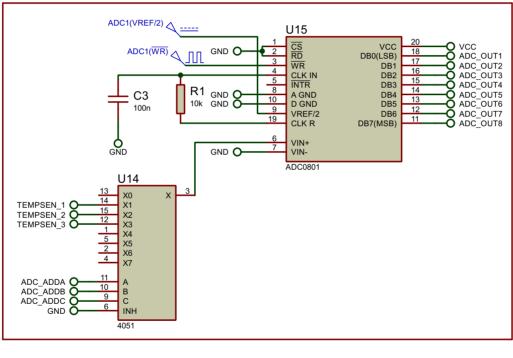
Documentation

BY: Aguilar, Sean Karl Tyrese G.

Alotaya, Isaac Jadon R.

Caños. Bea Belle Therese B.

ANALOG TO DIGITAL CONVERTER



DESIGN TITLE: Building Control System

11/27/2024 Software Documentation Hardware

DATE:

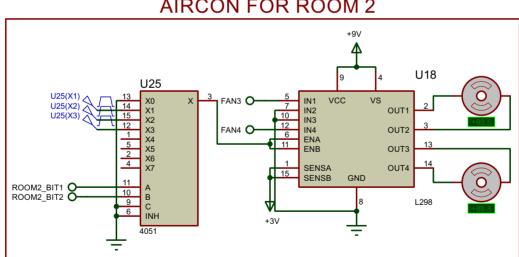
REV: 1

BY: Aguilar, Sean Karl Tyrese G.

Alotaya, Isaac Jadon R.

Caños. Bea Belle Therese B.

AIRCON FOR ROOM 1 U16 U17 U17(X1) U17(X2) FAN1 C VCC OUT1 X1 U17(X3) 15 IN3 X2 OUT2 FAN2 C X3 X4 ENA 5 2 4 OUT3 X5 X6 OUT4 **SENSA** SENSB GND ROOM1_BIT1 O 10 L298 +3V INH 4051 **AIRCON FOR ROOM 2**



DESIGN TITLE: Building Control System

BY: Aguilar, Sean Karl Tyrese G.

Alotaya, Isaac Jadon R.

Caños. Bea Belle Therese B.

DATE:

Software

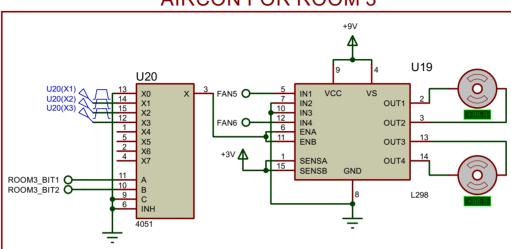
Documentation

Hardware

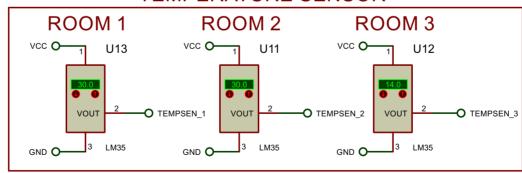
11/27/2024

USER INTERFACE

AIRCON FOR ROOM 3



TEMPERATURE SENSOR



USER INTERFACE

DESIGN TITLE: Building Control System

DEV.

REV:

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Alotaya, Isaac Jadon R. Documentation
Caños, Bea Belle Therese B. Hardware

11/27/2024

