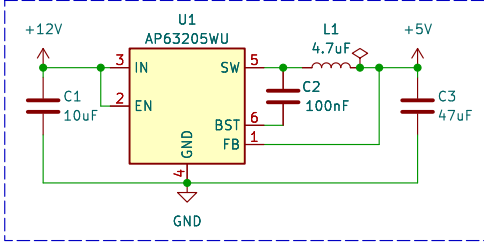
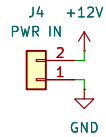
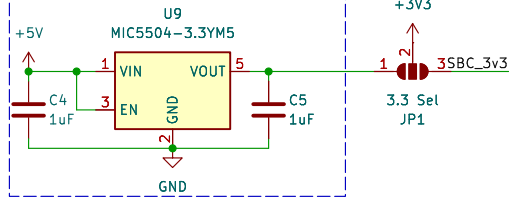


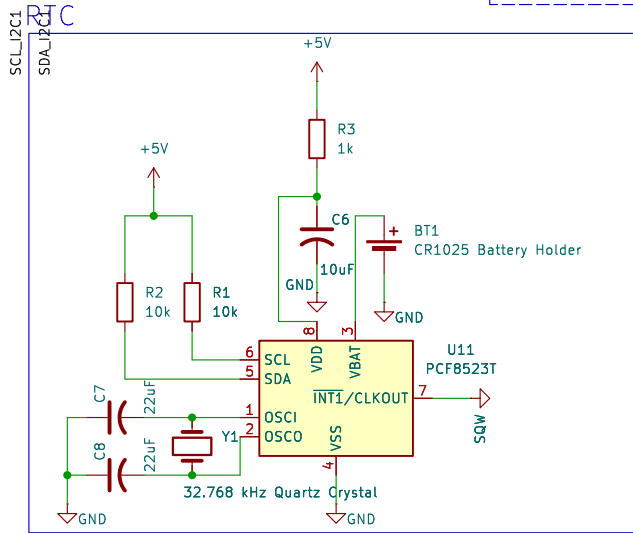
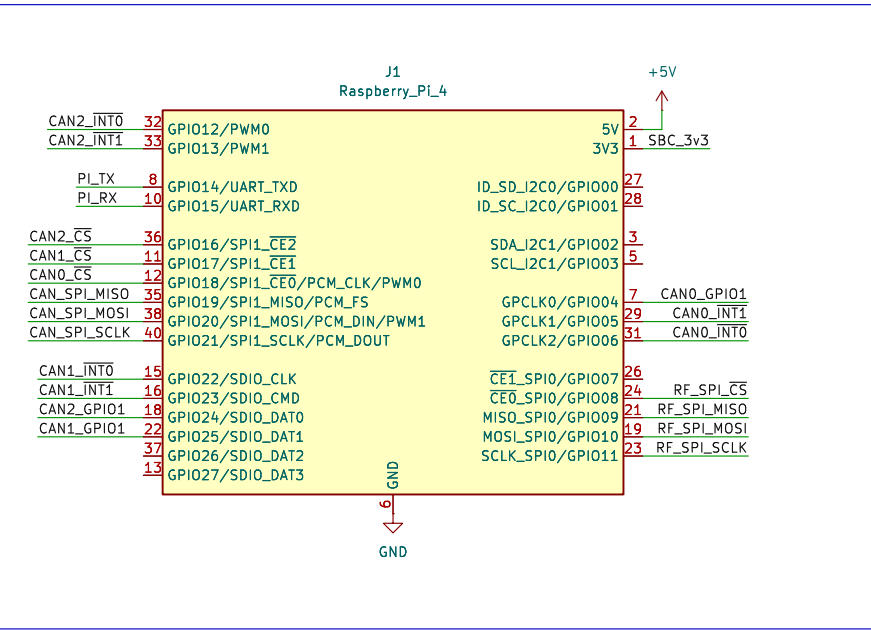
5v Regulator Design Block



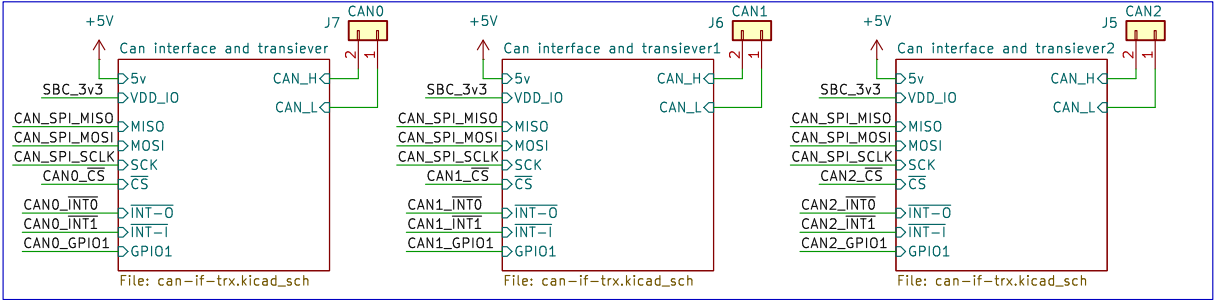
3.3 Reg Design Block (OPT)



Rpi (Z2, 4, 5)



CAN Interfaces

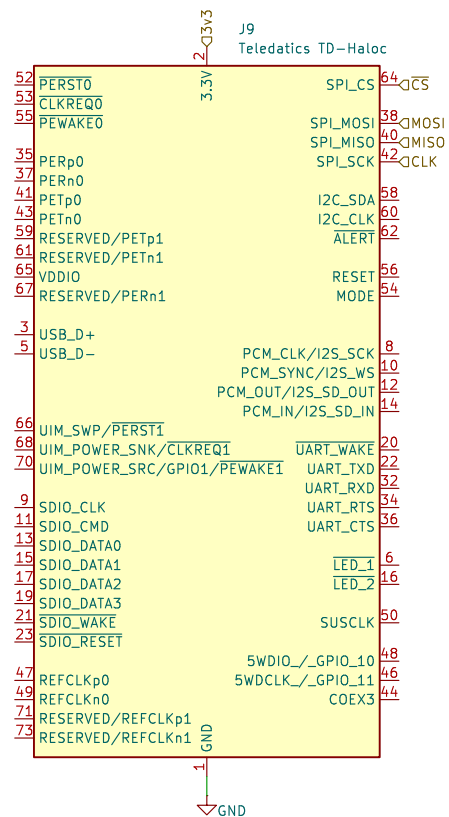


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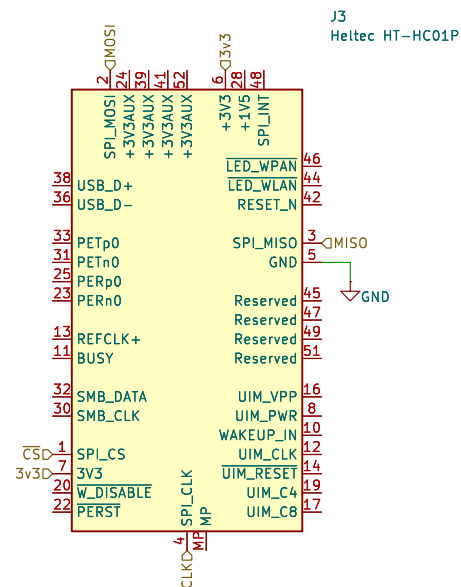
Rev:
Id: 4/7

Teledatics TD-HALOC



Heltec HT-HC01P

This is not the same pin-->footprint mapping as the datasheet!!



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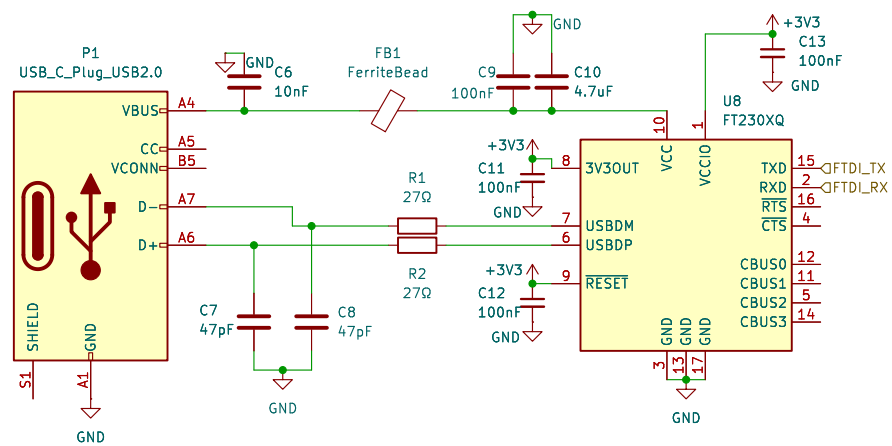
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Rev:

Id: 5/7



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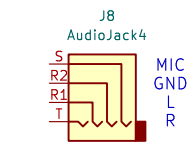
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Rev:

Id: 6/7



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		Id: 7/7