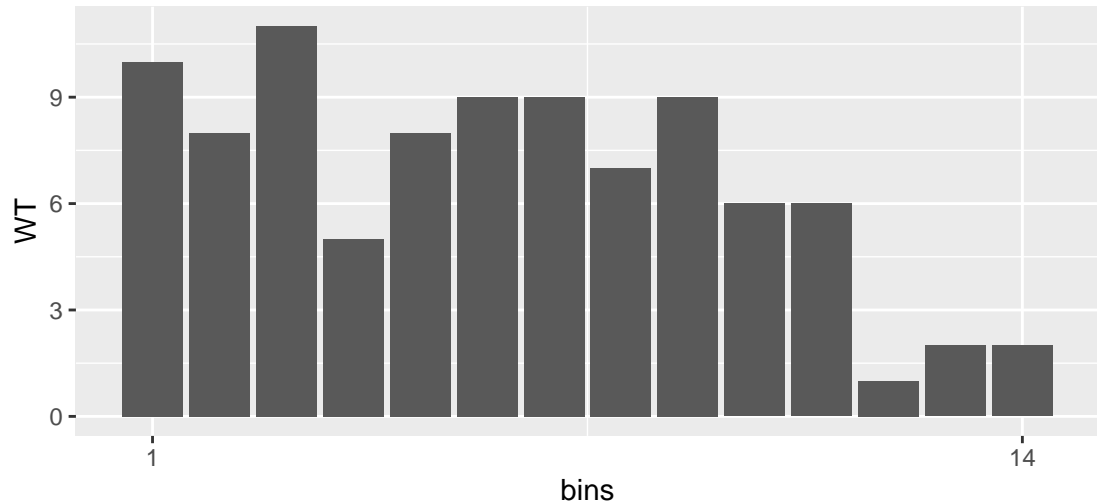
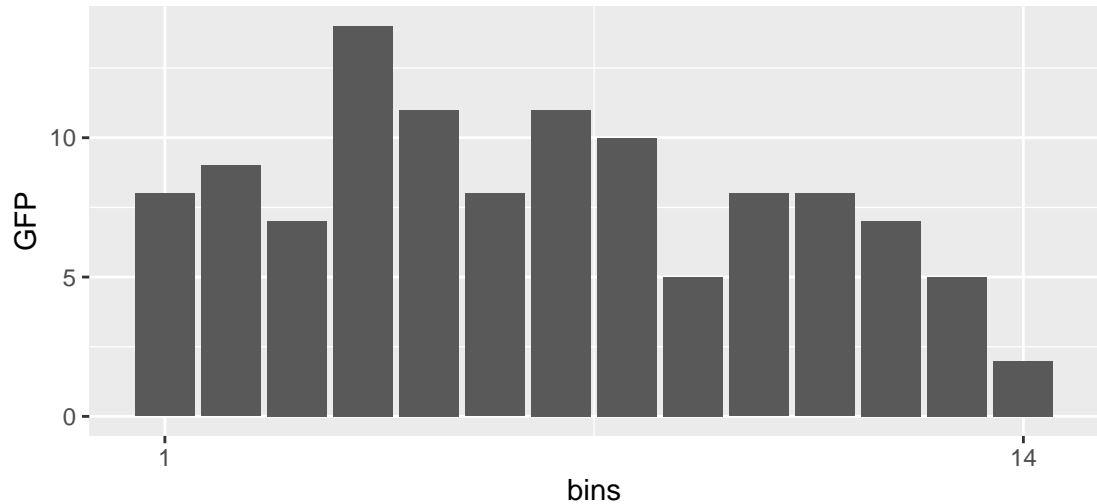


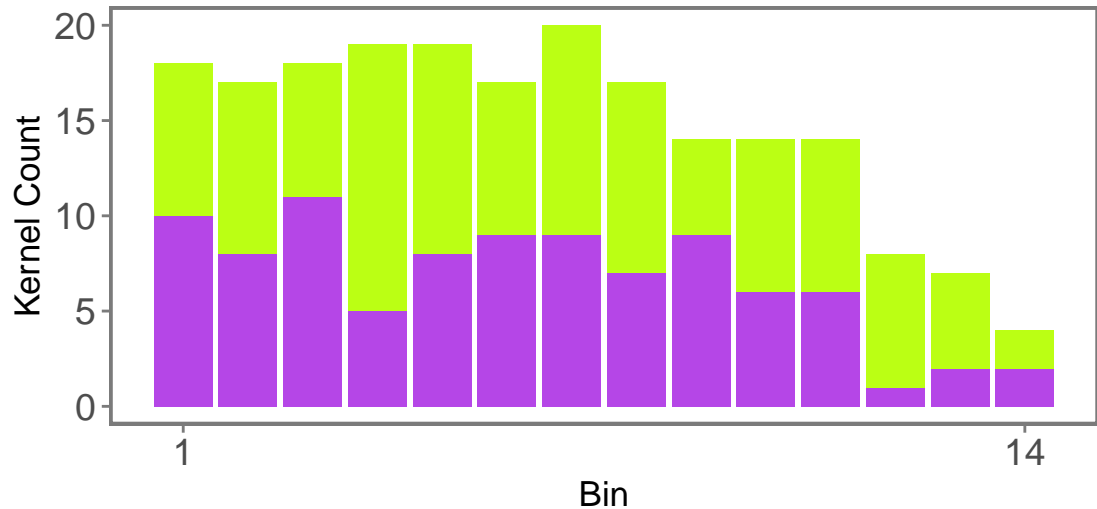
X450-1x402\_inference.xml



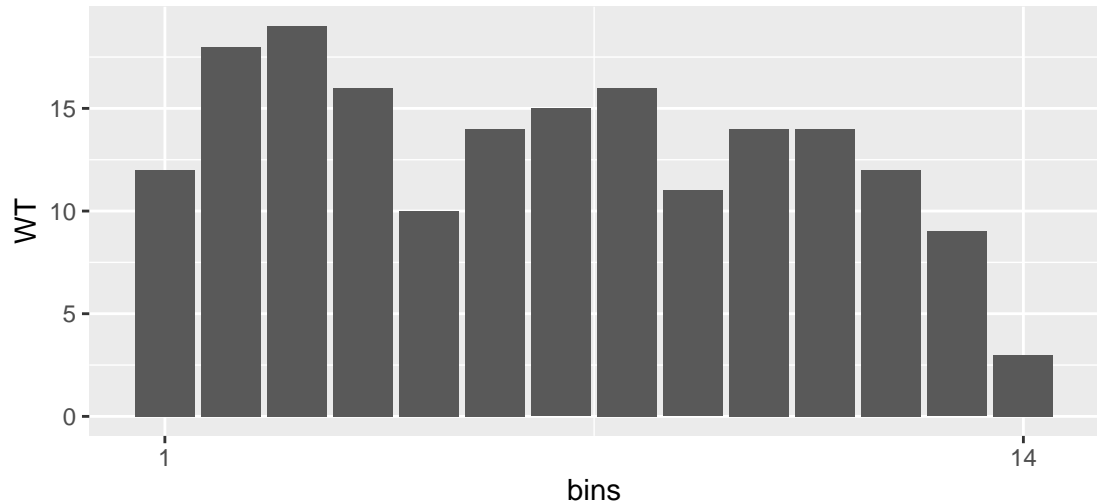
X450-1x402\_inference.xml



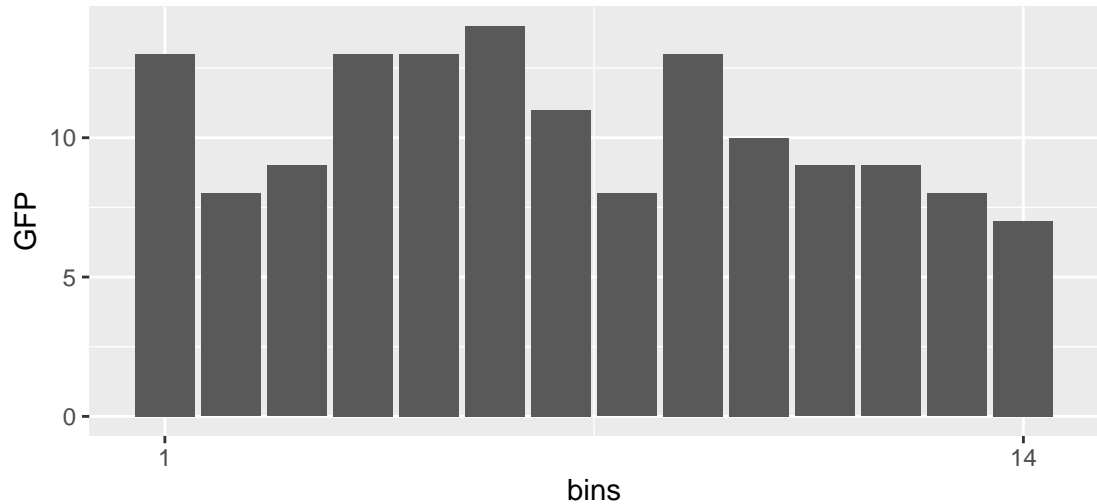
X450-1x402\_inference.xml



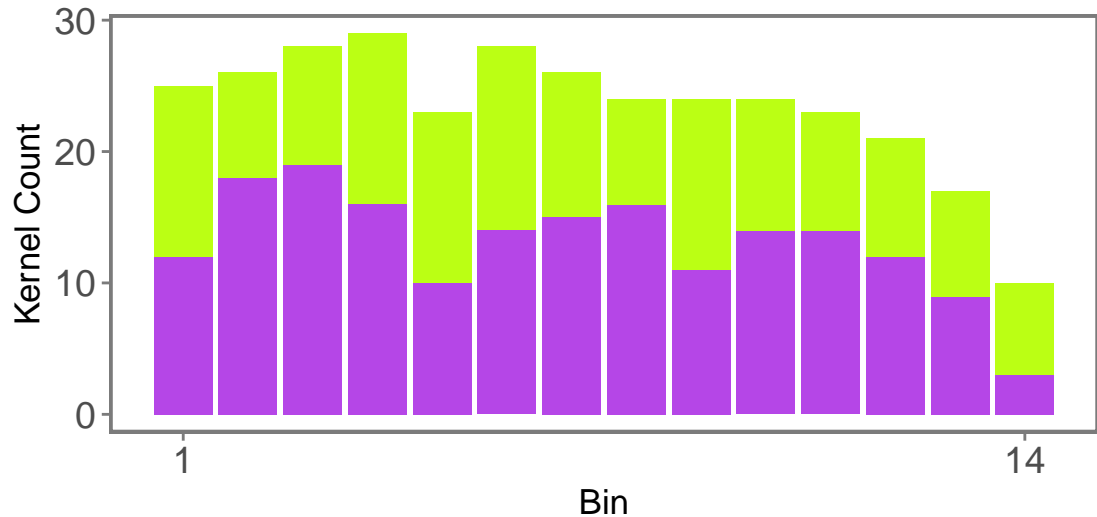
X450-2x402\_inference.xml



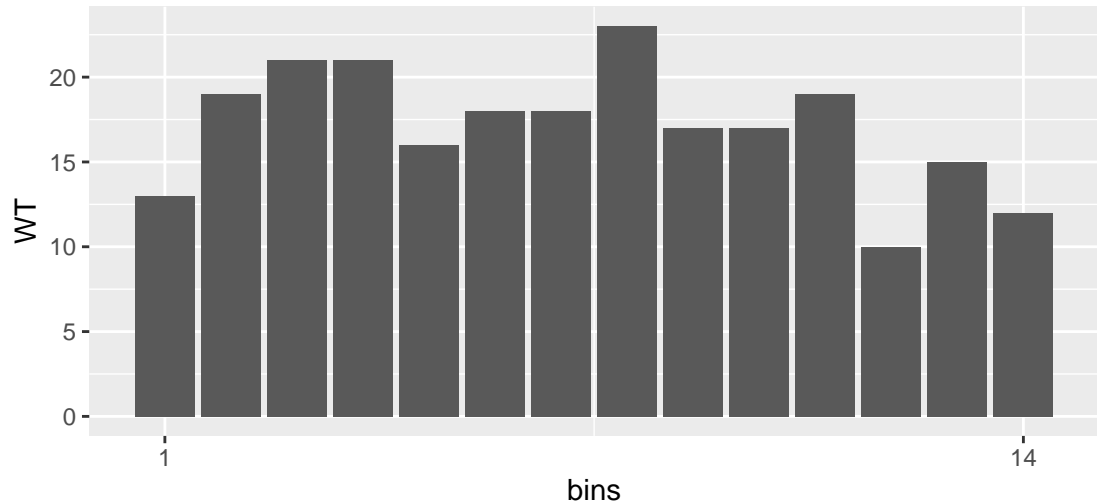
X450-2x402\_inference.xml



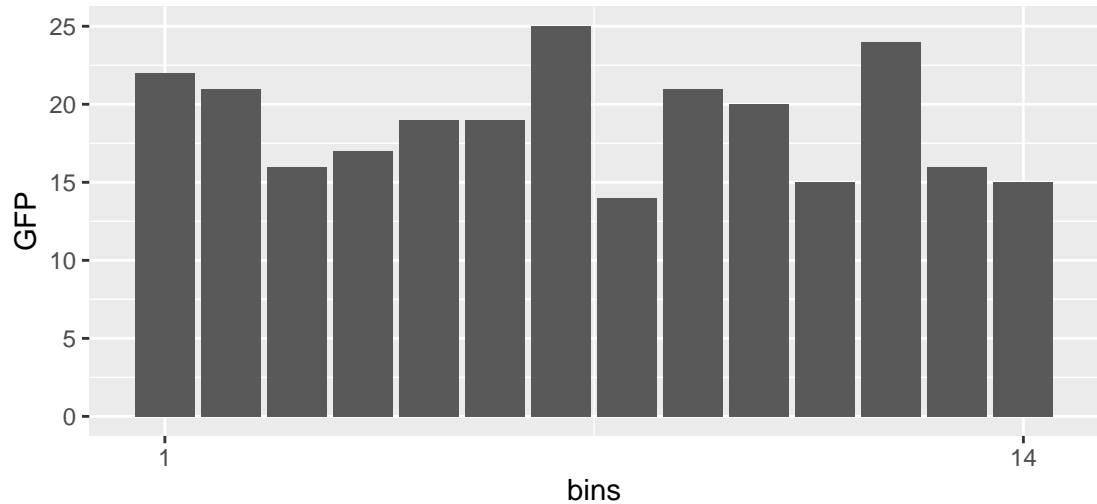
X450-2x402\_inference.xml



Y51-1x1\_inference.xml

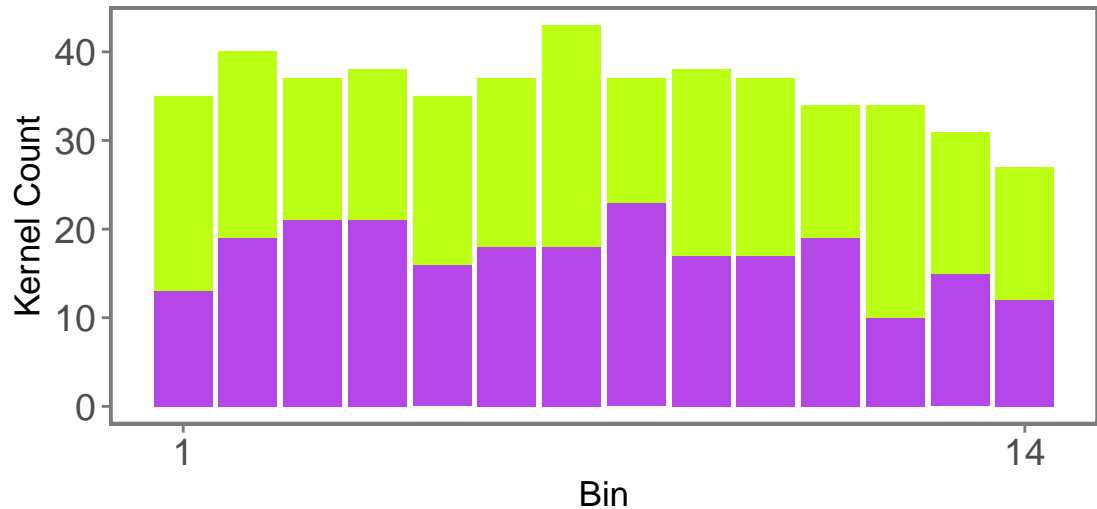


Y51-1x1\_inference.xml

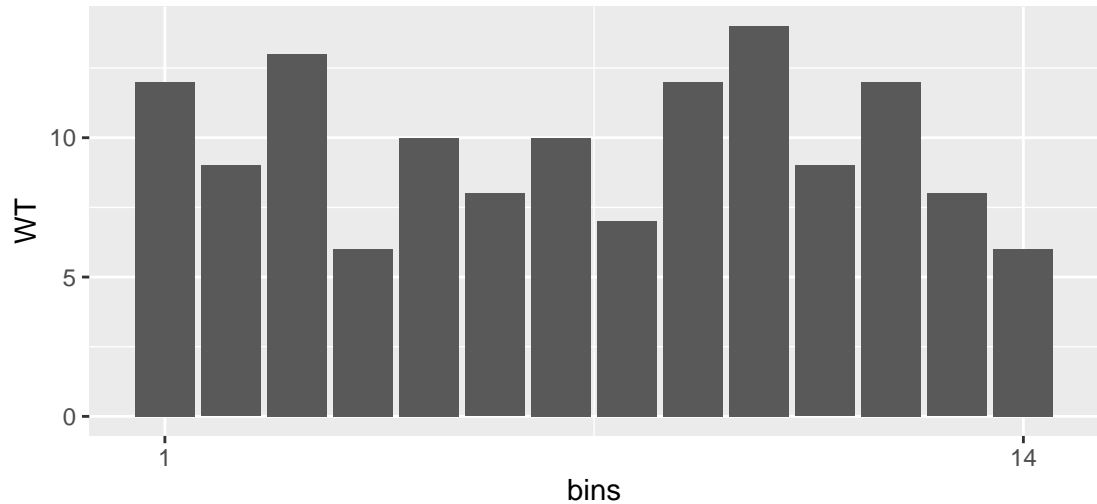




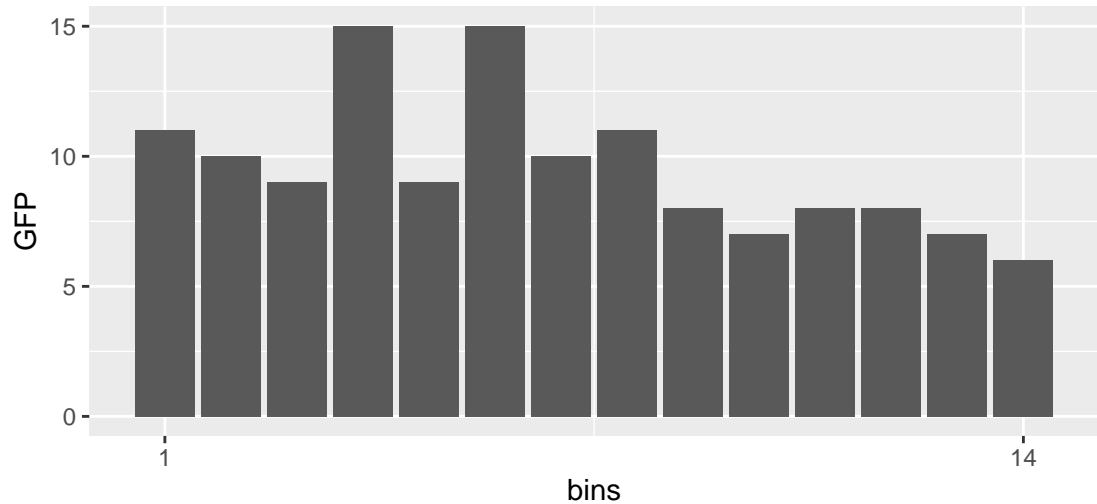
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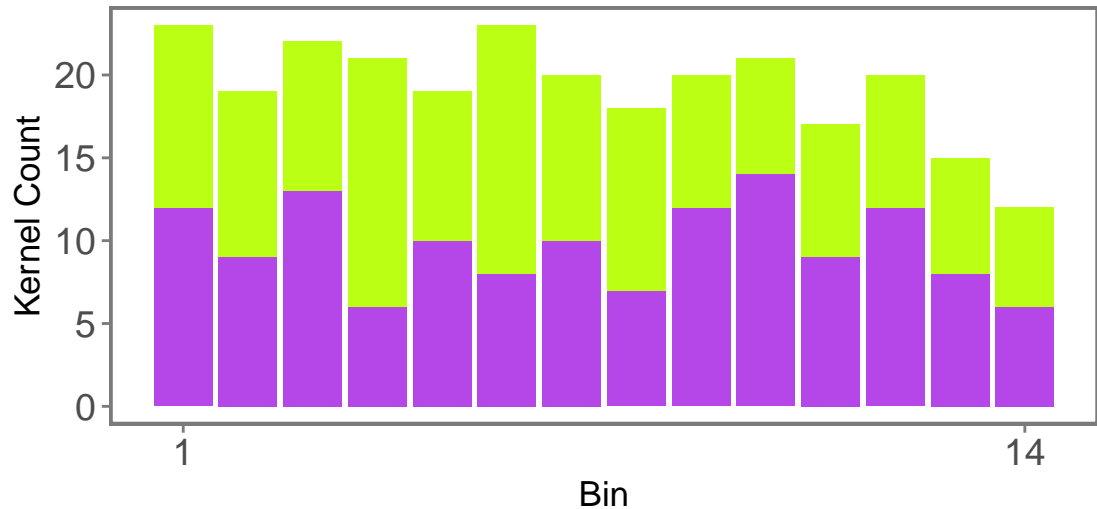
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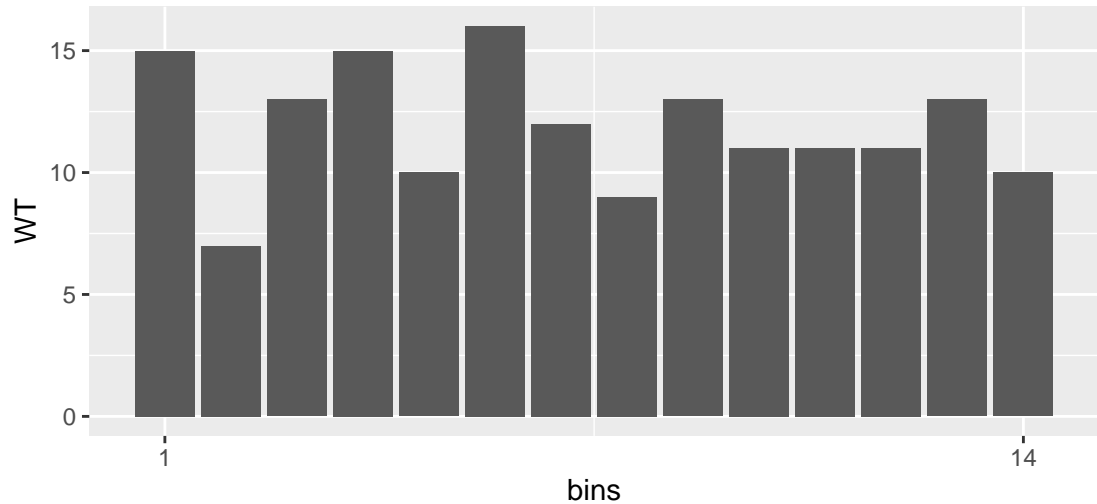
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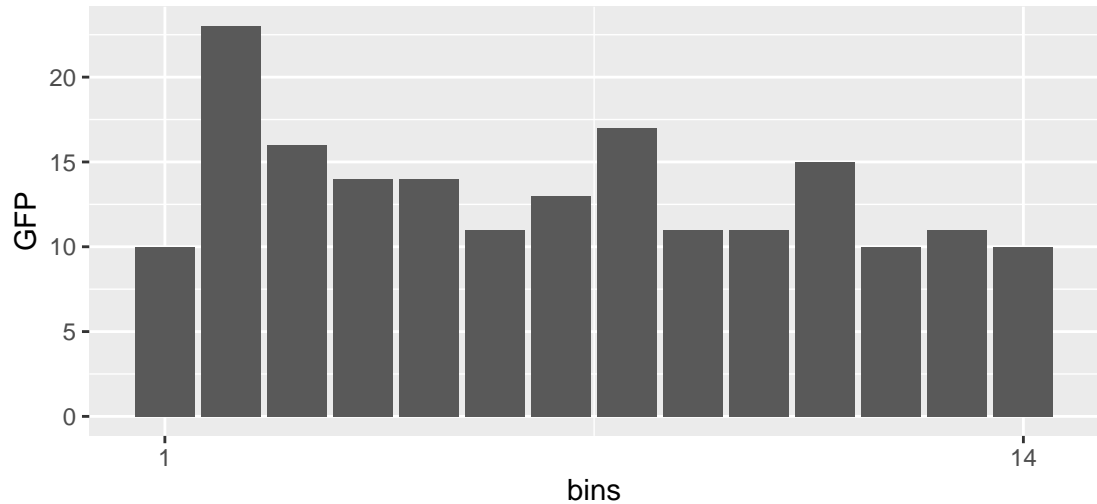
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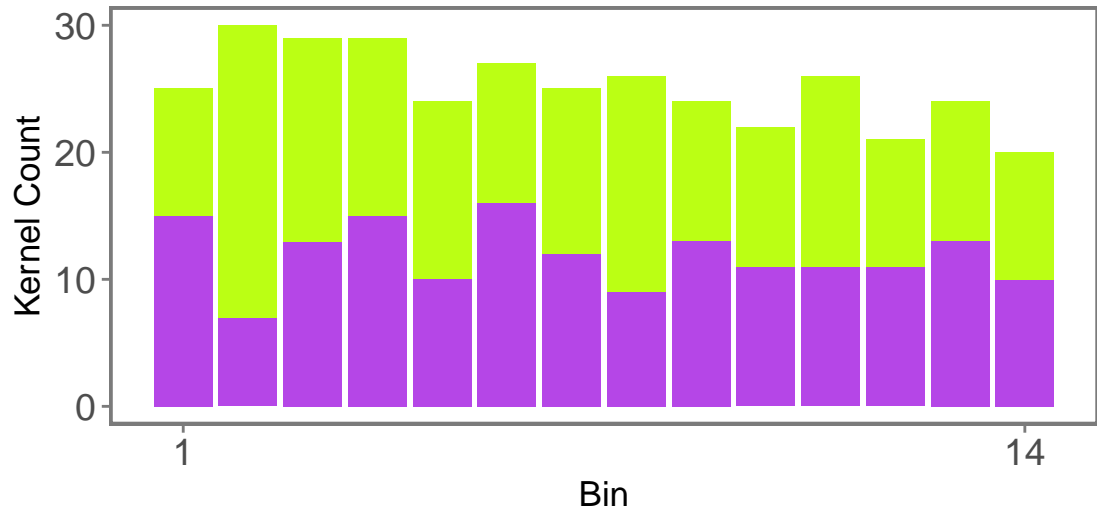
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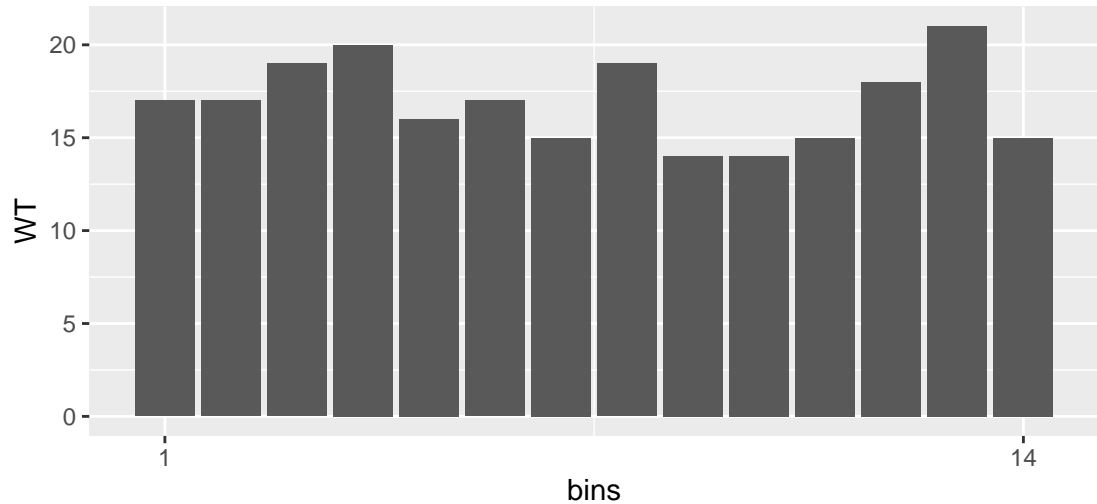
Y51-4x1\_inference.xml



Y51-4x1\_inference.xml

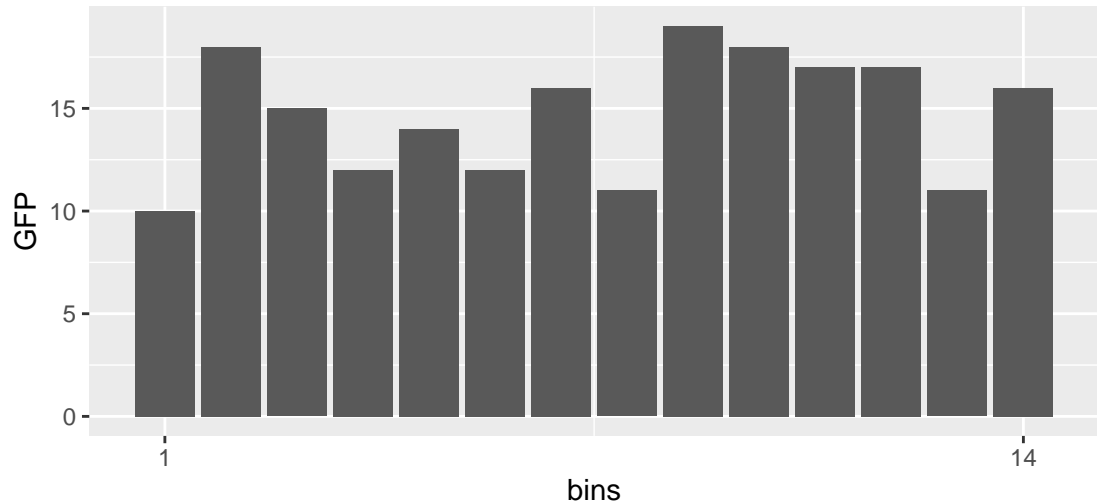


Y51-6x1\_inference.xml

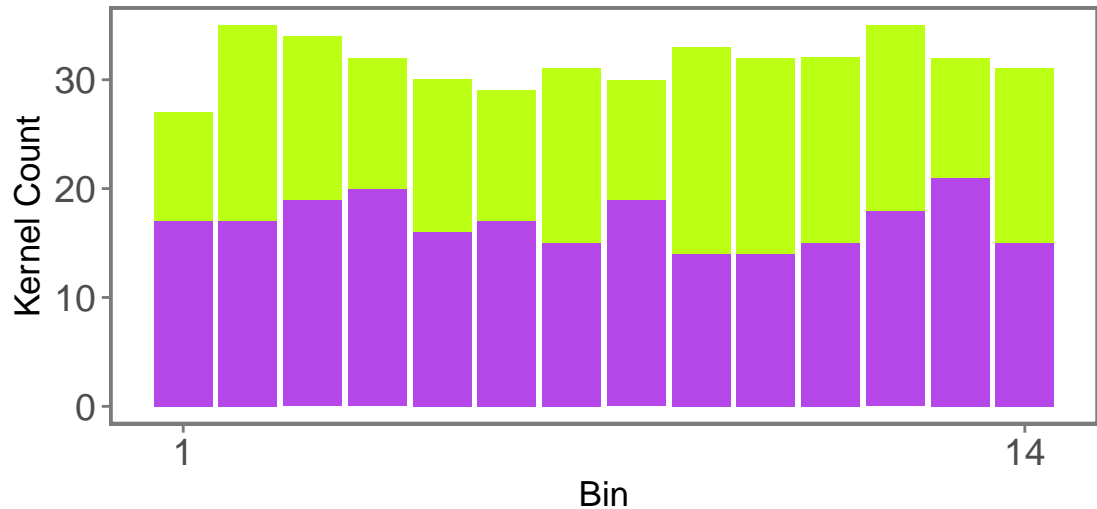




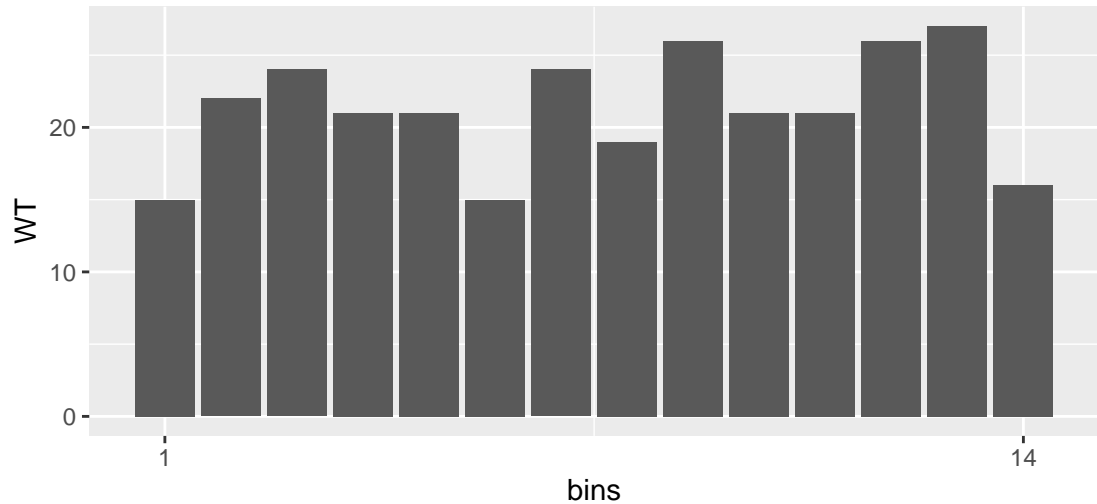
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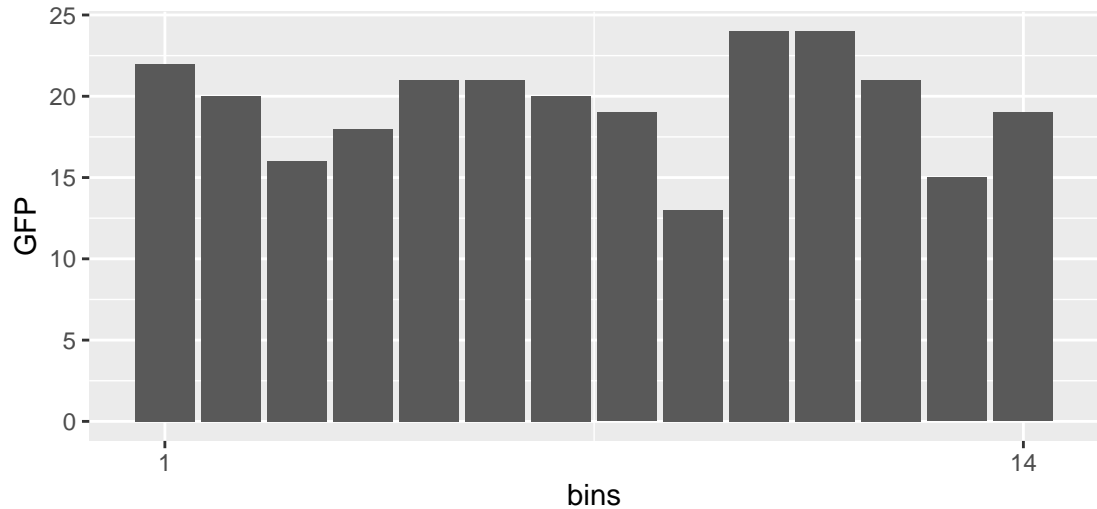
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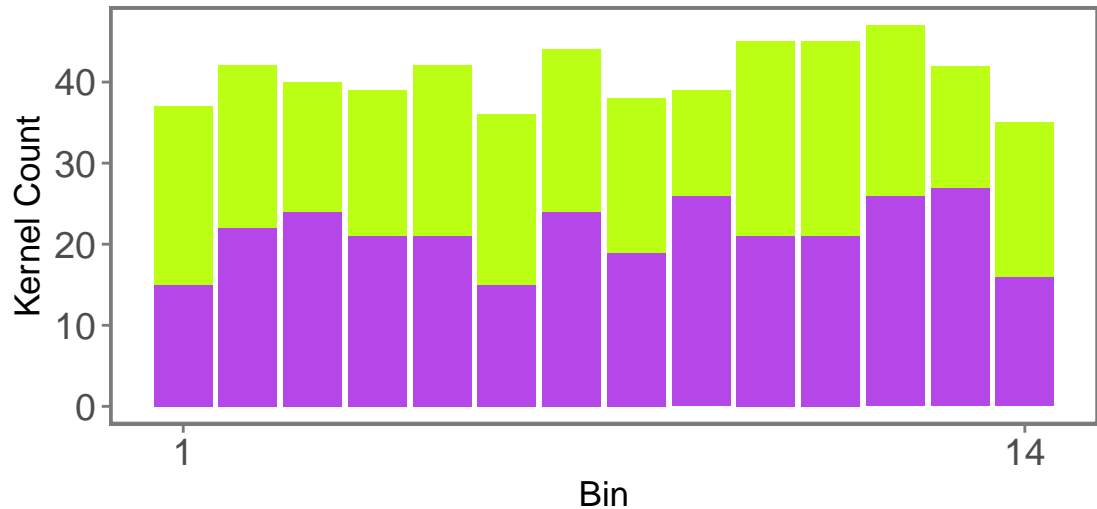
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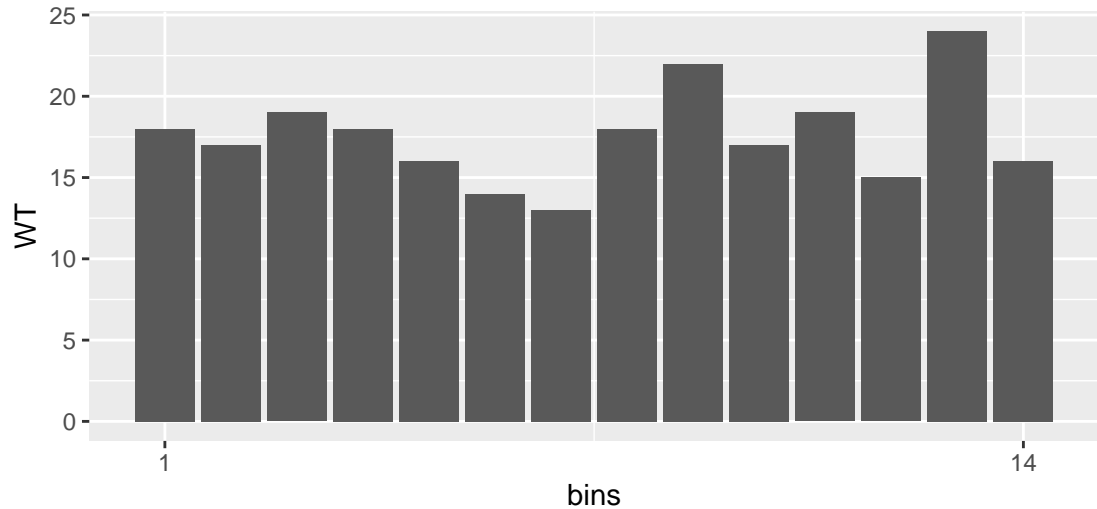
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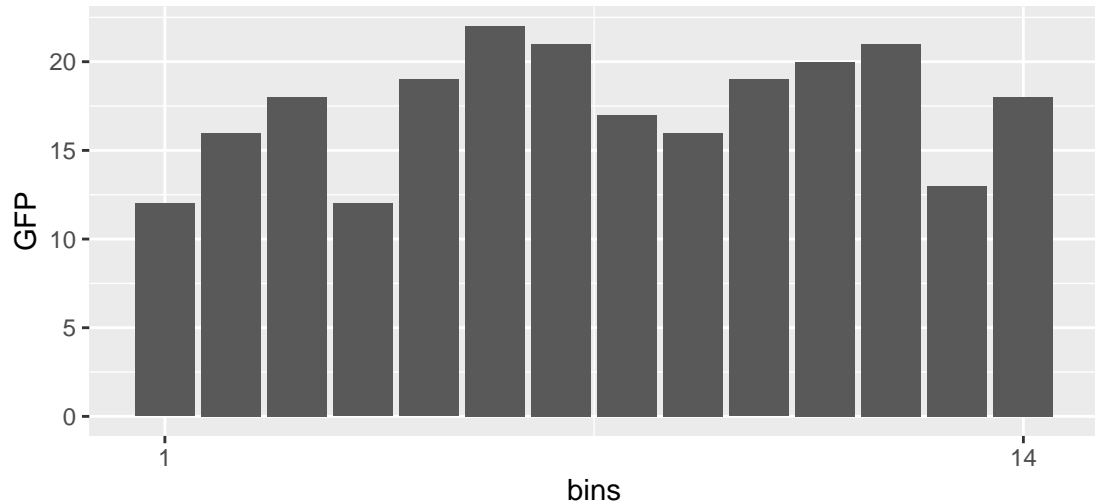
Y52-1x1\_inference.xml



Y52-2x1\_inference.xml



Y52-2x1\_inference.xml



Y52-2x1\_inference.xml

