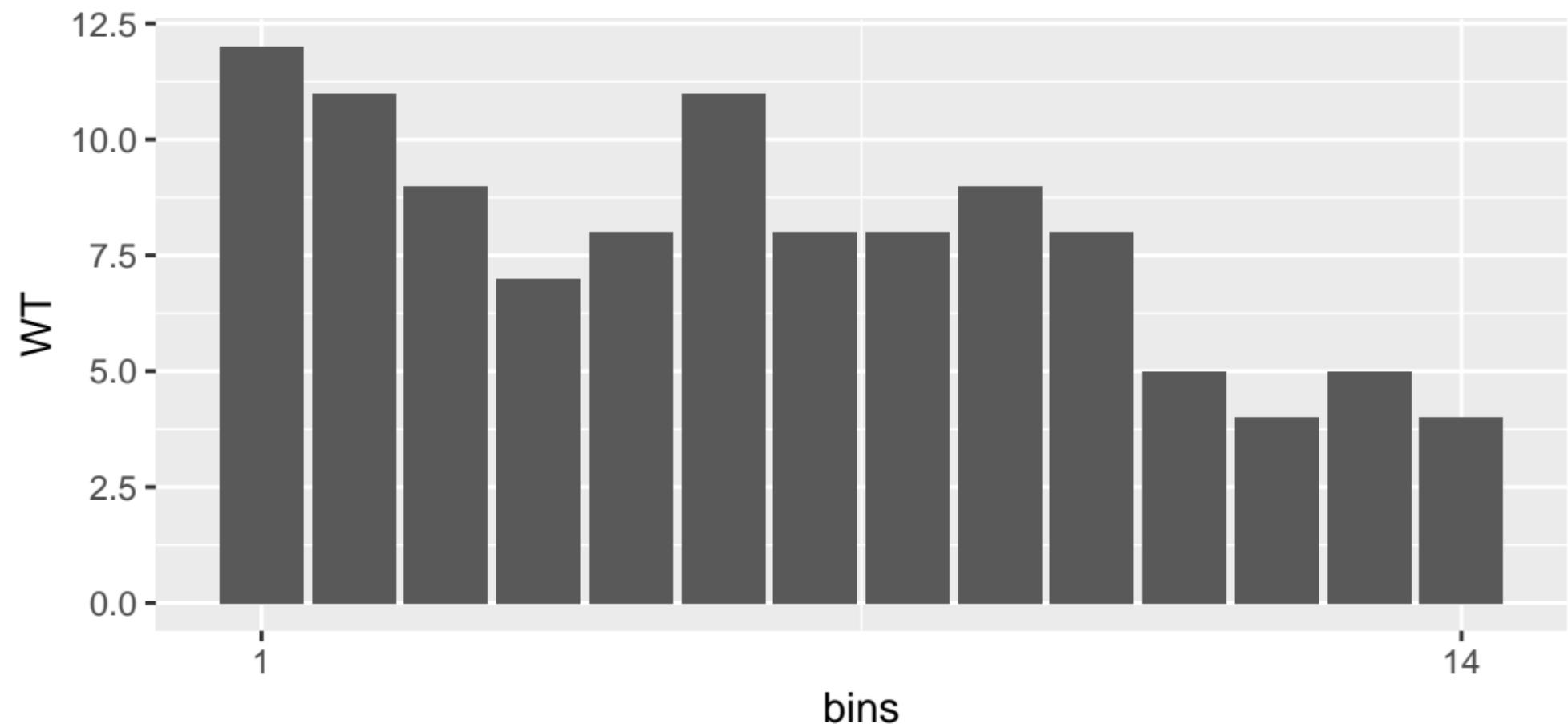
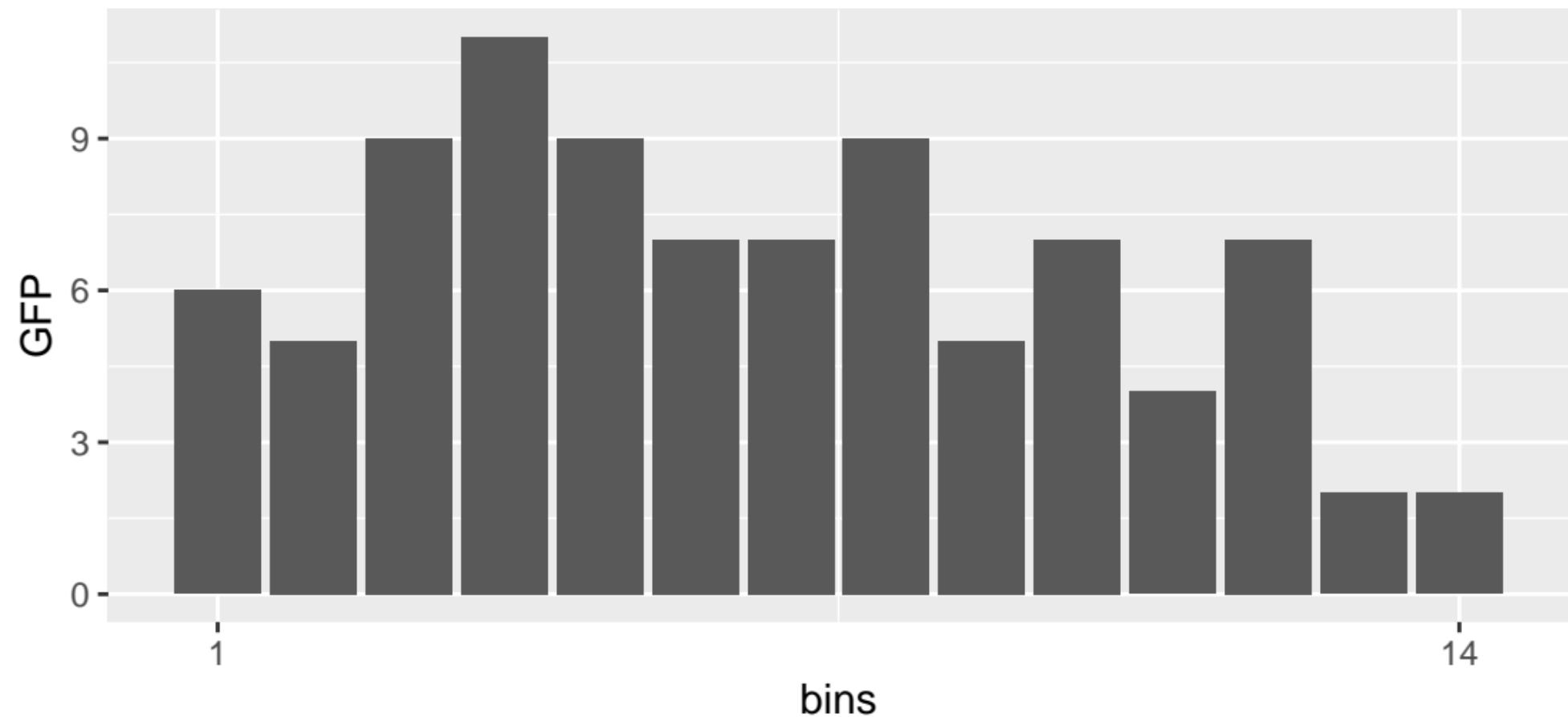


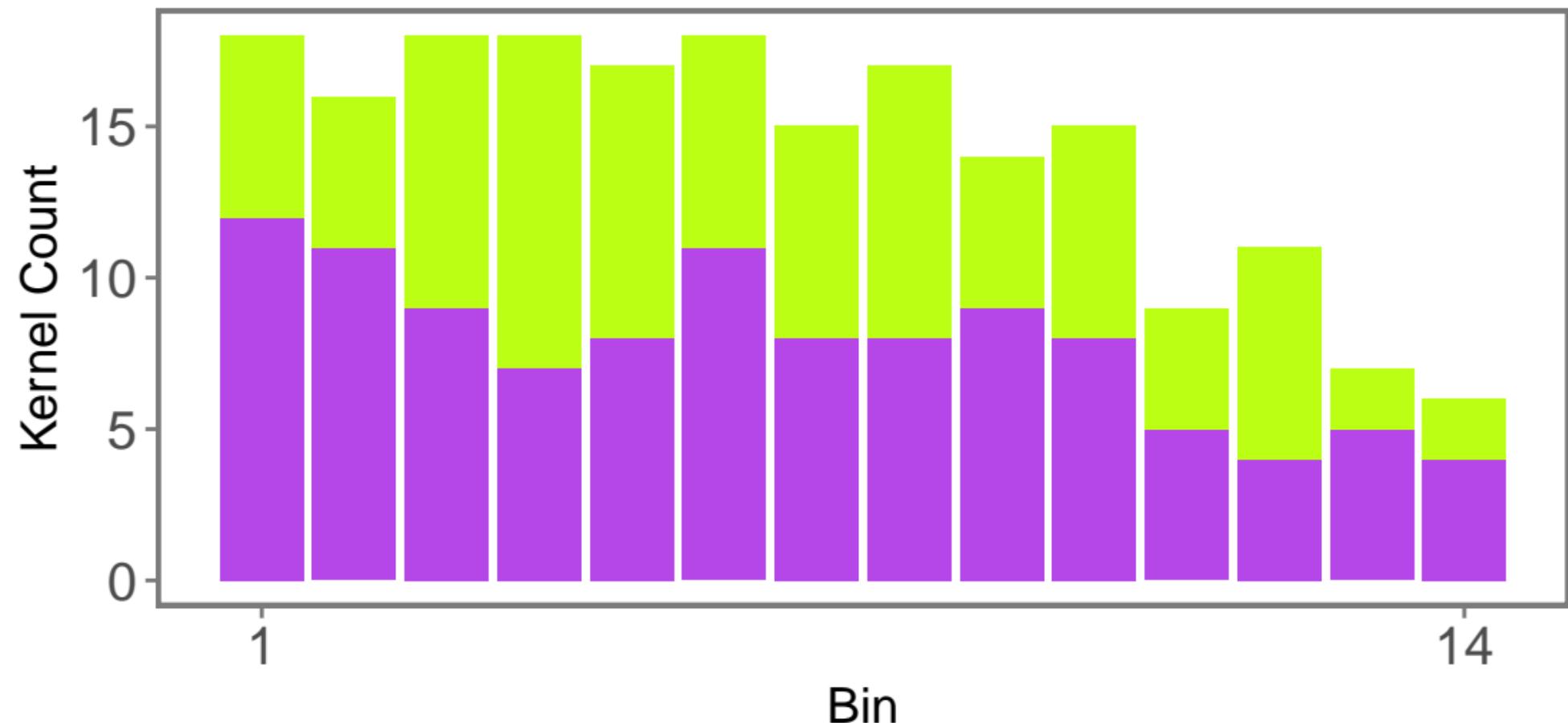
# X432-1x402\_inference.xml



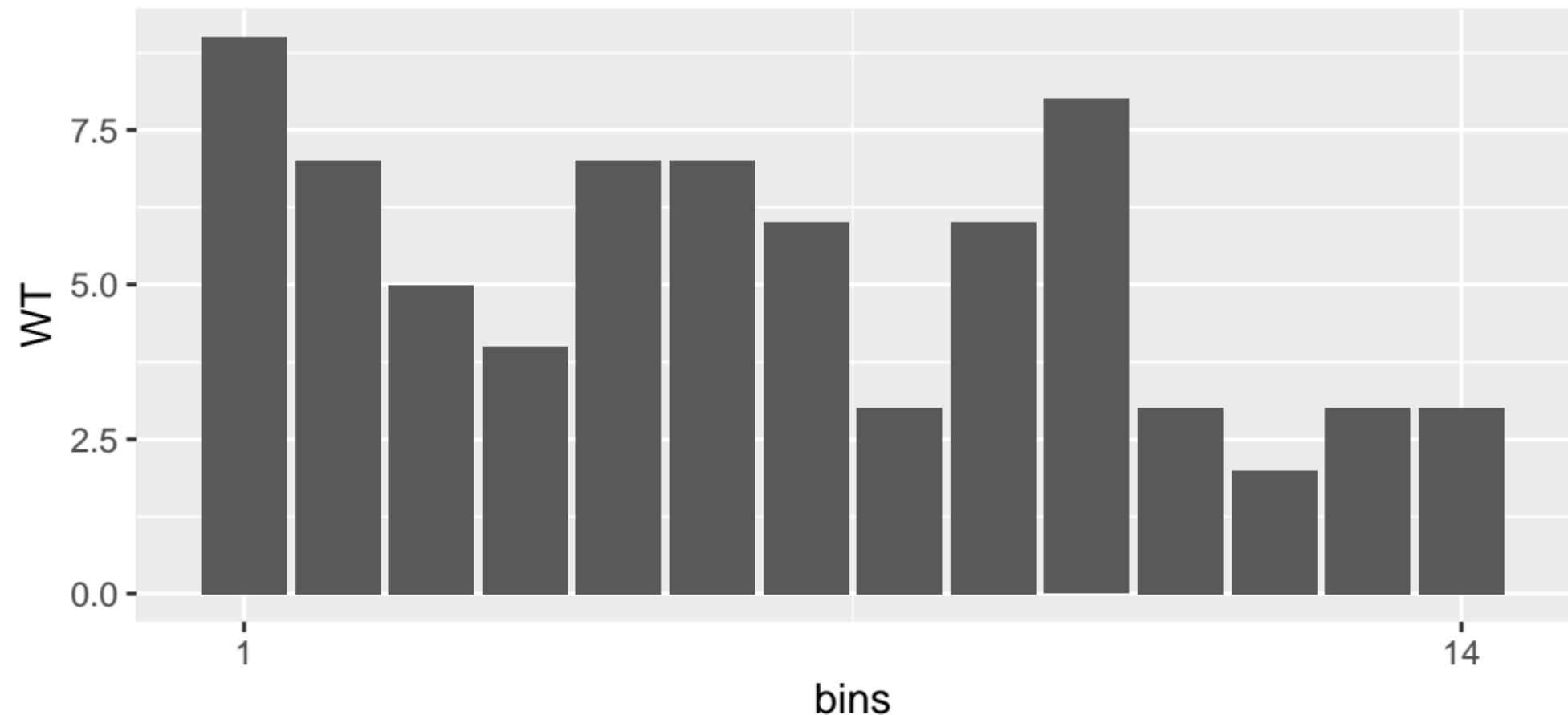
# X432-1x402\_inference.xml



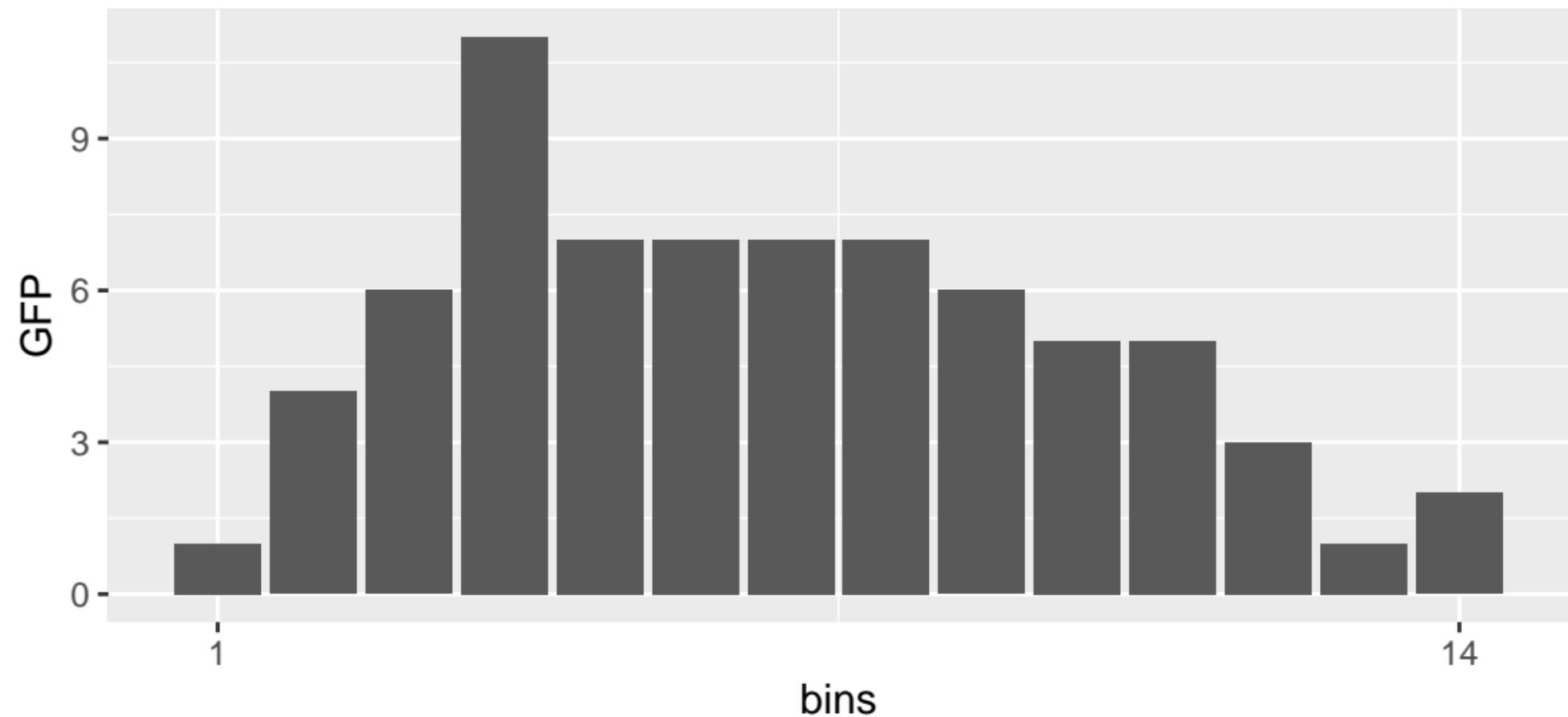
# X432-1x402\_inference.xml



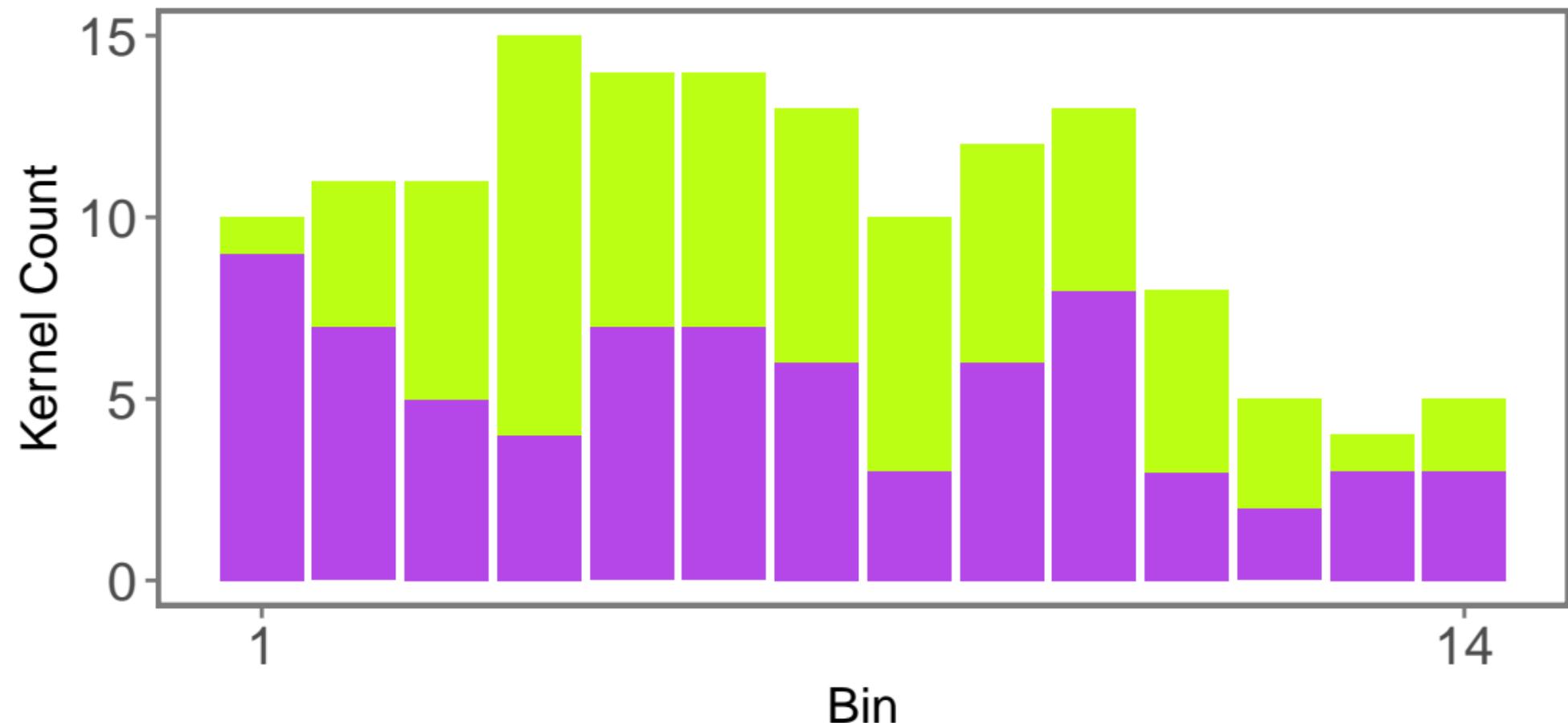
# X432-2x402\_inference.xml



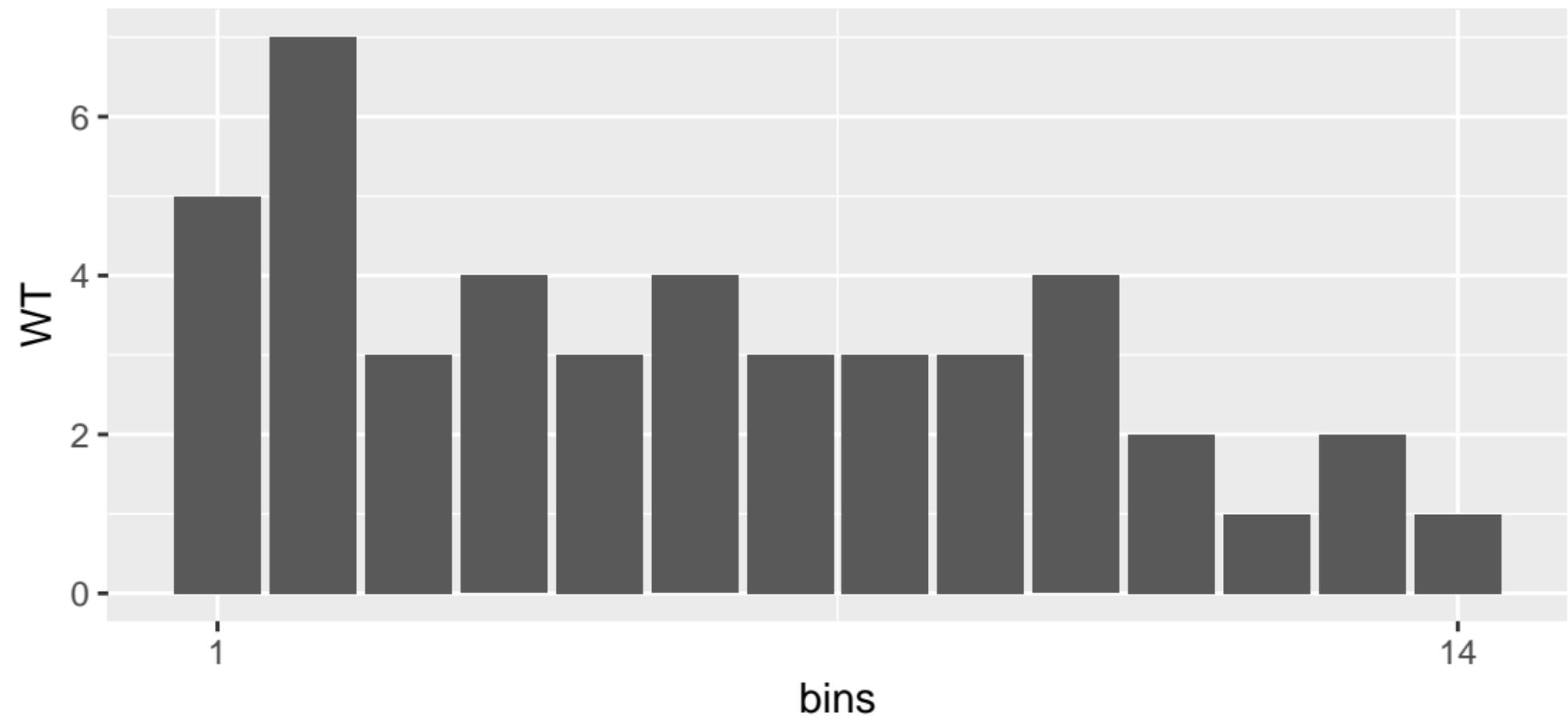
# X432-2x402\_inference.xml



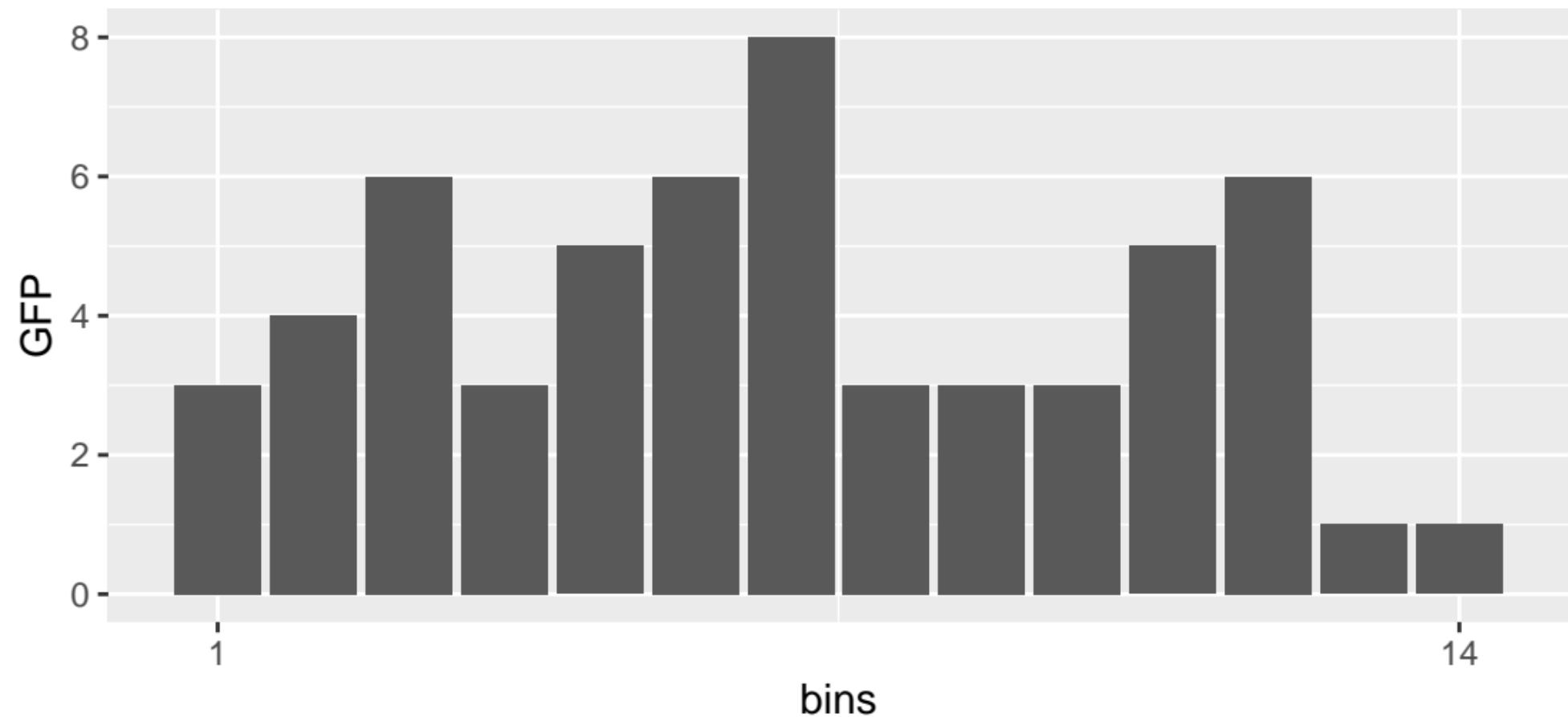
X432-2x402\_inference.xml



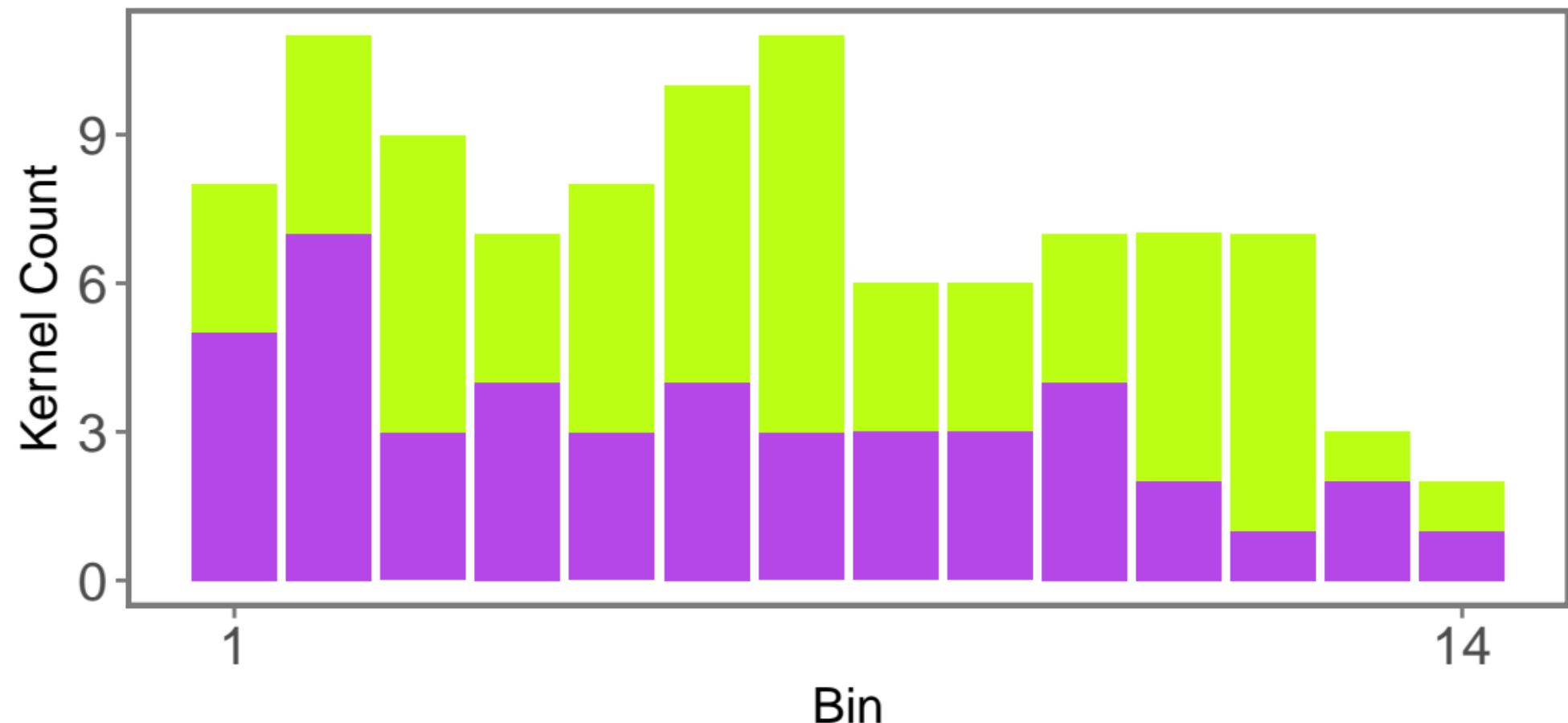
# X432-5x402\_inference.xml



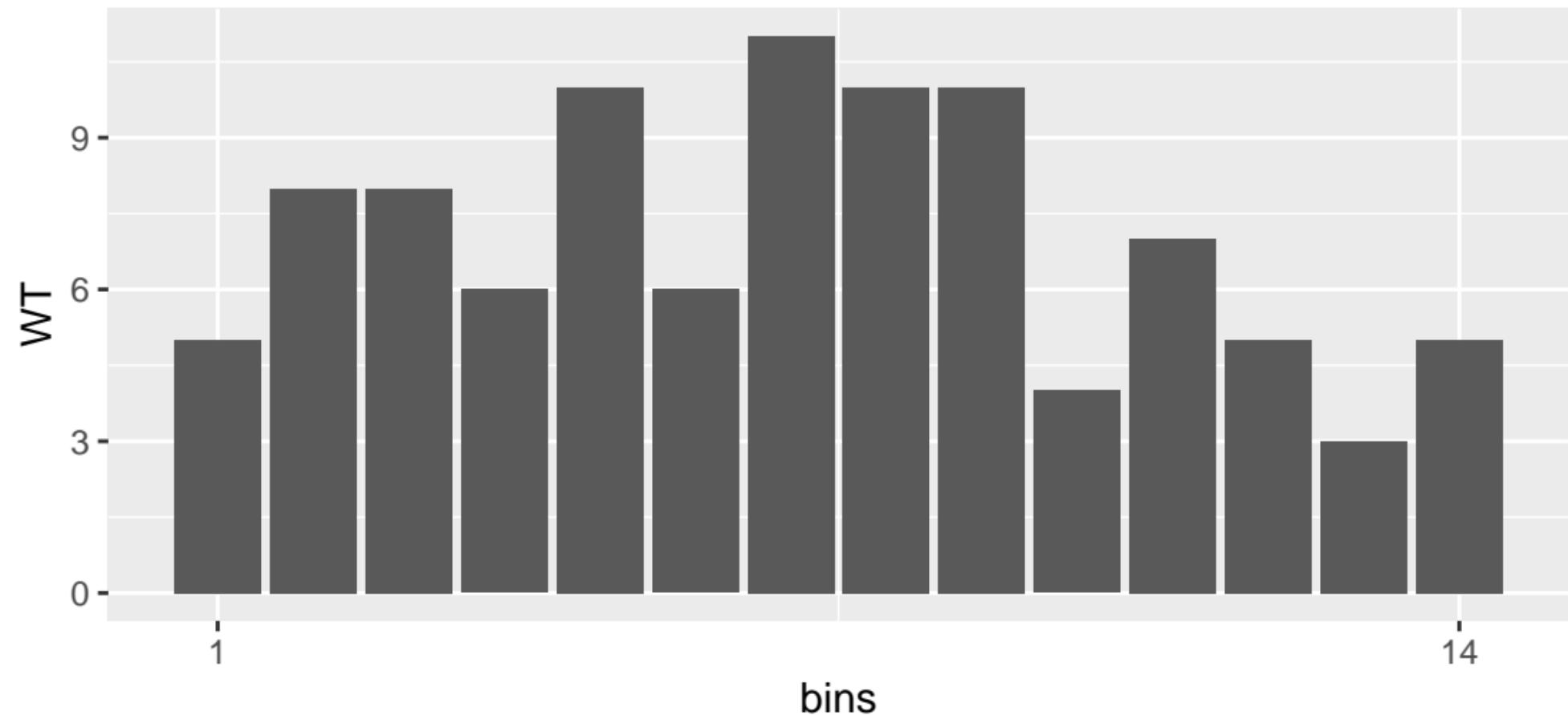
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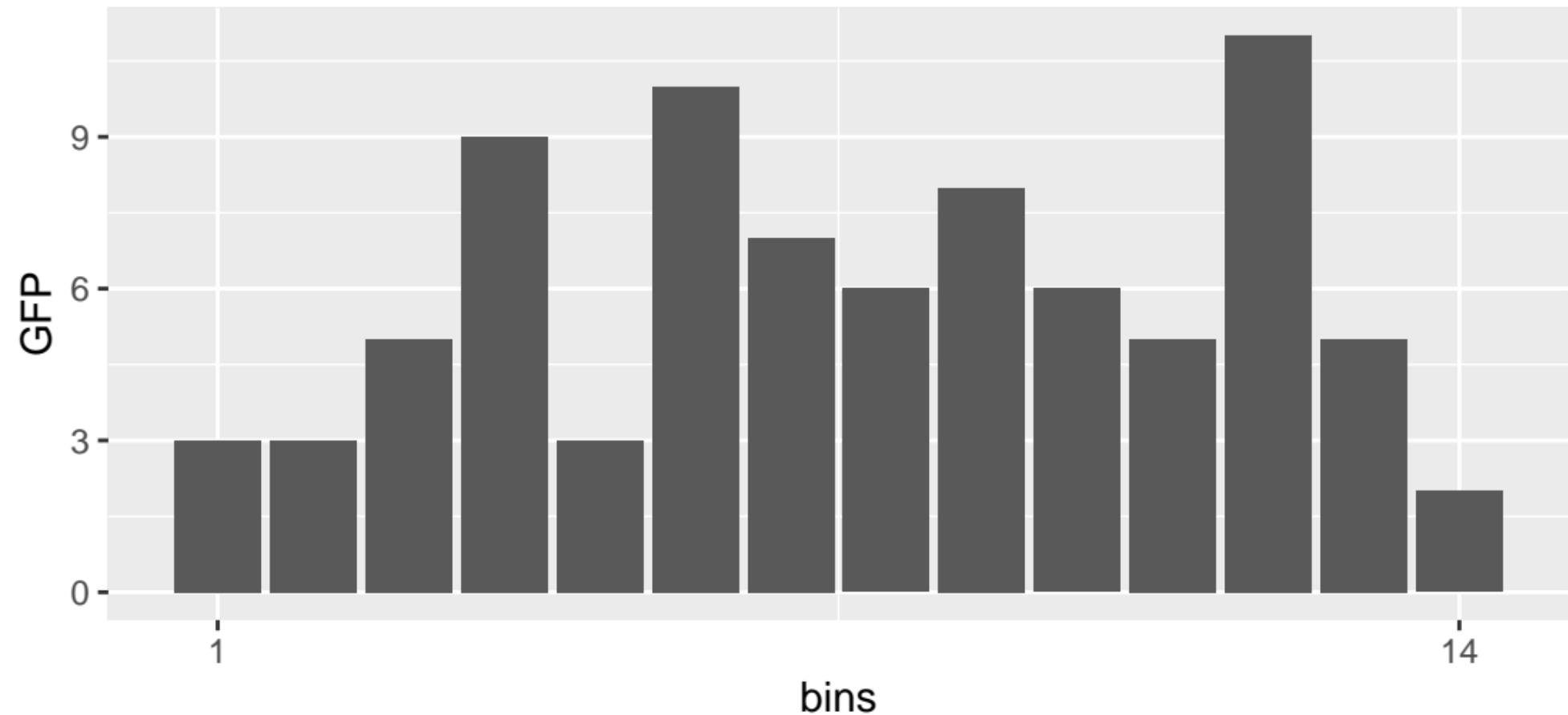
X432-5x402\_inference.xml



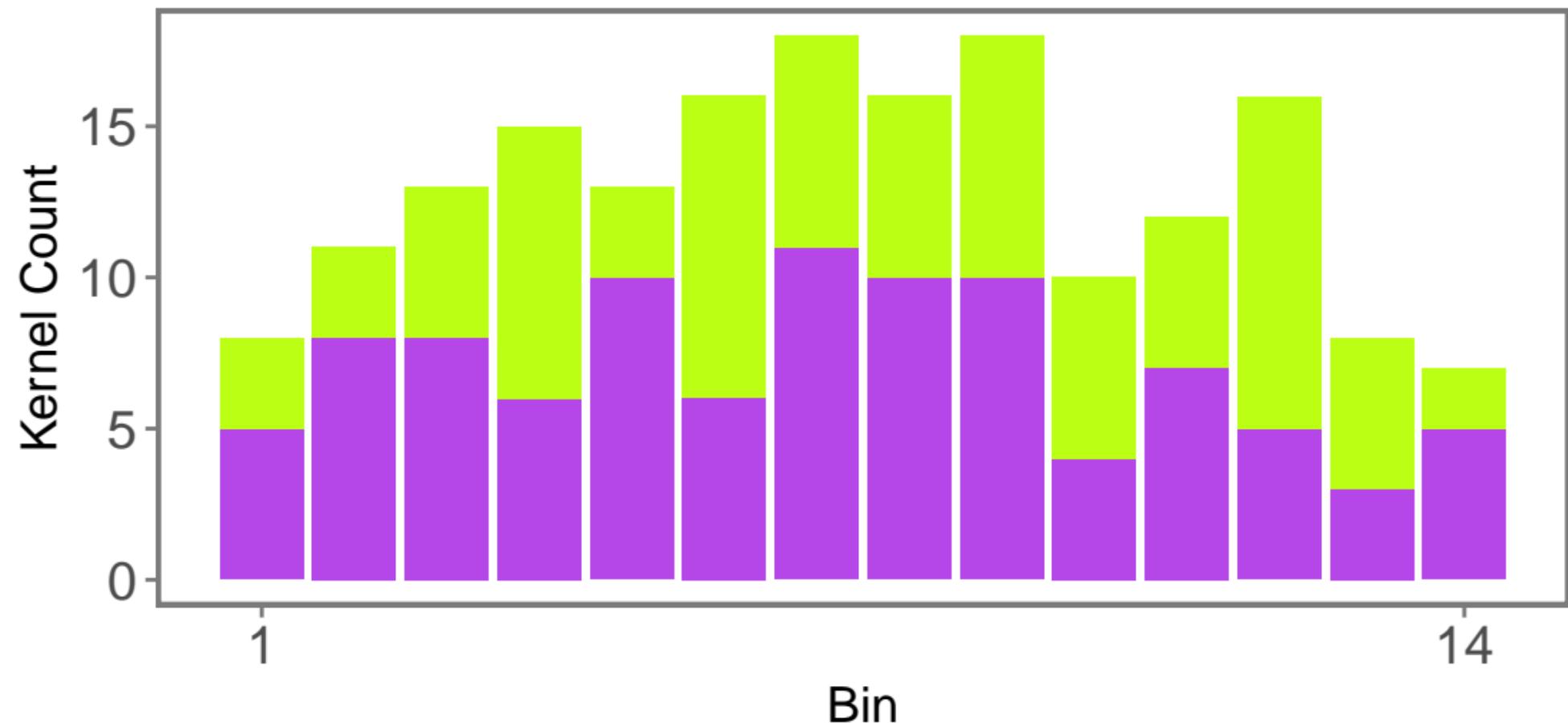
# X432L-3x402\_inference.xml



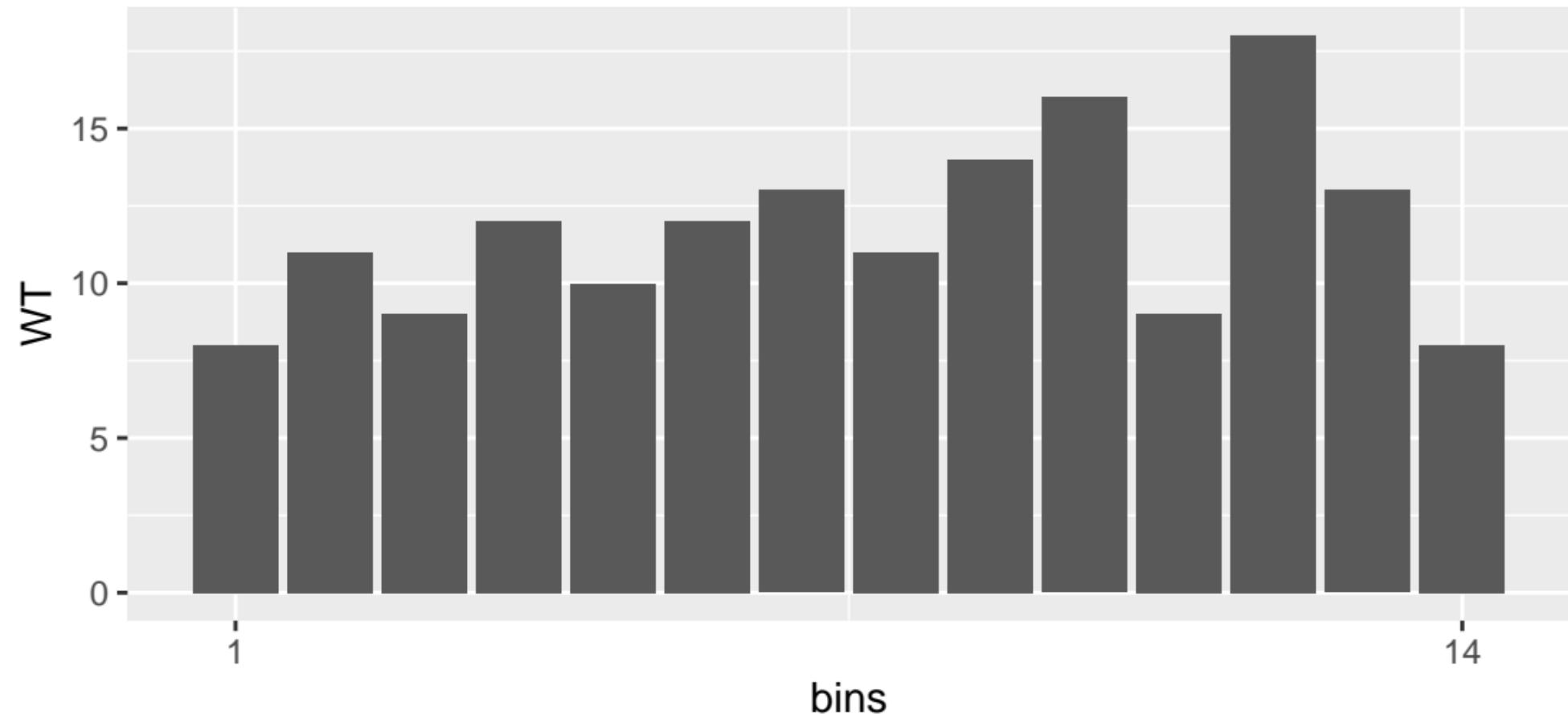
# X432L-3x402\_inference.xml



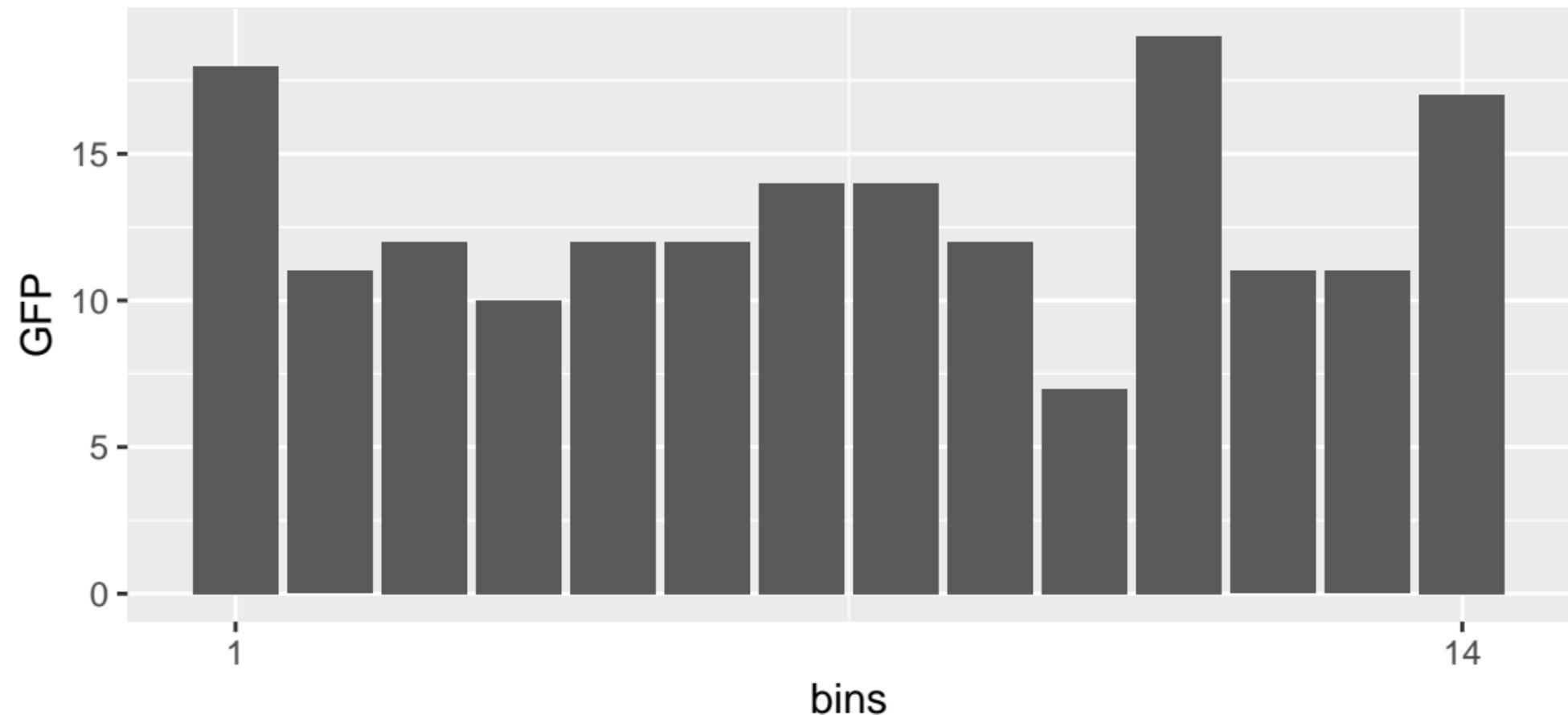
# X432L-3x402\_inference.xml



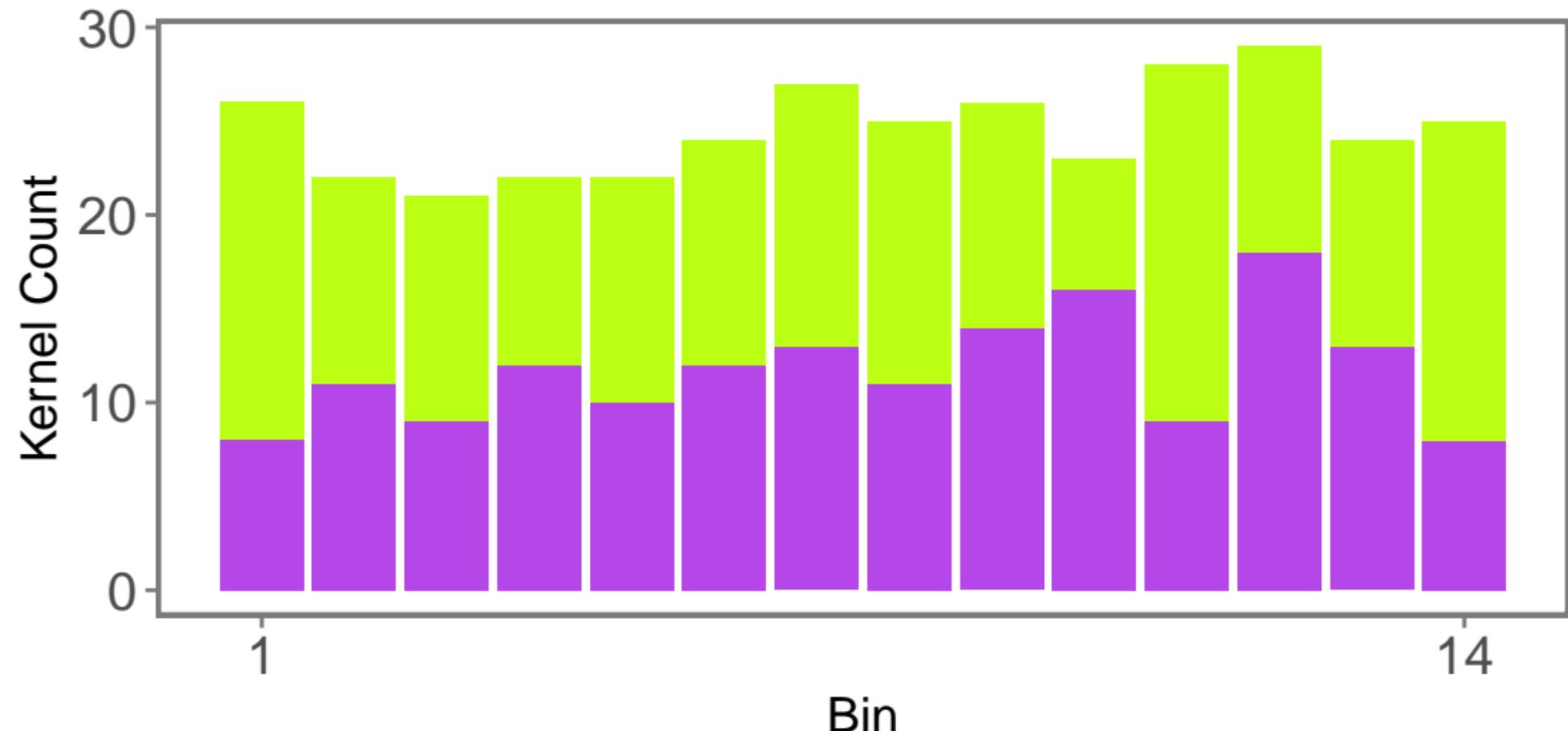
# Y20-2x1\_inference.xml



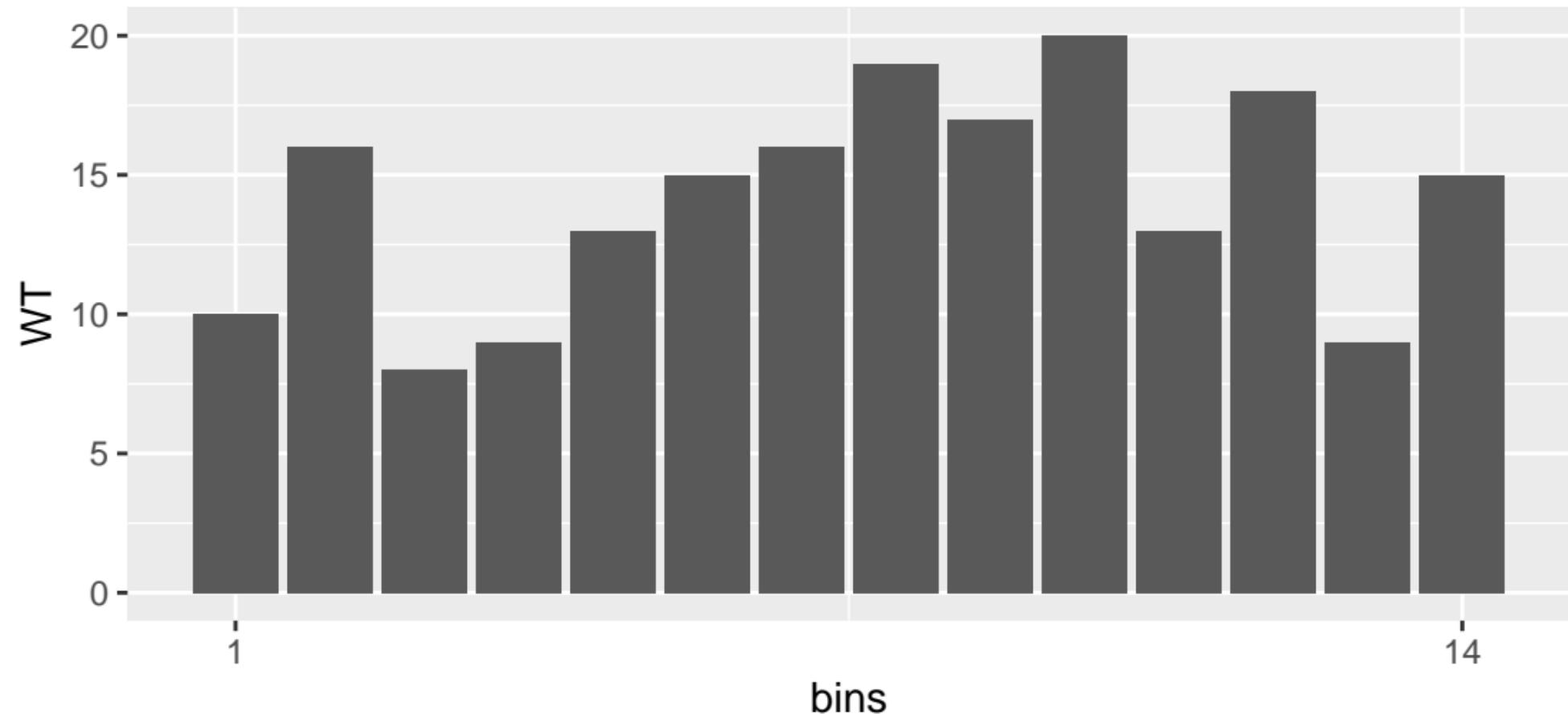
# Y20-2x1\_inference.xml



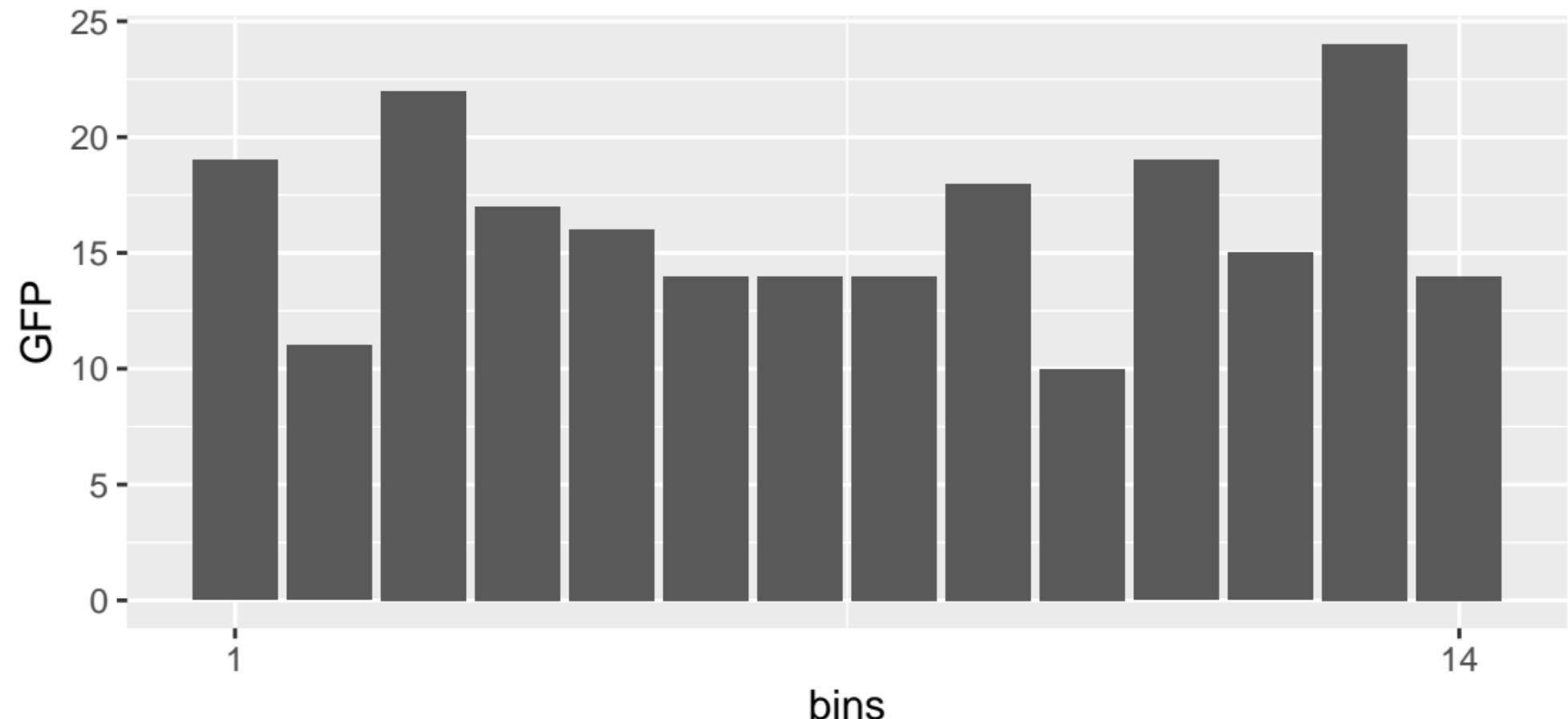
Y20-2x1\_inference.xml



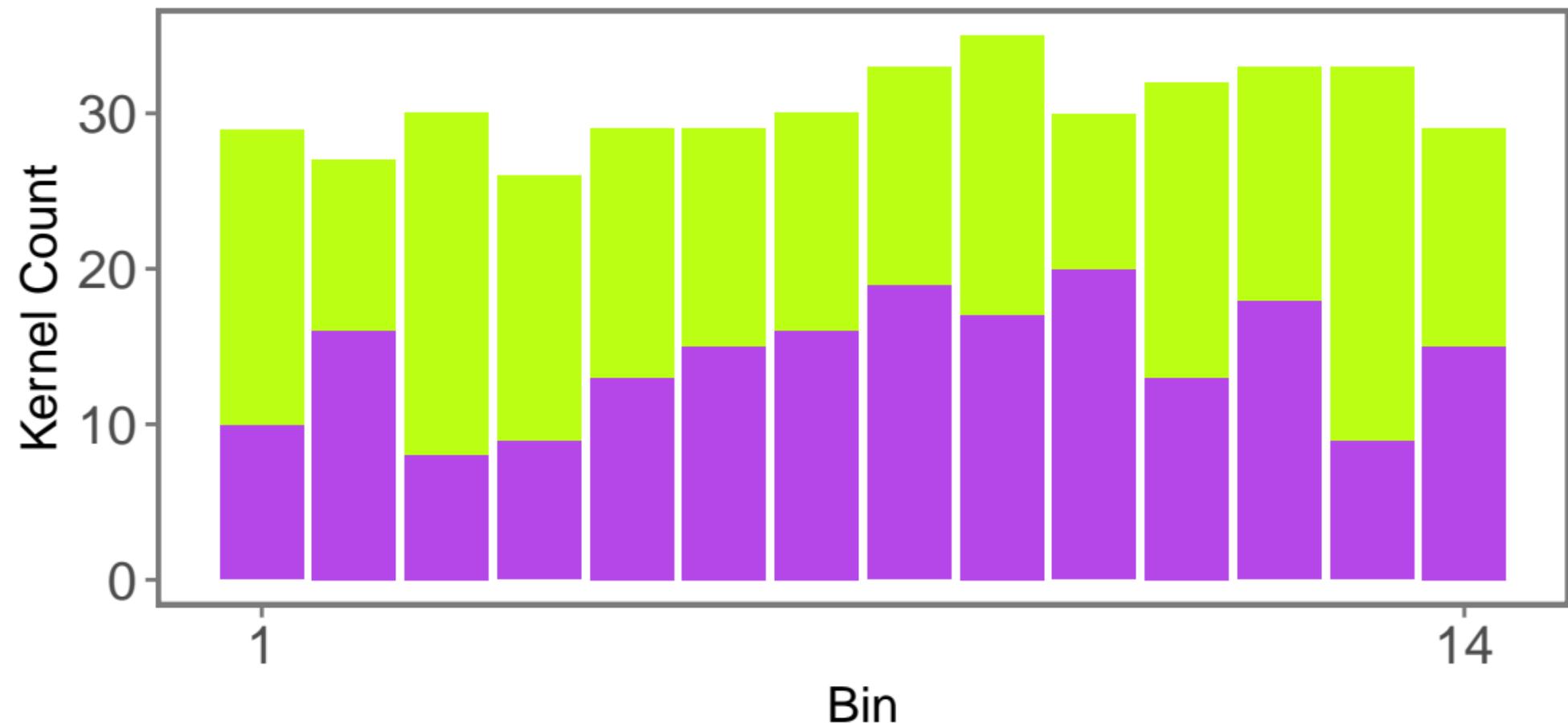
# Y20-3x1\_inference.xml



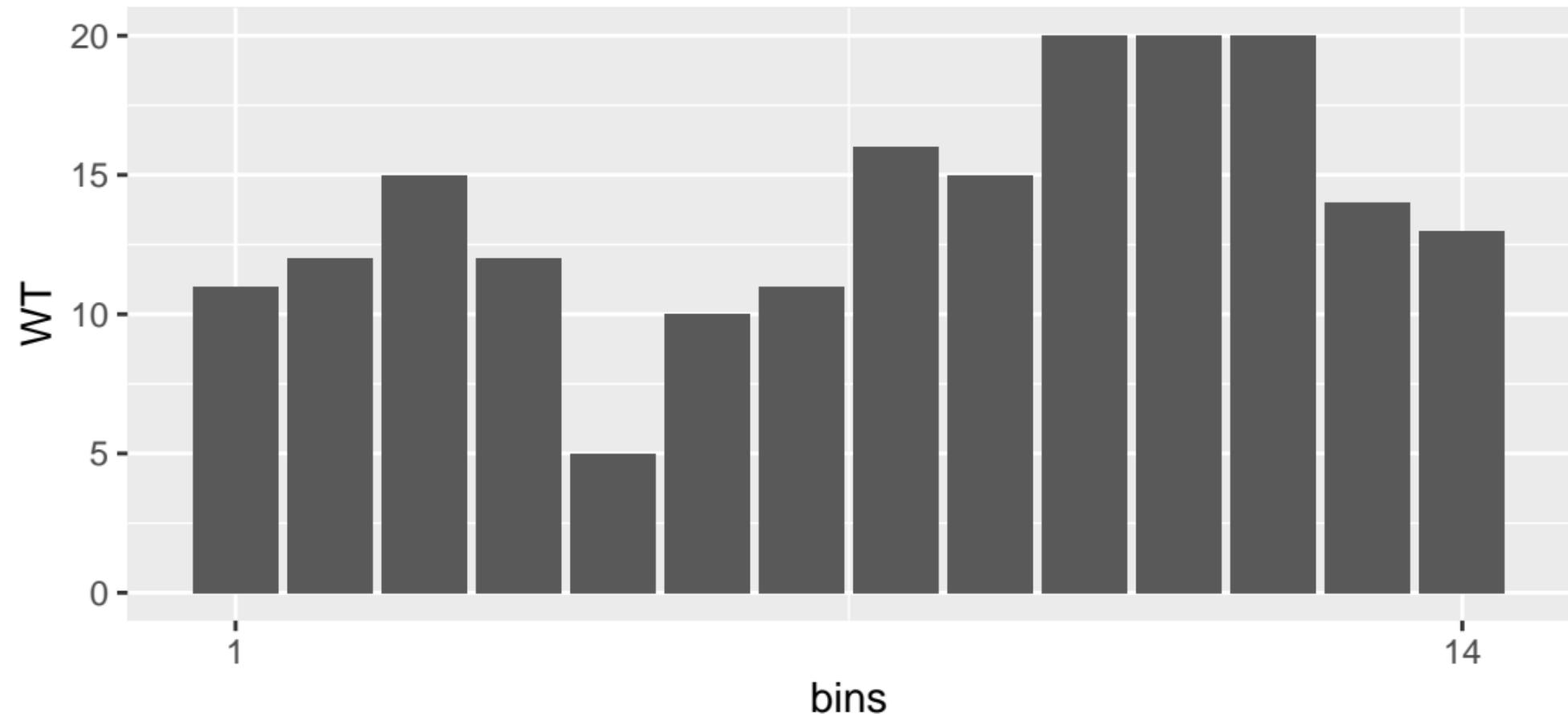
# Y20-3x1\_inference.xml



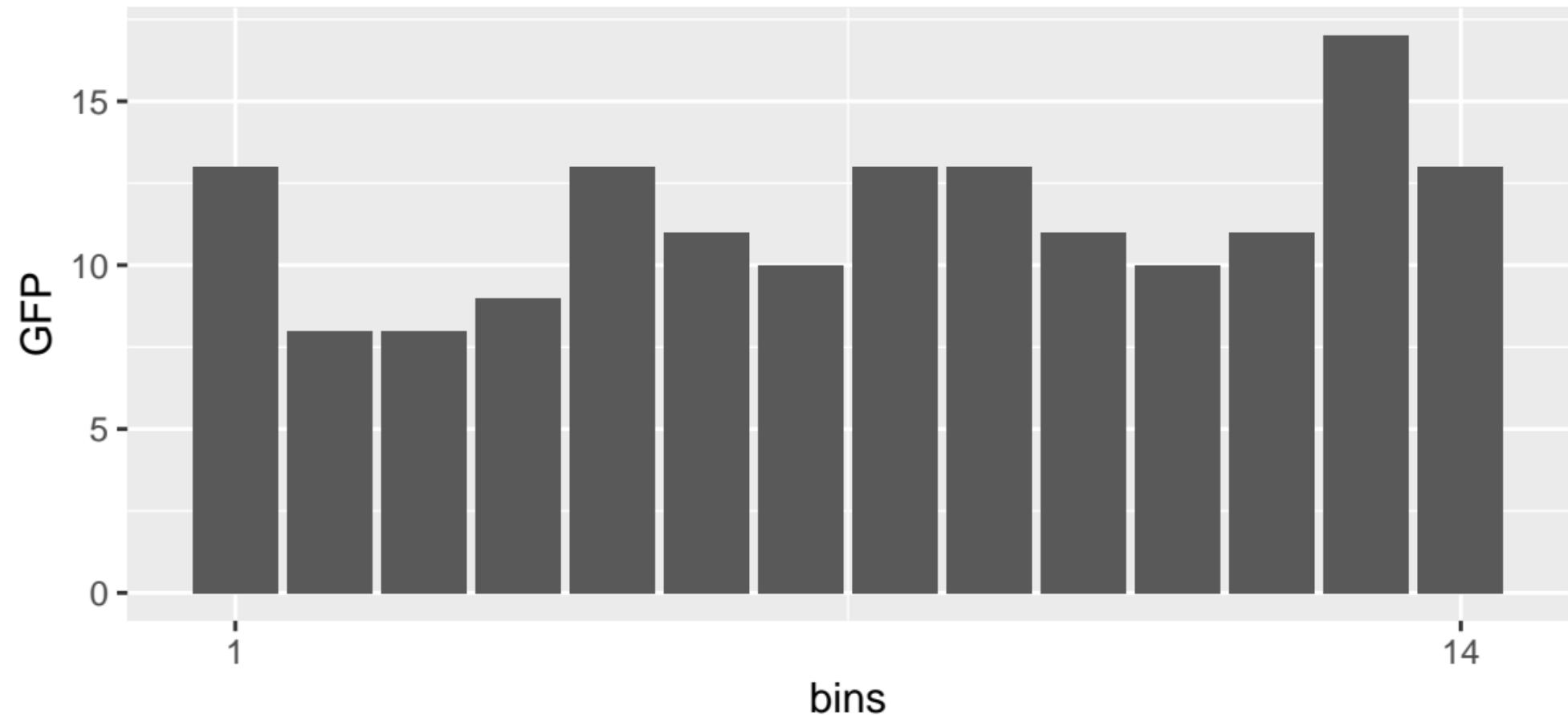
Y20-3x1\_inference.xml



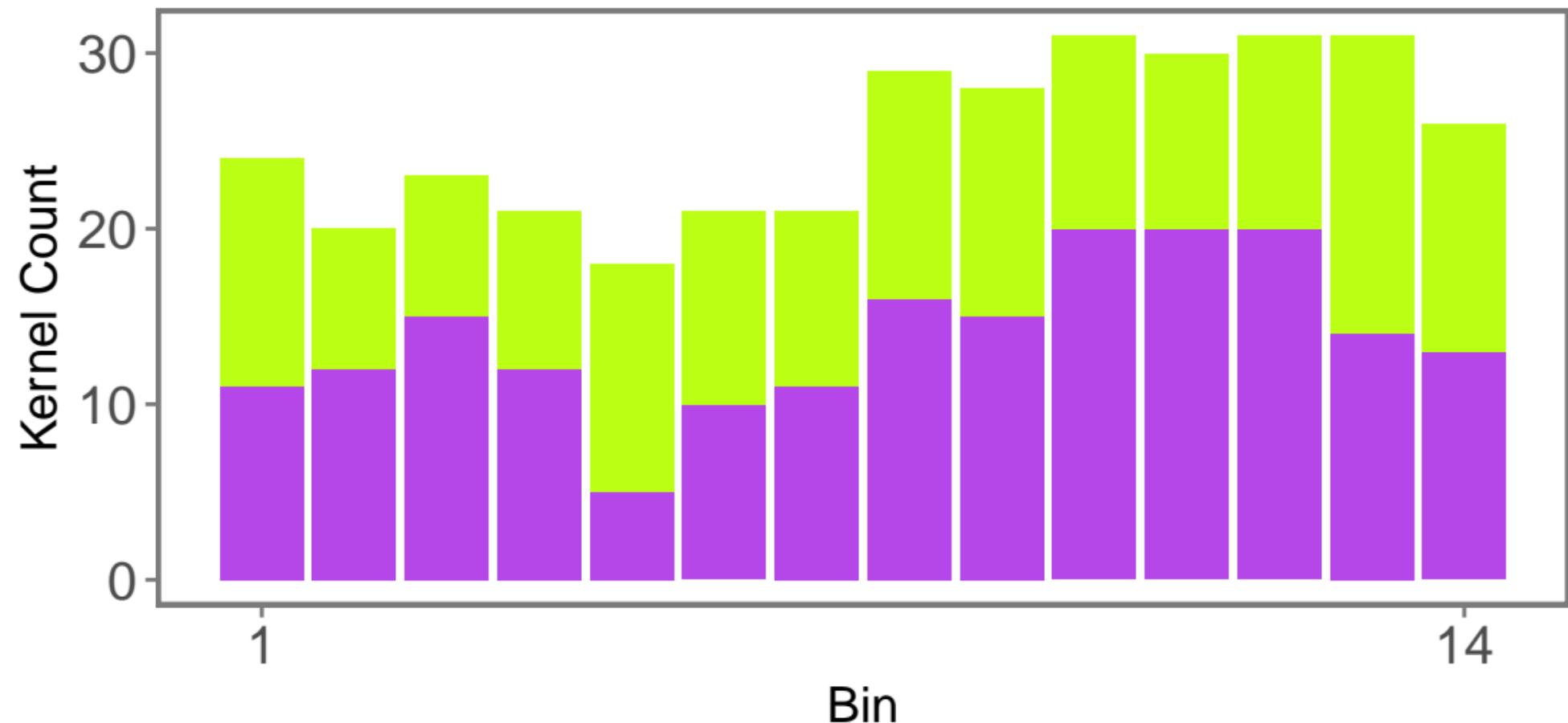
# Y20-4x1\_inference.xml



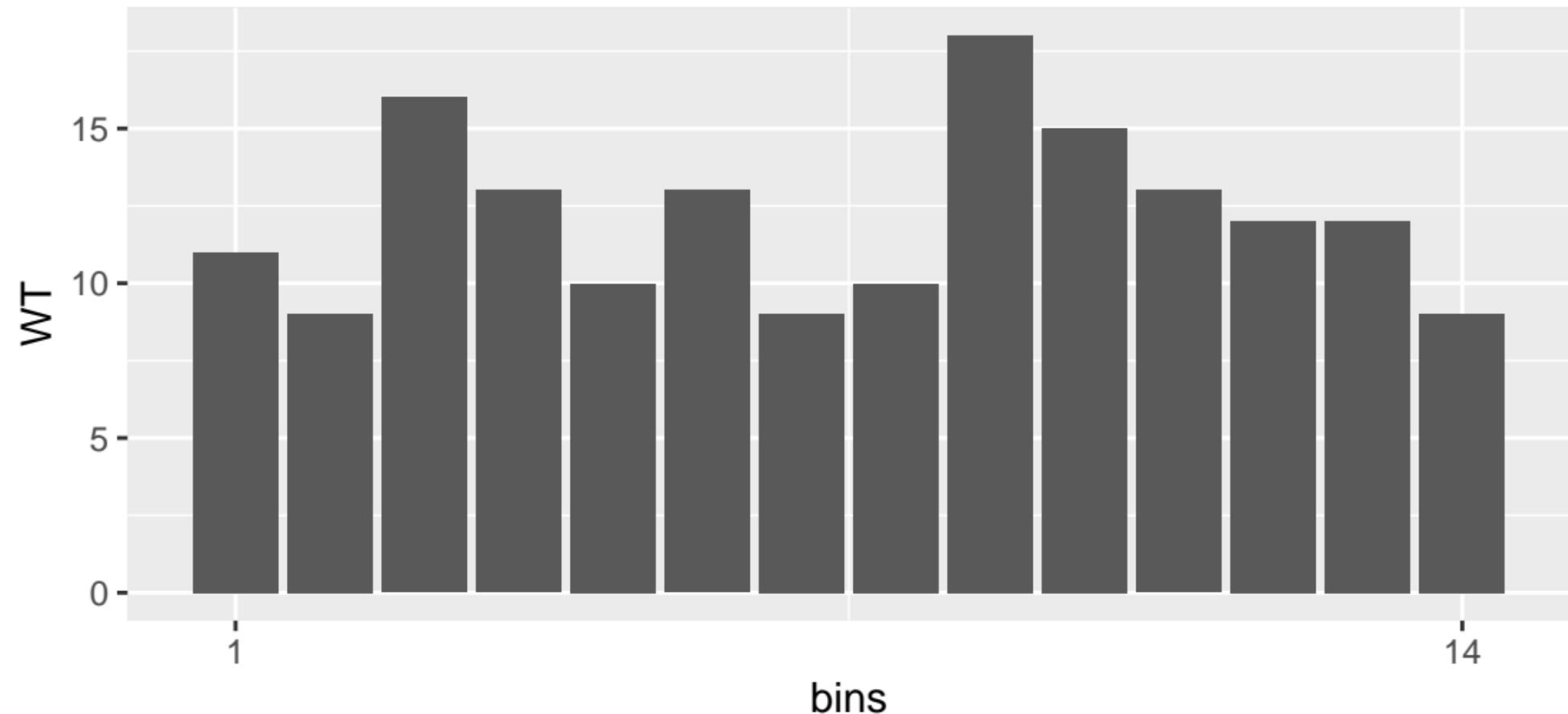
# Y20-4x1\_inference.xml



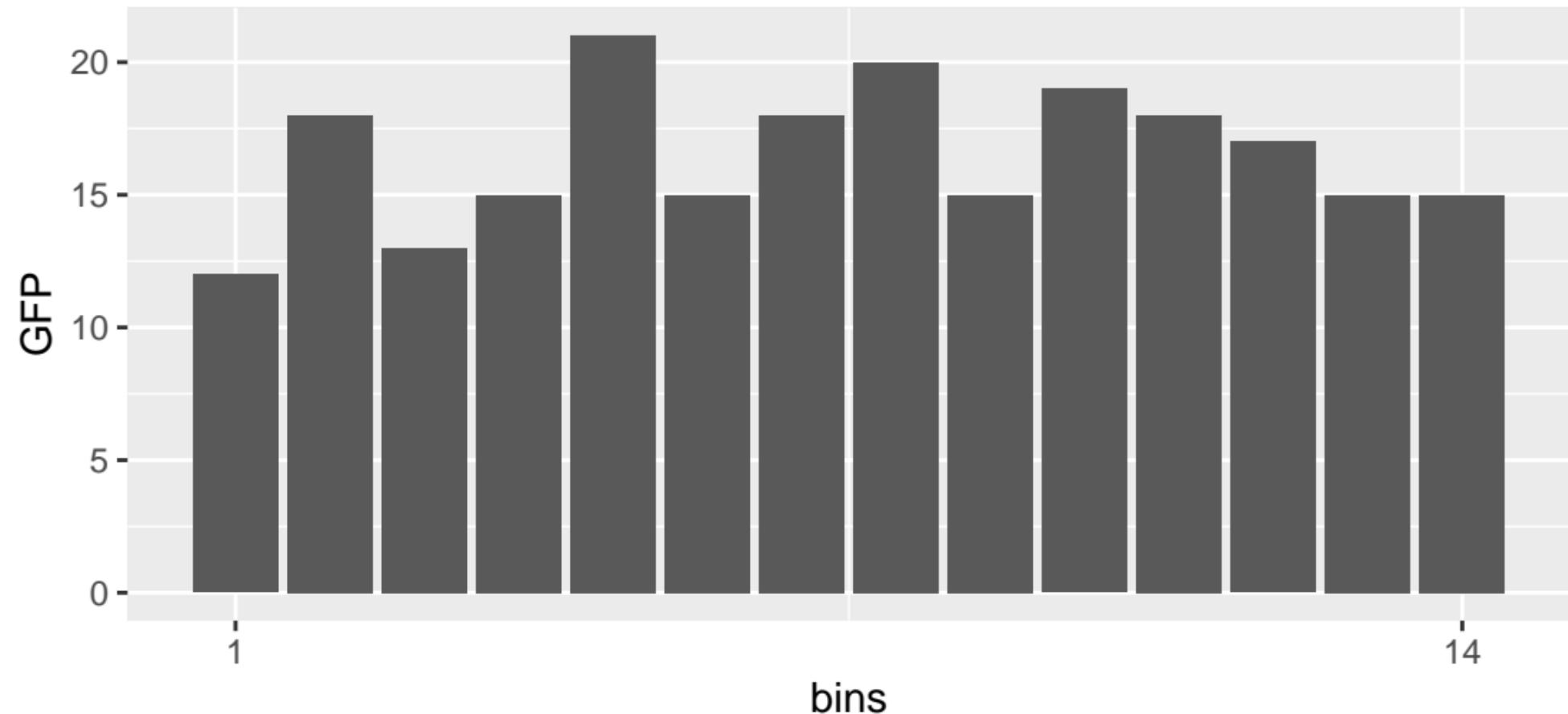
Y20-4x1\_inference.xml



# Y20-5x1\_inference.xml



# Y20-5x1\_inference.xml



Y20-5x1\_inference.xml

