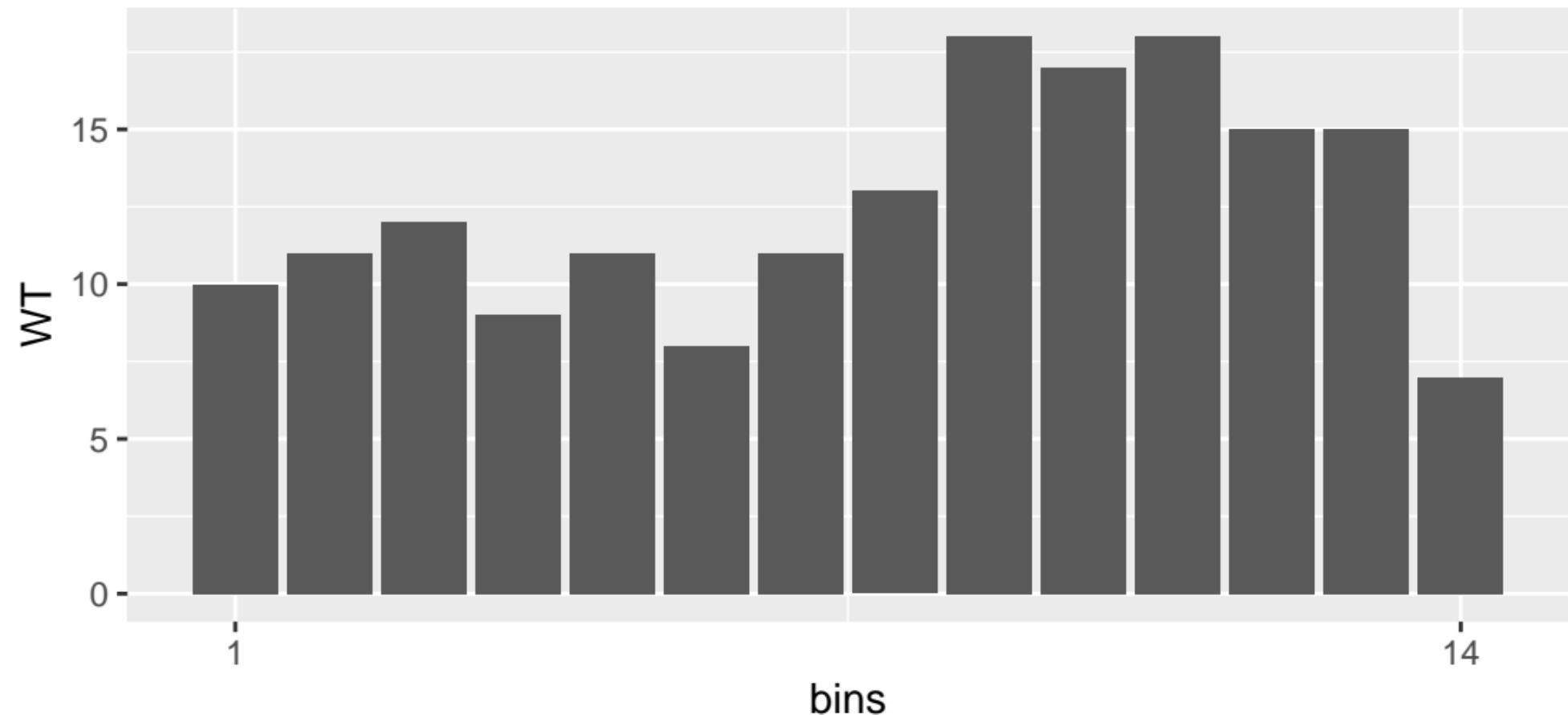
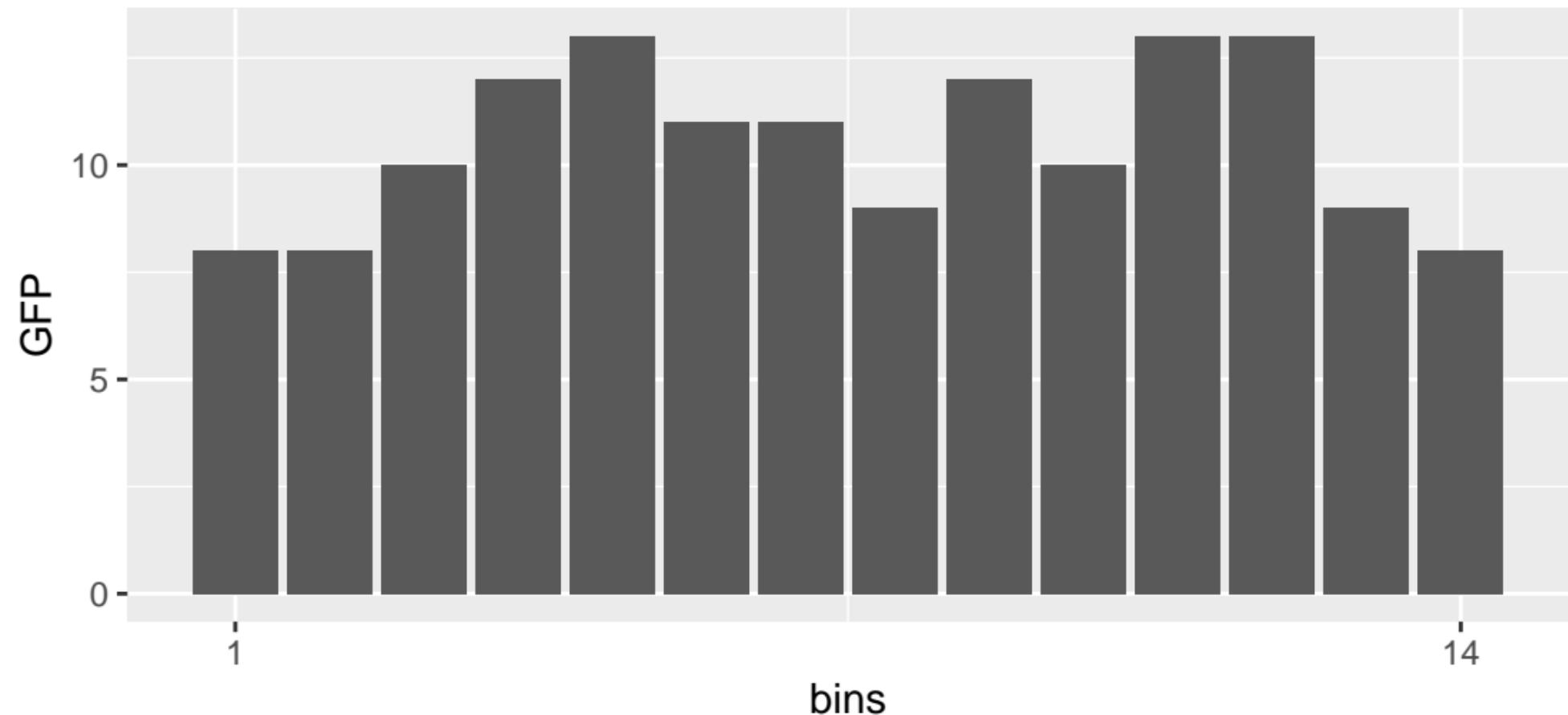


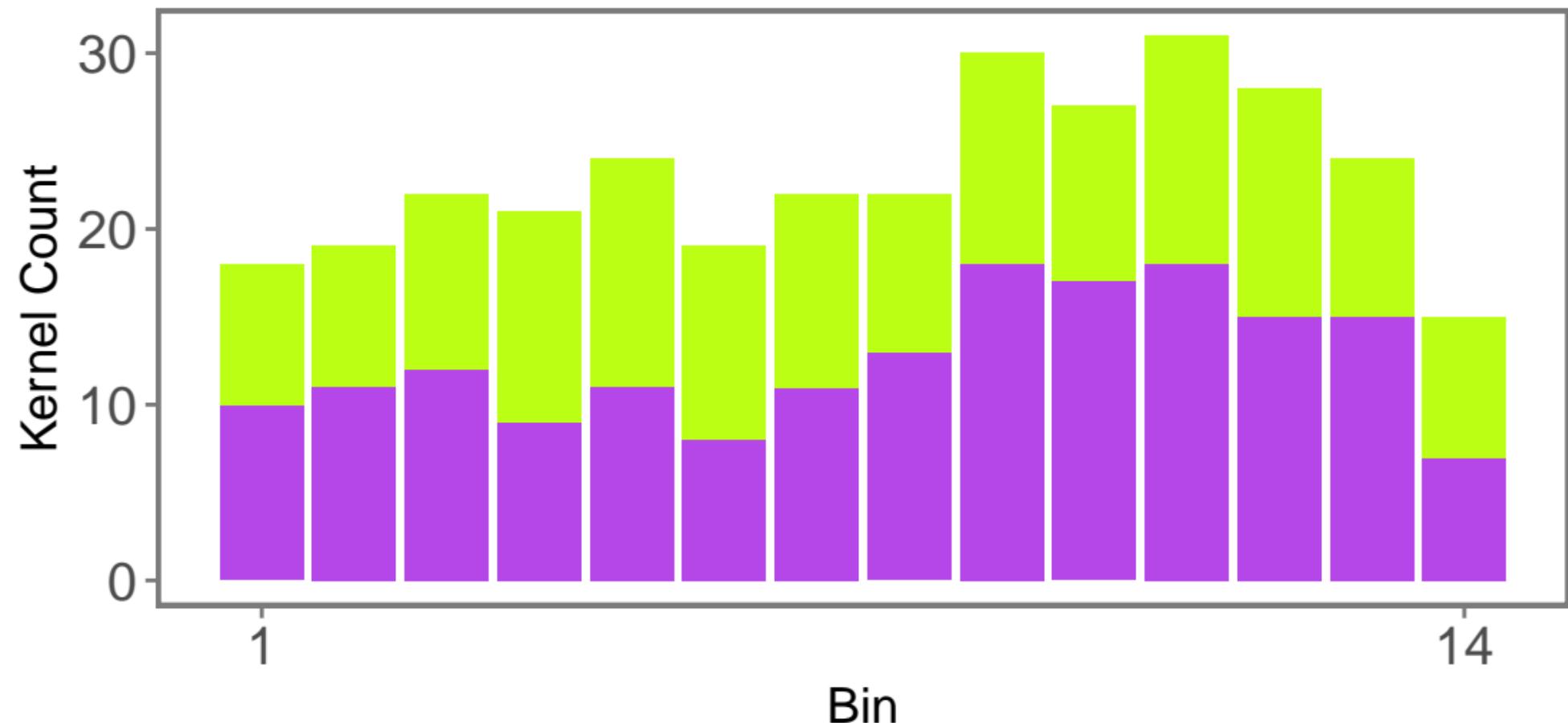
# X421-1x402\_inference.xml



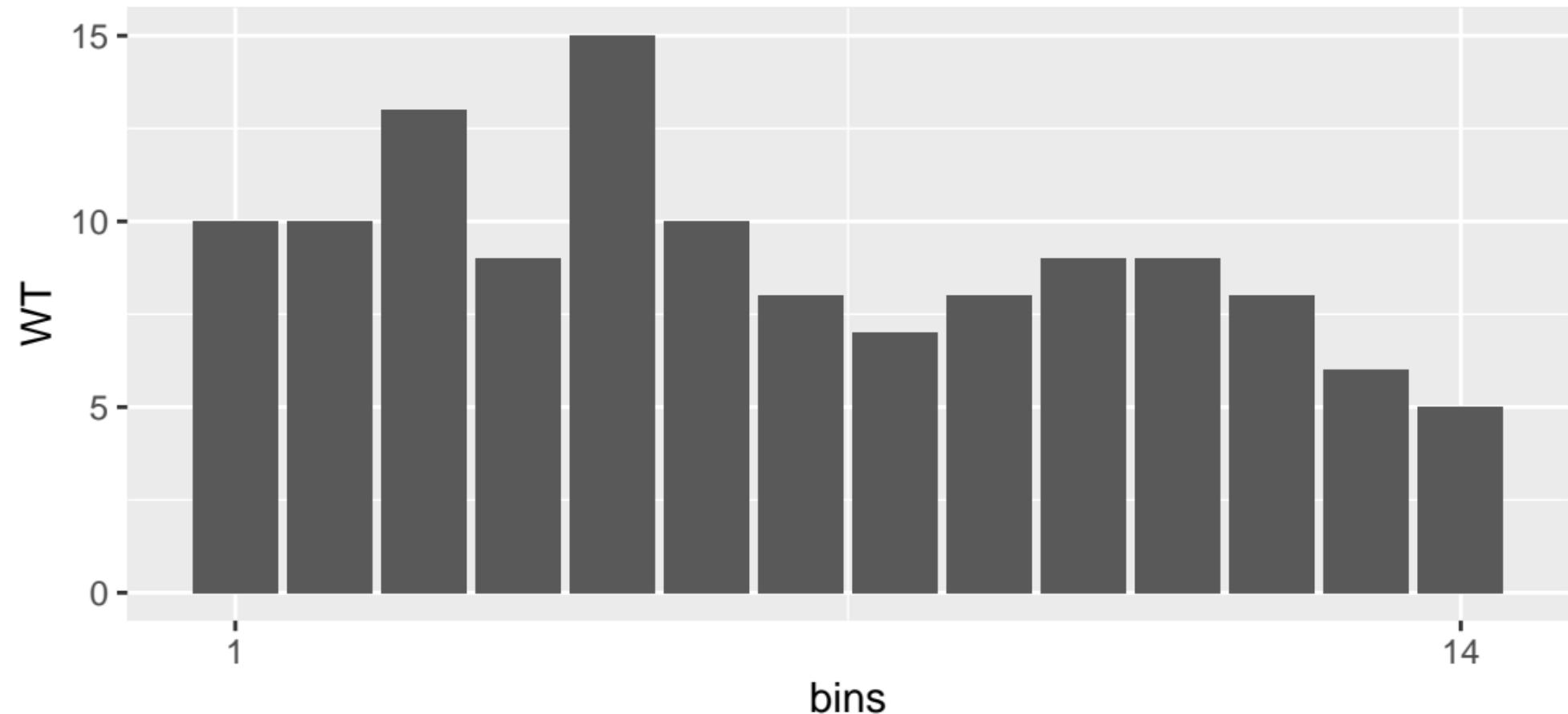
# X421-1x402\_inference.xml



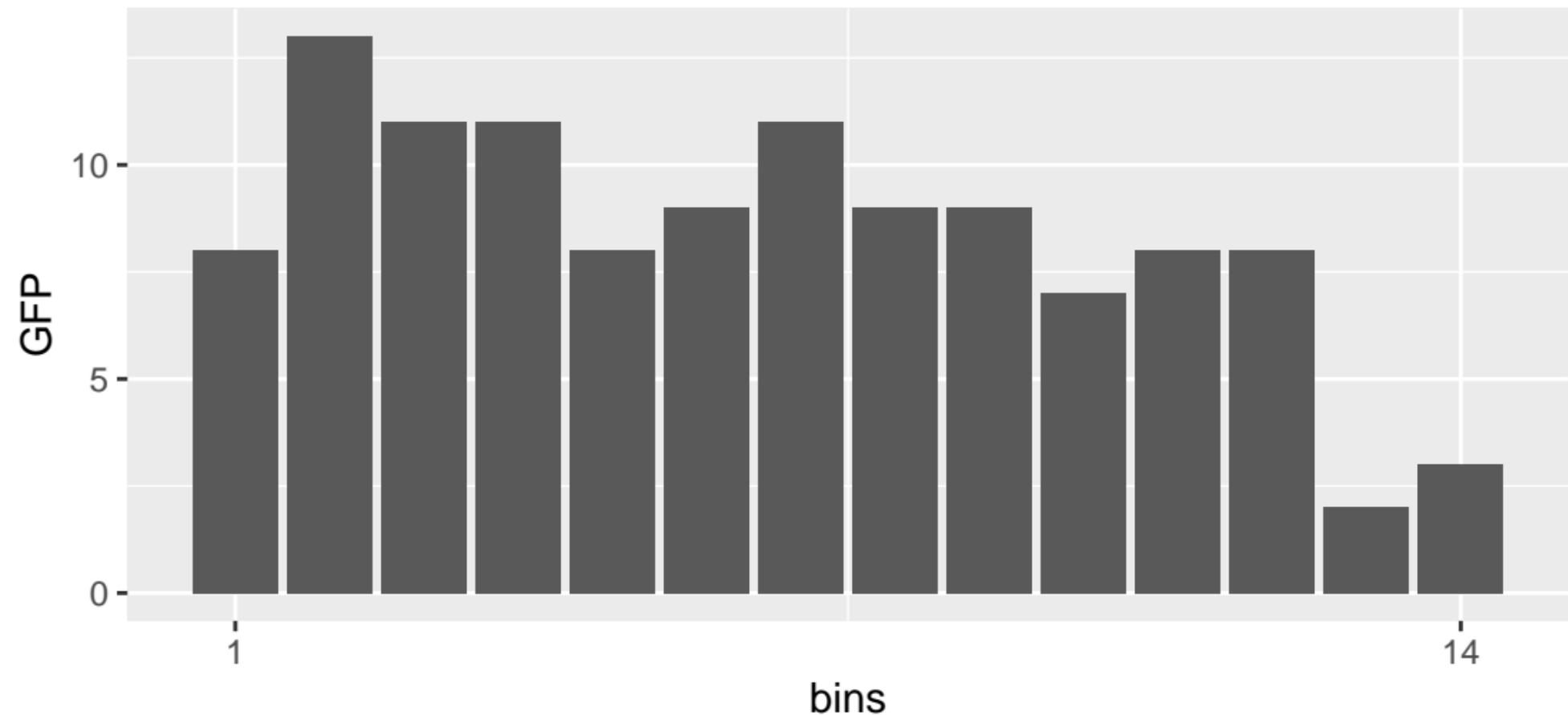
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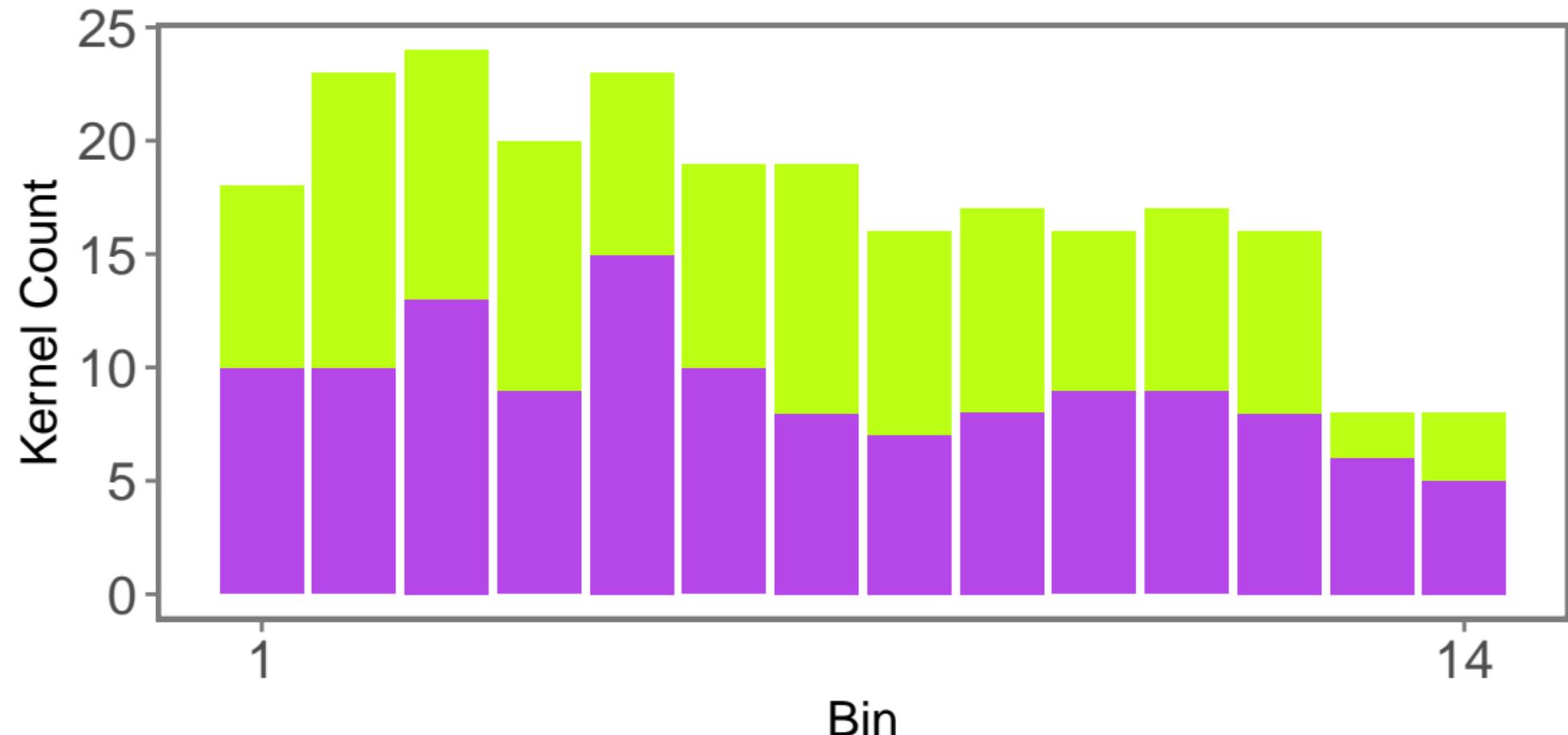
# X421-4x402\_inference.xml



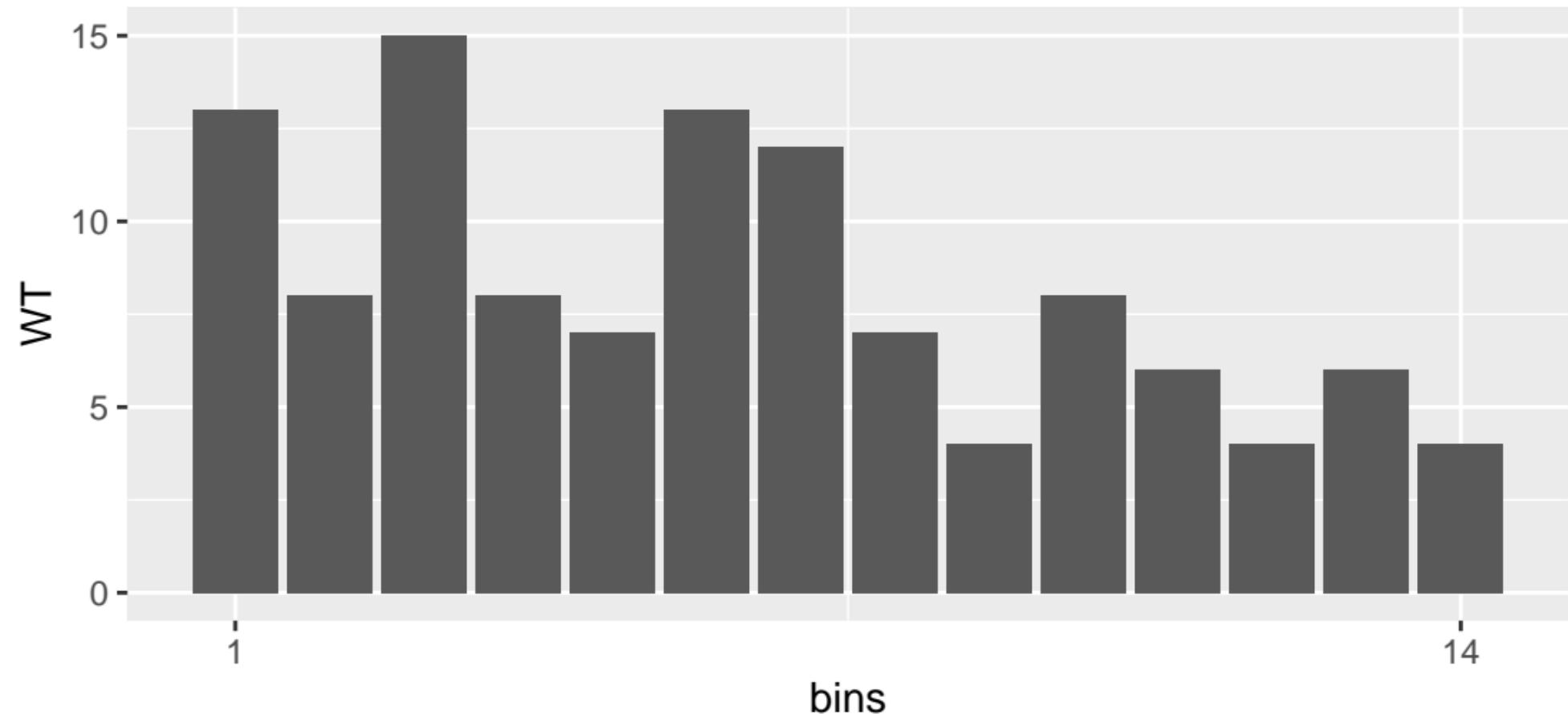
# X421-4x402\_inference.xml



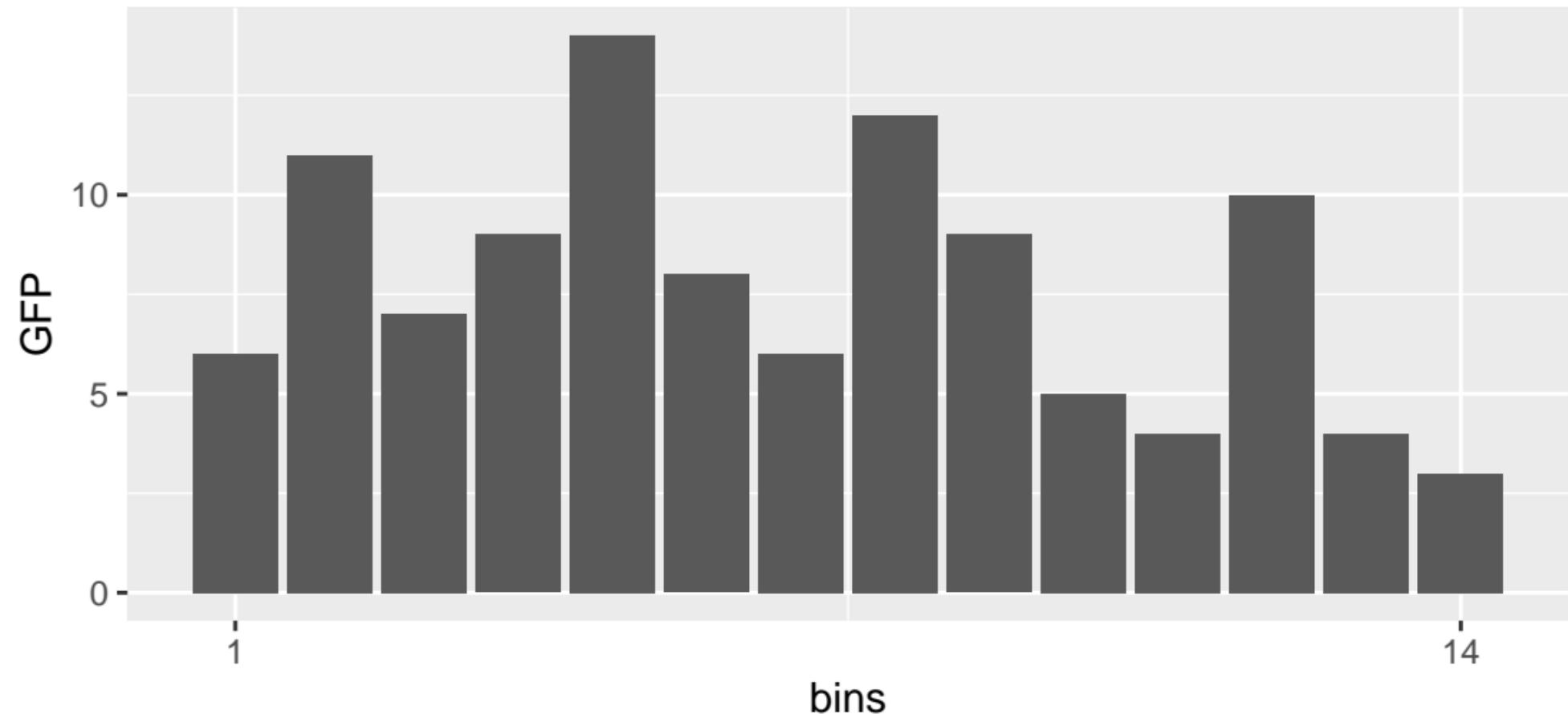
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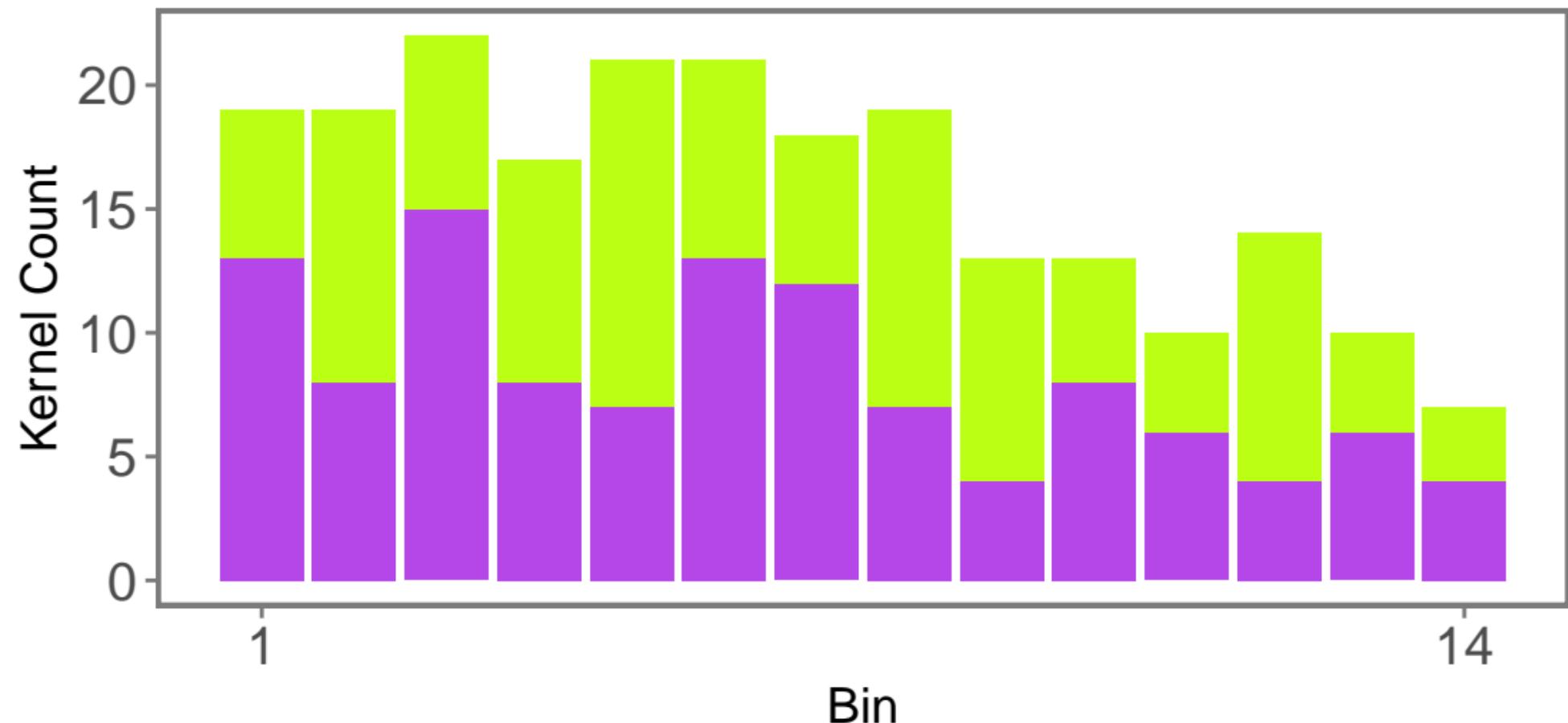
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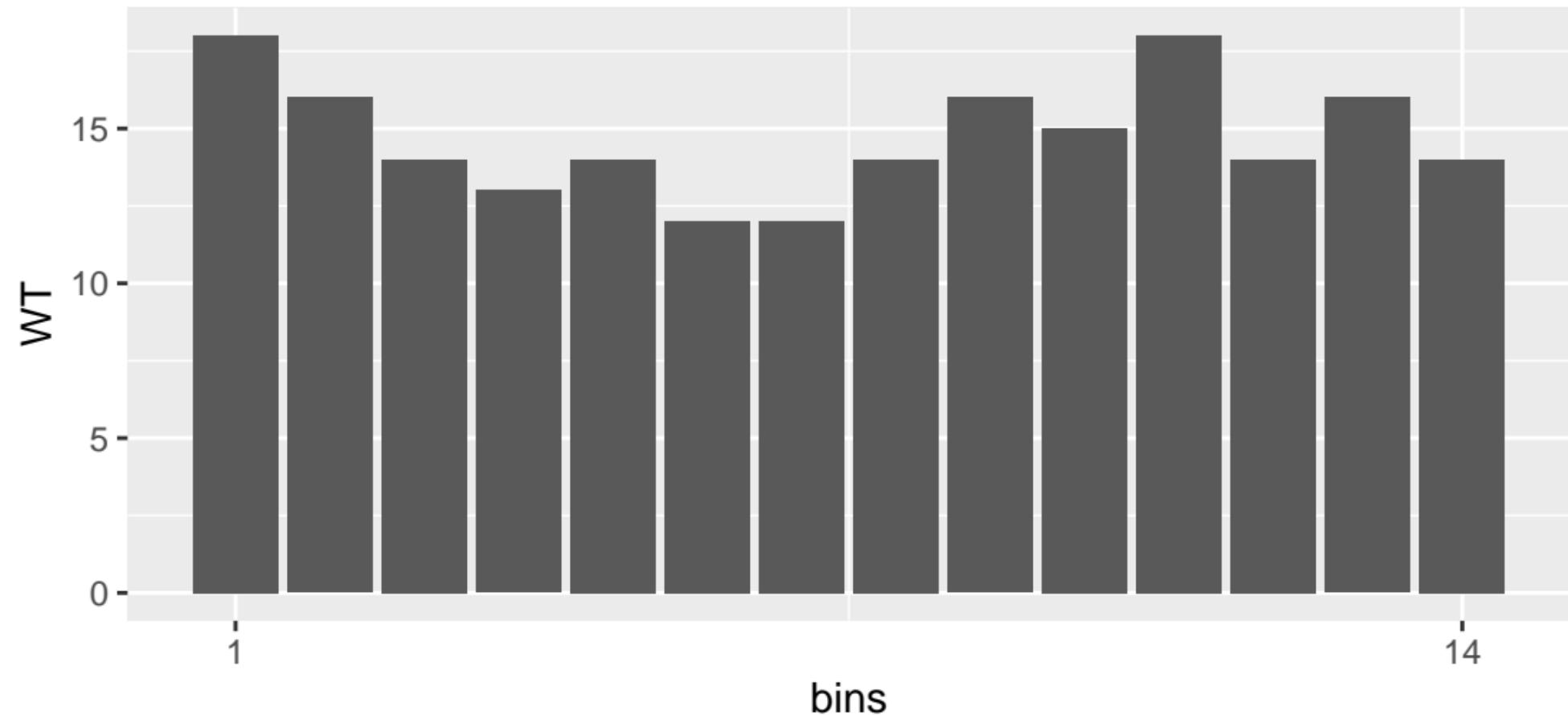
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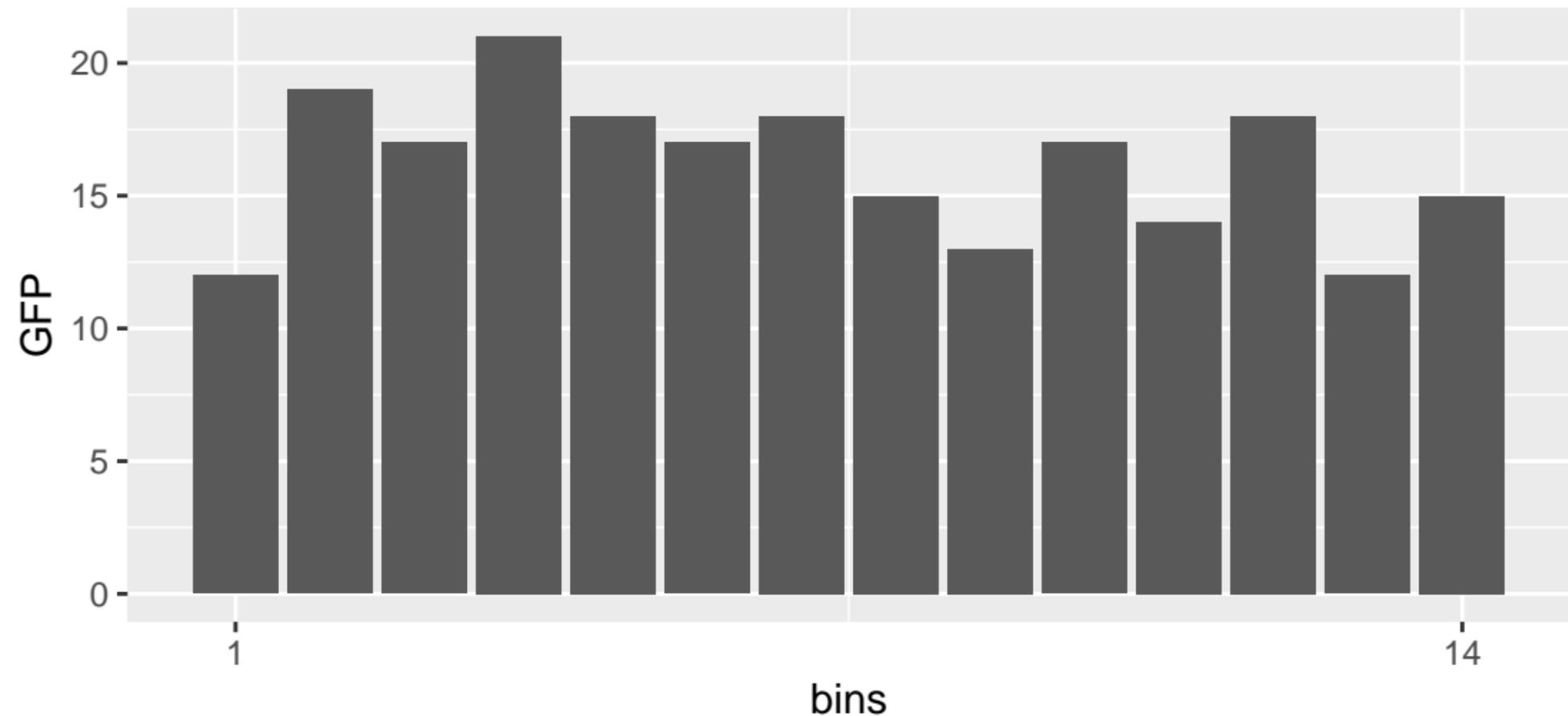
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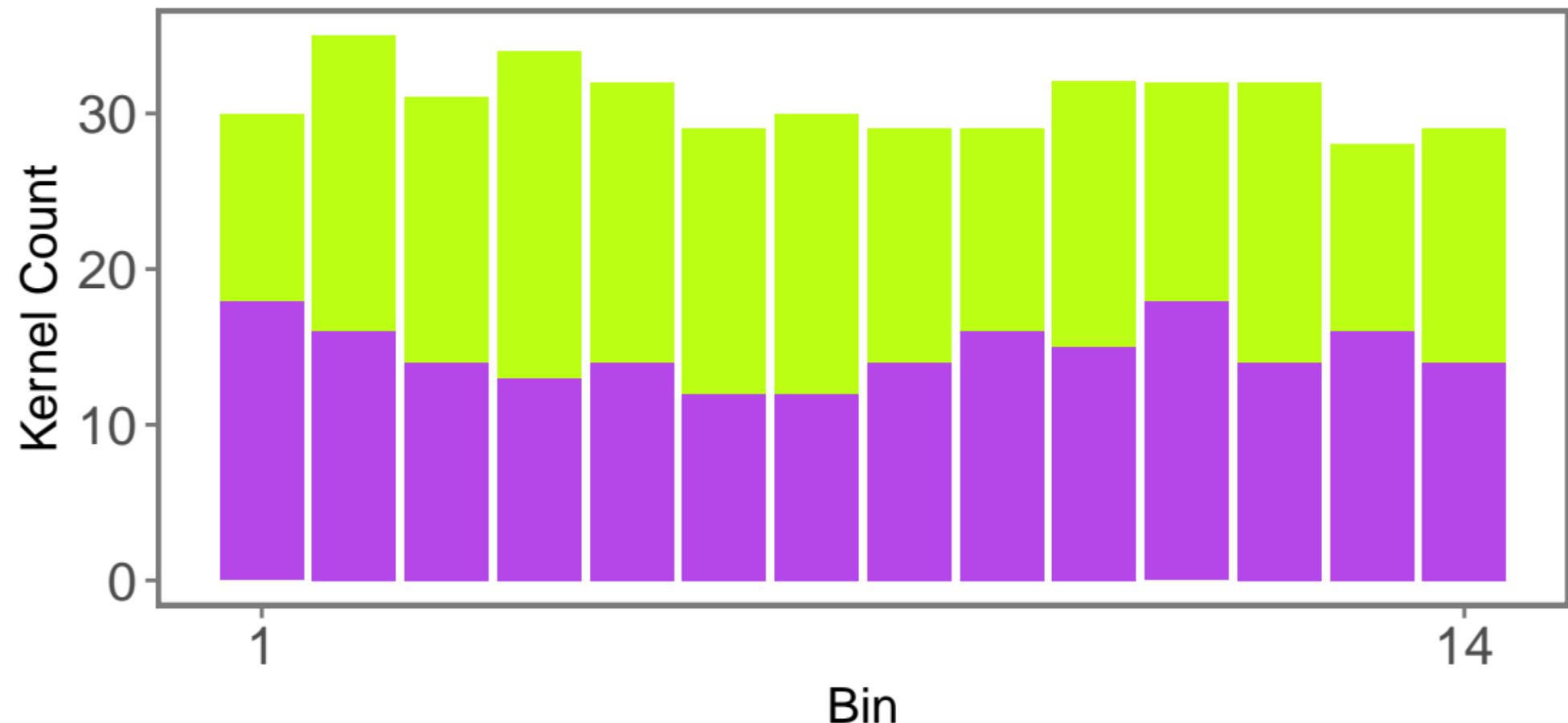
# Y34-1x1\_inference.xml



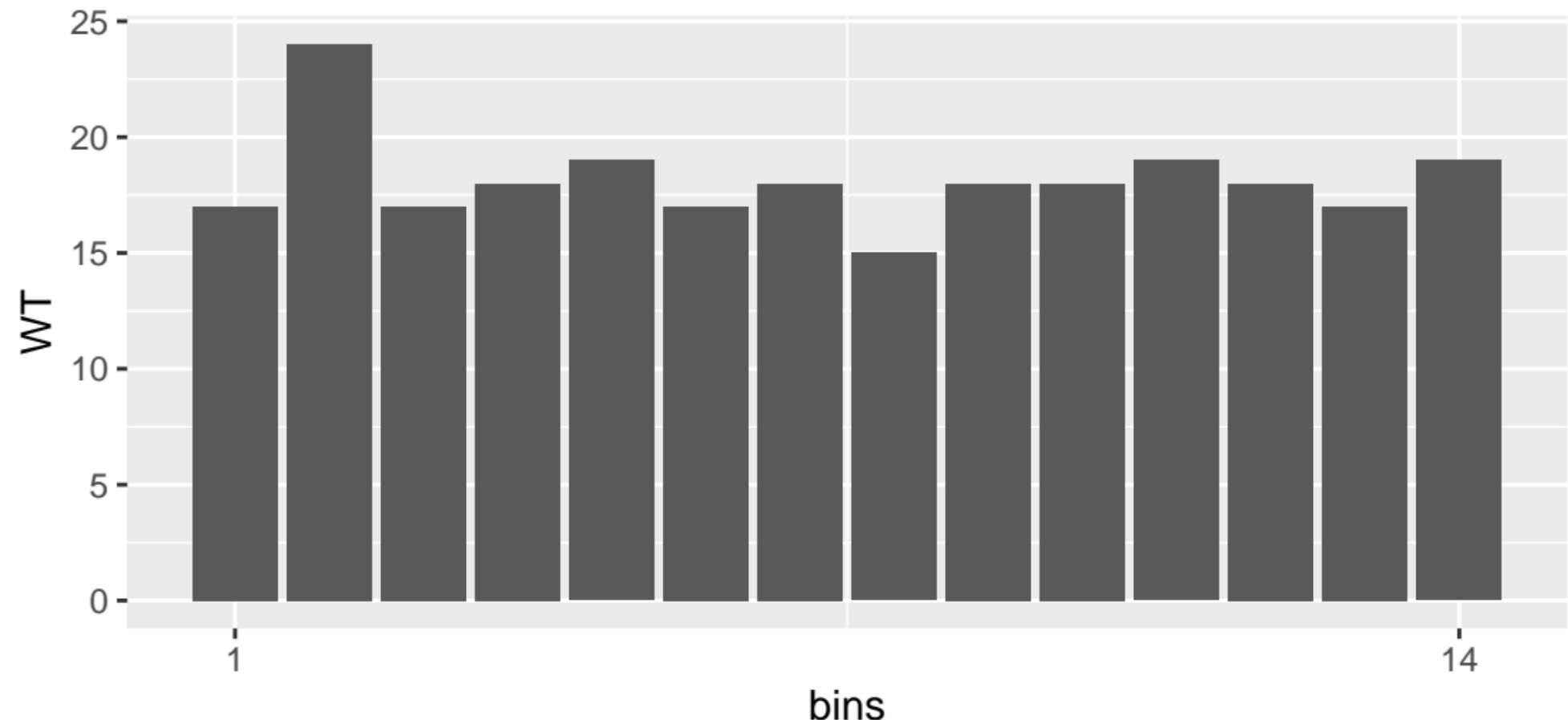
# Y34-1x1\_inference.xml



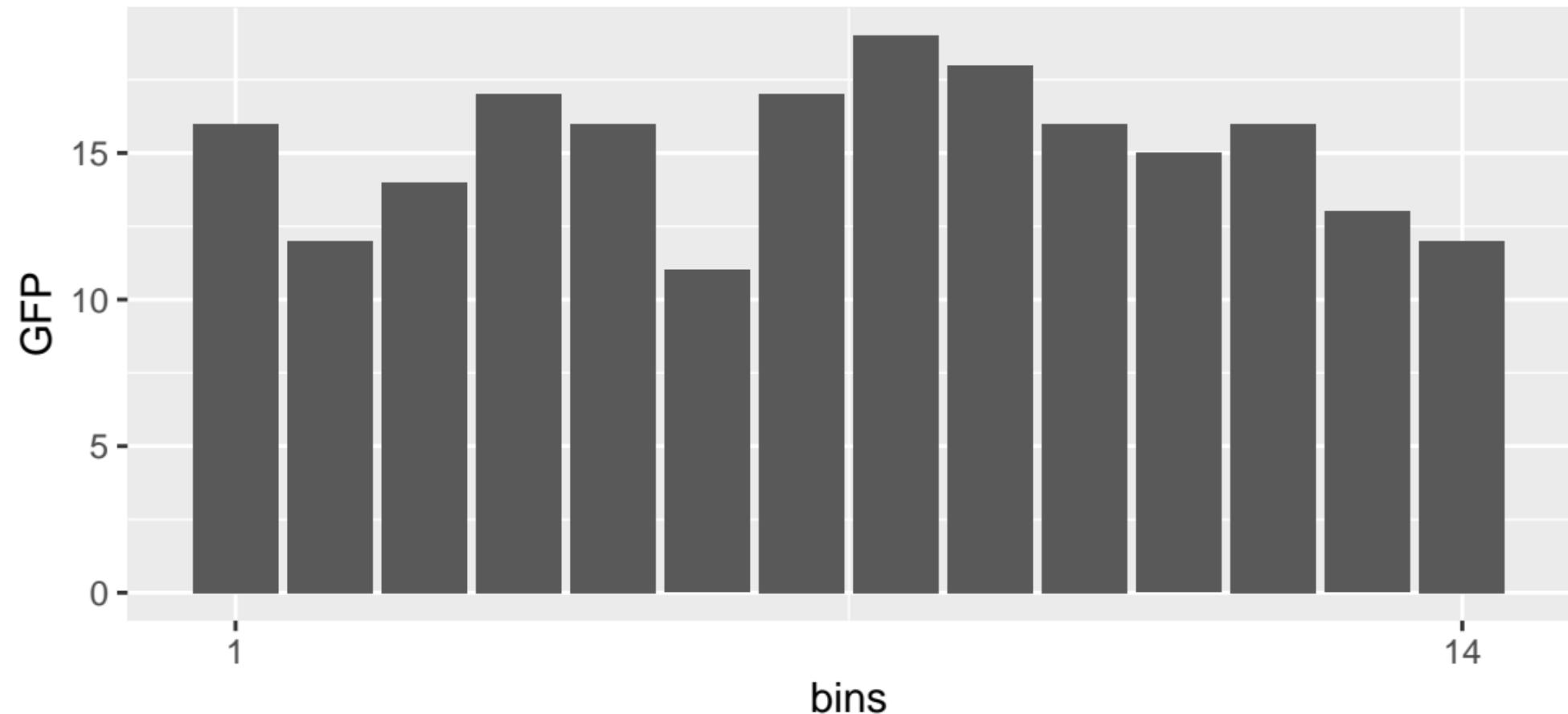
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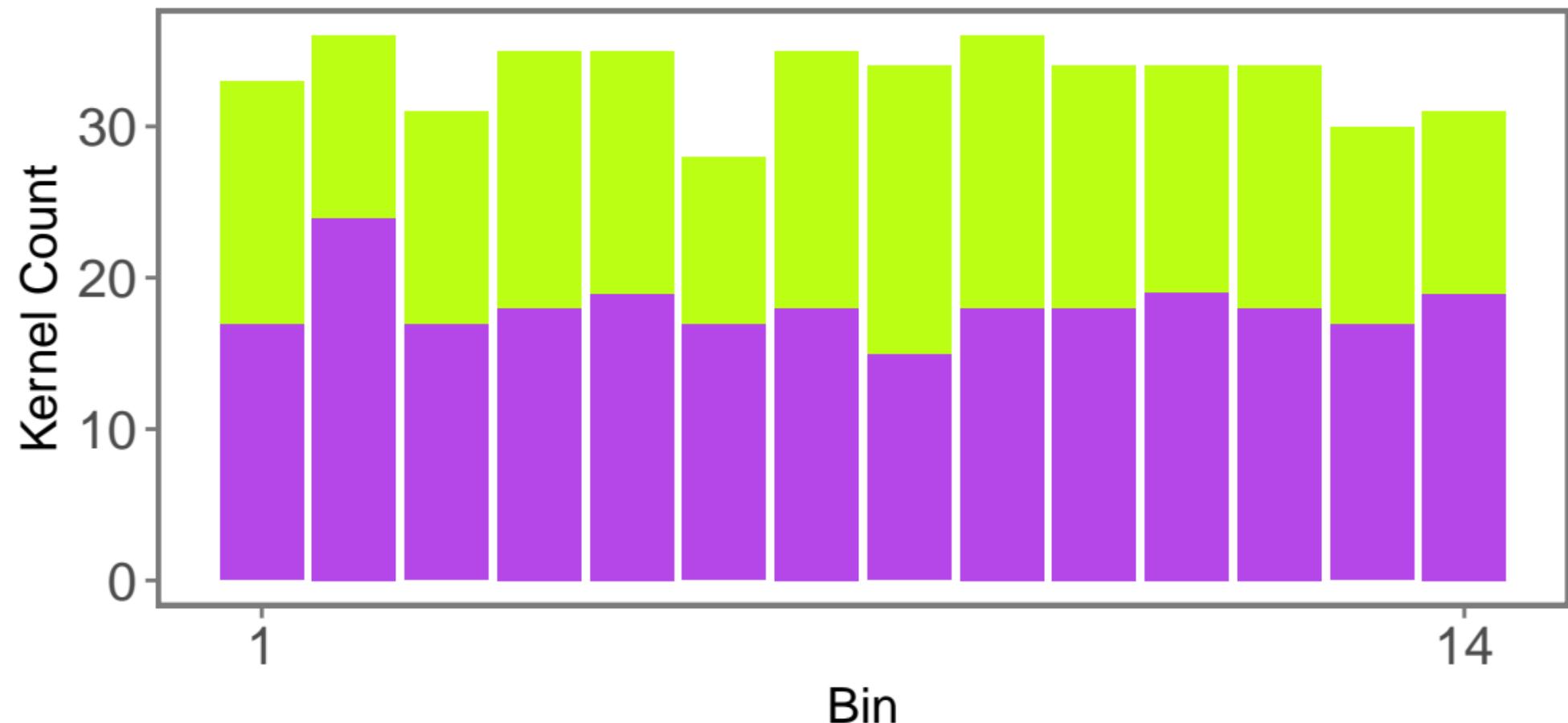
# Y34-2x1\_inference.xml



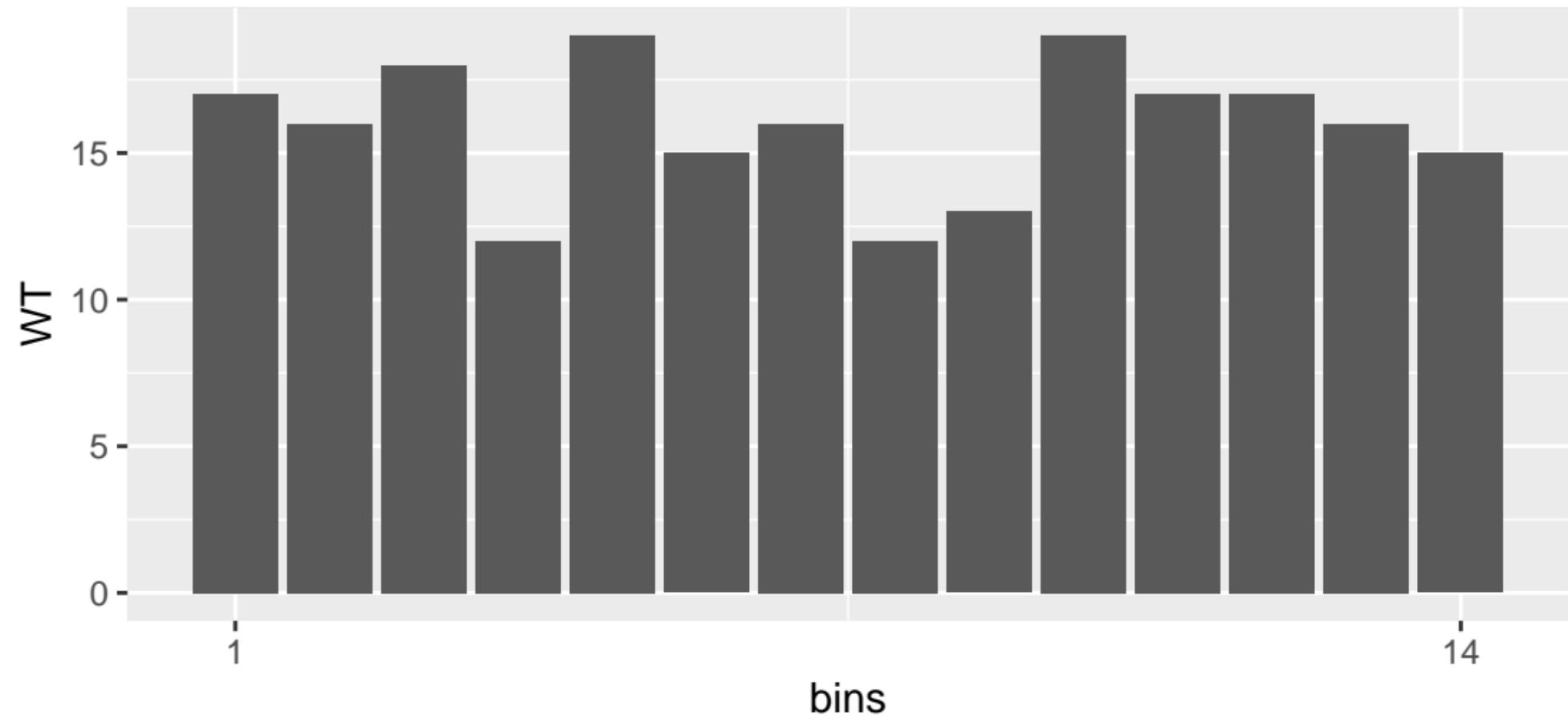
# Y34-2x1\_inference.xml



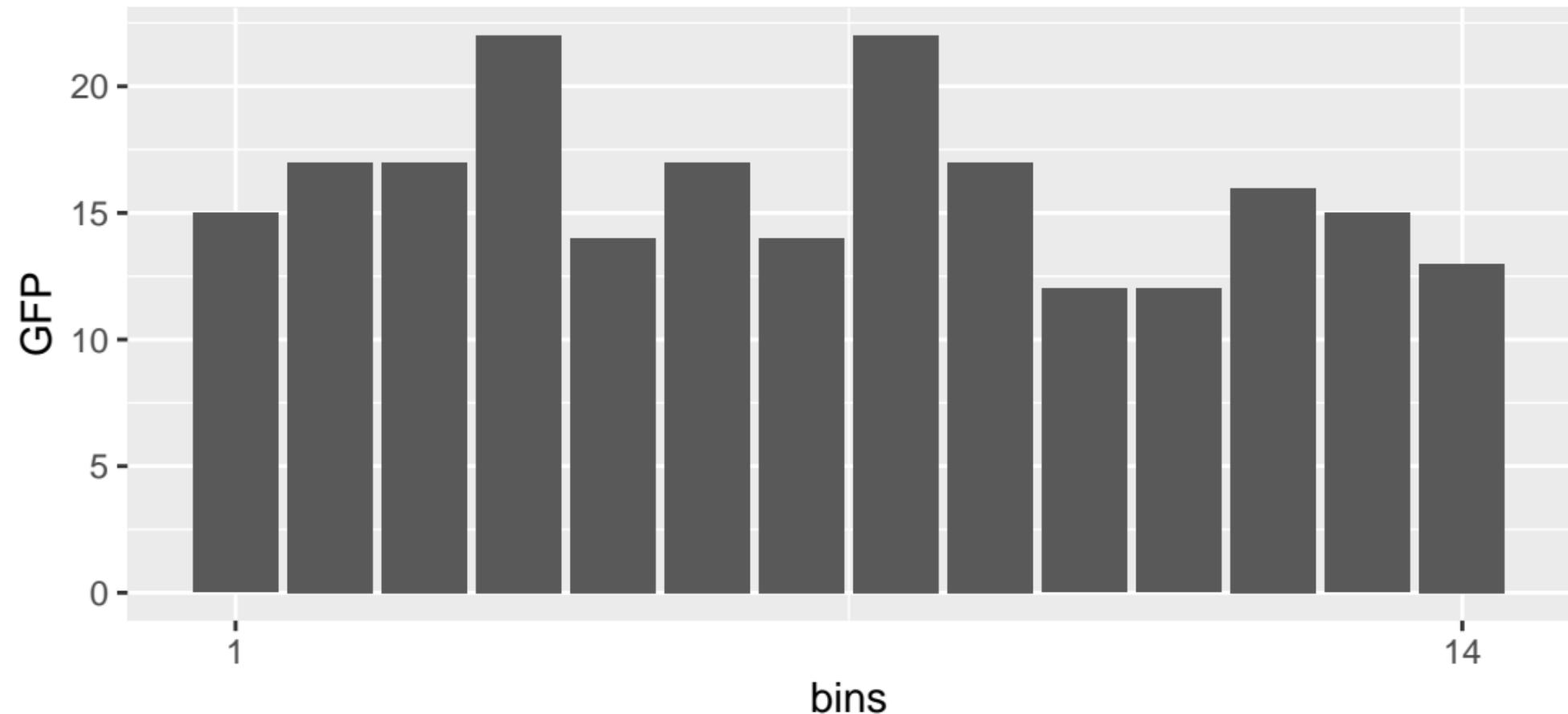
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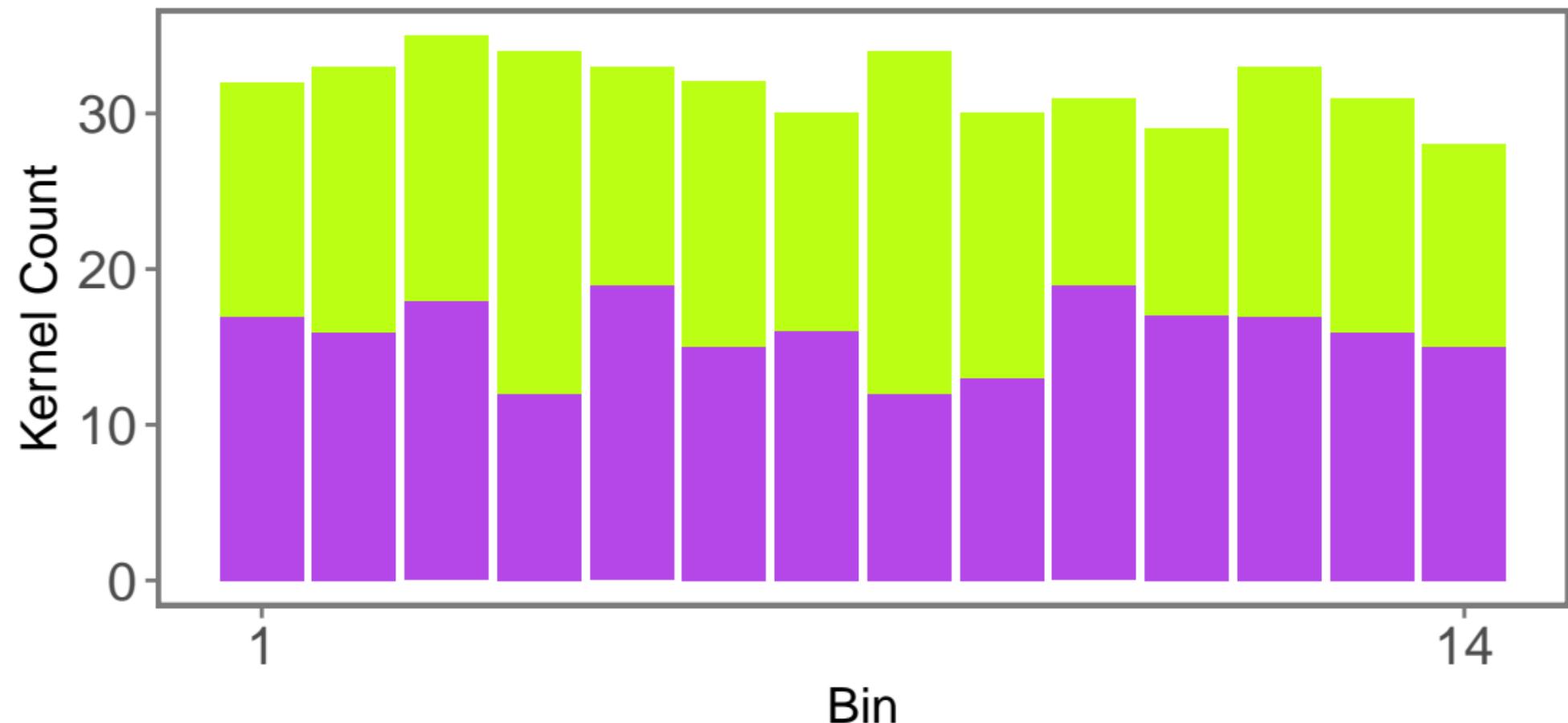
# Y34-3x1\_inference.xml



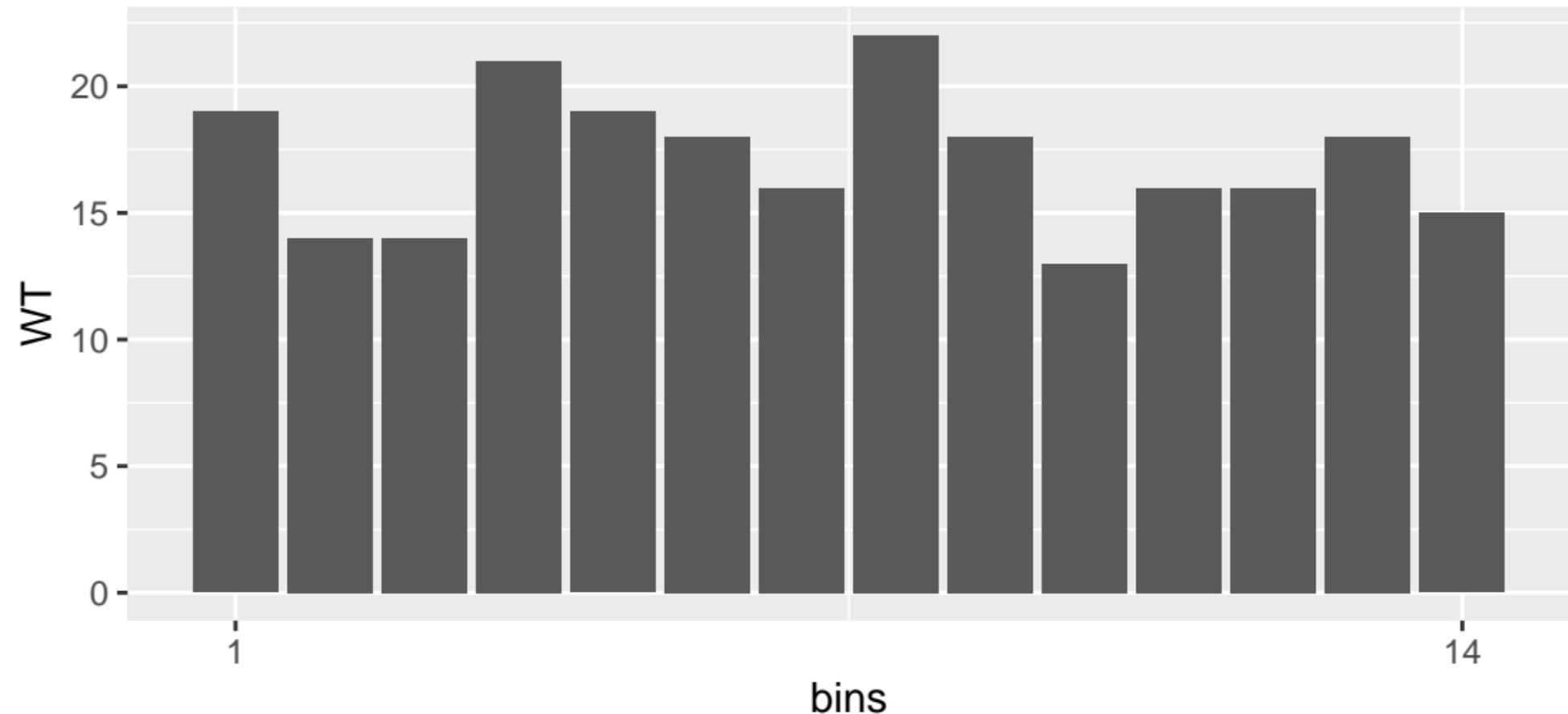
# Y34-3x1\_inference.xml



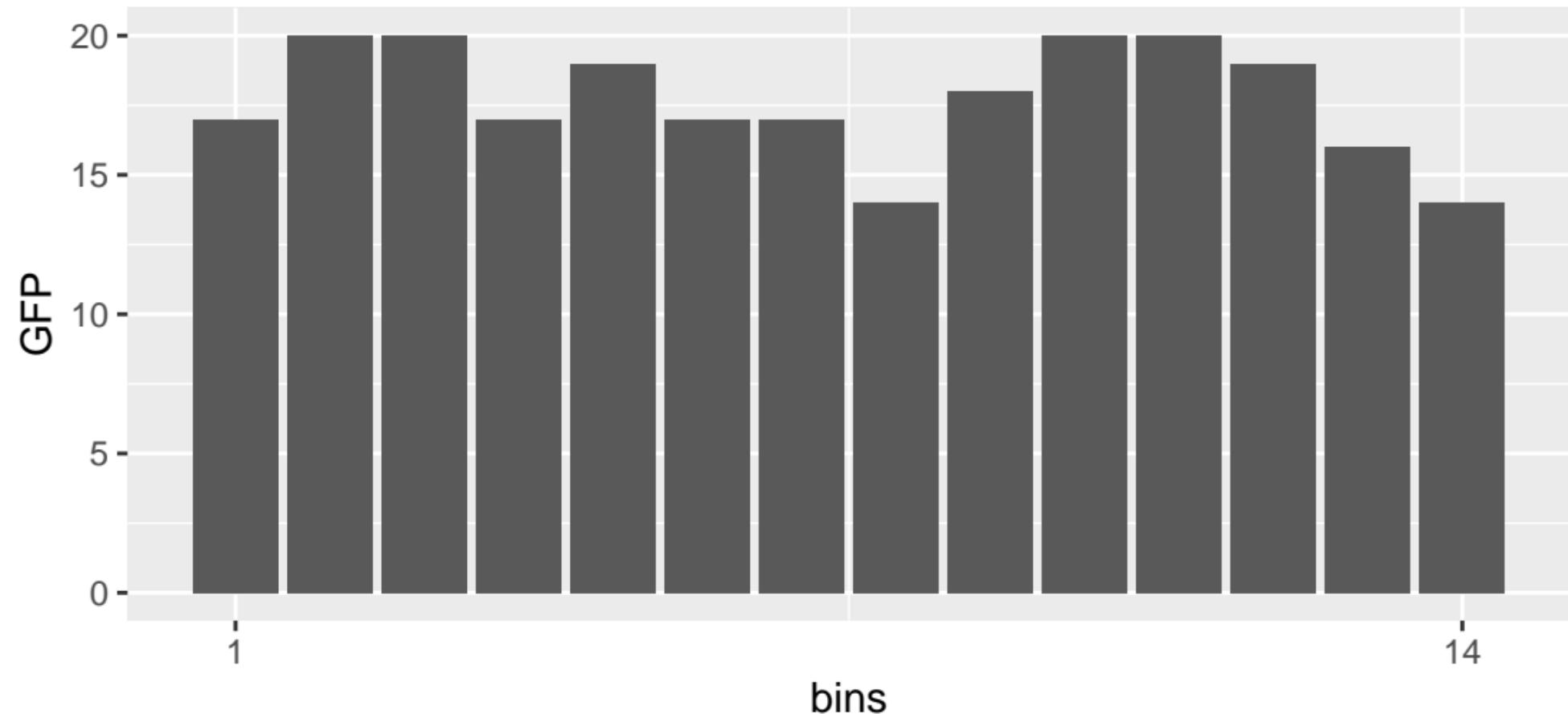
Y34-3x1\_inference.xml



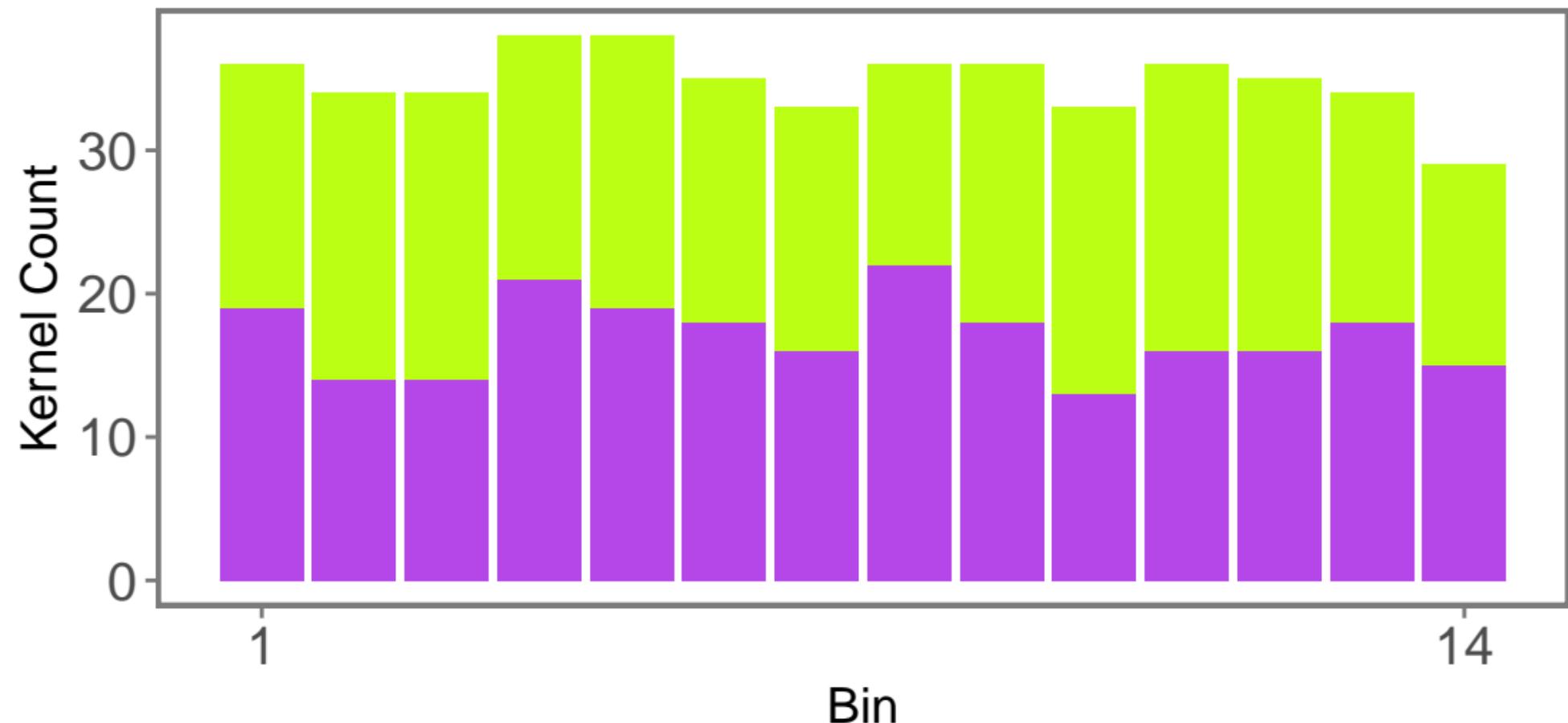
# Y34-5x1\_inference.xml



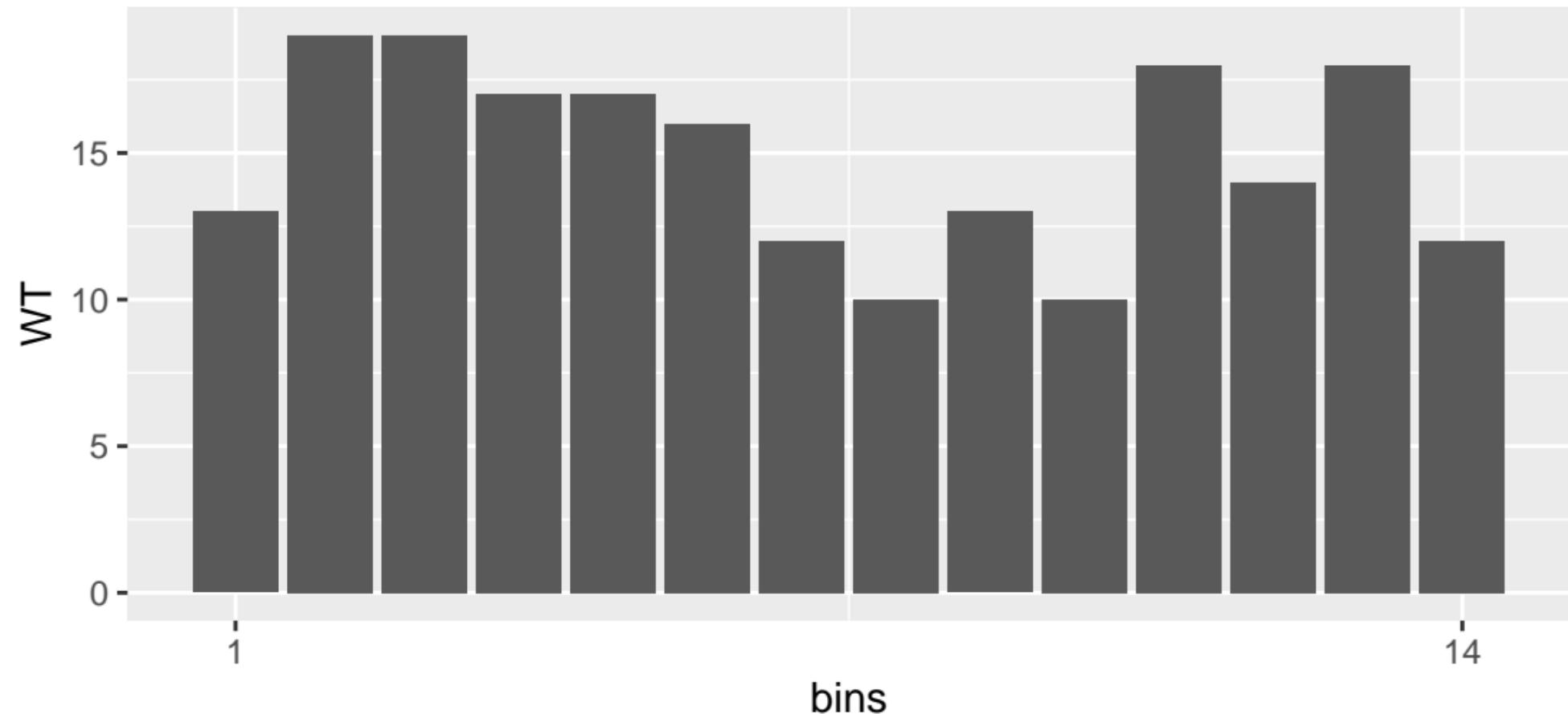
# Y34-5x1\_inference.xml



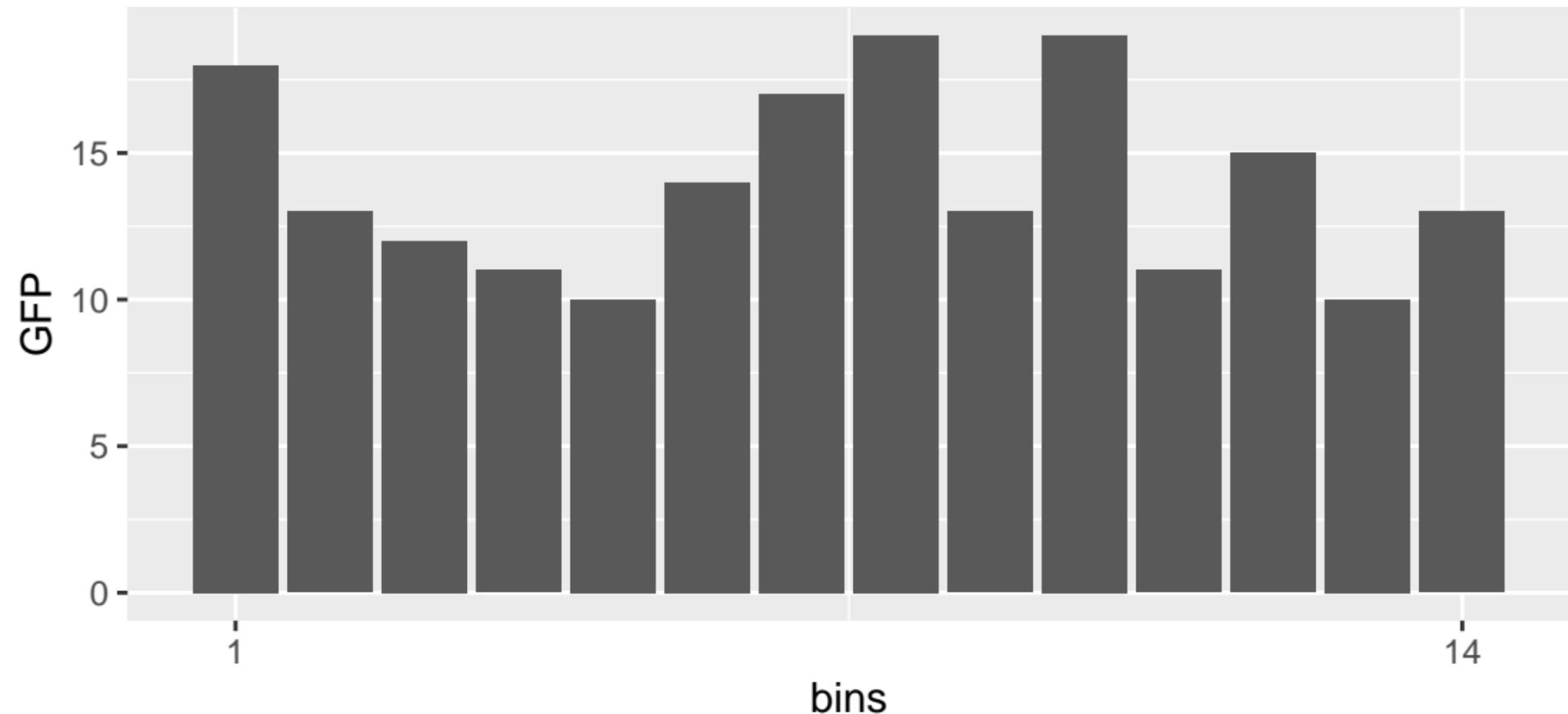
Y34-5x1\_inference.xml



# Y34-6x1\_inference.xml



# Y34-6x1\_inference.xml



Y34-6x1\_inference.xml

