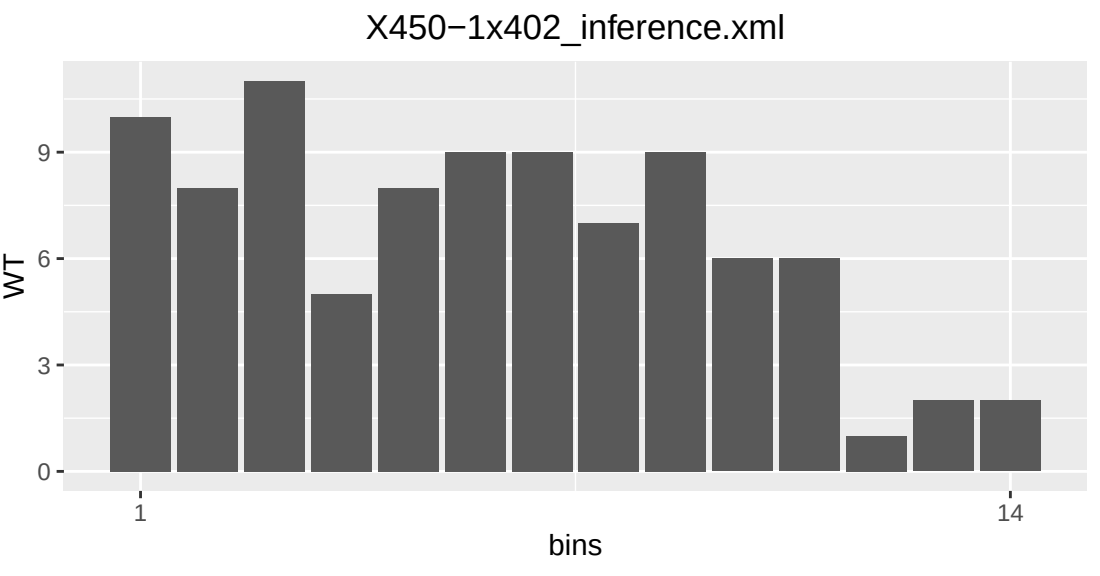
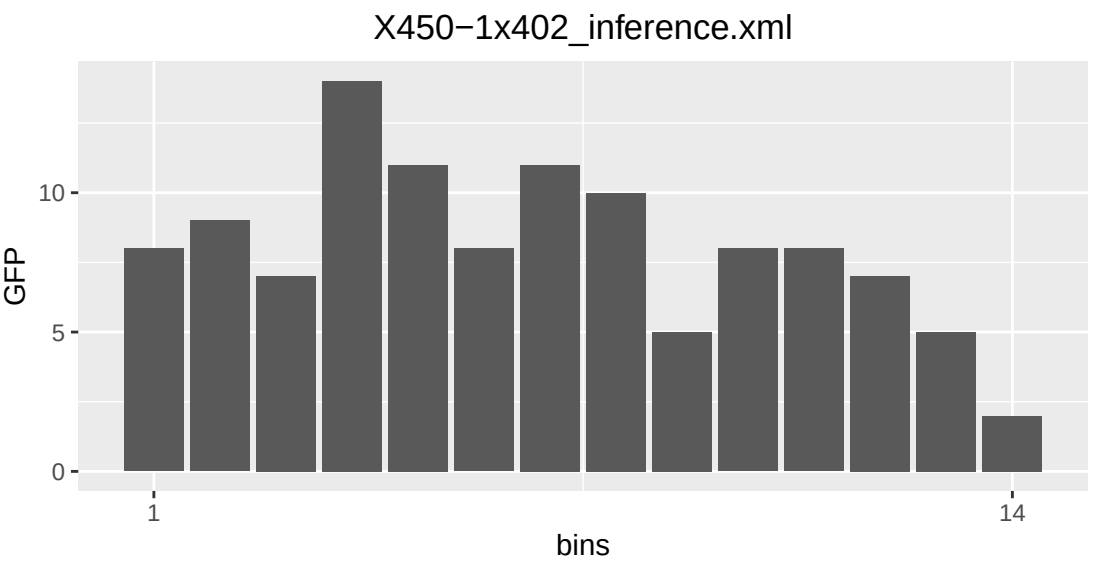
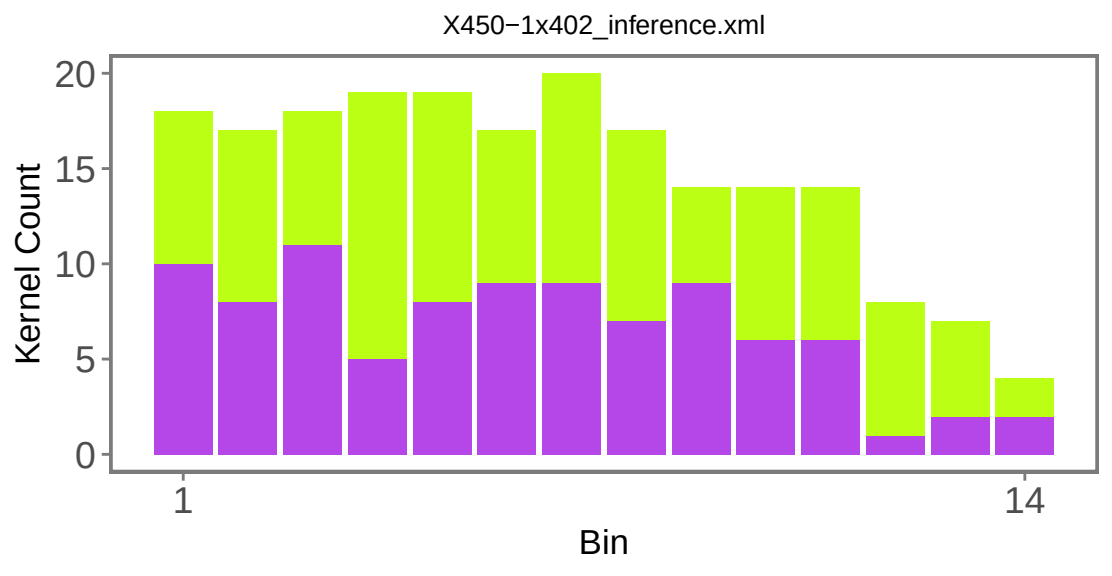
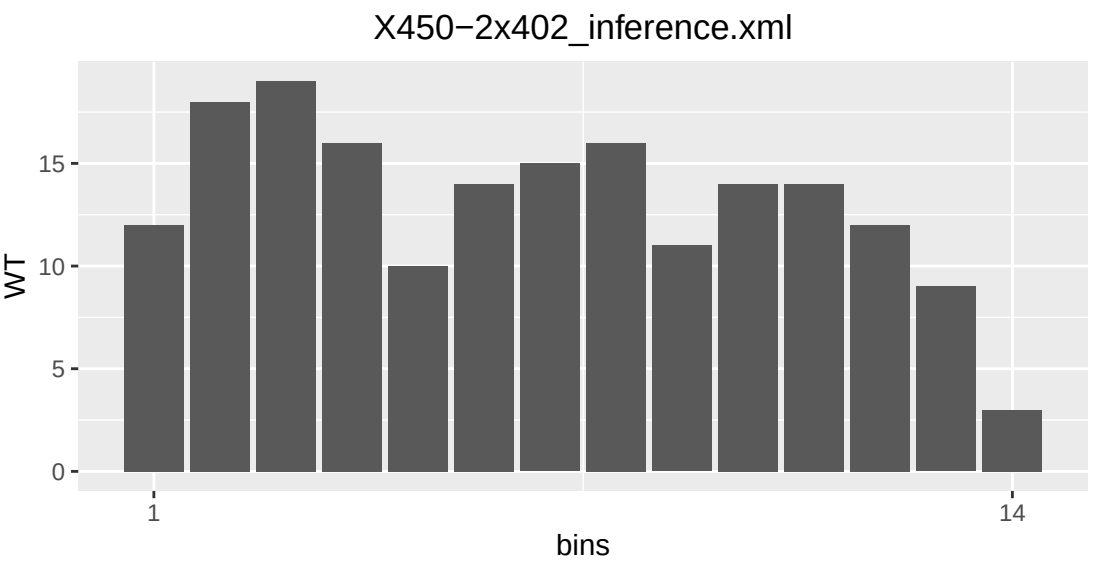
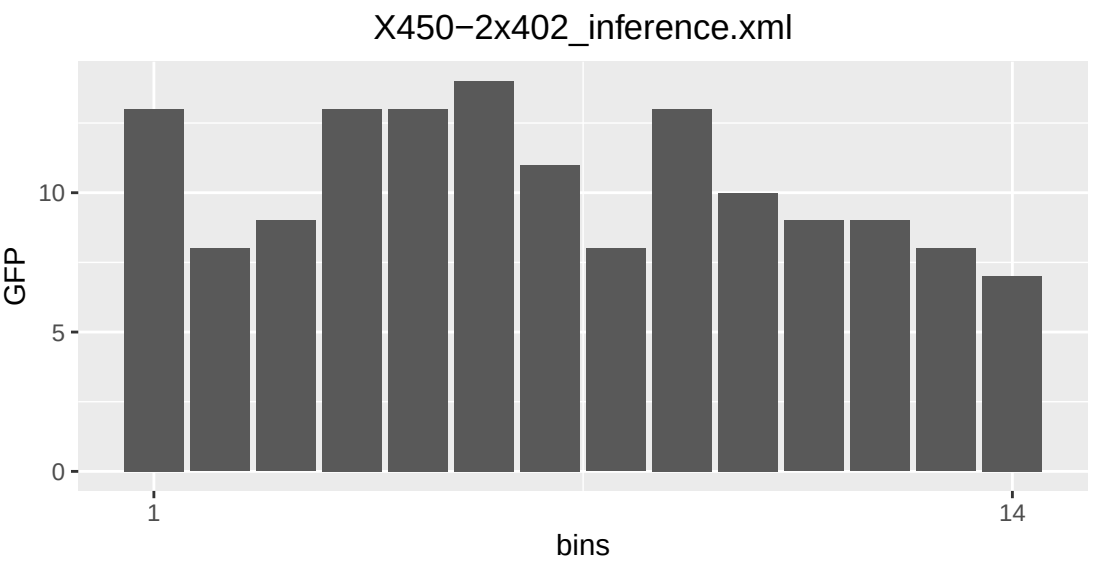
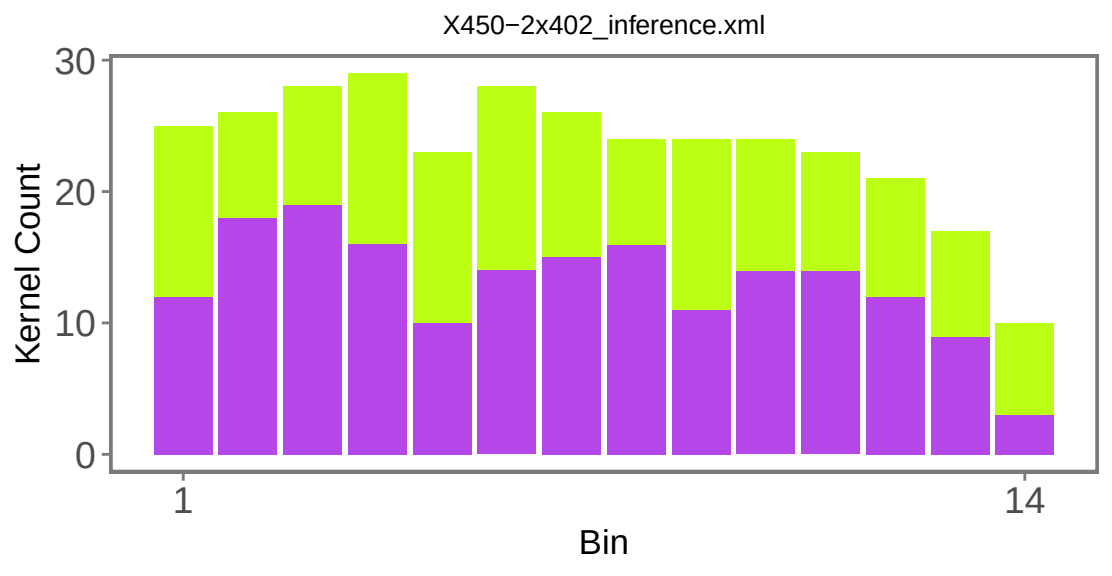

X450−1x402_inference.xml
WT
9
6
3
0
1
14
bins

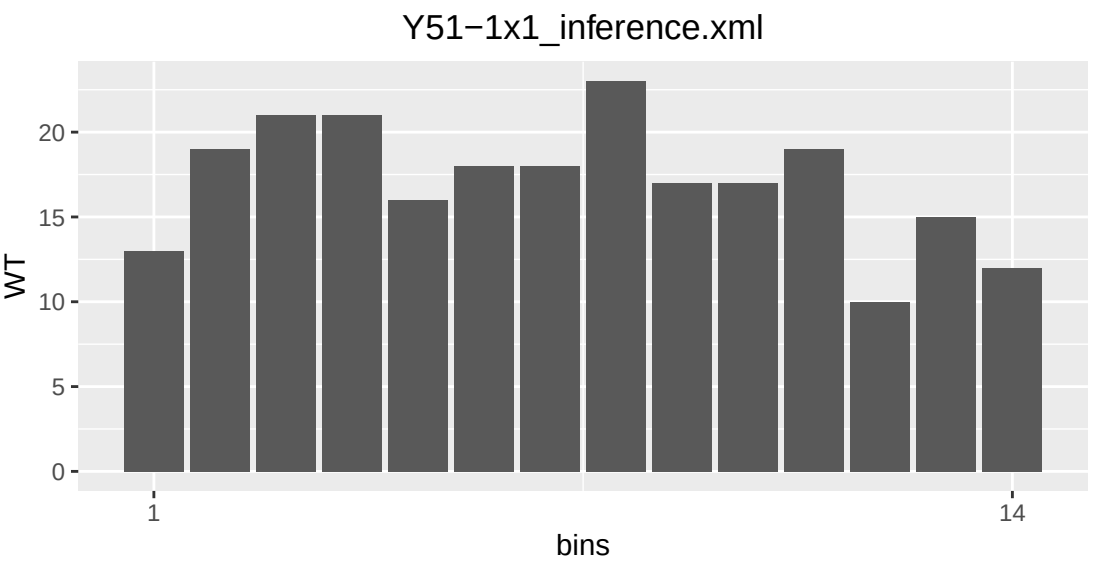

X450−1x402_inference.xml
GFP
10
5
0
1
14
bins

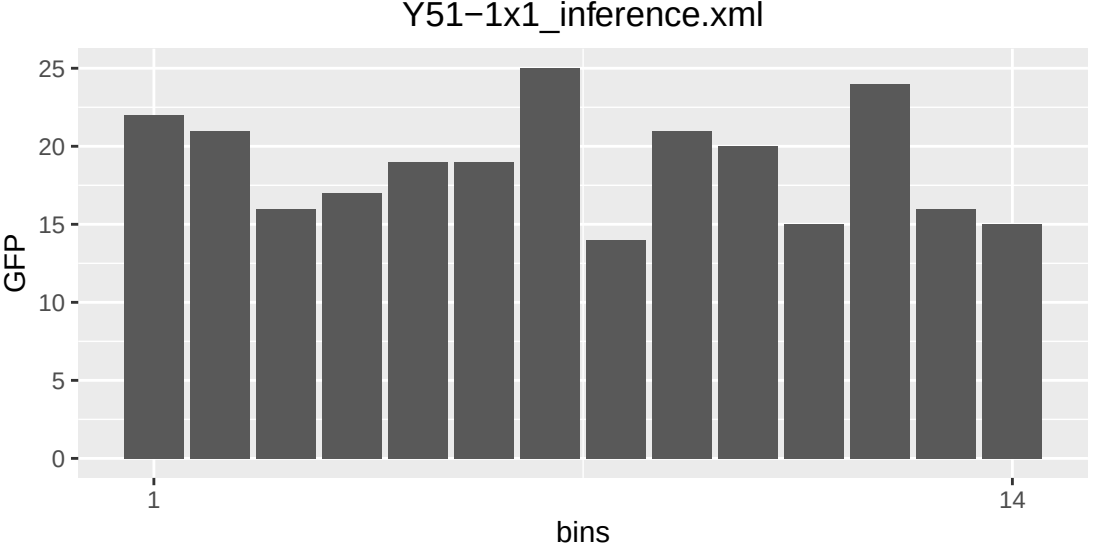

X450−1x402_inference.xml
Kernel Count
20
15
10
5
0
1
14
Bin

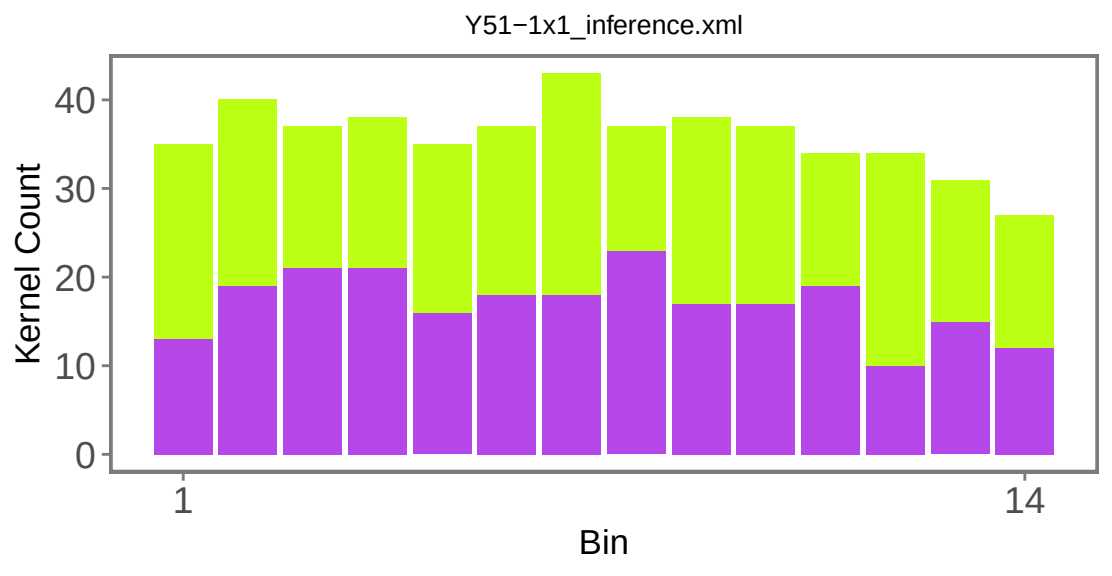

X450−2x402_inference.xml
WT
15
10
5
0
1
14
bins

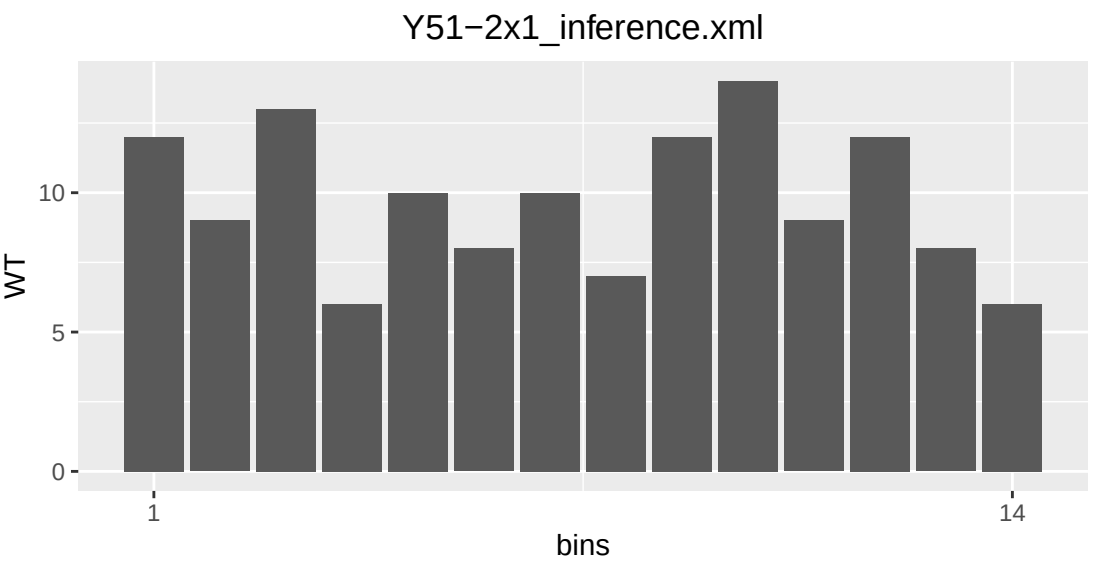

X450−2x402_inference.xml
GFP
10
5
0
1
14
bins

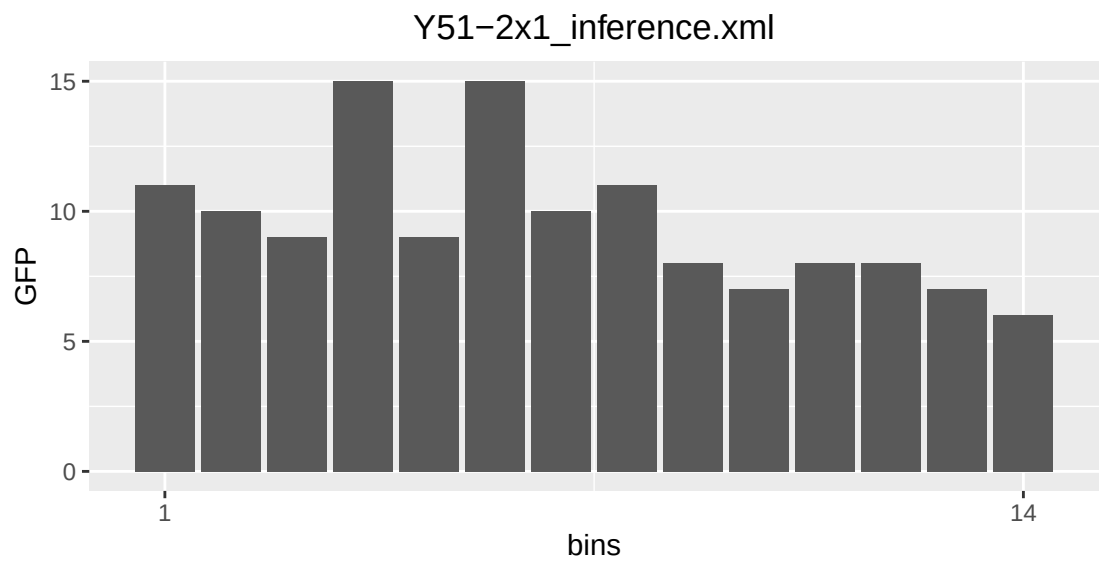

X450−2x402_inference.xml
30
20
10
0
Kernel Count
1
14
Bin

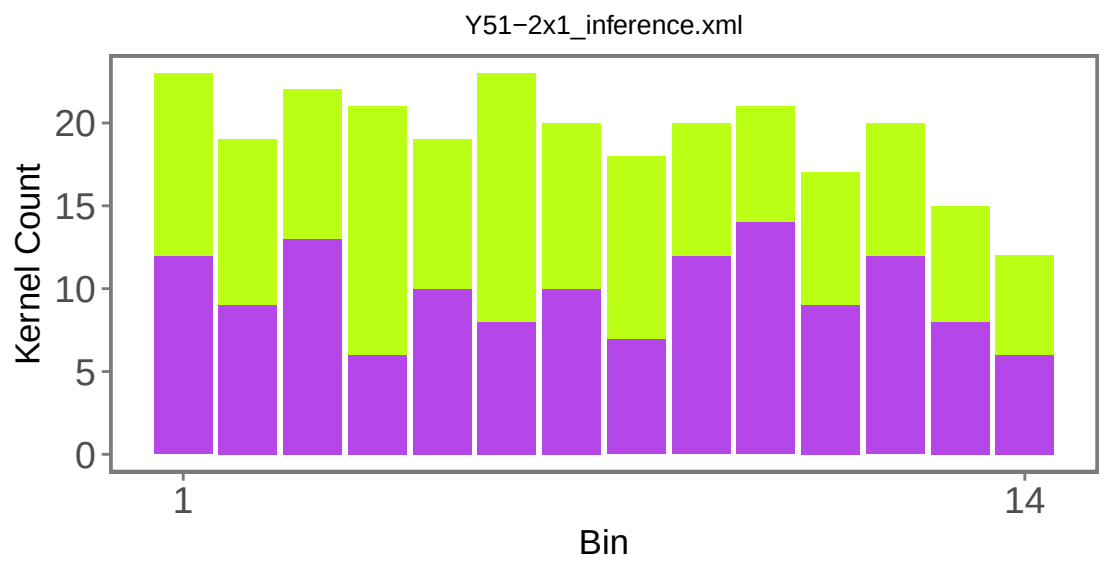

Y51−1x1_inference.xml
WT
20
15
10
5
0
1
14
bins

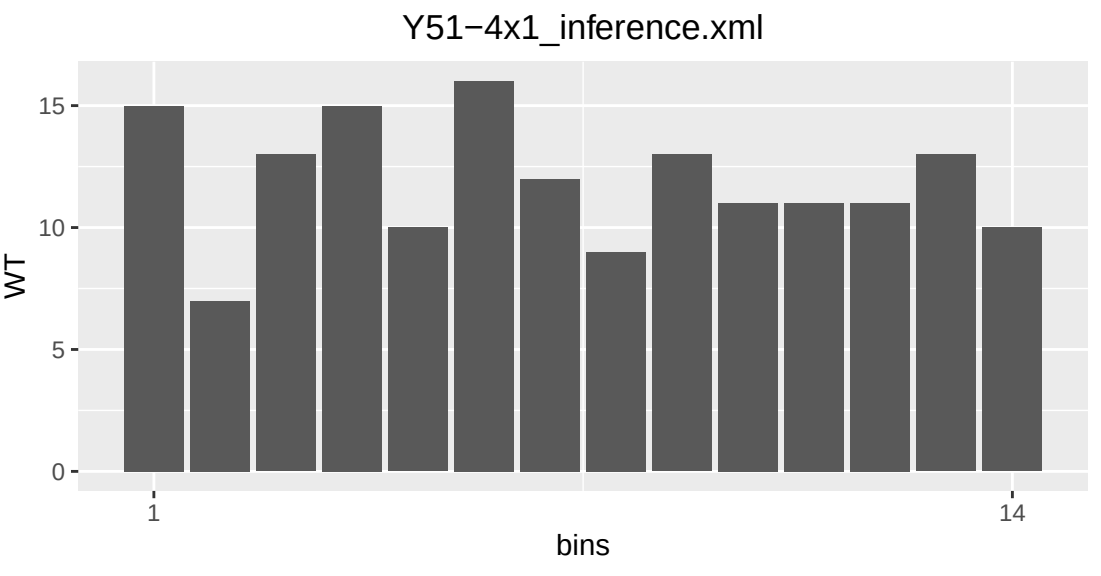

Y51−1x1_inference.xml
GFP
25
20
15
10
5
0
1
14
bins

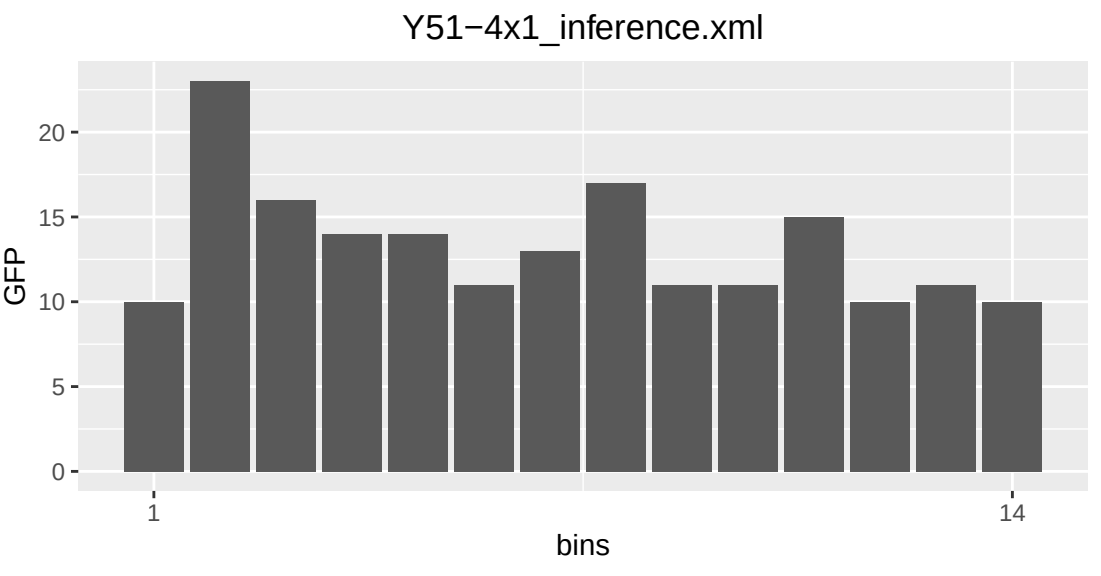

Y51−1x1_inference.xml
Kernel Count
0
10
20
30
40
1
14
Bin

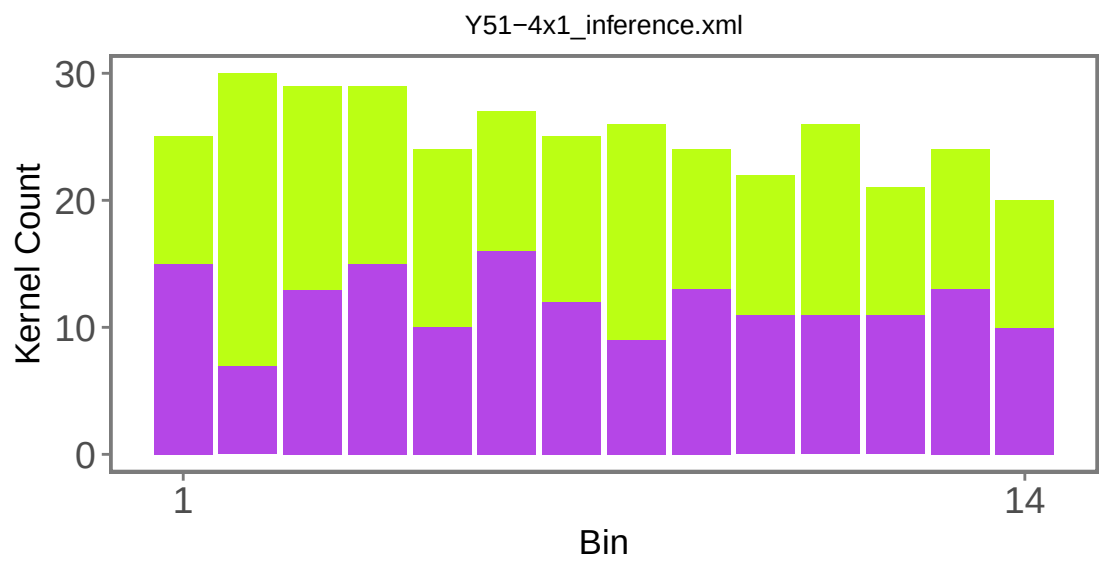

Y51−2x1_inference.xml
WT
10
5
0
1
14
bins

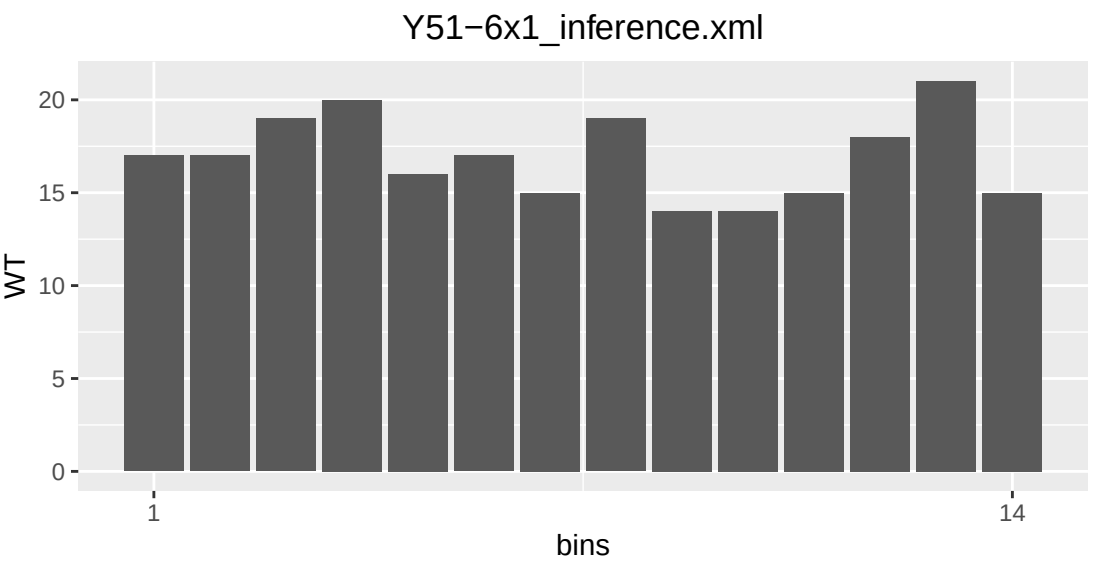

Y51−2x1_inference.xml
GFP
15
10
5
0
1
14
bins

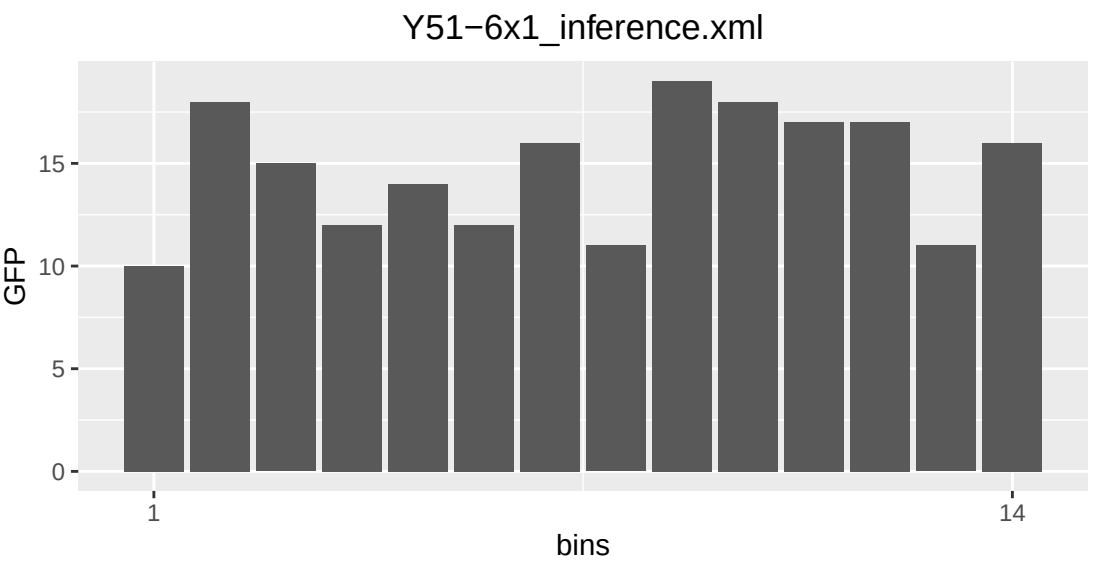

Y51−2x1_inference.xml
Kernel Count
20
15
10
5
0
1
14
Bin

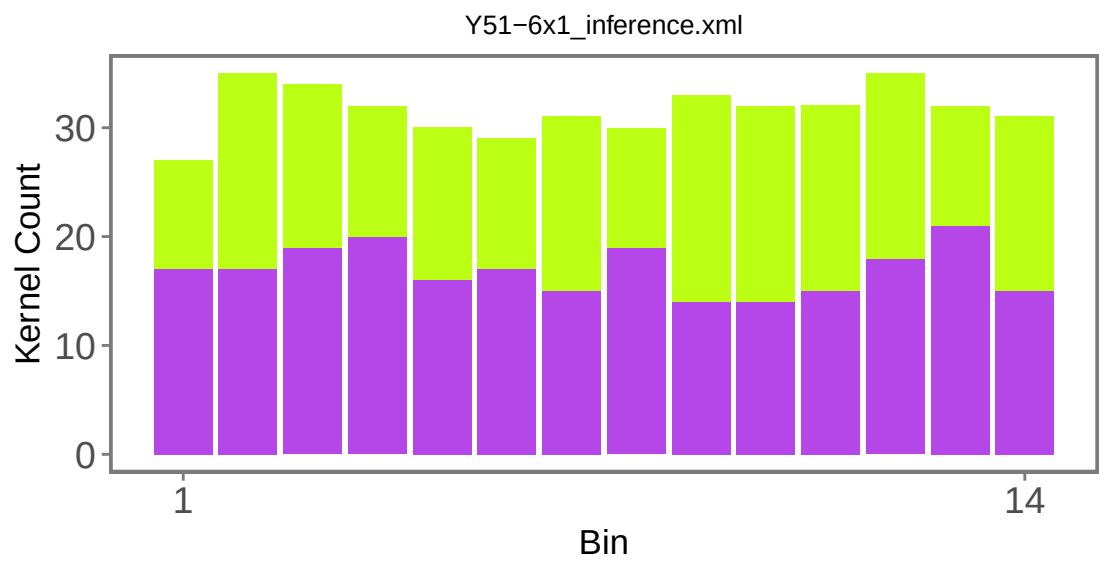

Y51−4x1_inference.xml
WT
15
10
5
0
1
14
bins

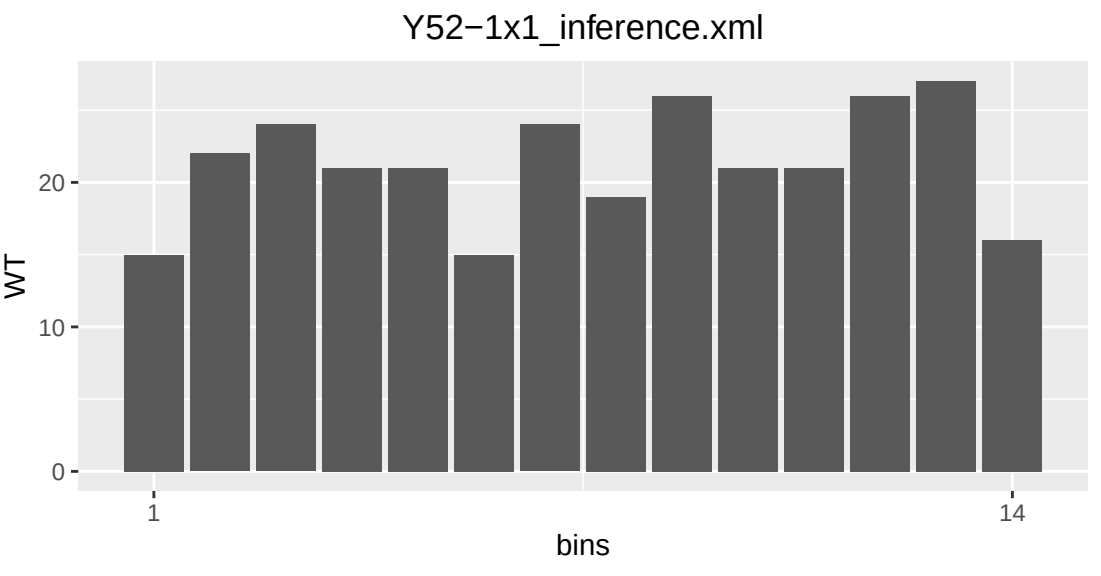

Y51−4x1_inference.xml
GFP
20
15
10
5
0
1
14
bins

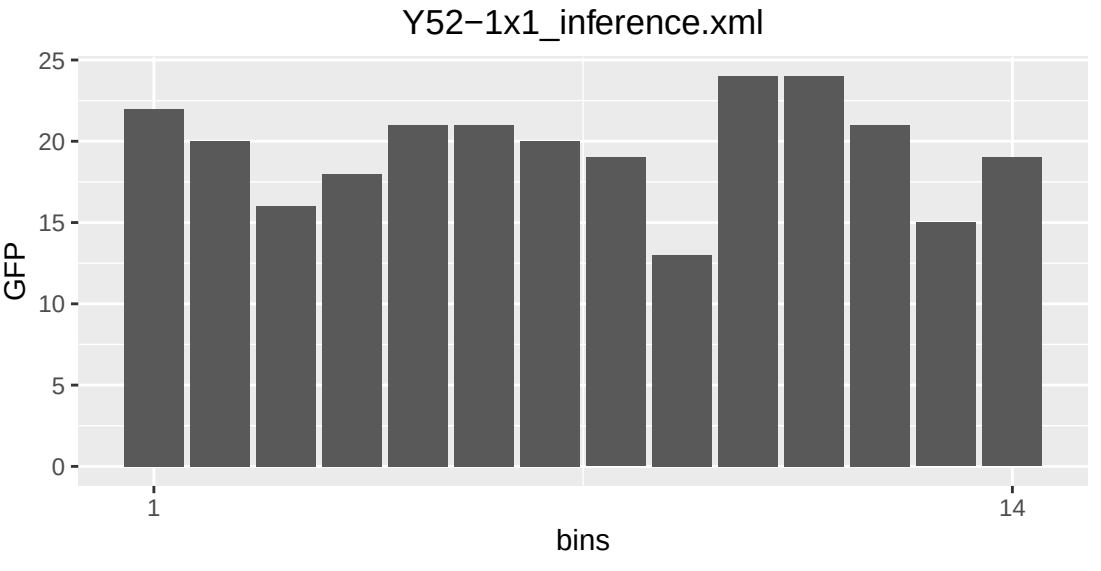

Y51−4x1_inference.xml
Kernel Count
30
20
10
0
1
14
Bin

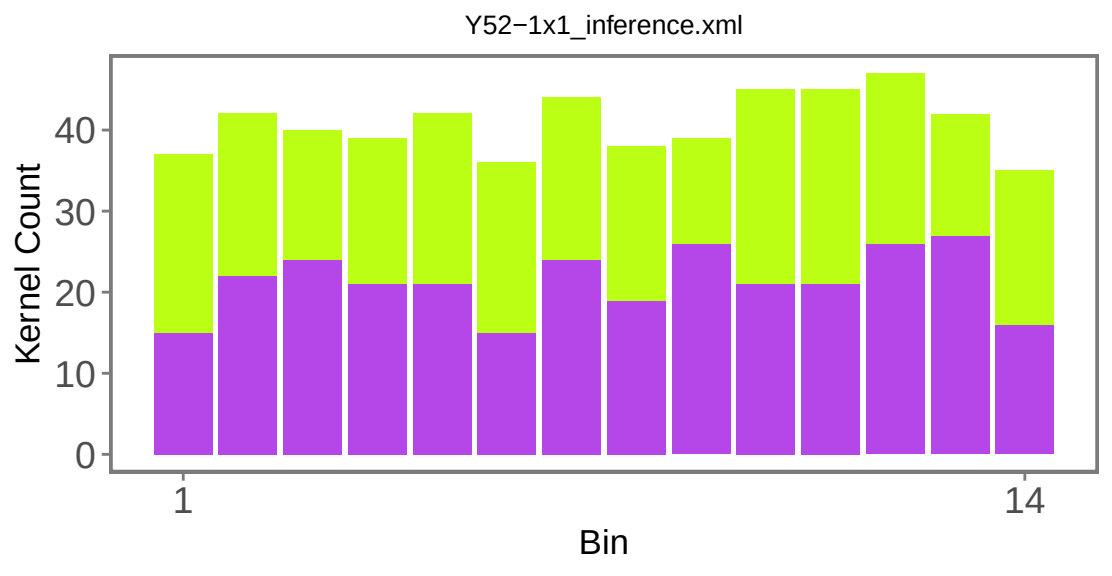

Y51−6x1_inference.xml
WT
20
15
10
5
0
1
14
bins

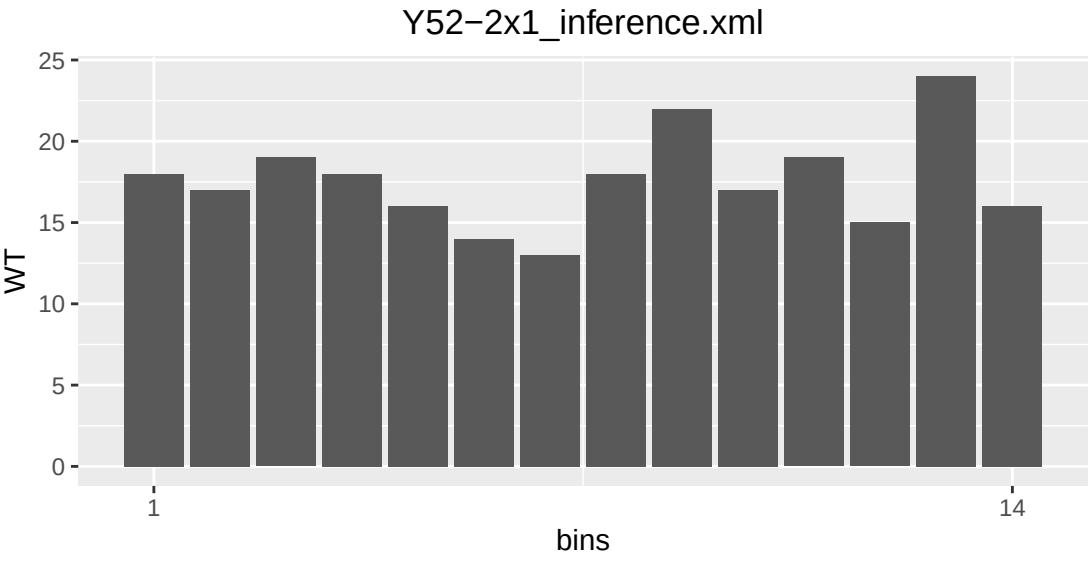

Y51−6x1_inference.xml
GFP
15
10
5
0
1
14
bins

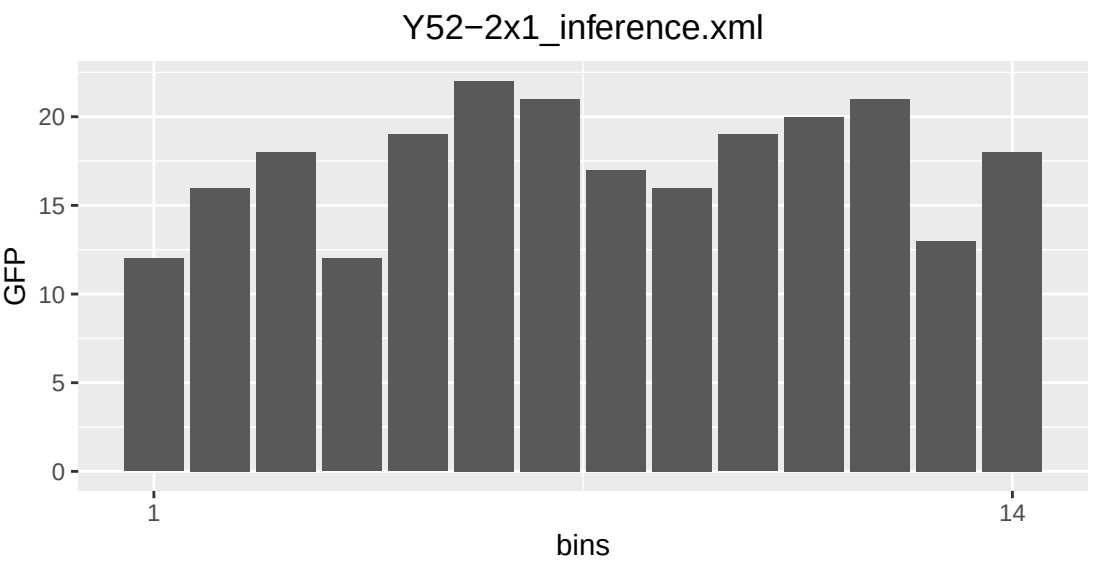

Y51−6x1_inference.xml
Kernel Count
30
20
10
0
1
14
Bin

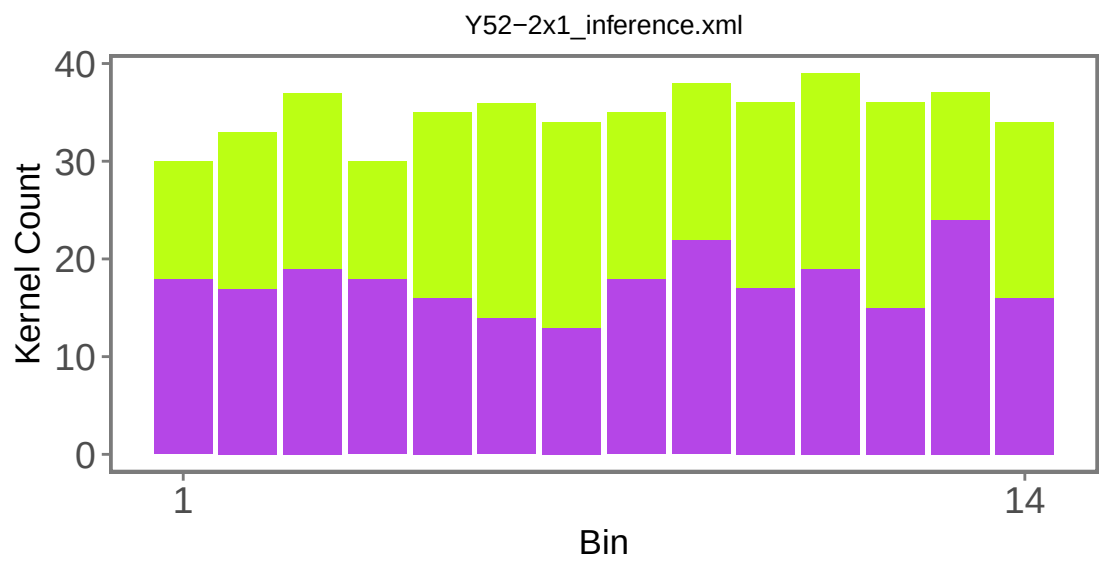

Y52−1x1_inference.xml
WT
20
10
0
1
14
bins


Y52−1x1_inference.xml
25
20
15
10
5
0
GFP
1
14
bins


Y52−1x1_inference.xml
Kernel Count
0
10
20
30
40
1
14
Bin


Y52−2x1_inference.xml
25
20
15
10
5
0
WT
1
14
bins


Y52−2x1_inference.xml
GFP
20
15
10
5
0
1
14
bins


Y52−2x1_inference.xml
Kernel Count
40
30
20
10
0
1
14
Bin