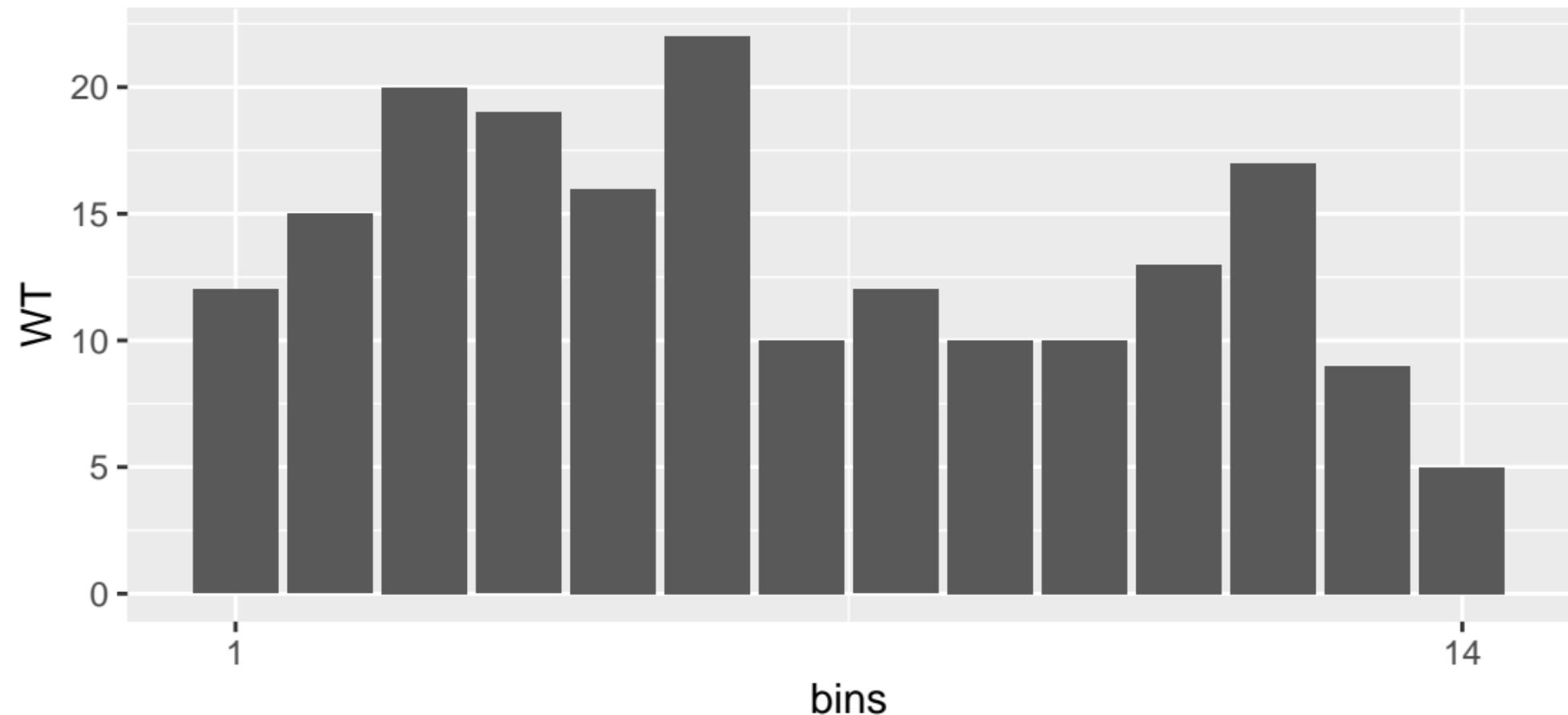
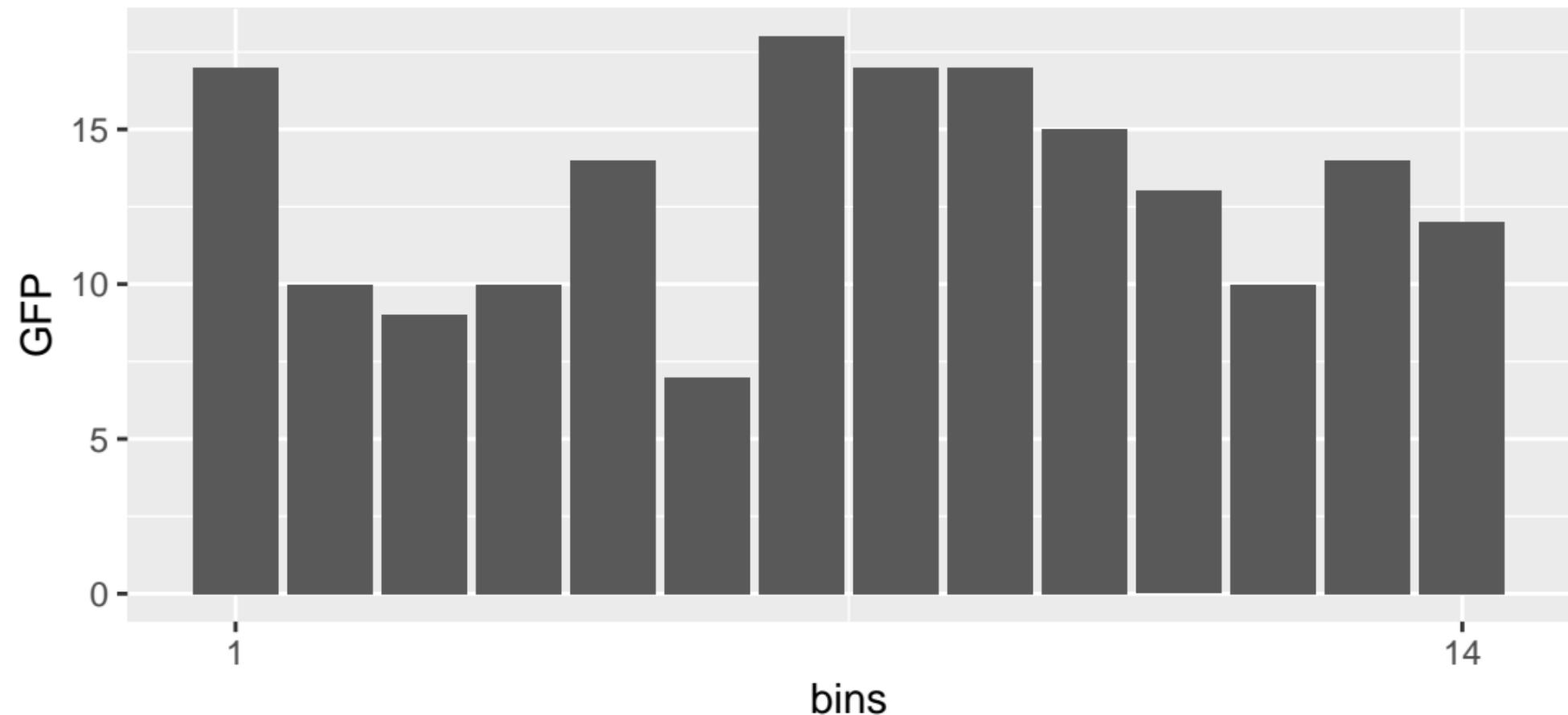


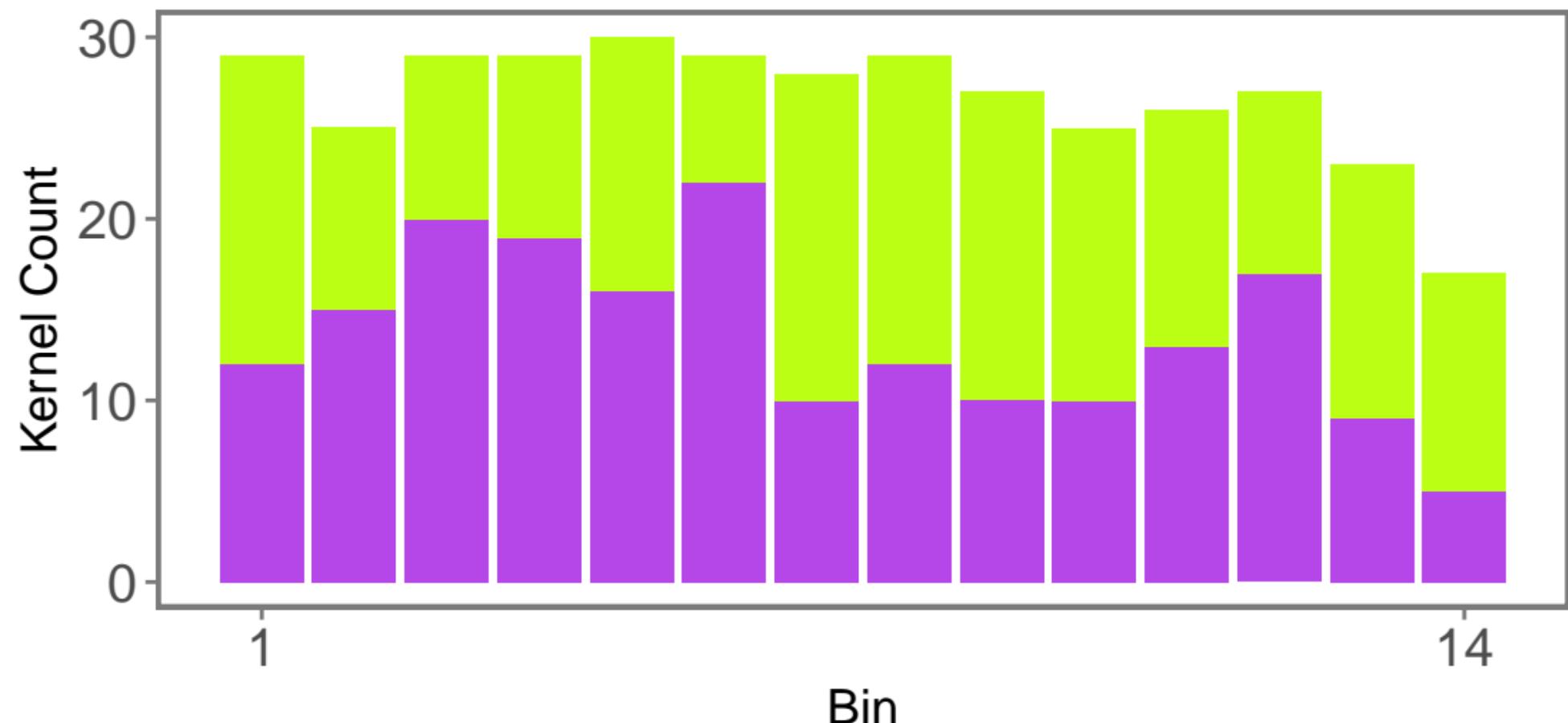
# X487A-1x402\_inference.xml



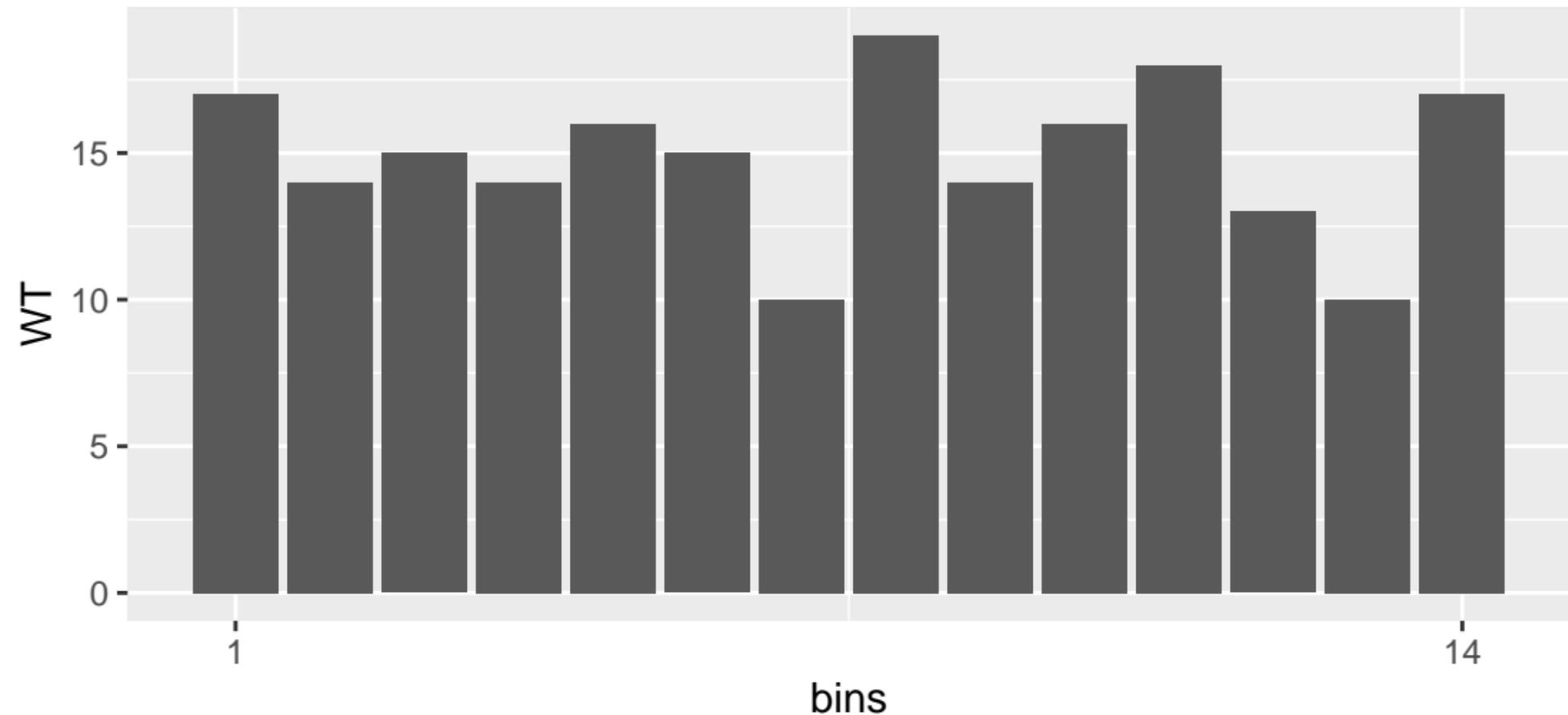
# X487A-1x402\_inference.xml



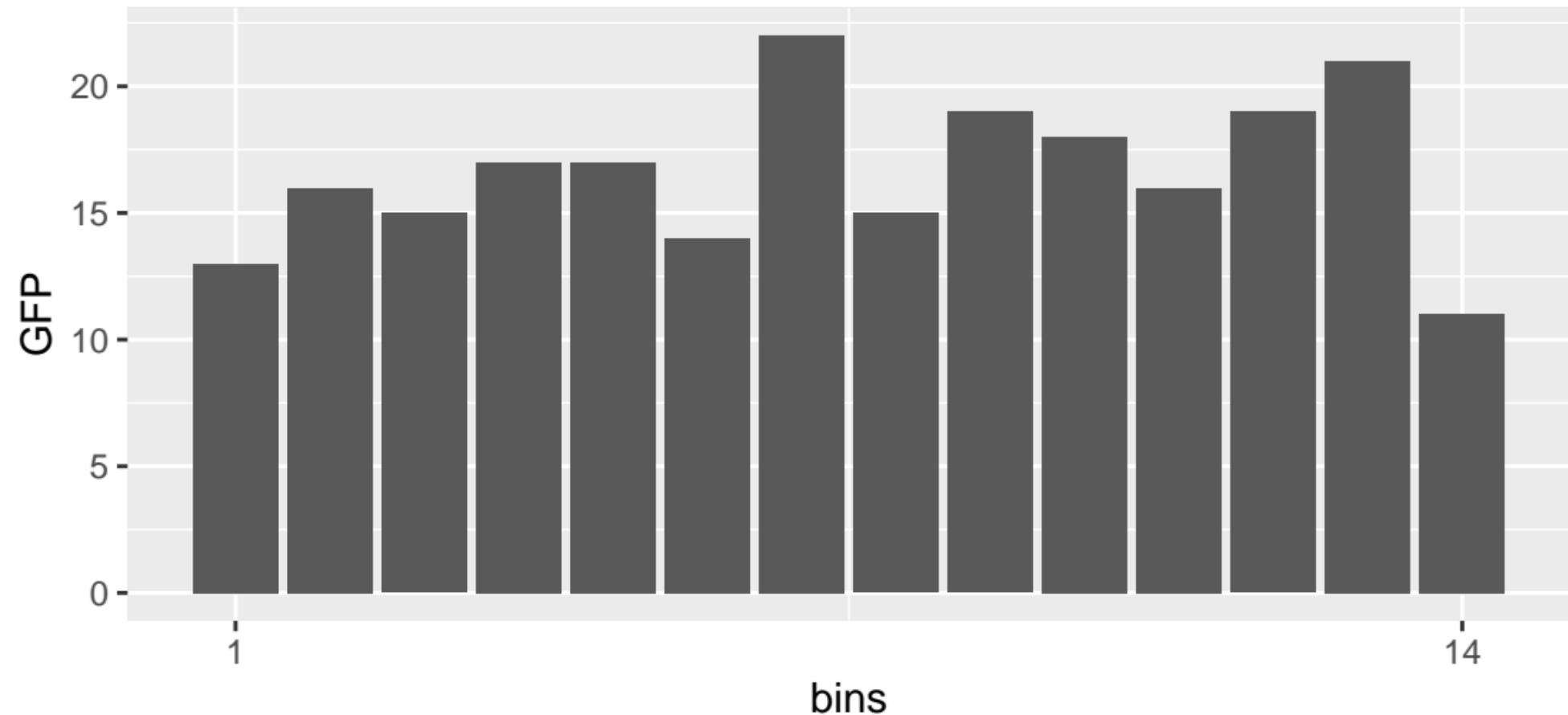
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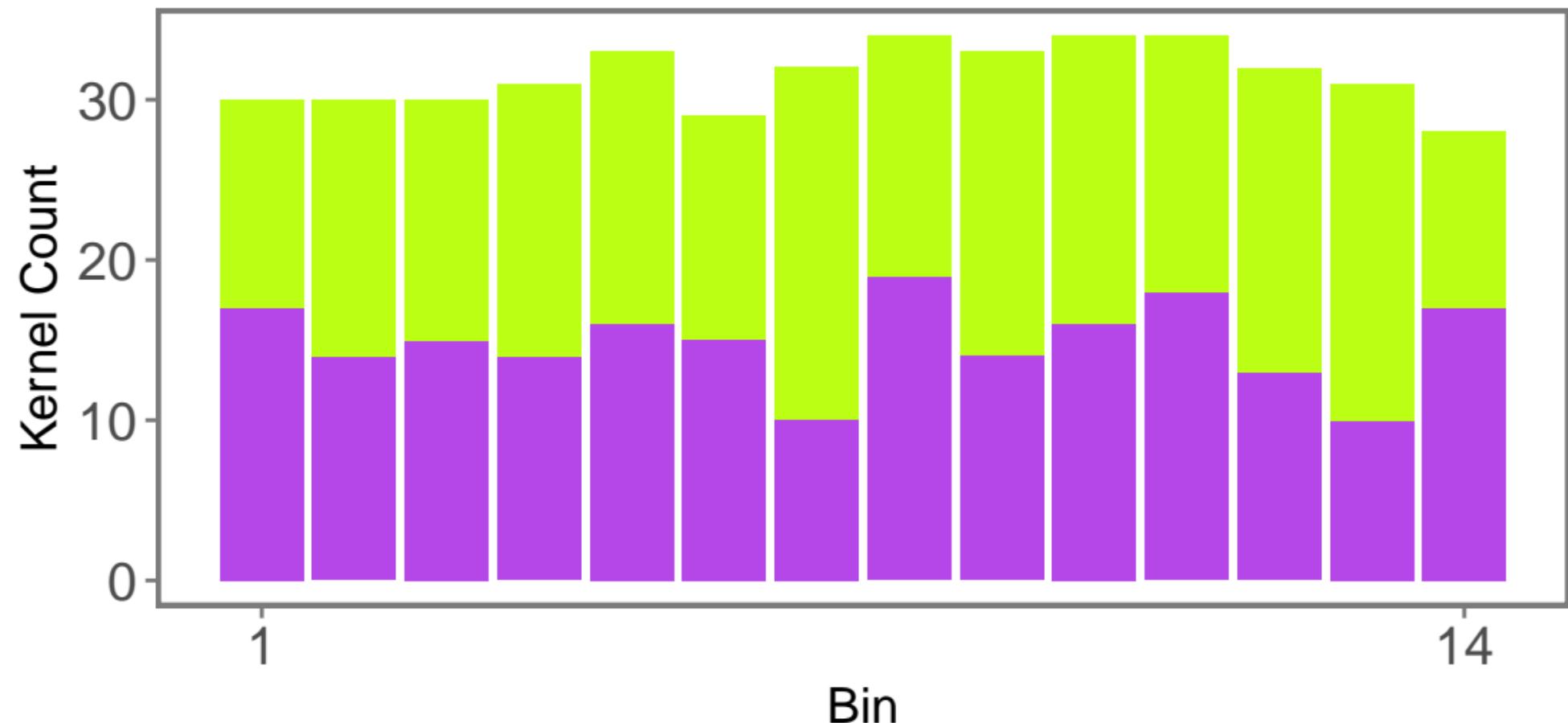
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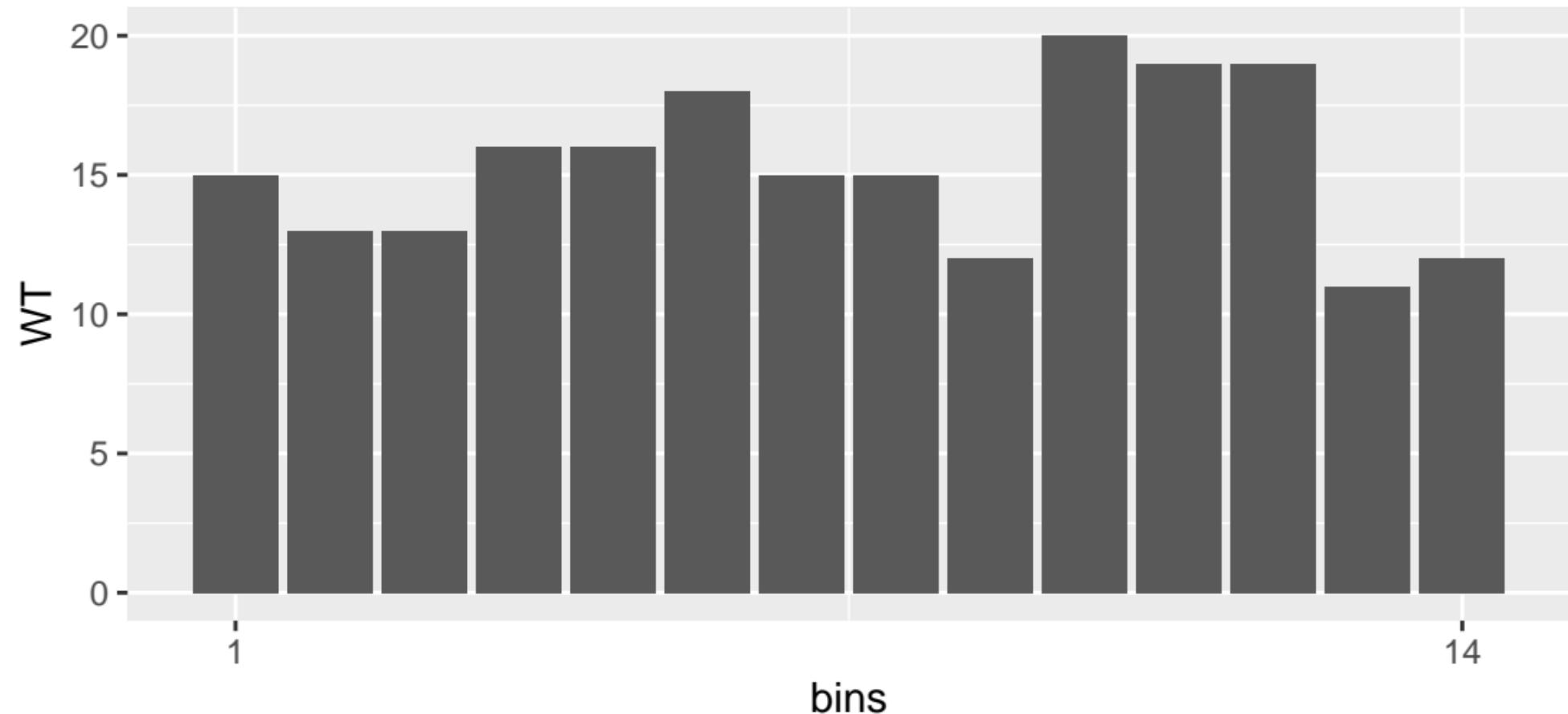
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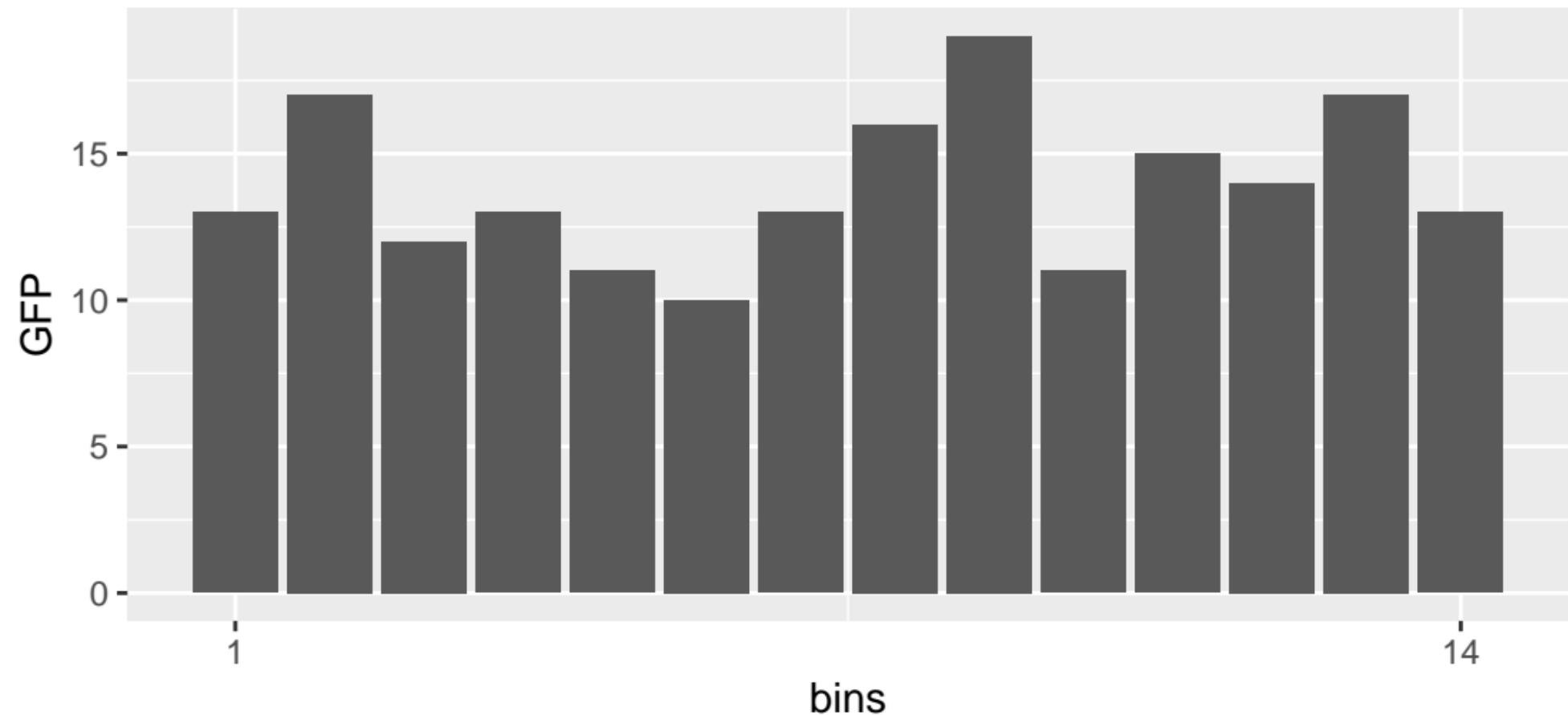
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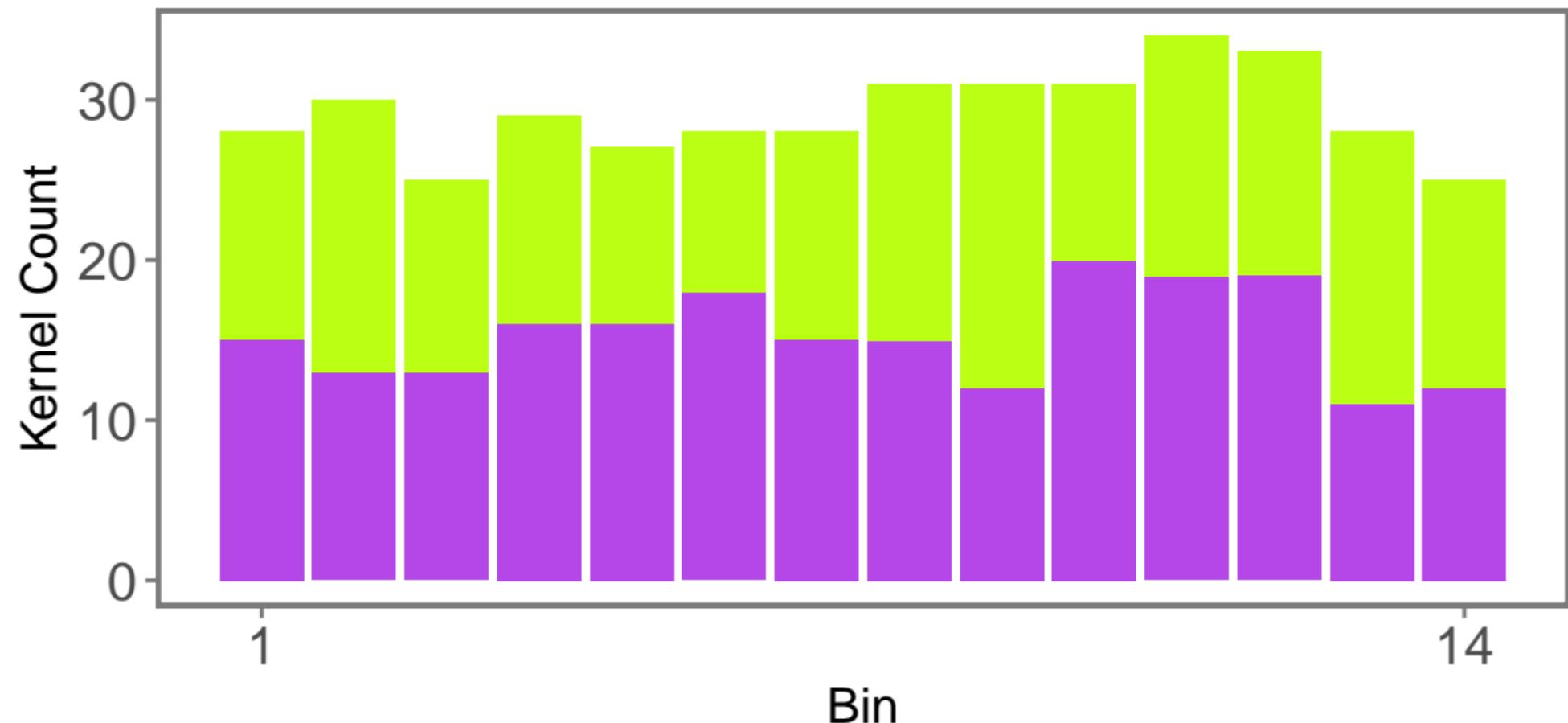
# Y17-2x1\_inference.xml



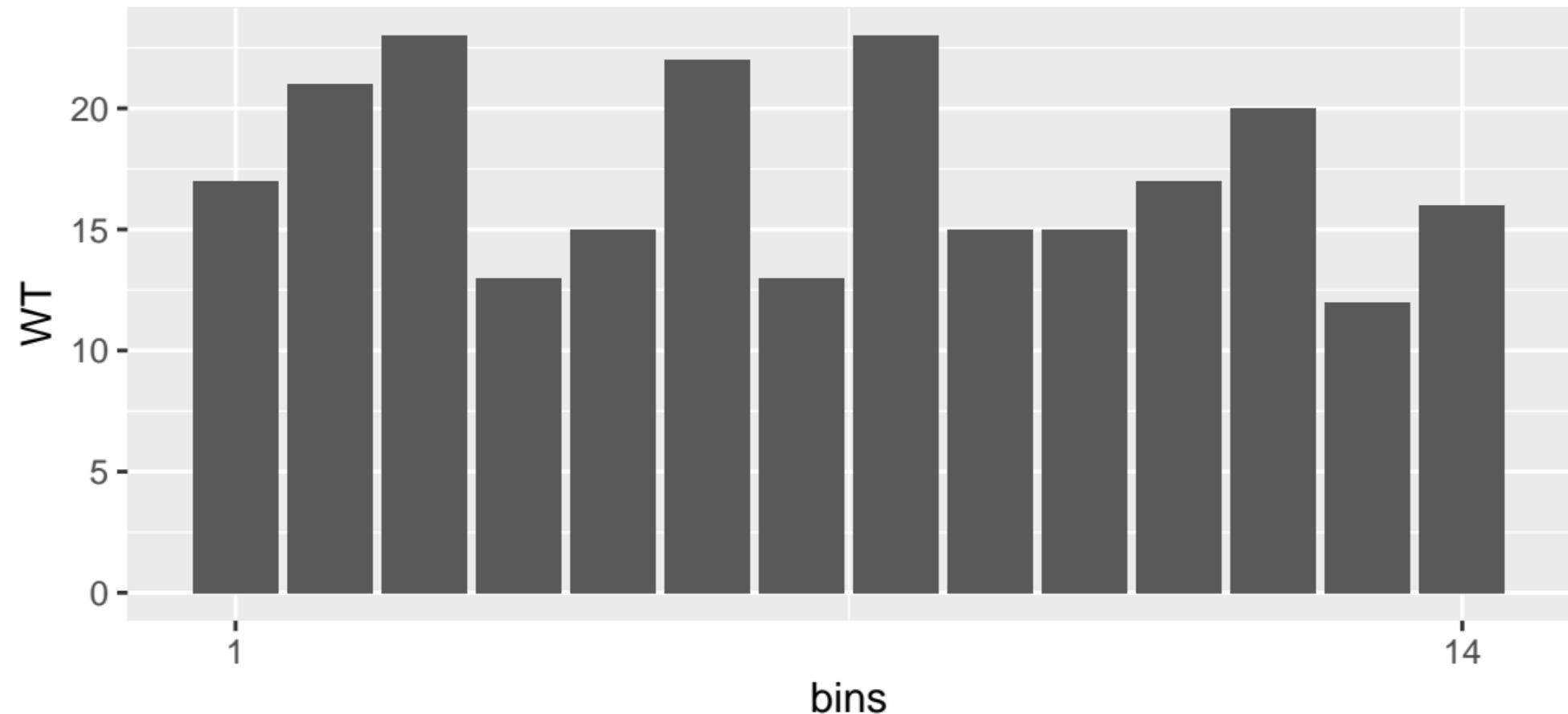
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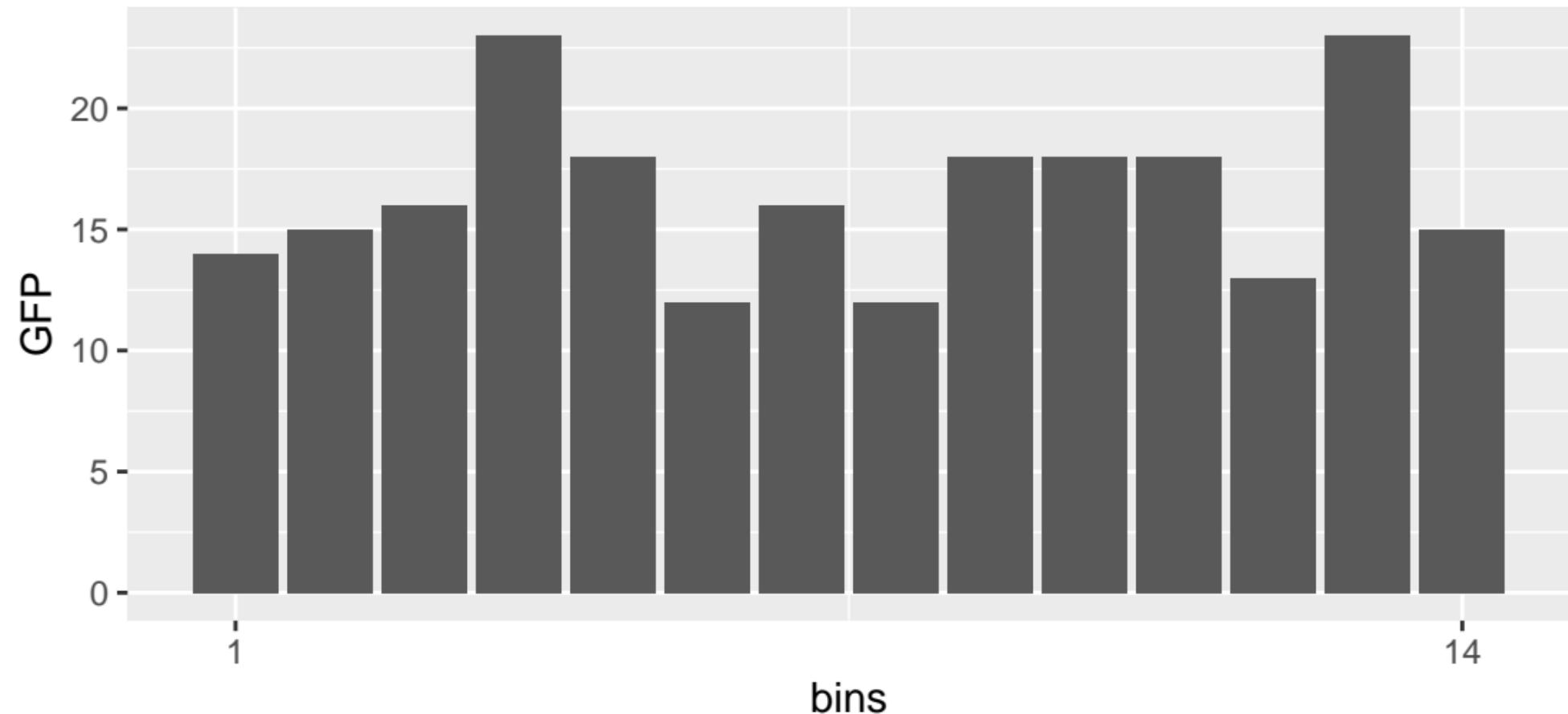
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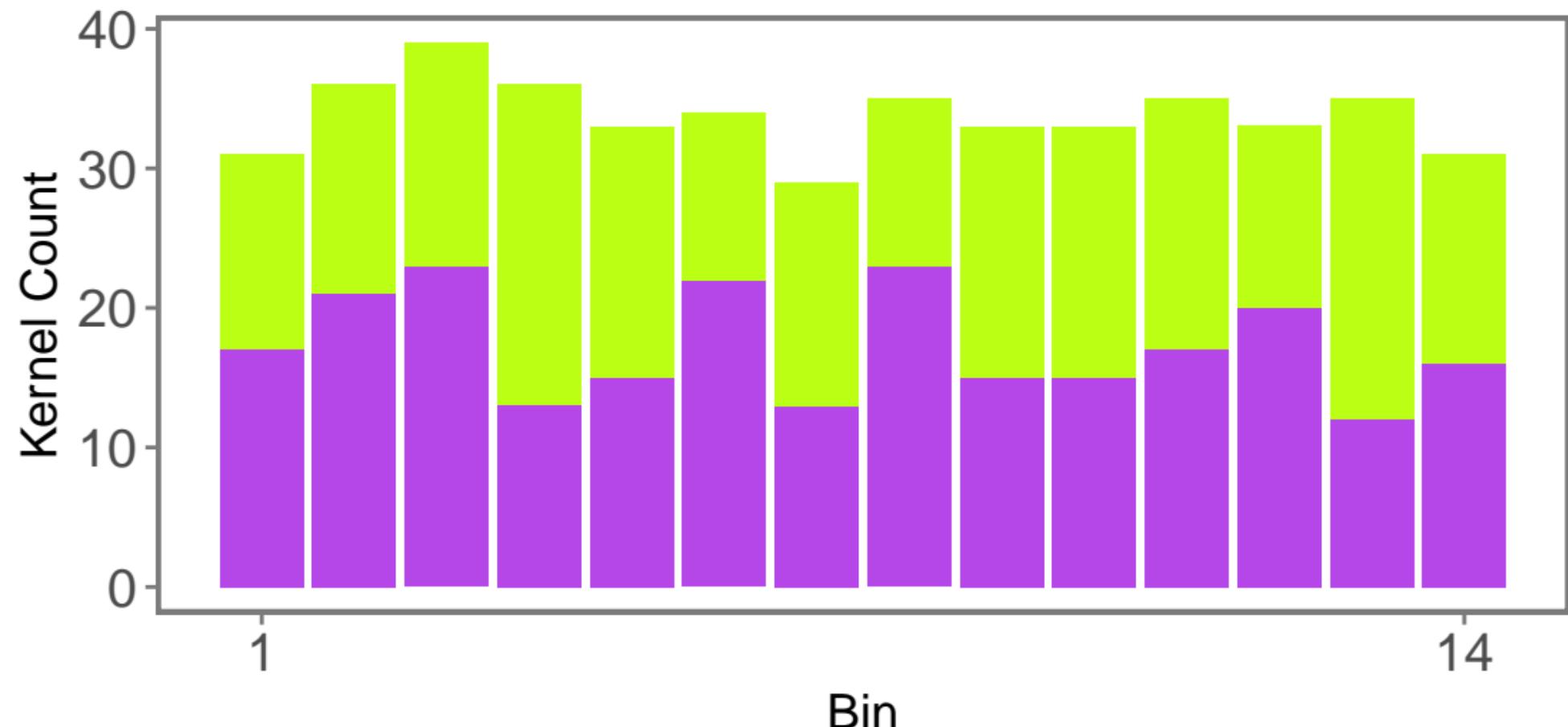
# Y17-3x1\_inference.xml



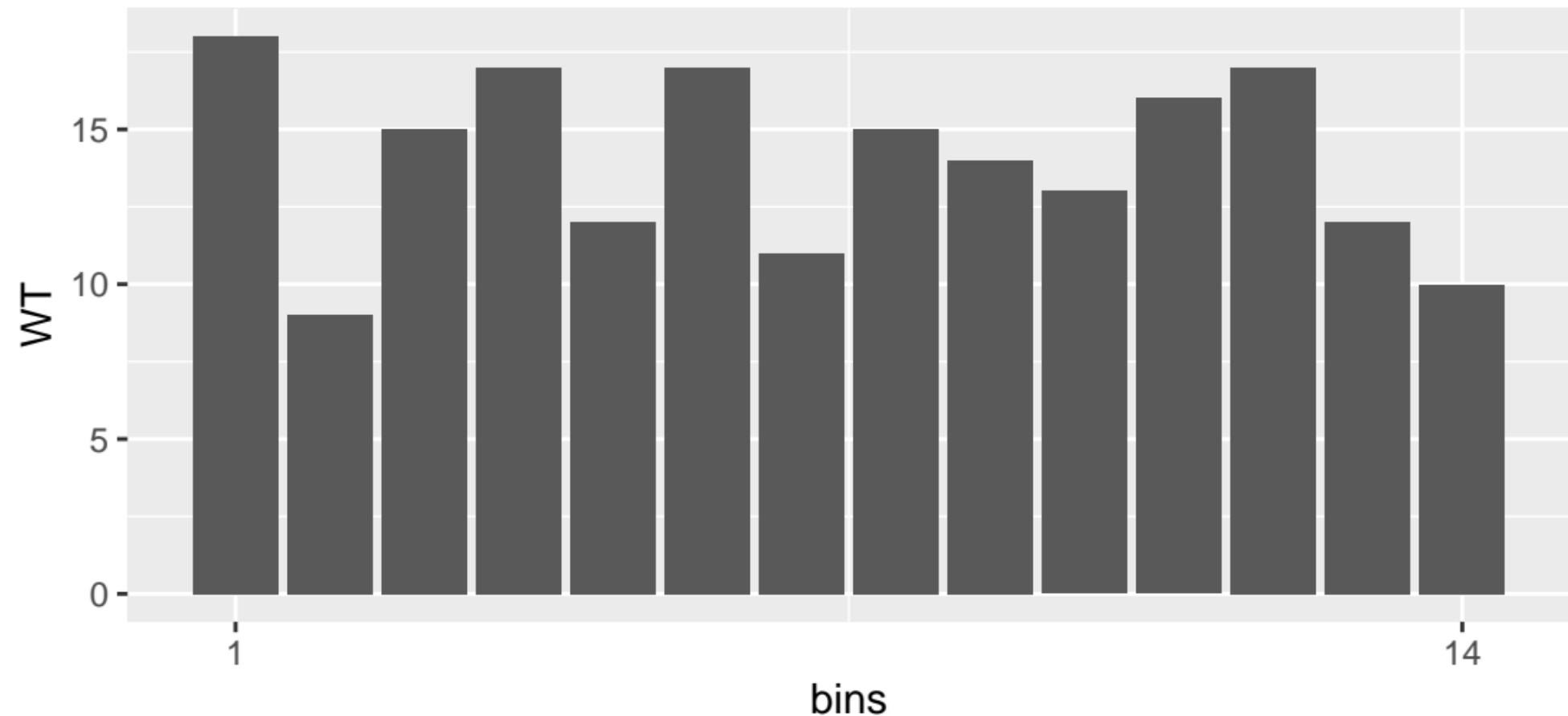
# Y17-3x1\_inference.xml



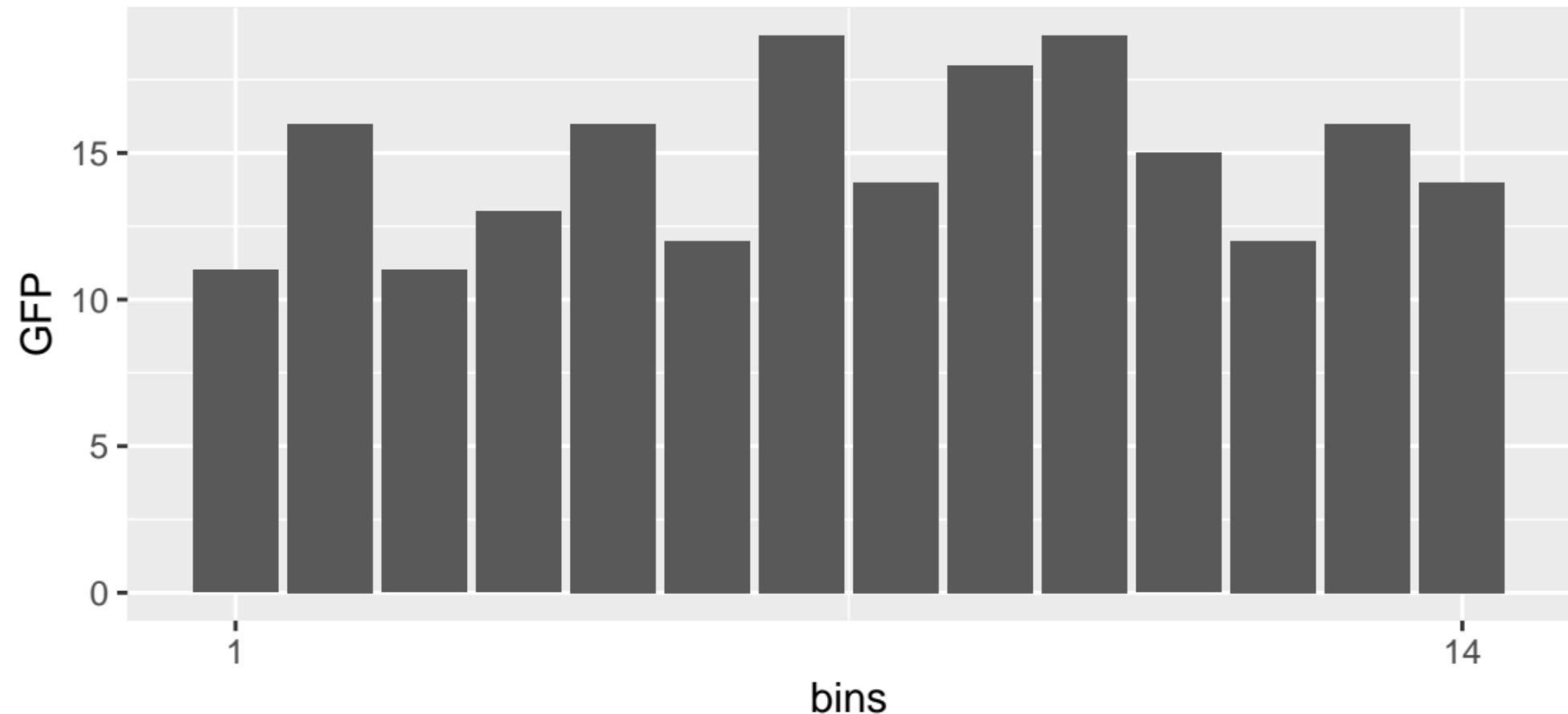
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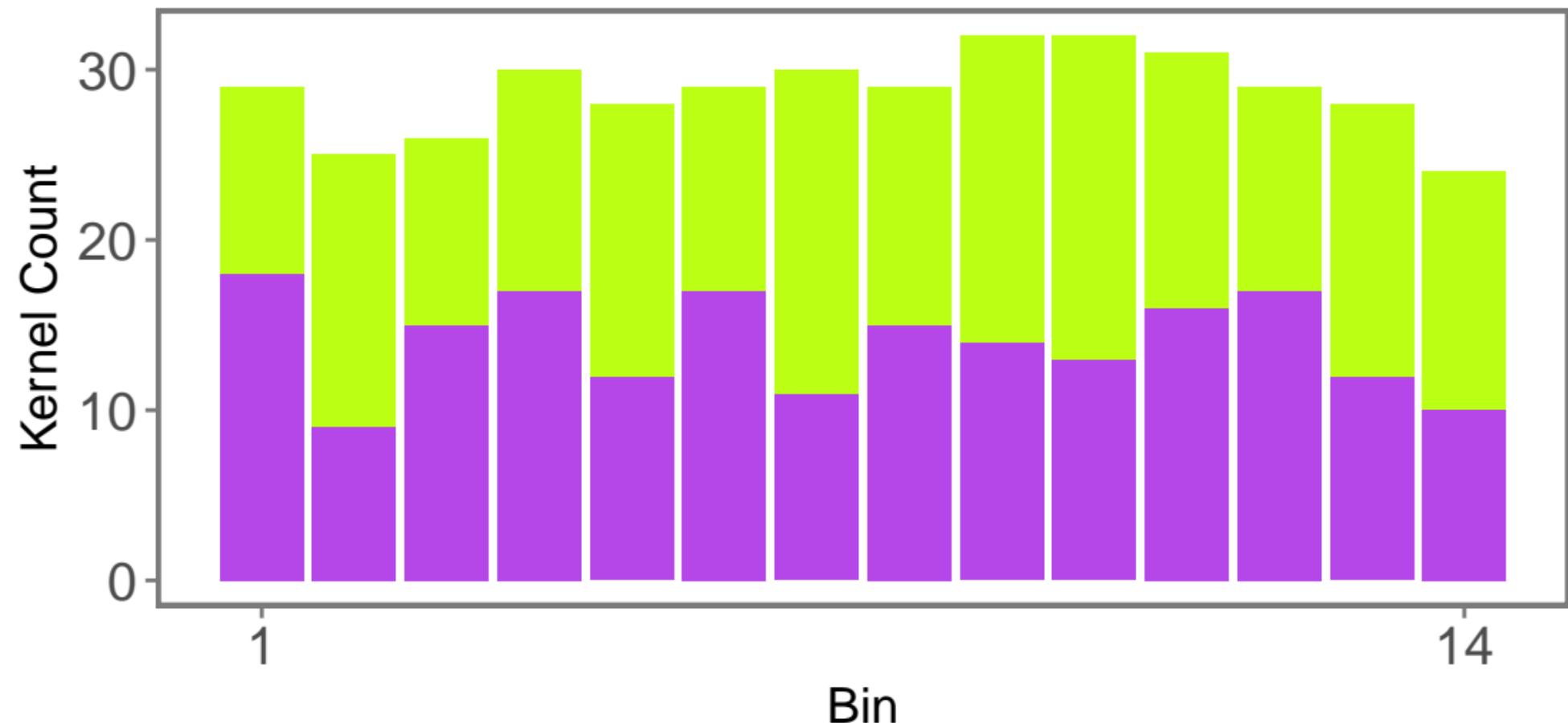
# Y17-4x1\_inference.xml



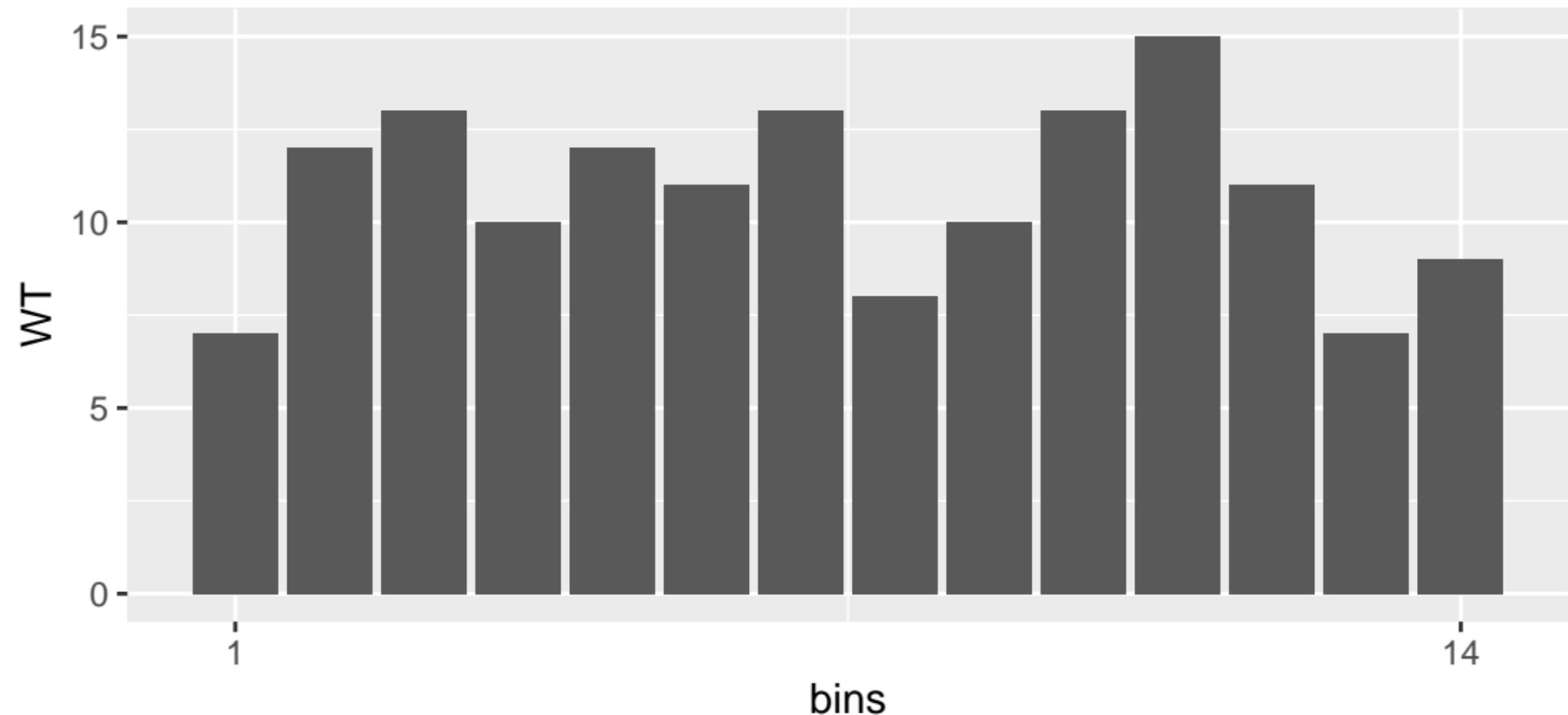
# Y17-4x1\_inference.xml



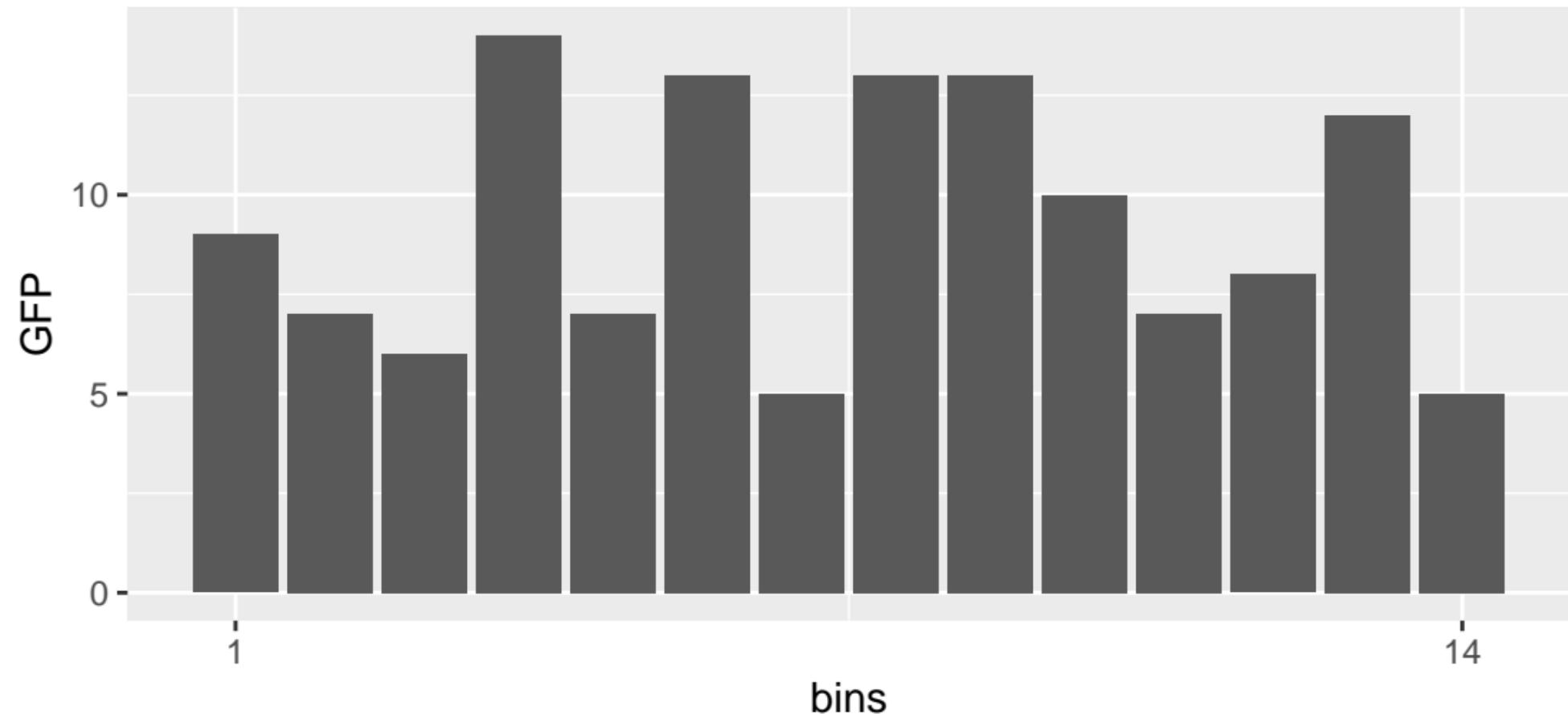
Y17-4x1\_inference.xml



# Y17-5x1\_inference.xml



# Y17-5x1\_inference.xml



Y17-5x1\_inference.xml

