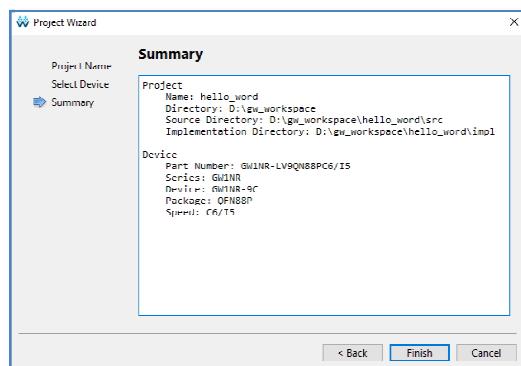
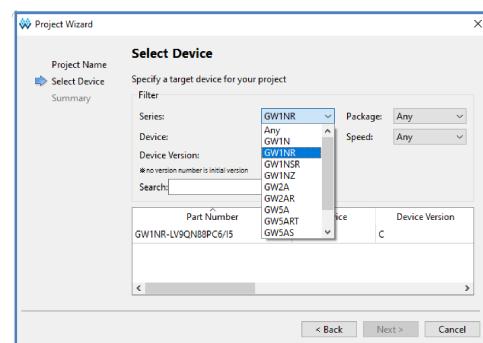
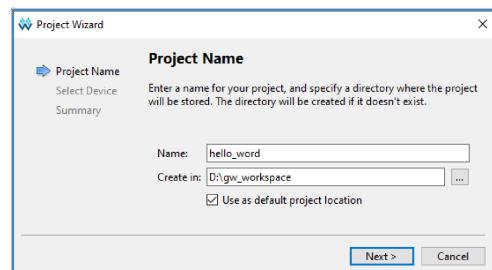
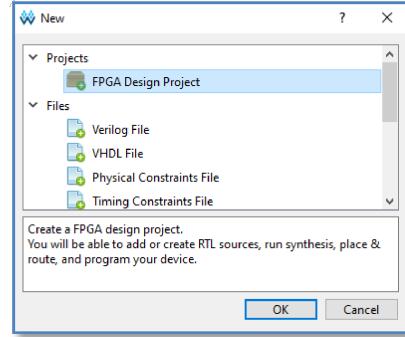
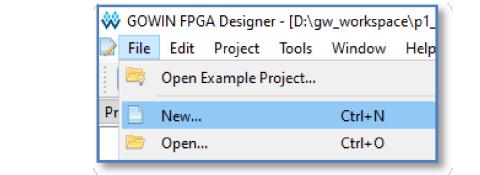
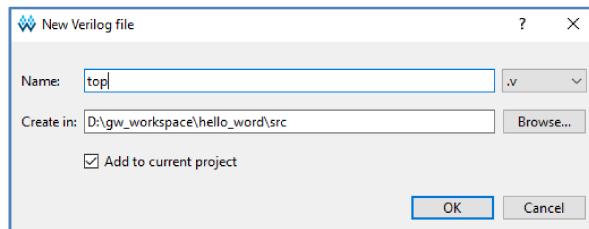
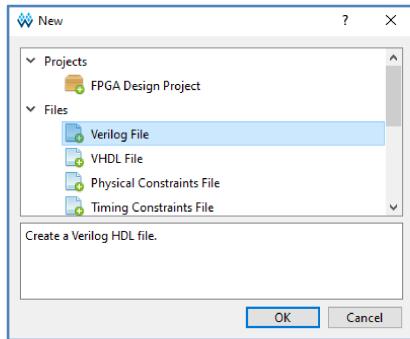


Setup Gowin for TangNano9K



https://gowlsemi.com/en/support/download_eda/





```

module top (
    input wire button1,
    input wire button2,
    output wire [1:0] leds
);
    assign leds[0] = button1;
    assign leds[1] = button2;
endmodule

```

top.v

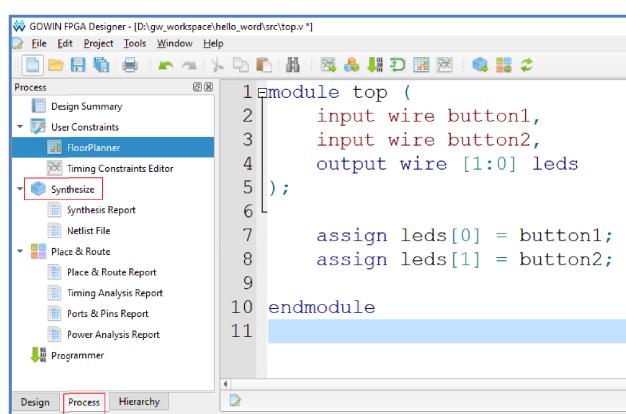
```

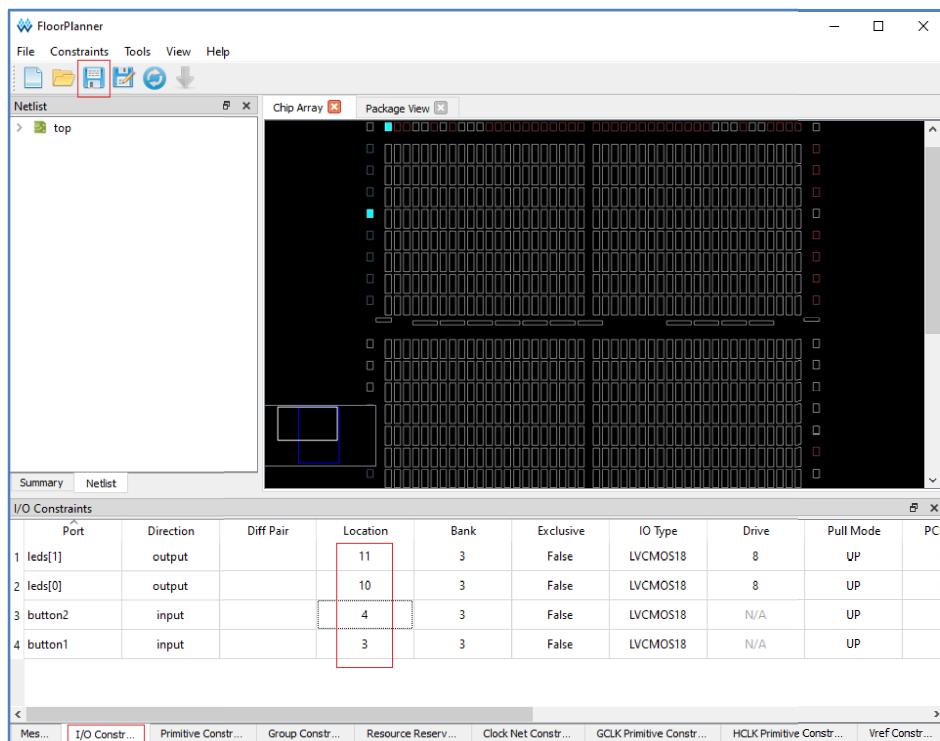
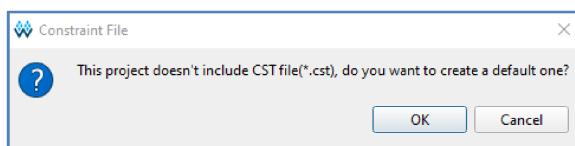
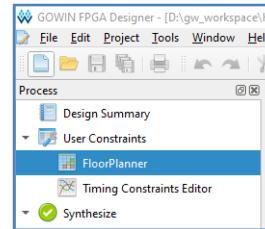
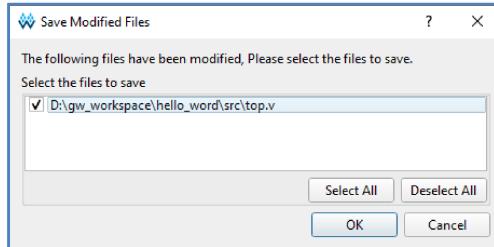
module top (
    input wire button1,
    input wire button2,
    output wire [1:0] leds
);

    assign leds[0] = button1;
    assign leds[1] = button2;

endmodule

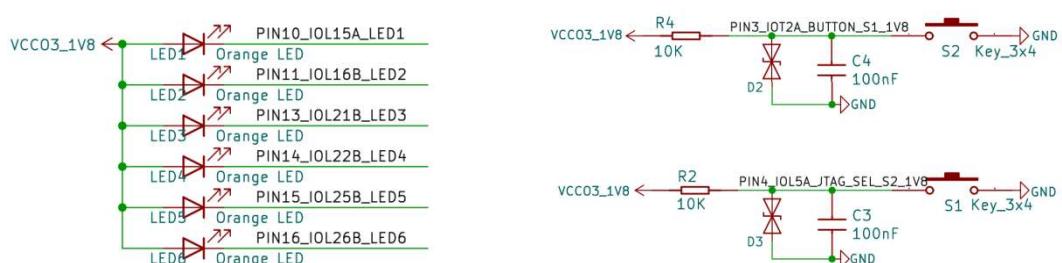
```





Hardware Schematic

<https://wiki.sipeed.com/hardware/en/tang/Tang-Nano-9K/Nano-9K.html>



```

1 IO_LOC "leds[1]" 11;
2 IO_PORT "leds[1]" IO_TYPE=LVC MOS18 PULL_MODE=UP DRIVE=8 BANK_VCCIO=1.8;
3 IO_LOC "leds[0]" 10;
4 IO_PORT "leds[0]" IO_TYPE=LVC MOS18 PULL_MODE=UP DRIVE=8 BANK_VCCIO=1.8;
5 IO_LOC "button2" 4;
6 IO_PORT "button2" IO_TYPE=LVC MOS18 PULL_MODE=UP BANK_VCCIO 1.8;
7 IO_LOC "button1" 3;
8 IO_PORT "button1" IO_TYPE=LVC MOS18 PULL_MODE=UP BANK_VCCIO=1.8;
9

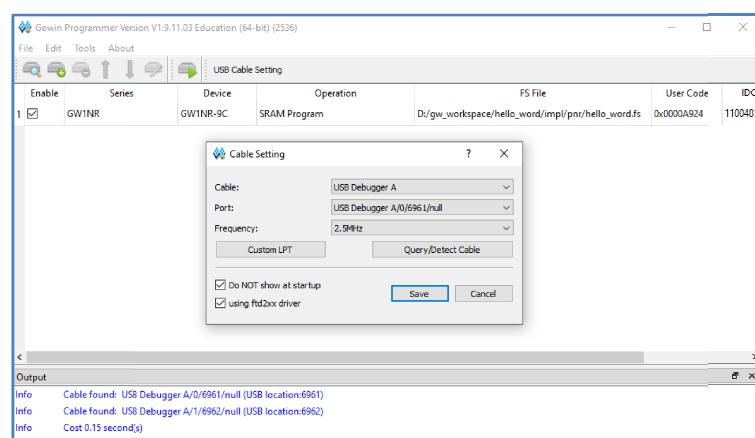
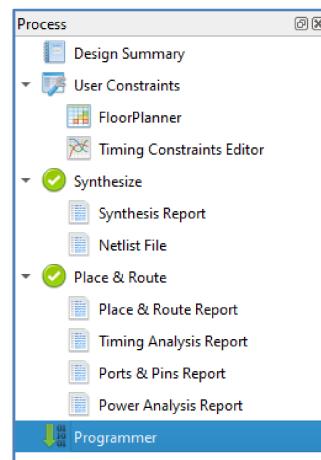
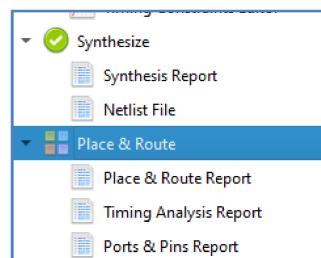
```

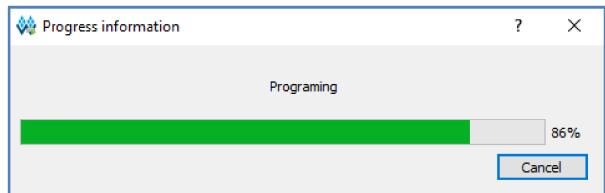
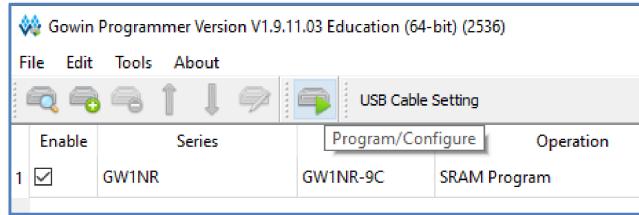
hello_word.cst

```

IO_LOC "leds[1]" 11;
IO_PORT "leds[1]" IO_TYPE=LVC MOS18 PULL_MODE=UP DRIVE=8 BANK_VCCIO=1.8;
IO_LOC "leds[0]" 10;
IO_PORT "leds[0]" IO_TYPE=LVC MOS18 PULL_MODE=UP DRIVE=8 BANK_VCCIO=1.8;
IO_LOC "button2" 4;
IO_PORT "button2" IO_TYPE=LVC MOS18 PULL_MODE=UP BANK_VCCIO=1.8;
IO_LOC "button1" 3;
IO_PORT "button1" IO_TYPE=LVC MOS18 PULL_MODE=UP BANK_VCCIO=1.8;

```





Tang Nano 9K

