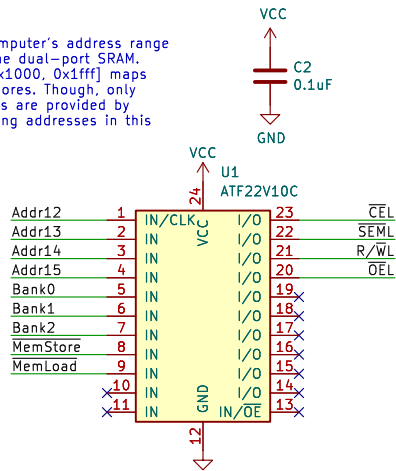


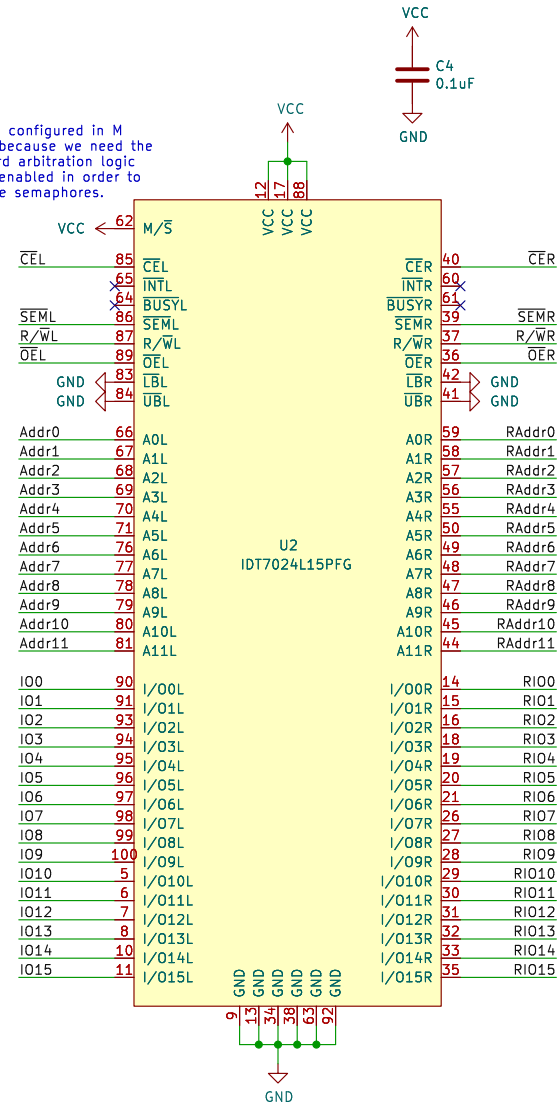
Addr[0..15]D Addr[0..15]
IO[0..15]C IO[0..15]
Bank[0..2]D Bank[0..2]
MemStoreC MemStore
MemLoadC MemLoad

RAddr[0..15]D RAddr[0..15]
RIO[0..15]C RIO[0..15]
CERD CER
SEMRD SEMR
R/WRD R/WL
OERD OER

The PLD maps the computer's address range [0x0000, 0x0fff] to the dual-port SRAM. The address range [0x1000, 0x1fff] maps to the 7024's semaphores. Though, only eight such semaphores are provided by hardware. The remaining addresses in this region are unused.



RAM is configured in M mode because we need the onboard arbitration logic to be enabled in order to use the semaphores.



RAM shared between the processor and the Arduino.

Sheet: /RAM/
File: RAM.kicad_sch

Title: Shared Memory

Size: USLetter Date: 2023-11-27
KiCad E.D.A. kicad 7.0.8

Rev:
Id: 2/5

