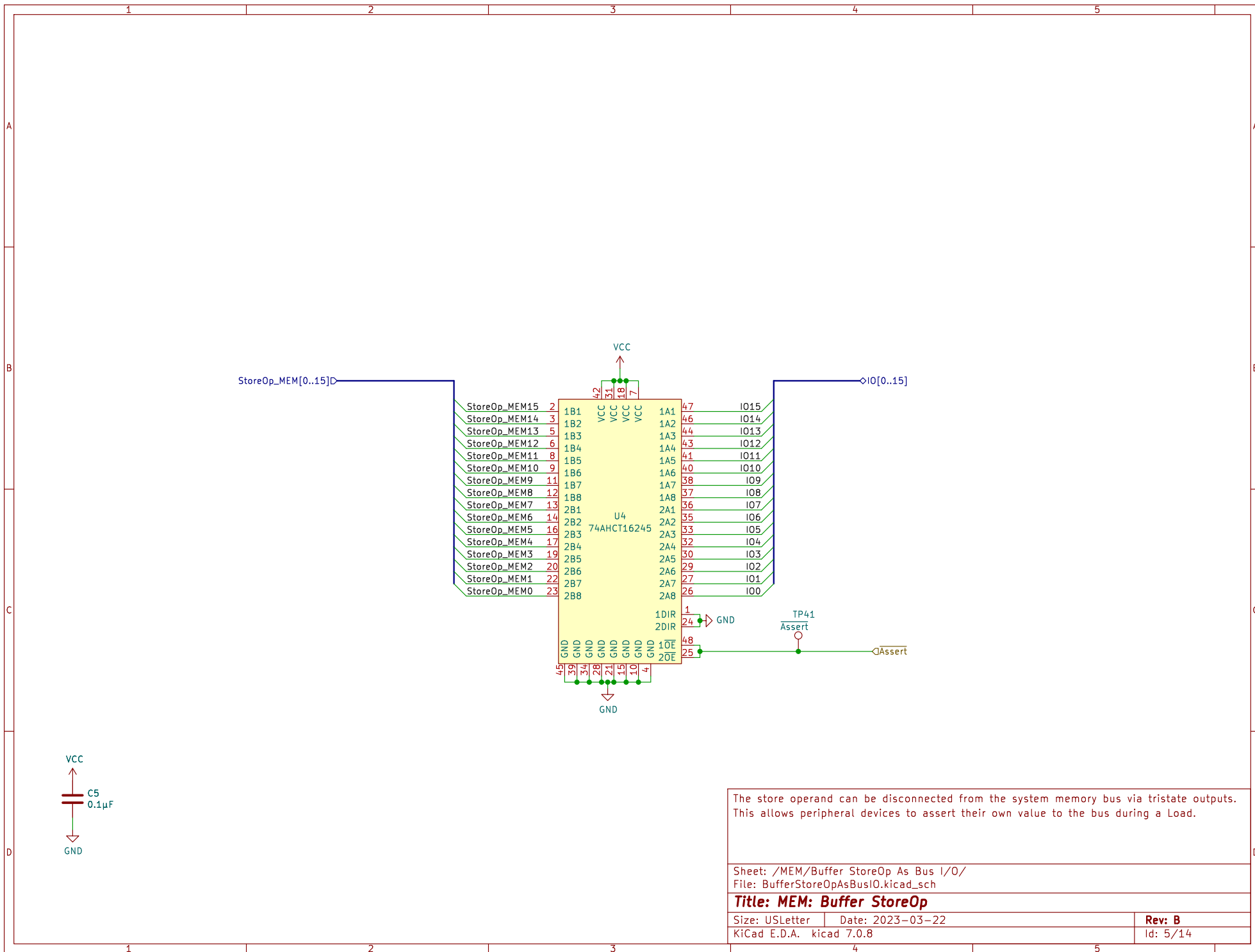


Sheet: /MEM/Buffer Memory Control Signals/ File: BufferMemoryControlSignals.kicad_sch		
Title:		
Size: USLetter	Date: 2023-03-22	Rev: B
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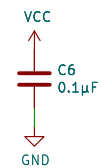
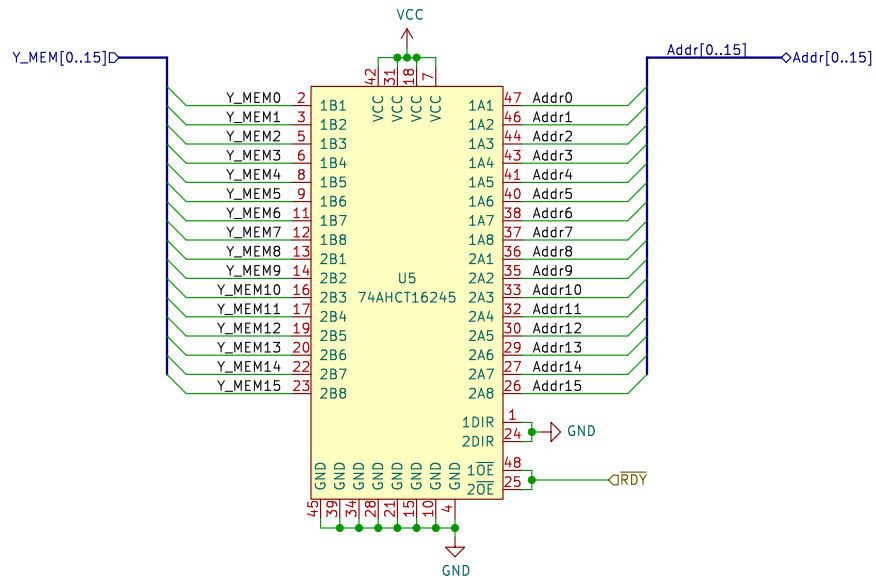
The store operand can be disconnected from the system memory bus via tristate outputs. This allows peripheral devices to assert their own value to the bus during a Load.

Sheet: /MEM/Buffer StoreOp As Bus I/O/
File: BufferStoreOpAsBusIO.kicad_sch

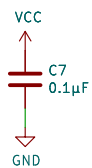
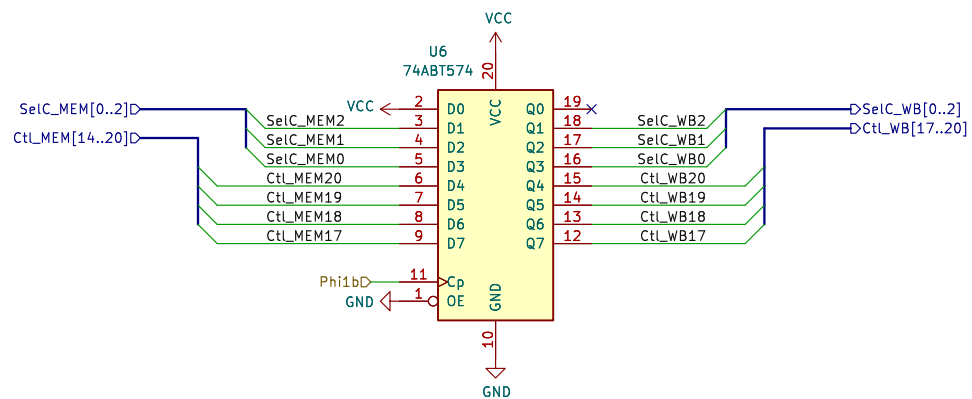
Title: MEM: Buffer StoreOp

Size: USLetter Date: 2023-03-22
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Buffer the effective address before it leaves the main board.	
Sheet: /MEM/Buffer Addr/ File: BufferALUResultAsAddr.kicad_sch	
Title: MEM: Buffer ALUResult	
Size: USLetter	Date: 2023-03-22
KiCad E.D.A. kicad 7.0.8	Rev: B Id: 6/14

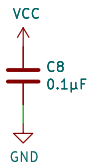


Sheet: /MEM/sheet5FD643DA/
File: Ctl_15_23_Register.kicad_sch

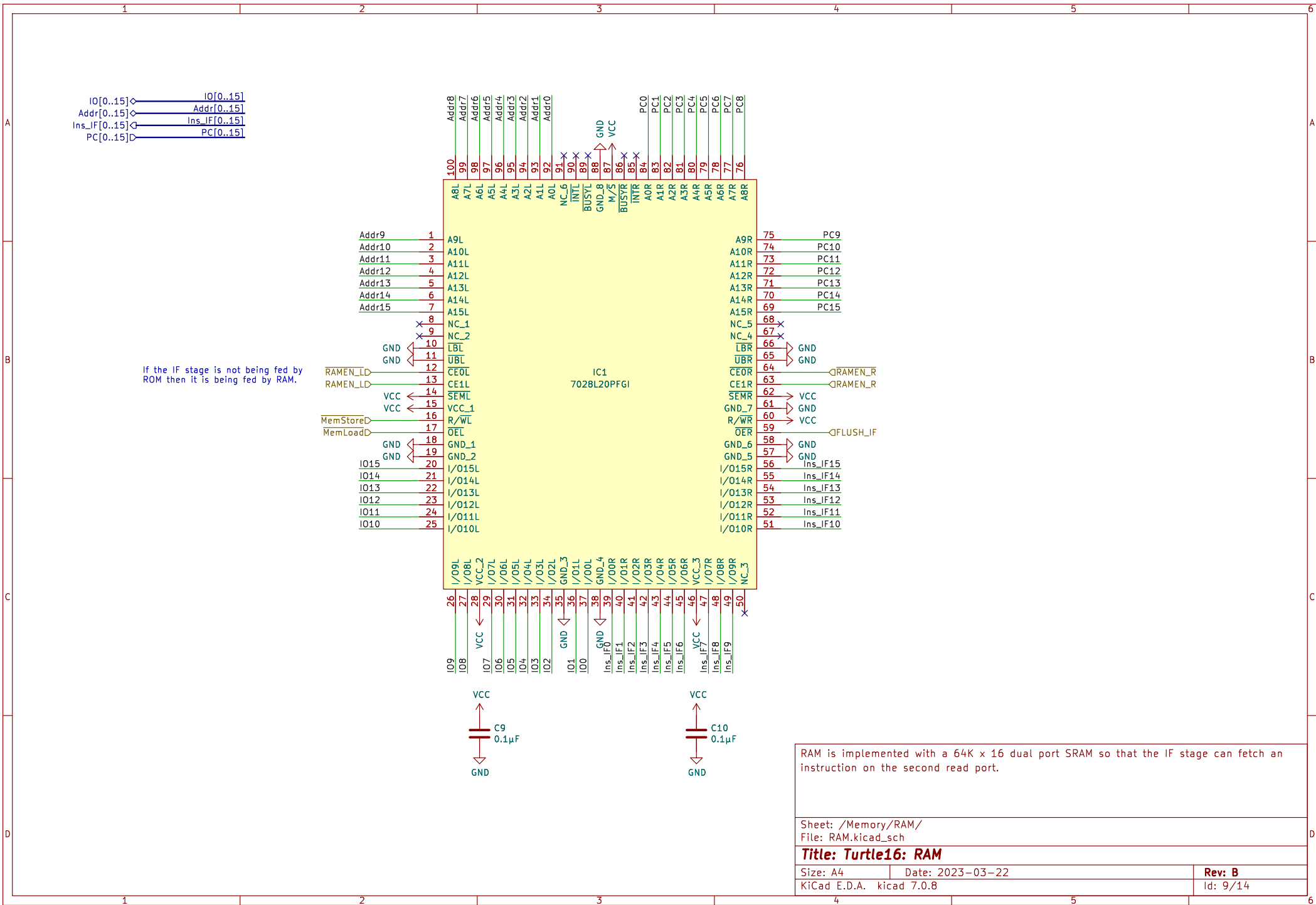
Title: MEM/WB: Control Word Register

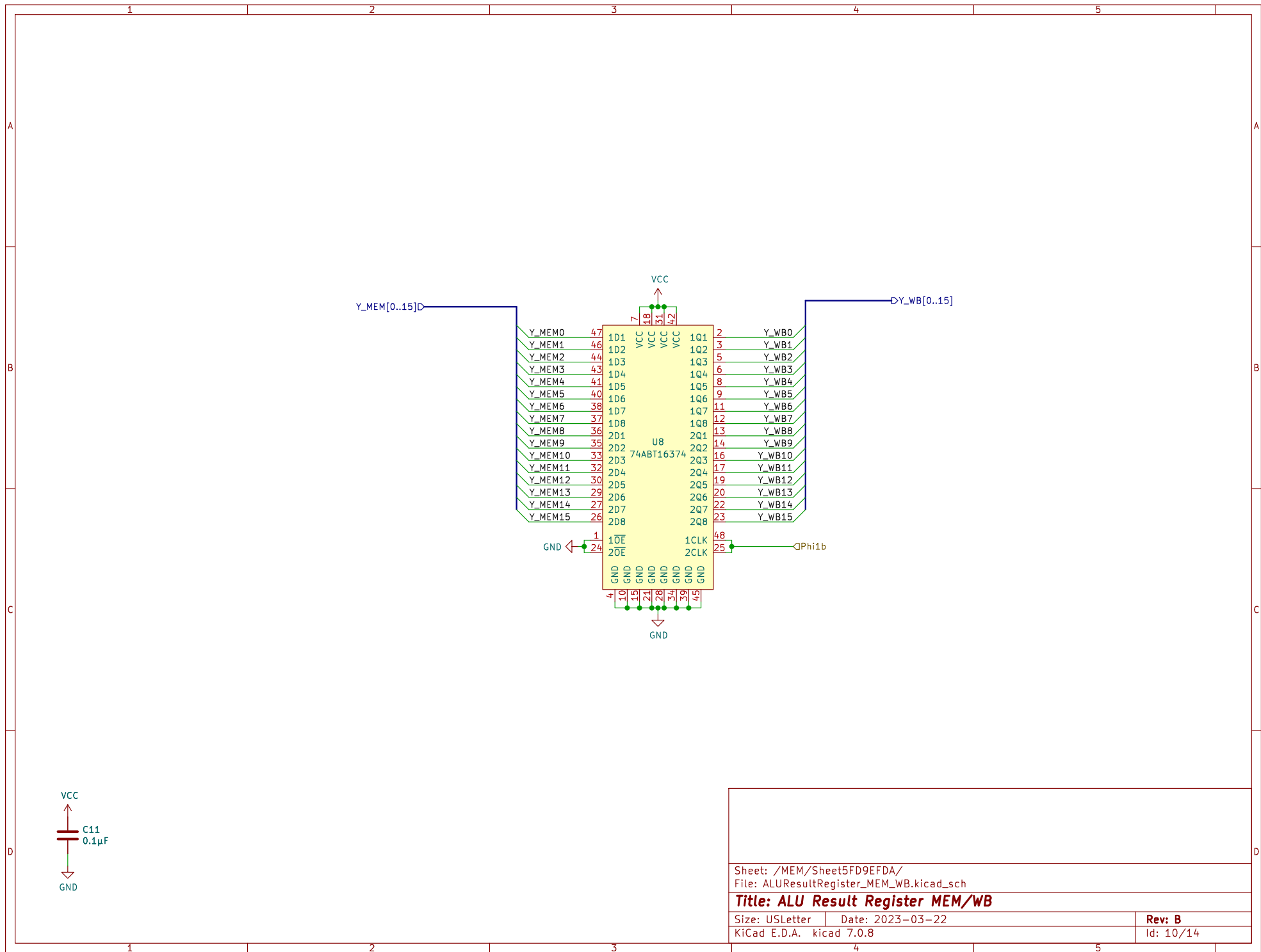
Size: USLetter Date: 2023-03-22
KiCad E.D.A. kicad 7.0.8

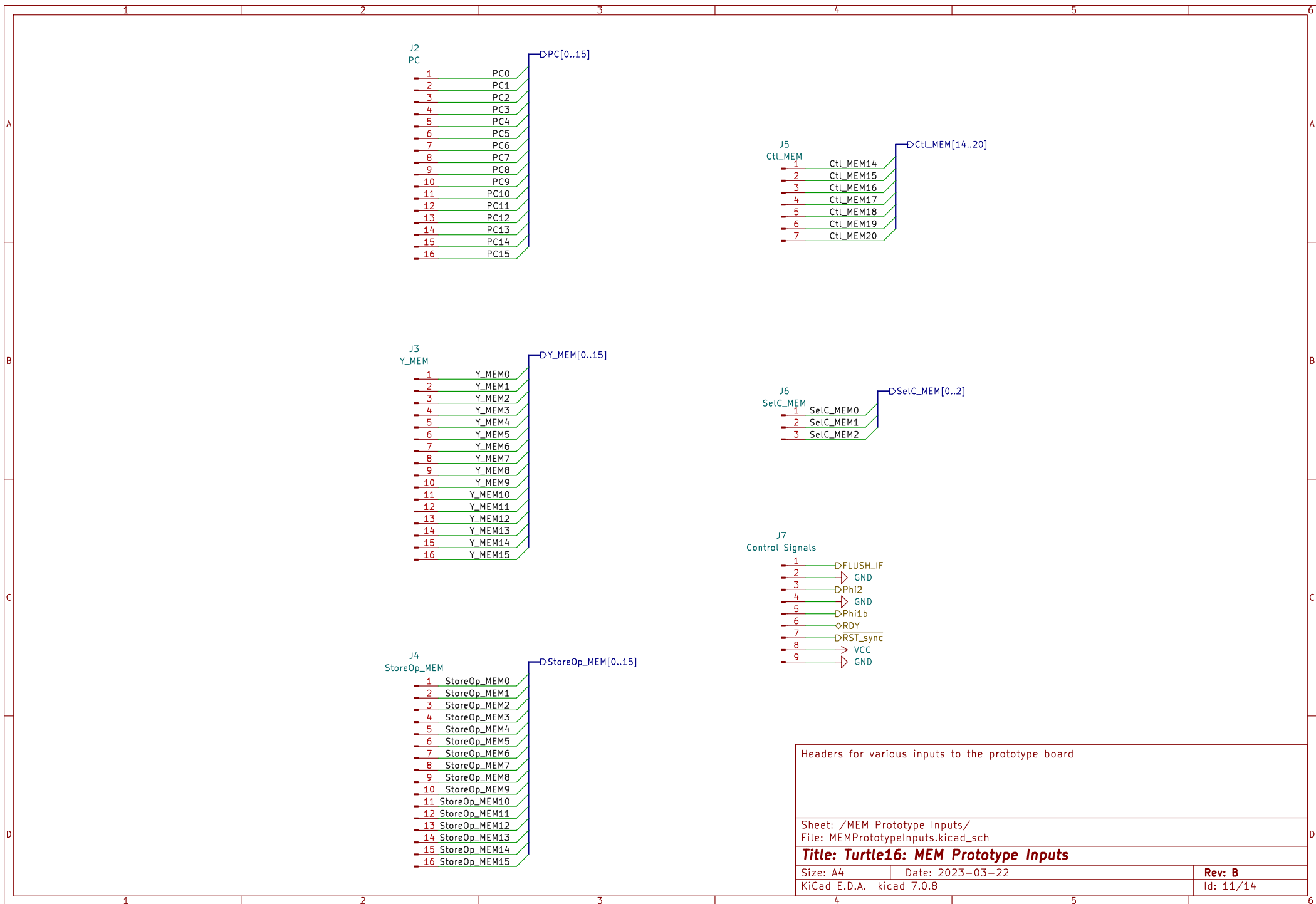
Rev: B
Id: 7/14



Rev: B
Id: 8/14







Headers for various inputs to the prototype board

Sheet: /MEM Prototype Inputs/
File: MEMPrototypeInputs.kicad_sch

Title: Turtle16: MEM Prototype Inputs

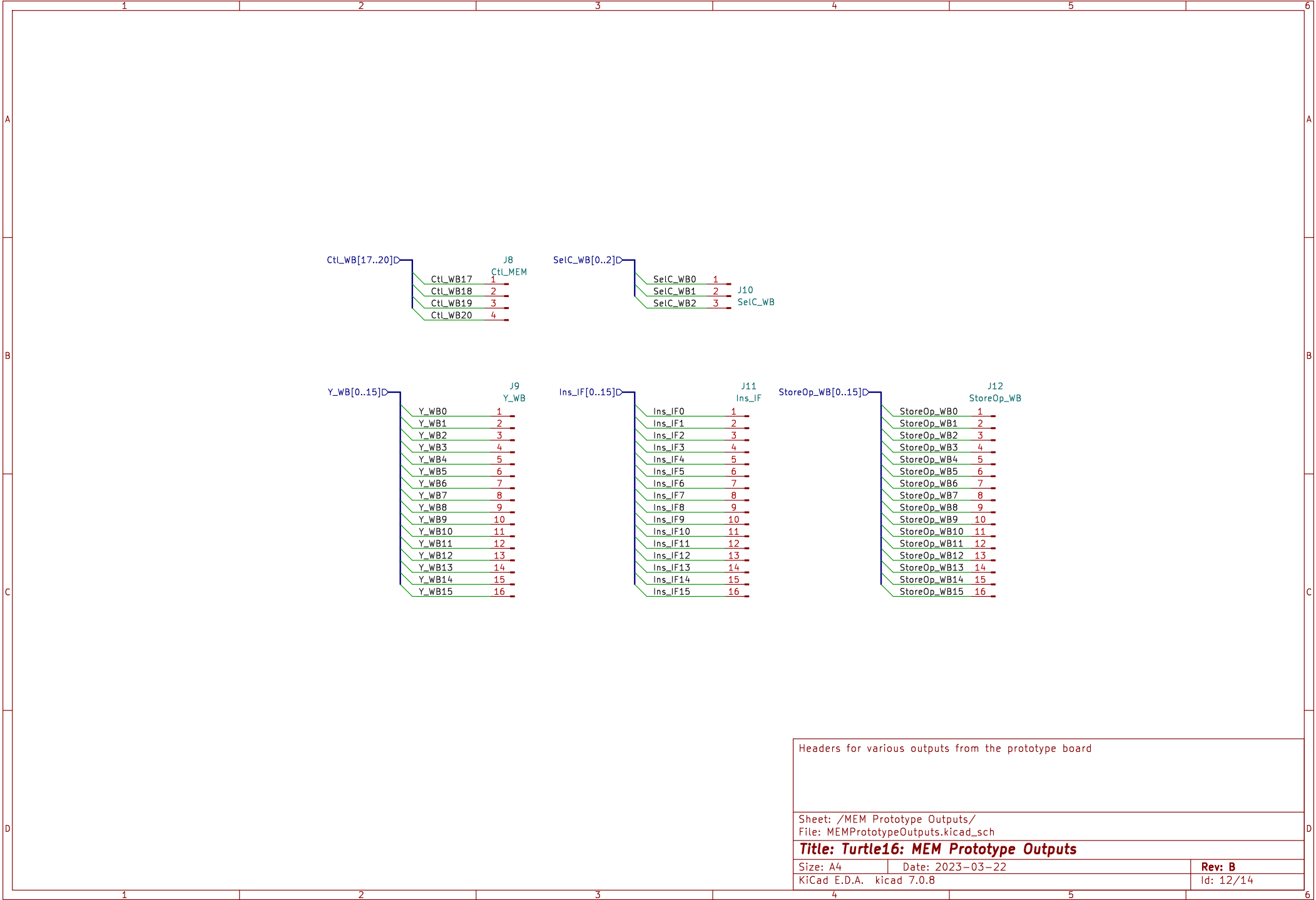
Size: A4

Date: 2023-03-22

Rev: B

KiCad E.D.A. kicad 7.0.8

Id: 11/14



Headers for various outputs from the prototype board		
Sheet: /MEM Prototype Outputs/ File: MEMPrototypeOutputs.kicad_sch		
Title: Turtle16: MEM Prototype Outputs		
Size: A4	Date: 2023-03-22	Rev: B
KiCad E.D.A. kicad 7.0.8		Id: 12/14

