



The diagram shows the pin connections for the PWR_FLAG signal between two headers, J1 and J2.

J1 LowerHeader	J2 UpperHeader
1	PC0
2	PC1
3	PC2
4	PC3
5	PC4
6	PC5
7	PC6
8	PC7
9	PC8
10	PC9
11	PC10
12	PC11
13	PC12
14	PC13
15	PC14
16	PC15
17	FLUSH_IF
18	RAMEN_R

Connections shown in the diagram:

- Pin 17 of J1 is connected to Pin 17 of J2 (FLUSH_IF).
- Pin 18 of J1 is connected to Pin 18 of J2 (RAMEN_R).
- Pin 18 of J1 is also connected to GND and VCC.

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