

Sheet: Speed Control

File: Speed Control.sch

Sheet: Clock

File: Clock.sch

External board to generate a clock signal.

Sheet: /

File: ExternalClock.sch

Title: TurtleTTL: External Clock

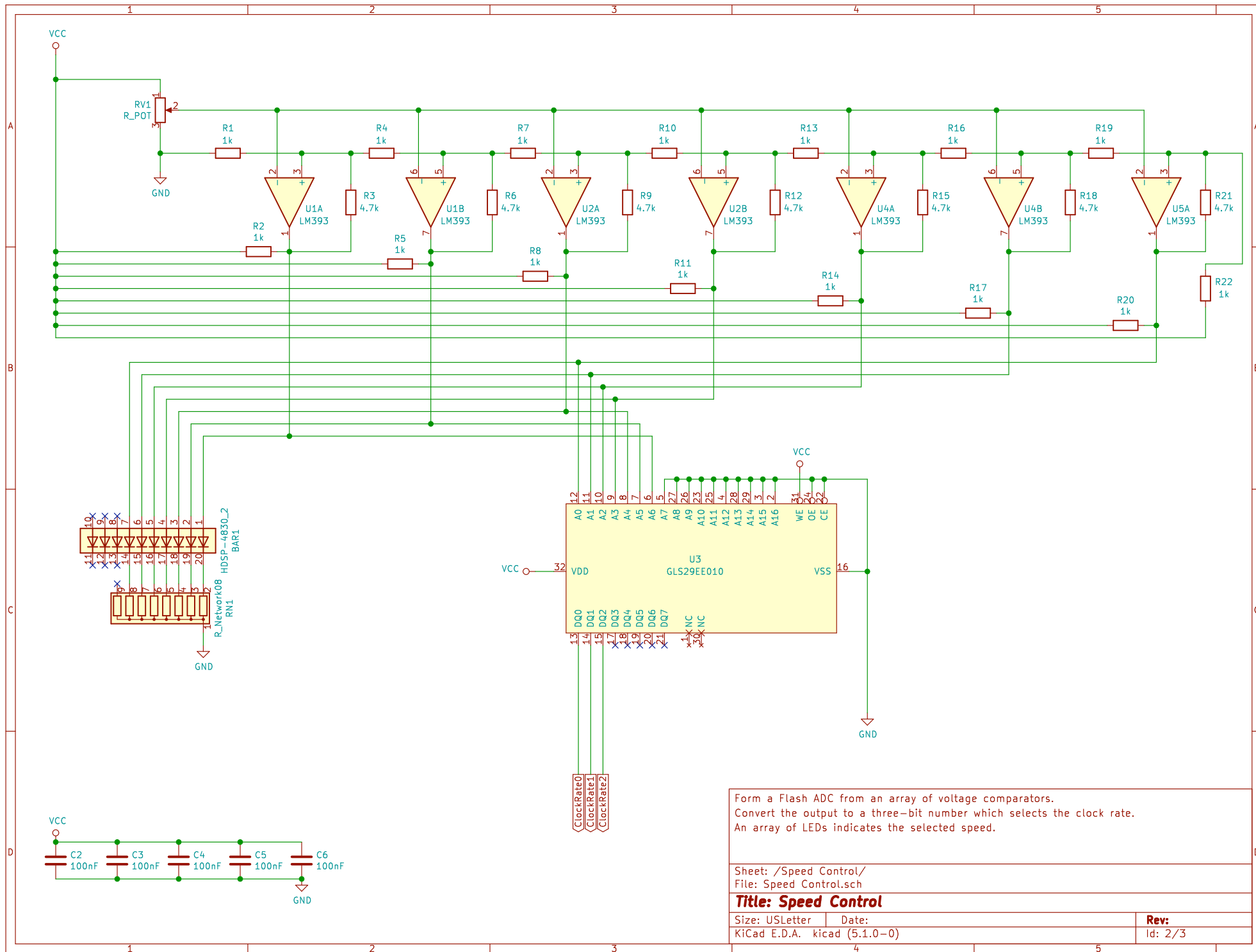
Size: A

Date:

KiCad E.D.A. kicad (5.1.0-0)

Rev:

Id: 1/3



Form a Flash ADC from an array of voltage comparators.
Convert the output to a three-bit number which selects the clock rate.
An array of LEDs indicates the selected speed.

Sheet: /Speed Control/
File: Speed Control.sch

Title: Speed Control

Size: USLetter Date:
KiCad E.D.A. kicad (5.1.0-0)

Rev:
Id: 2/3

