

Sheet: Speed Control

File: Speed Control.sch

Sheet: Clock

File: Clock.sch

External board to generate a clock signal.

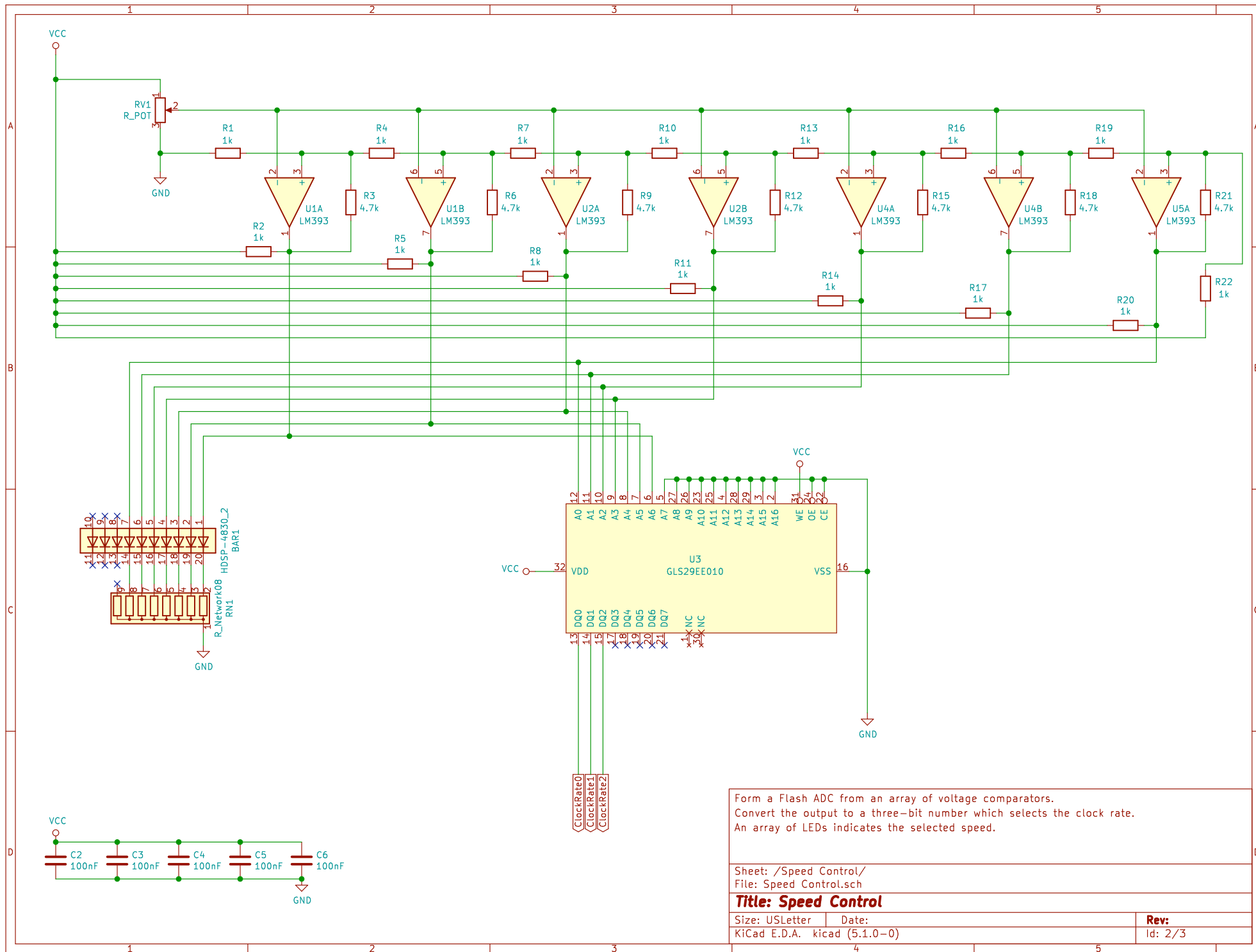
Sheet: /  
File: ExternalClock.sch

**Title: TurtleTTL: External Clock**

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KiCad E.D.A. kicad (5.1.0-0)

Date:

Rev:  
Id: 1/3



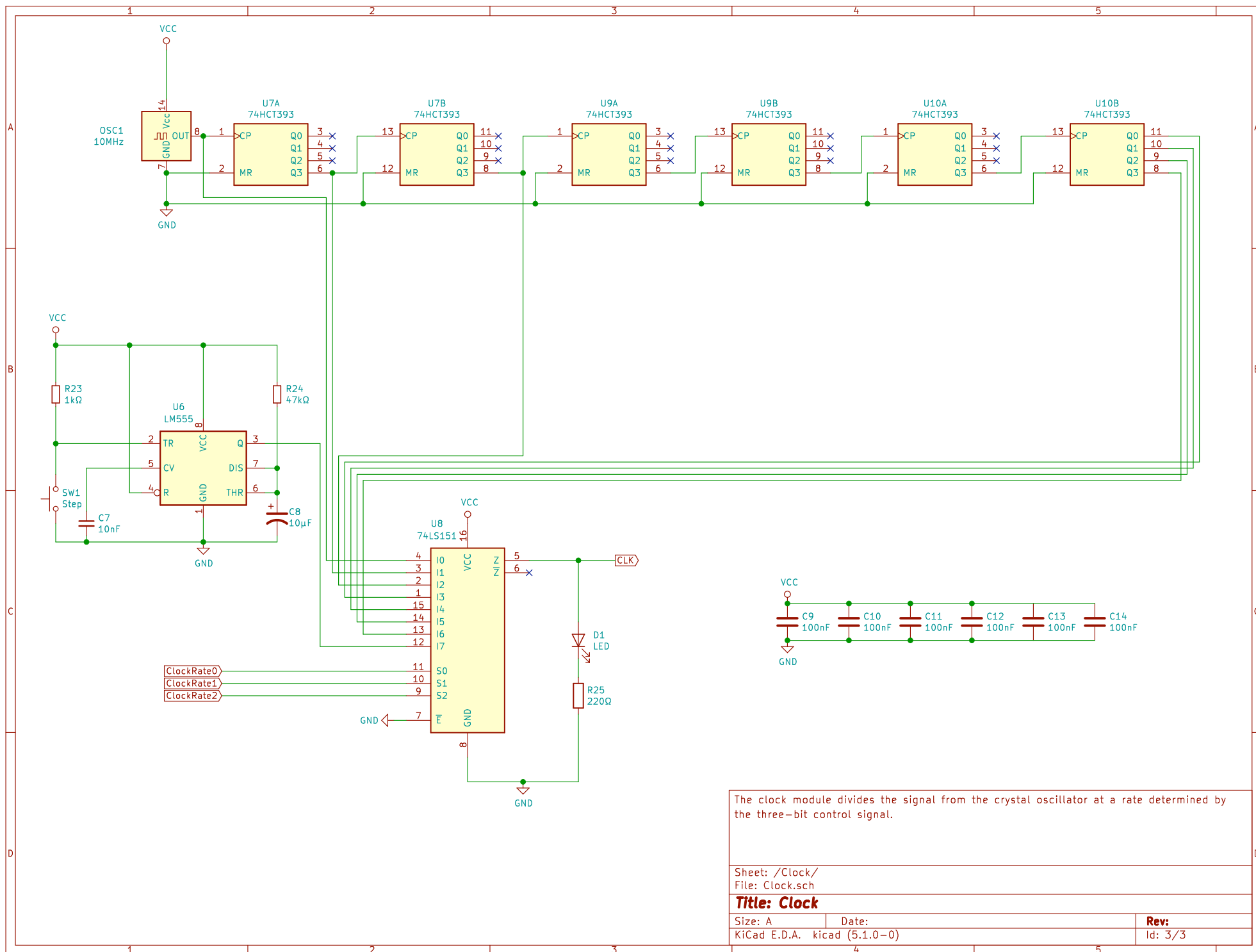
Form a Flash ADC from an array of voltage comparators.  
Convert the output to a three-bit number which selects the clock rate.  
An array of LEDs indicates the selected speed.

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The clock module divides the signal from the crystal oscillator at a rate determined by the three-bit control signal.

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**Title: Clock**

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