FPGA Ignite Summer School 2022

Heidelberg University, June 1-3

The FPGA Ignate Summer School series is a forum for bringing together established and new researches in the field of reconfigurable computing and FPGAs.
The goal is to identify and form new trends as well as facilitating collaborations with in the reconfigurable computing community.

Program

Day 1 (June 1st)

14:00	Get together in Heidelberg Exploring Germanies oldest University

Day 2 (June 2nd)

9:00 – 09:15	Welcome to FPGA Ignite 2022 at ZITI (Dirk Koch)

Security and Runtime

9:15 – 10:15	• Kaspar Matas "*Resource-Elastic Dynamic Stream Processing*" • Bardia Babaei "*Precise Characterizing of FPGAs in Production Systems*" • Shaden Alismail "A *Memory subsystem in Multi-tenant FPGAs*"
Break	

FPGA Technology

10:30 – 11:30	• Nguyen Dao "*Custom eFPGA Design Using Open-source Tools*" • Myrtle Shah "*Place and Route in an Open-source World*" • Bea Healey "*The VPR Tool Flow Inside FABulous*"
Break	

	Applications
11:45 – 12:25	• Kristiyan Manev "*FPGA-Acceleration for Databases*"
	• Steffan Proß "*MassiveSort: Sorting Networks with Hundreds of Thousands of Sort PEs*"
Lunch	

	Workshop
14:00 – 16:00	"***Which FPGA challenge would we tackle with EUR 1M Funding (and how)?***"
	Goal: don't rebuild the world – impact it!

Break	
16:30 – 17:30	Group presentations

Day 3 (June 3rd)

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	New Tends in Methods and Tools
	• Andreas Becher "*Something something FPGA*"
9:00 – 10:00	• Syed Muhammad Ali Asghar "*Increasing Side-Channel Resistance by NetlistRandomization and FPGA-based Reconfiguration*"
Break	

	Grand Challenges for FPGAs (Killerapps)
10:15 – 11:15	
	Daniel Ziener University of Ilmenau

Break	
11:30 – 12:30	**People and Technology** – ethical aspects of technology
Lunch	
14:00 – 15:00	Tour through ZITI
15:00 – 16:00	Wrap-up and networking

Registration

FPGA Ignite works hard to keep financial barrier at the lowest possible

Full registration (until June 27th)	400 €
Student registration (until June 27th)	290 €

Register here with your name and contribution

Venue

The FPGA Ignite Summer School wild be held at ZITI, Heidelberg University:

Universität Heidelberg
Institut für Technische Informatik
(ZITI)
Im Neuenheimer Feld 368
69120 Heidelberg

The Lecture hall will be

INF 340 Room U014 (basement)

Hotel

We have reserved rooms at the hotel *Goldener Hecht* Heidelberg (use code FPGA Ignite) (Steingasse 2, 69117 Heidelberg)

Contact

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TOP   C Zuletzt aktualisiert: Mittwoch, 21. Dezember 2022 15:28