# UPF Commands Supported by VCS NLP

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# 1 Introduction

This document describes the Unified Power Format (UPF) commands supported by VCS Native Low Power (NLP). These commands are used to specify the power intent for low power designs.

# 2 UPF Commands Supported by VCS NLP

The following section describe the IEEE 1801 UPF commands supported by VCS NLP. For more information about using the UPF commands, see the IEEE 1801 (UPF) specification:

- Basic Power Network Commands
- Isolation Commands
- Retention Commands
- Logic Editing Commands
- Query Commands
- Simulation/Verification Extension
- Utility Commands
- Other Supported Power Intent Commands

## 2.1 Basic Power Network Commands

The basic power commands define the power domains of the design and the supply ports, supply nets, and power switches of each domain. The following are the basic power network commands supported by VCS NLP:

- associate supply set
- create power domain
- create power switch
- create supply port
- create\_supply\_net
- connect\_supply\_net
- create supply set
- set\_domain\_supply\_net

## 2.1.1 associate\_supply\_set

The <code>associate\_supply\_set</code> command associates a supply set handle to another supply set. The <code>supply\_set\_name</code> specifies the name of the supply set that must be associated with the handle specified with the <code>-handle</code> option.

#### **Syntax**

```
associate_supply_set supply_set_name_list
  [ -handle supply set handle ]
```

## 2.1.2 create\_power\_domain

The <code>create\_power\_domain</code> command defines a power supply distribution network at the current scope (hierarchical level) or at the scope of a specified hierarchical instance.

#### **Syntax**

```
create_power_domain domain_name
  [-elements element_list]
  [-include_scope]
  [-available_supplies supply_set_sef_list]
  [-supply {supply_set_handle [supply_set_ref]}*]
  [-scope instance_name]
  [-update]
```

## 2.1.3 create\_power\_switch

The create\_power\_switch command creates an instance of a power switch in the scope of a power domain. The switch has at least one input supply port and one output supply port.

#### **Syntax**

```
create_power_switch switch_name
   -output_supply_port {port_name [supply_net_name]}
{-input_supply_port {port_name [supply_net_name]}}*
{-control_port {port_name [net_name]}}*
{-on_state {state_name input_supply_port {boolean_function}}}*
[-off_state {state_name {boolean_function}}]*
[-supply_set supply_set_name]
[-on_partial_state {state_name input_supply_port {boolean_function}}]*
[-ack_port {port_name net_name [{boolean_function}]}]*
[-ack_delay {port_name delay}]*
[-error_state {state_name {boolean_function}}]*
[-domain domain name]
```

## 2.1.4 create supply port

The <code>create\_supply\_port</code> command creates a supply port at the current scope or at the scope of a power domain specified with the -domain option.

#### **Syntax**

```
create_supply_port port_name
  [-domain domain_name]
  [-direction <in | out | inout>]
```

## 2.1.5 create\_supply\_net

The <code>create\_supply\_net</code> command creates a supply net to supply power or ground to a power domain.

```
create_supply_net net_name
  [-domain domain_name] [-reuse]
  [-resolve <unresolved | one hot | parallel>]
```

<sup>\*</sup> Indicates that the item in curly braces can be repeated in the command.

<sup>\*</sup> Indicates that the item in curly braces can be repeated in the command.

## 2.1.6 connect\_supply\_net

The connect\_supply\_net command specifies an explicit connection of a supply net to a list of supply ports, thereby overriding any implicit connections that might otherwise apply.

#### **Syntax**

```
connect_supply_net net_name
  [-ports list]
  [-vct vct name]
```

## 2.1.7 create\_supply\_set

The create\_supply\_set command creates a supply set at the current level of logic hierarchy. A supply set is a collection of supply nets. A supply set consists of the following functions:

- Power
- Ground
- pwell
- nwell

#### **Syntax**

```
create_supply_set set_name
  [-function {func_name [net_name]}]*
  [-update]
```

## 2.1.8 set\_domain\_supply\_net

The <code>set\_domain\_supply\_net</code> command specifies the primary power net and primary ground net for an existing power domain.

#### **Syntax**

```
set_domain_supply_net domain_name
-primary_power_net supply_net_name
-primary ground net supply net name
```

#### 2.2 Isolation Commands

The isolation commands specify the strategy for inserting isolation cells at the outputs of switched (power-down) domains. Following are the isolation commands supported by VCS NLP:

- set isolation
- · set isolation control
- map\_isolation\_cell

#### 2.2.1 set isolation

The set\_isolation command specifies the isolation strategy for a power domain and the elements in that domain on which the strategy is applied.

```
set isolation isolation name
```

<sup>\*</sup> Indicates that the item in curly braces can be repeated in the command.

```
-domain ref domain name
[-elements element list]
[-exclude elements exclude list]
-source source supply ref
-sink sink supply ref
[-diff supply only <TRUE | FALSE>]
[-applies to <inputs | outputs | both>]
[-isolation_power_net net_name]
[-isolation ground net net name]
[-no isolation]
[-location <automatic | self | fanout | parent >]
[-clamp value {<0 | 1 | Z | latch>*}]
[-isolation signal signal list <>]
[-isolation sense <high | low | {<high | low>*}>]]
[-isolation supply set supply set list]
[-isolation sense {<high | low>*}]
[-update]
```

## 2.2.2 set\_isolation\_control

The set\_isolation\_control command allows the specification of the isolation control signal and the logical sense of that signal.

#### **Syntax**

```
set_isolation_control isolation_name
  -domain domain_name
  -isolation_signal signal_name
  [-isolation_sense <high | low>]
  [-location <self | parent| fanout>]
```

## 2.2.3 map\_isolation\_cell

The map\_isolation\_cell command maps a particular isolation strategy to a library cell or range of library cells.

#### **Syntax**

```
map_isolation_cell isolation_name
  -domain domain_name
  [-lib_cell_type lib_cell_type]
  [-lib model name model name {-port {port name net name}}*]
```

## 2.3 Retention Commands

The retention commands specify the strategy for inserting retention cells inside switched (power-down) domains. The following are the retention commands supported by VCS NLP:

- set\_retention
- set retention control
- · set retention elements

<sup>\*</sup> Indicates that the item in curly braces can be repeated in the command.

<sup>\*</sup> Indicates that the item in curly braces can be repeated in the command.

## 2.3.1 set retention

The set\_retention command specifies the registers in the domain that need to be implemented as retention registers and identifies the save and restore signals for the retention functionality.

#### **Syntax**

```
set retention retention name
  -domain domain name
   [-elements element list]
   [-retention power net net name]
   [-retention ground net net name]
   [-retention supply set ret supply set]
   [-no retention]
   [-save_signal {logic_net <high | low | posedge | negedge>}
  -restore signal {logic net <high | low | posedge | negedge>}]
   [-save condition {{boolean function}}]
   [-restore condition {{boolean function}}]
   [-retention condition {{boolean function}}]
   [-use retention as primary]
   [-parameters {<RET SUP COR | NO RET SUP COR | SAV RES COR | NO SAV RES COR> *}]
   [-transitive <TRUE | FALSE>]
   [-update]
```

## 2.3.2 set\_retention\_control

The set\_retention\_control command allows the specification of the retention control signal and the logical sense of that signal.

**Note**: The set\_retention\_control command is deprecated as per UPF 2.0 LRM (IEEE Std 1801-2013).

#### **Syntax**

```
set_retention_control retention_name
  -domain domain_name
  -save_signal {{net_name < high | low | posedge | negedge>}}
  -restore_signal {{net_name < high | low | posedge | negedge>}}
[-assert_r_mutex {{net_name < high | low | posedge | negedge>}}]*
[-assert_s_mutex {{net_name < high | low | posedge | negedge>}}]*
[-assert rs mutex {{net_name < high | low | posedge | negedge>}}]*
```

## 2.3.3 set\_retention\_elements

The set\_retention\_elements command defines a list of state elements whose collective state shall be maintained coherently if retention is applied to any of these elements in the list.

```
set_retention_elements retention_list_name
[-elements element list]
```

<sup>\*</sup> Indicates that the item in curly braces can be repeated in the command.

<sup>\*</sup> Indicates that the item in curly braces can be repeated in the command.

## 2.4 Logic Editing Commands

The logic editing commands create logic nets and logic ports and make connections between these nets and ports, irrespective of the power network. The following are the logic editing commands supported by VCS NLP:

- create\_logic\_net
- create logic port
- connect\_logic\_net

## 2.4.1 create\_logic\_net

The create logic net command creates a new logic net in the active scope.

#### **Syntax**

```
create logic net net name
```

## 2.4.2 create\_logic\_port

The <code>create\_logic\_port</code> command creates a new logic port in the active scope and specifies the port direction (in for input and out for output).

#### **Syntax**

```
create_logic_port port_name
   [-direction <in | out>]
```

## 2.4.3 connect\_logic\_net

The connect\_logic\_net command connects a specified logic net to one or more specified logic ports.

## **Syntax**

```
connect_logic_net net_name
  -ports port_list
[-reconnect]
```

# 2.5 Query Commands

Each query commands returns a list of objects in the design that match the criteria you specify in the command.

**Note**: Query commands are not supported in the UPF. You can specify query commands in a configuration file and pass it to the vcs command line using the <code>-lpa\_bind</code> <filename> option.

The following are the guery commands supported by VCS NLP:

- find objects
- query cell instances
- query\_cell\_mapped
- query pst
- query\_pst\_state
- query power switch
- query\_design\_attributes
- query\_hdl2upf\_vct
- query\_isolation
- query\_power\_domain
- query\_retention

- query retention control
- query supply net

## 2.5.1 find\_objects

The find\_objects command finds instances, nets, or ports defined in the logic hierarchy in a specified scope and returns a list of object names that match a given search pattern.

## **Syntax**

```
find_objects scope
  -pattern search_ pattern
  [-object_type <inst | port | net>]
  [-direction <in | out | inout>]
  [-transitive [<TRUE | FALSE>]]
  [-regexp | -exact]
  [-ignore_case]
  [-non leaf | -leaf only]
```

## 2.5.2 query\_cell\_instances

The <code>query\_cell\_instances</code> command returns a list of instance names for all instances of a given reference cell in the current scope of the design. You can optionally restrict the query to a given power domain.

#### **Syntax**

```
query_cell_instances cell_name
  [-domain domain_name]
```

## 2.5.3 query cell mapped

The query cell mapped command returns the reference cell name of a given cell instance.

#### Syntax

```
query cell mapped instance name
```

## 2.5.4 query\_pst

The <code>query\_pst</code> command returns information about previously defined power state tables in the active scope. The command <code>query\_pst \* returns</code> a list of the power state table names.

#### **Syntax**

```
query_pst table_name
  [-detailed]
```

## 2.5.5 query\_pst\_state

The <code>query\_pst\_state</code> command returns information about the states that have been previously defined for a specified power state table.

```
query_pst_state state_name
    -pst table_name
[-detailed]
```

## 2.5.6 query\_power\_switch

The query\_power\_switch command returns information about previously defined power switches.

#### **Syntax**

```
query_power_switch switch_name
[-detailed]
```

## 2.5.7 query\_design\_attributes

The  $query\_design\_attributes$  command queries attribute information for a specified element name or model name.

#### **Syntax**

```
query_design_attributes
    <-element element_name | -model model_name>
    [-detailed]
```

## 2.5.8 query\_hdl2upf\_vct

The  $query\_hdl2upf\_vct$  command can list and query any previously defined value conversion table (VCT).

#### **Syntax**

```
query_hdl2upf_vct vct_name
   [-detailed]
```

## 2.5.9 query\_isolation

The <code>query\_isolation</code> command can list the previously defined isolation strategies for the specified power domain domain name.

#### **Syntax**

```
query_isolation isolation_name
  -domain ref_domain_name
[-detailed]
```

## 2.5.10 query\_power\_domain

The query power domain command queries the parameters of a power domain.

#### **Syntax**

```
query_power_domain domain_name
[-detailed]
```

#### 2.5.11 query retention

The query\_retention command lists the previously defined retention strategies for the specified power domain domain\_name.

```
query_retention retention_name
  -domain domain_name
  [-detailed]
```

## 2.5.12 query\_retention\_control

The query\_retention\_control command queries the retention control information for a retention strategy.

#### **Syntax**

```
query_retention_control retention_name
   -domain domain_name
[-detailed]
```

## 2.5.13 query\_supply\_net

The query supply net command returns the information about a previously created supply net.

#### **Syntax**

```
query_supply_net net_name
    [-domain domain_name]
    [-detailed]
```

## 2.6 Simulation/Verification Extension

The simulation and verification extension commands support low-power checking by simulation and verification tools. The following are the simulation and verification extension commands supported by VCS NLP:

- set\_design\_top
- set partial on translation

## 2.6.1 set\_design\_top

The set design top command specifies the root of the design.

#### **Syntax**

```
set_design_top root
```

## 2.6.2 set\_partial\_on\_translation

The  $set_partial_on_translation$  command specifies the translation of the PARTIAL\_ON state to either FULL\_ON or FULL\_OFF to evaluate the power state of supply sets and power domains.

#### **Syntax**

```
set_partial_on_translation
[FULL ON | OFF]
```

# 2.7 Utility Commands

These are the commands to load and execute UPF commands from a file, to write a set of UPF commands to a file, and to set the hierarchical scope for subsequent UPF commands. The following are the utility commands supported by VCS NLP:

- load upf
- set scope
- set design attributes
- set\_port\_attributes

#### 2.7.1 load upf

The load upf command executes the UPF commands in a specified file.

#### **Syntax**

```
load_upf upf_file_name
  [-scope instance_name]
```

## 2.7.2 set\_scope

The set\_scope command specifies the hierarchical scope of subsequent commands, including UPF commands.

#### **Syntax**

```
set_scope [instance]
```

## 2.7.3 set\_design\_attributes

The set\_design\_attributes command sets the attributes of one or more cells. The -models option specifies a list of design models on which the attributes are set.

#### **Syntax**

```
set_design_attributes
  [-models model_list | -elements element_list]
  {-attribute name value}*
```

\* Indicates that the item in curly braces can be repeated in the command.

## 2.7.4 set port attributes

The set\_port\_attributes command specifies a collection of ports where the attributes must be set for the source or sink, for the interface of the power domains, when used with the set isolation command.

#### **Syntax**

```
set_port_attributes
  [-model name]
  [-elements element_list]
  [-applies_to <inputs | outputs | both>]
  [-attribute {name value}]*
  [-clamp_value <0 | 1 | Z | latch>]
  [-driver_supply supply_set_ref]
  [-receiver_supply supply_set_ref]
  [-pg_type pg_type_value]
  [-related_power_port supply_port_name]
  [-related_ground_port supply_port_name]
  [-related_bias_ports supply_port_name]list]
  [-repeater_supply supply_set_ref]
  [-feedthrough]
  [-unconnected]
```

\* Indicates that the item in curly braces can be repeated in the command.

## 2.8 Other Supported Power Intent Commands

Following are the other UPF commands supported by VCS NLP to specify power intent:

- bind checker
- describe state transition
- set\_design\_top
- set equivalent
- set\_simstate\_behavior

## 2.8.1 bind\_checker

The bind\_checker command inserts checker modules into a design without modifying the design code or introducing functional changes.

#### **Syntax**

```
bind_checker instance_name
  -module checker_name
  [-elements element_list]
  [-ports {{port_name net_name}*}]
  [-parameters {{param name param value}*}]
```

## 2.8.2 describe\_state\_transition

The describe\_state\_transition command specifies the legality of a transition from one object's named power state to another.

#### **Syntax**

```
describe_state_transition transition_name
  -object object_name
  -from {from_list} -to {to_list} -paired {{from_state to_state}*}}
[-legal | -illegal]
```

## 2.8.3 set\_design\_top

The set design top command specifies the root of the design.

## **Syntax**

```
set_design_top root
```

## 2.8.4 set\_equivalent

The  $\mathtt{set\_equivalent}$  command declares that the specified supplies are electrically or functionally equivalent.

```
set_equivalent
  [-function_only]
  [-nets supply_net_name_list]
  [-sets supply_set_name_list]]
```

<sup>\*</sup> Indicates that the item in curly braces can be repeated in the command.

<sup>\*</sup> Indicates that the item in curly braces can be repeated in the command

## 2.8.5 set\_simstate\_behavior

The  $\mathtt{set\_simstate\_behavior}$  command specifies the simstate behavior for models or instances.

```
set_simstate_behavior <ENABLE | DISABLE>
  [-models list]
  [-elements element_list]
  [-exclude_elements exclude_list]
```

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