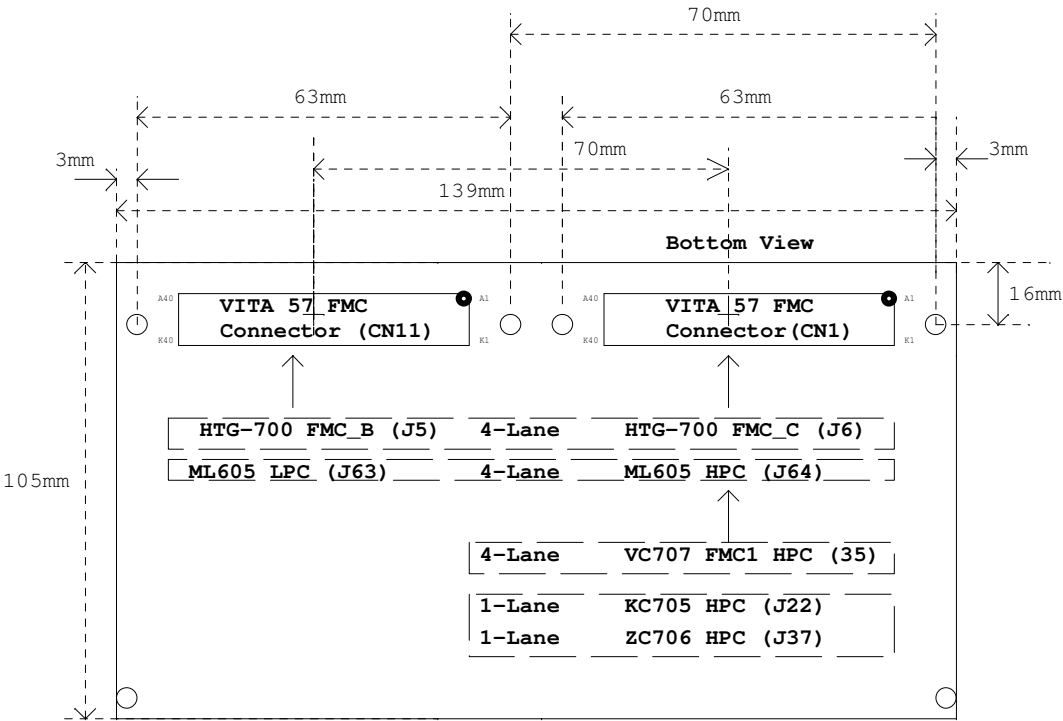
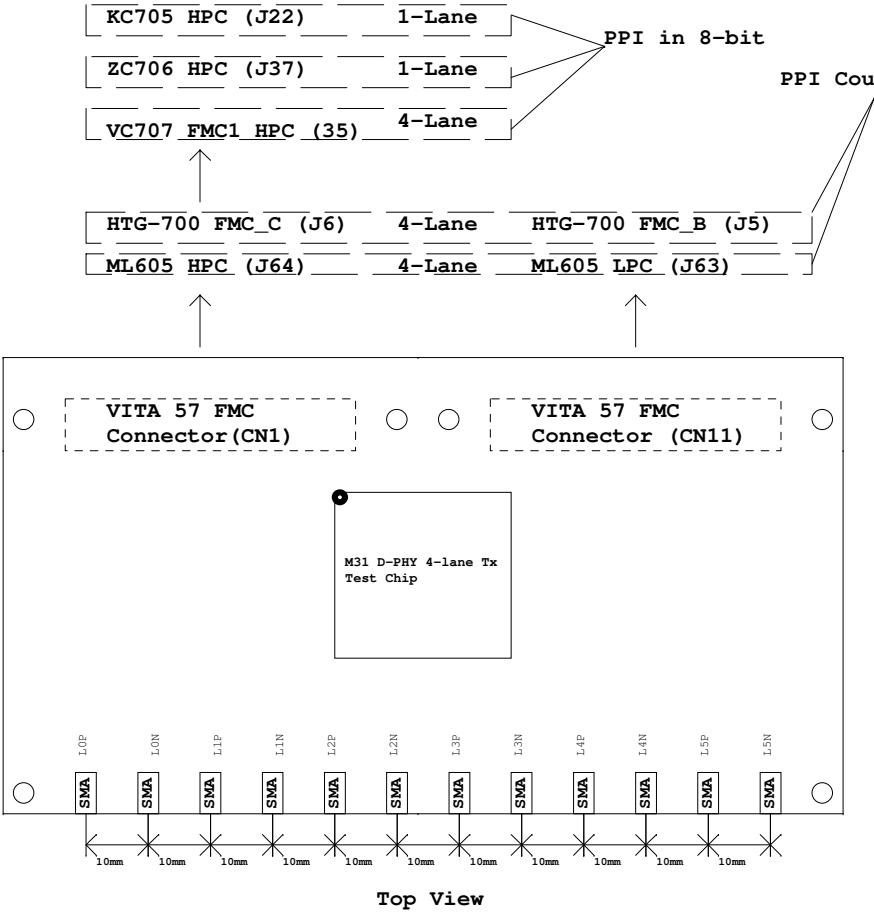
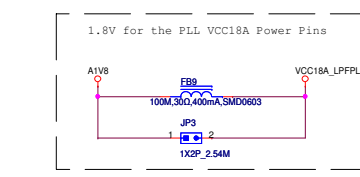
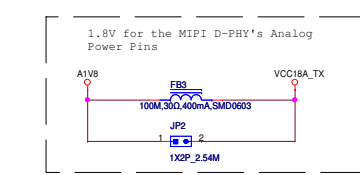
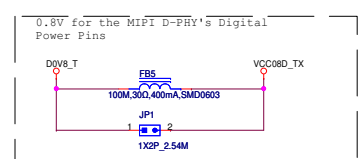
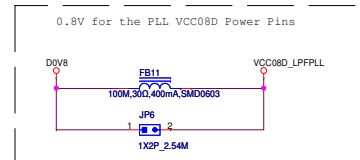
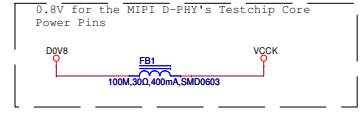
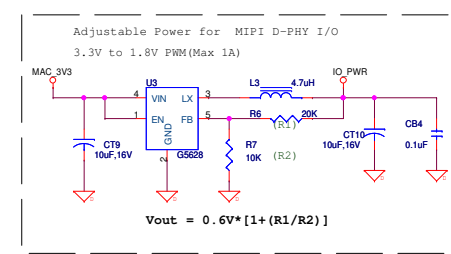
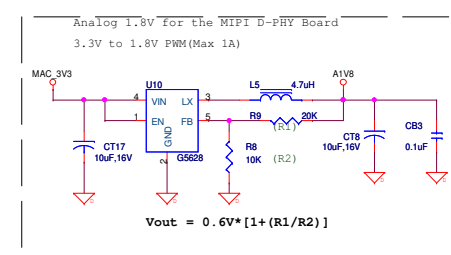
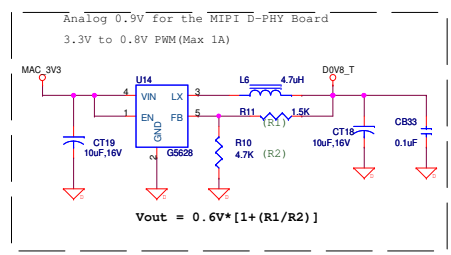
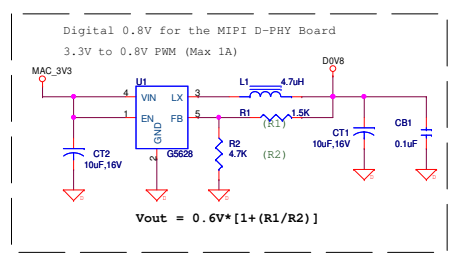
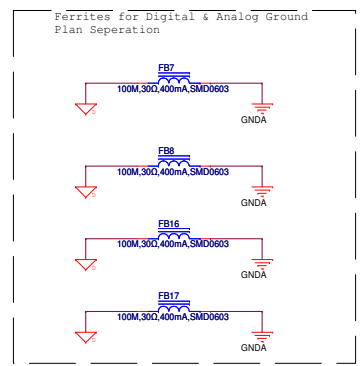
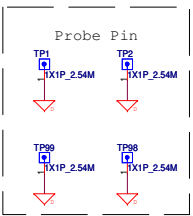
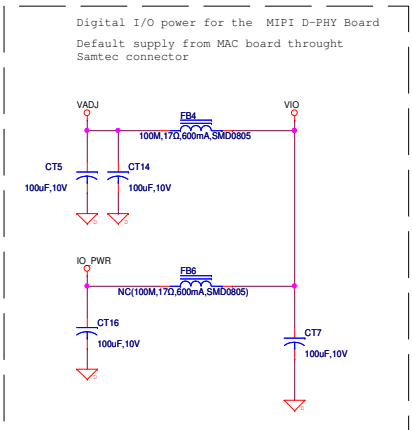
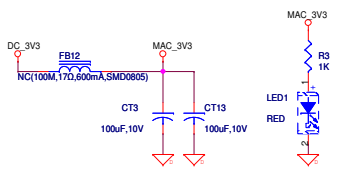
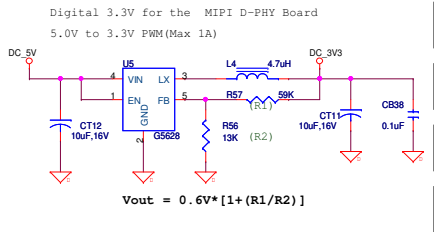
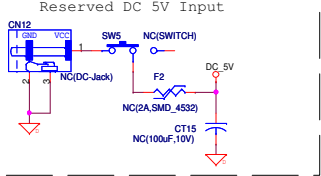
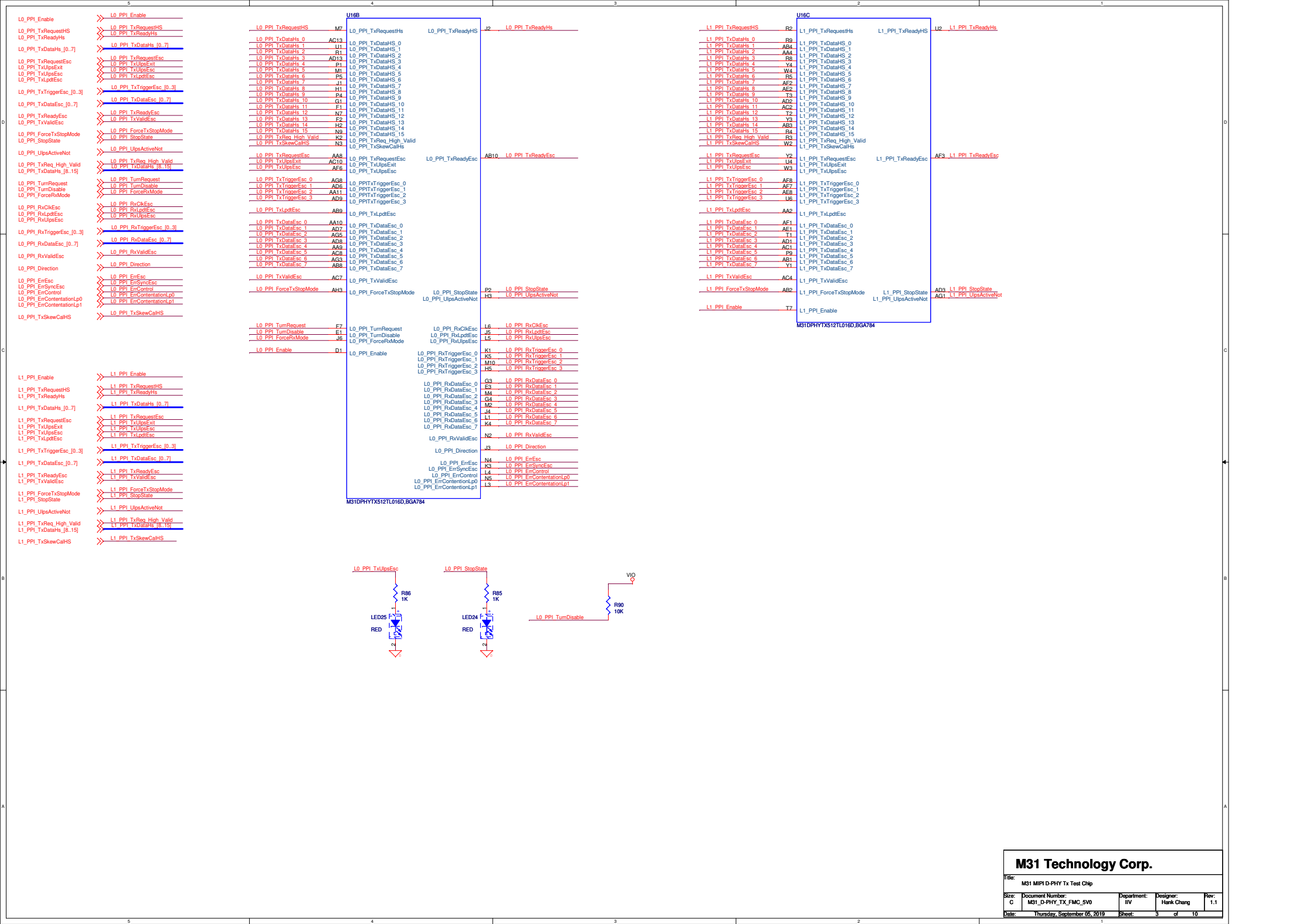


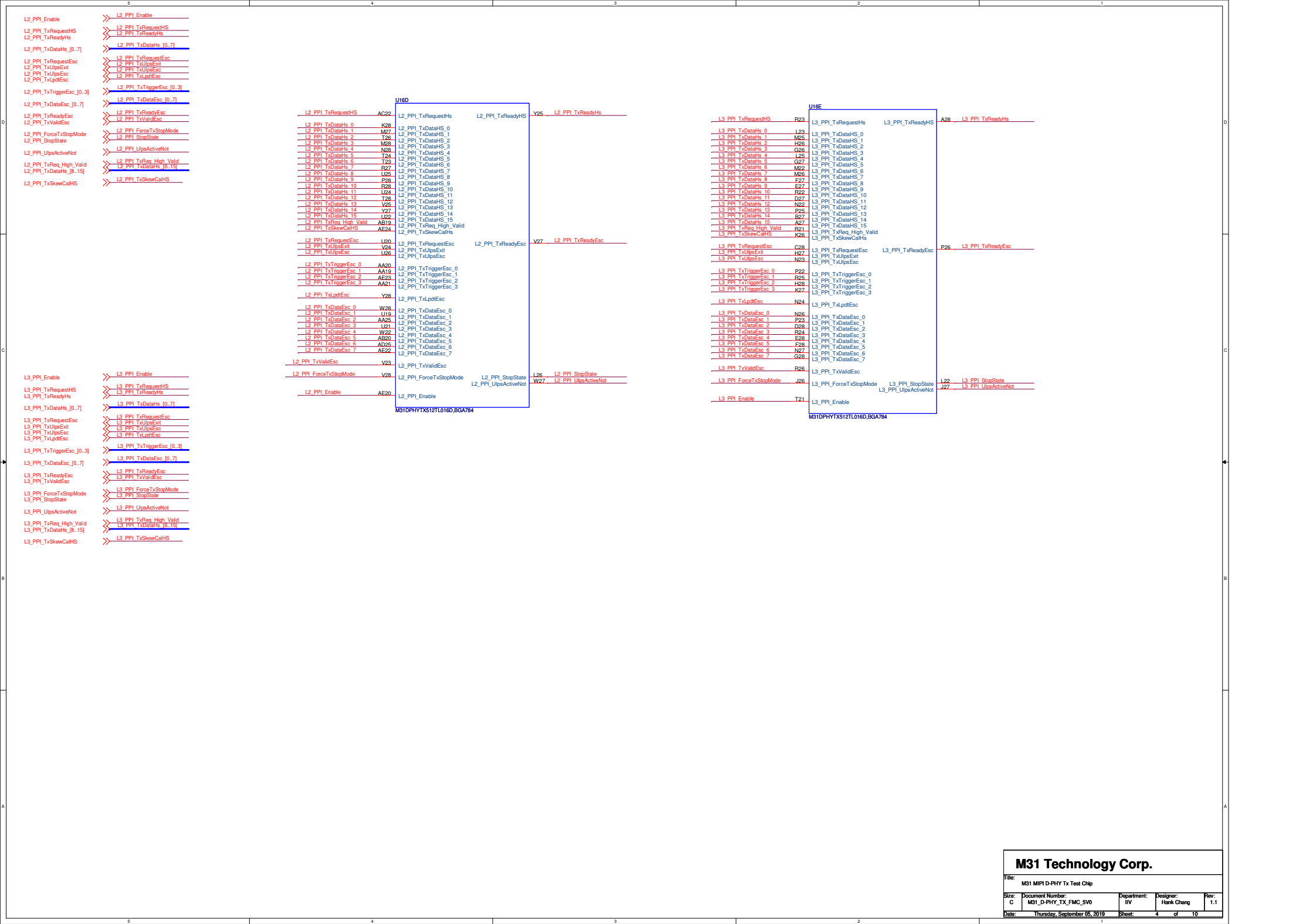
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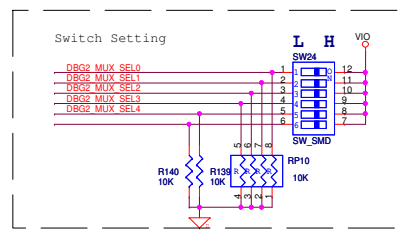
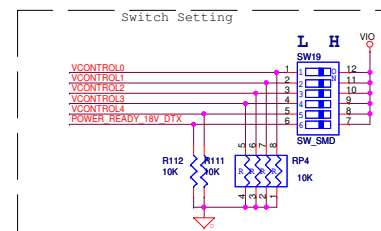
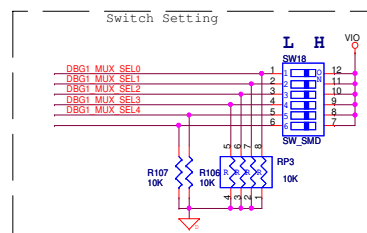
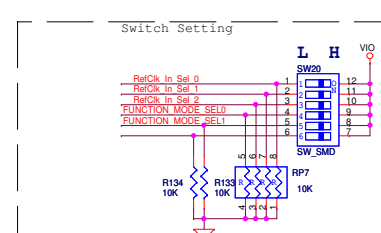
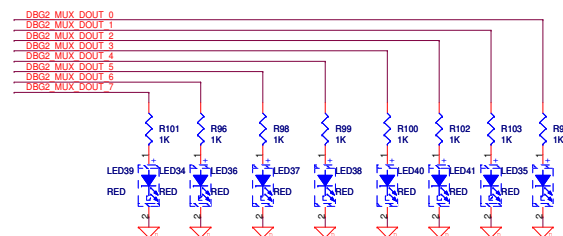
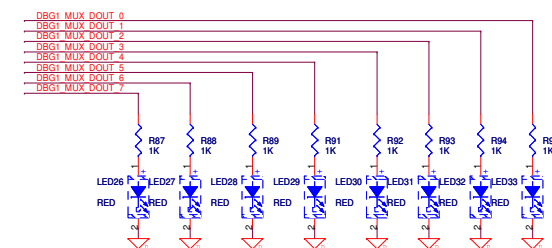
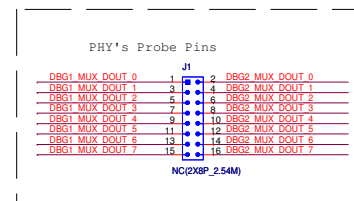
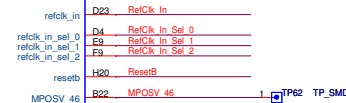
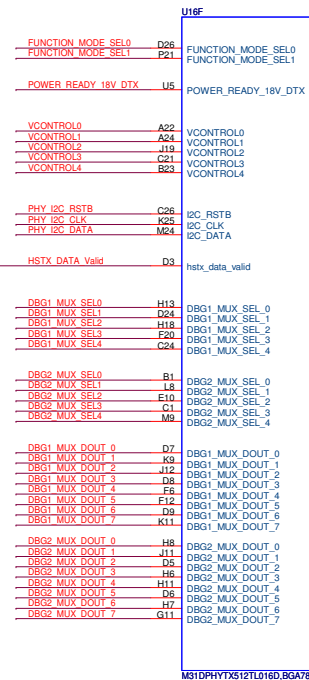
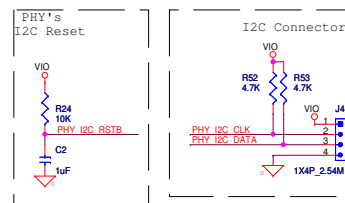
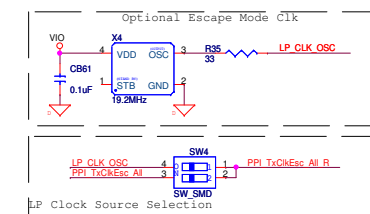
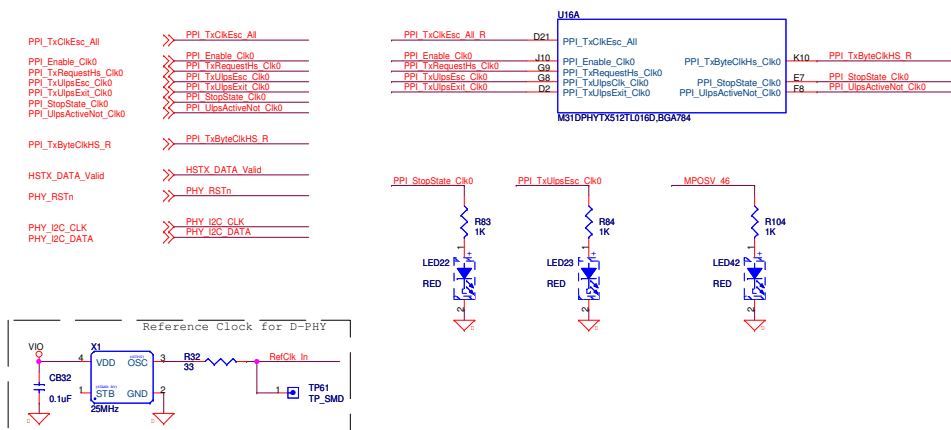
Version	Date	Revision Reason
1.0	05-01-2018	For T28HPC+ CD-PHY Combo Tx Testchip (BGA676)
1.1	02-12-2019	1. Add pull-up for TX-SyncHS_A11 2. More compact size
4.0	02-21-2019	For HI055G D-PHY Tx v1.2 Testchip (LQFP256) PPI only have L0, L2 and CLK Lane
5.0	09-05-2019	For TL016D D-PHY Tx Testchip (BGA784)

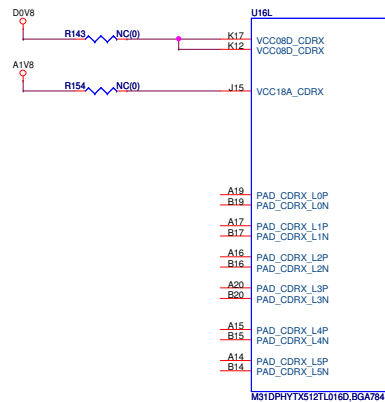
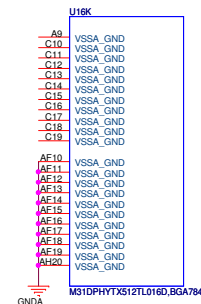
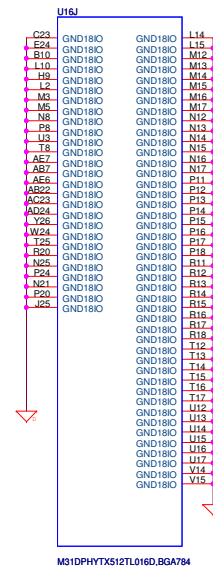
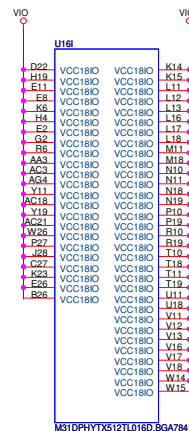
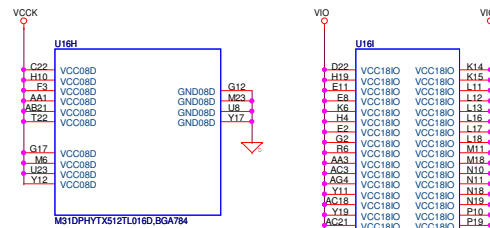
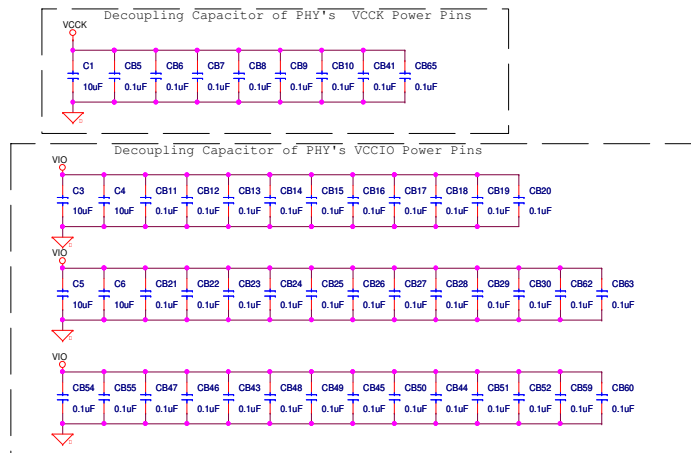


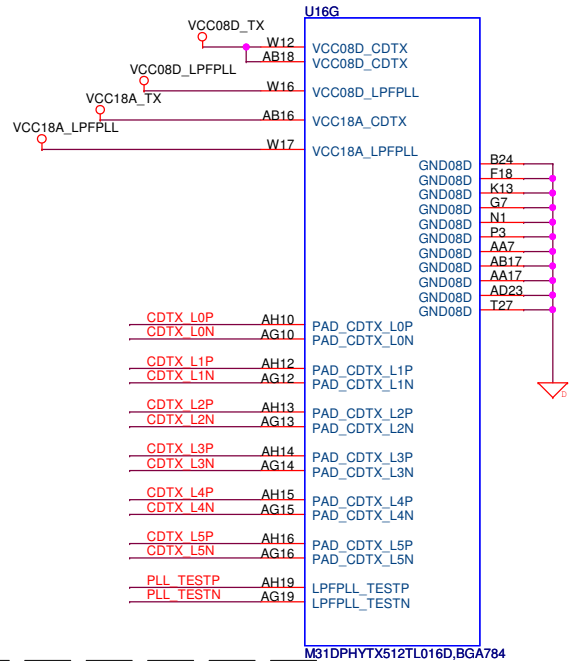




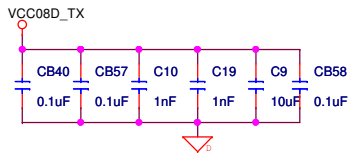




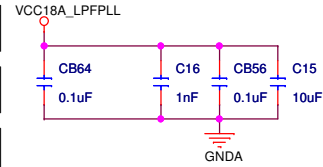




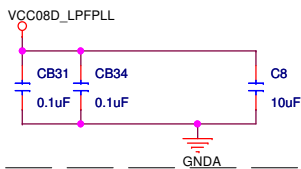
Decoupling Capacitor of PHY's Digital Power Pins



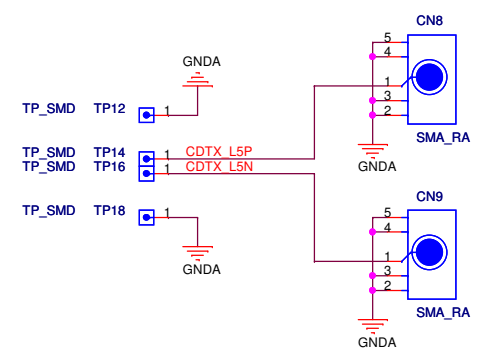
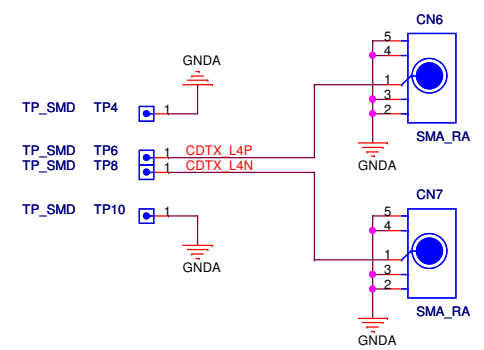
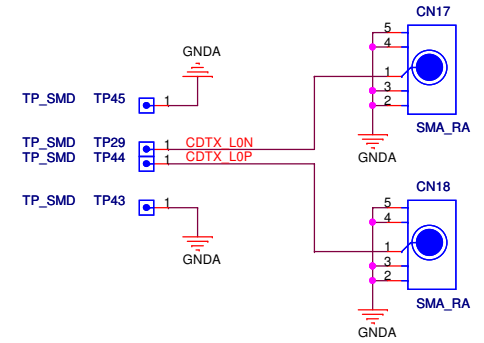
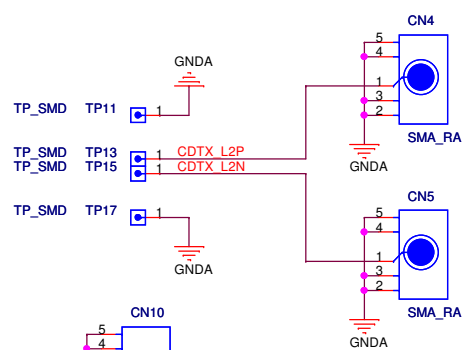
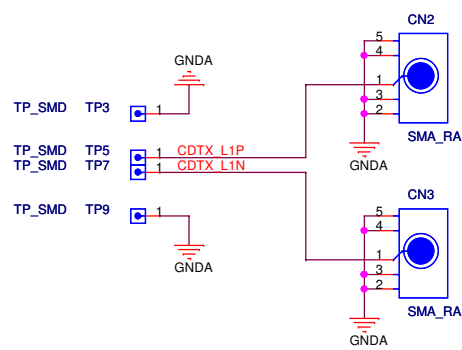
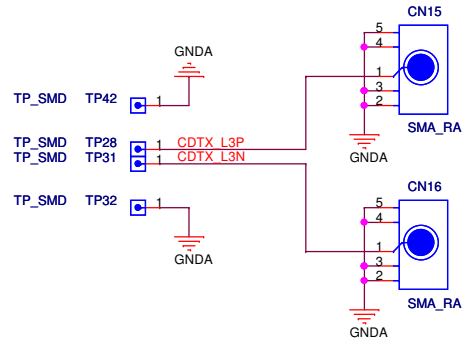
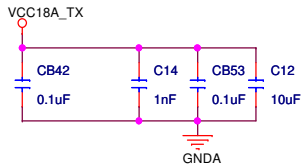
Decoupling Capacitor of PLL's Analog Power Pins



Decoupling Capacitor of PLL's Digital Power Pins

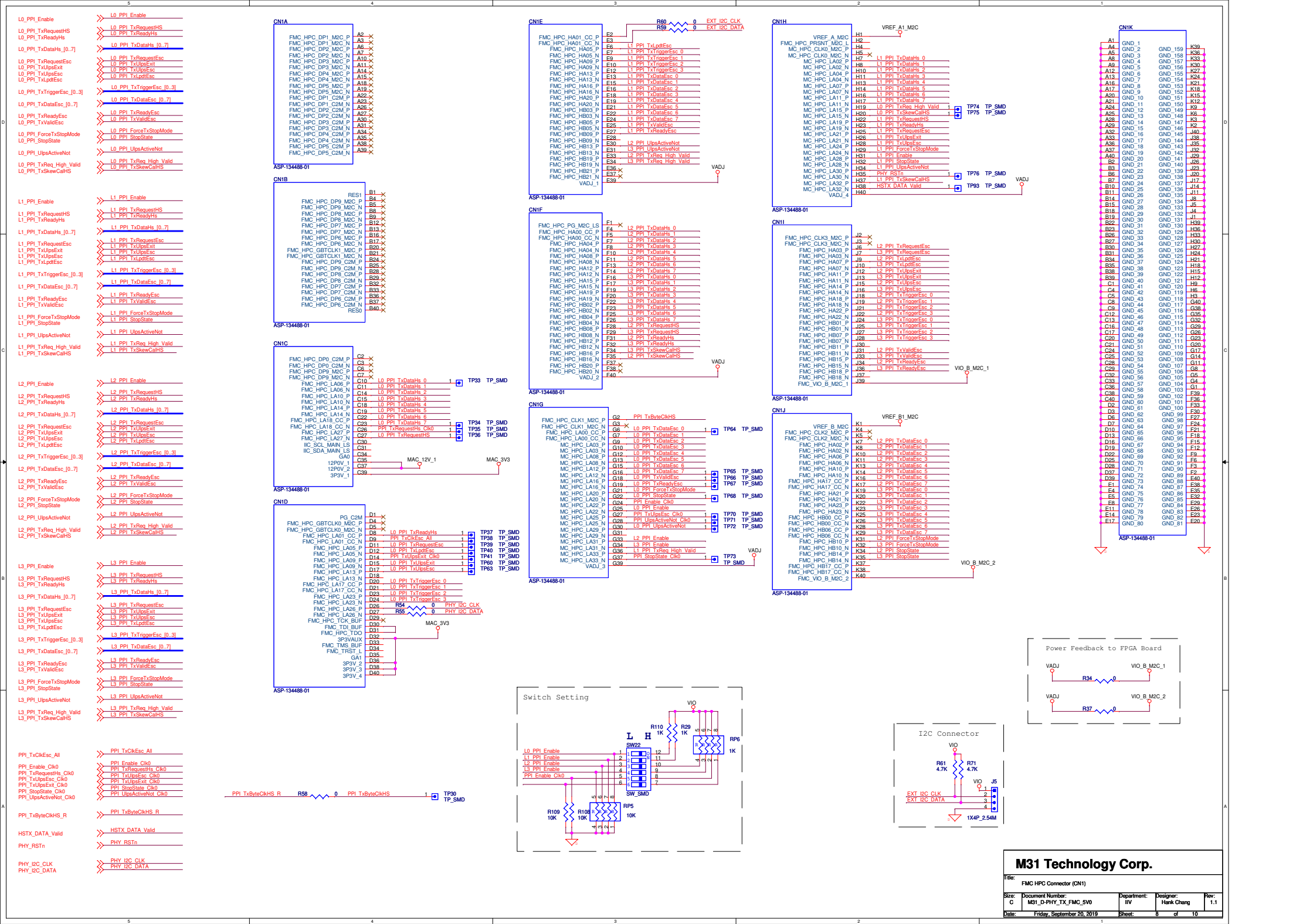


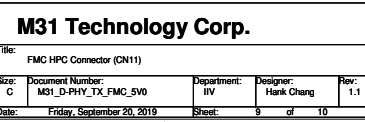
Decoupling Capacitor of PHY's Analog Power Pins

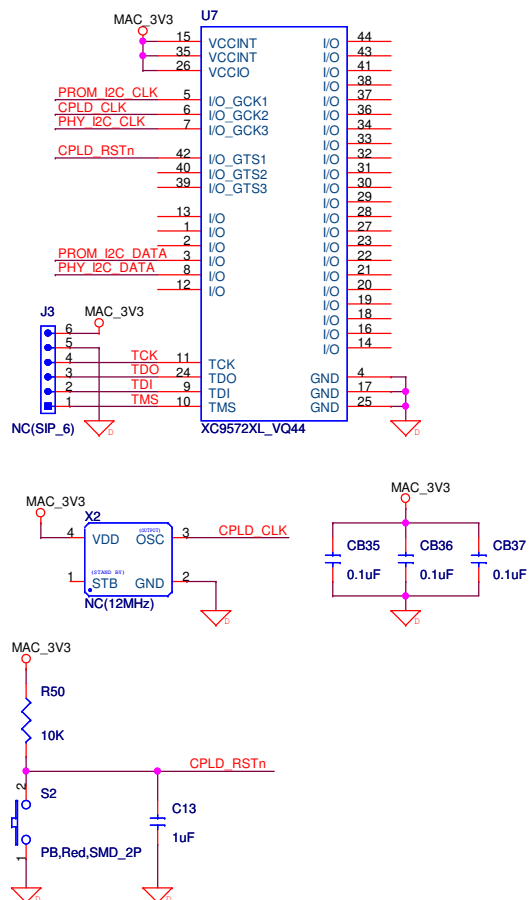
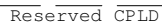


M31 Technology Corp.

Title: M31 MIPI D-PHY Tx Test Chip				
Size: B	Document Number: M31_D-PHY_TX_FMC_5V0	Department: IIV	Designer: Hank Chang	Rev: 1.1
Date: Monday, September 09, 2019	Sheet: 7	of 10		







M31 Technology Corp.

Title: Reserved CPLD

Size: B	Document Number: M31_D-PHY_TX_FMC_5V0	Department: IV	Designer: Hank Chang	Rev: 1.1
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Date:	Thursday, September 05, 2019	Sheet:	10	of	10
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