Version I	Description							
Version	Date	Revision Reason						
1.0	05-01-2018	For T28HPC+ CD-PHY Combo Tx Testchip (BGA676)						
1.1	02-12-2019	 Add pull-up for TX-SyncHS_All More compact size 						
4.0	02-21-2019	For HI055G D-PHY Tx v1.2 Testchip (LQFP256) PPI only have L0, L2 and CLK Lane						D
5.0	09-05-2019	For TL016D D-PHY Tx Testchip (BGA784)						
EC705 HPC (J22) 1-Lane PPI in 8-bit ZC706 HPC (J37) 1-Lane PPI Could be 8-bit or 16-bit								
	VC707 FMC1 HI	PC (35) 4-Lane	/		<u>K</u>	/ Or 	nm >	i
	\uparrow				2		C 3	
	HTG-700 FMC_0	C (J6) 4-Lane HTG-700 FMC_B (J5)	- {/	K	3mm	<u> </u>	63mm 	∤ ;
	ML605 HPC (J	- — — — — — — —	-√ 3	3mm	·	70mm	\rightarrow	3mm
			_		139mm	 	 - 	
						Bott	om View	
	VITA 57 FMC Connector (CN	VITA 57 FMC Connector (CN11)	0	A40 VITA 5	7 FMC Or (CN11)		57 FMC ector(CN1)	16mm
				HTG-700	FMC_B (J5) 4-Lan	e HTG-700	FMC_C (J6)	
_		M31 D-PHY 4-lane Tx Test Chip	105mm	ML605 LP0		e ML605 H	<u>DC (J64)</u>	
					4-Lan		MC1 HPC (35)	
					1-Lan		PC (J22) PC (J37)	
TOB		11						
\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	10mm 10mm 10mm	10mm 10mm 10mm 10mm 10mm 10mm		<u> </u>		W		<u> </u>
		Top View			Bottom	View	M31 Technology (Corp.
							Revision_History and PCB Placement Size: Document Number: Di M31_D-PHY_TX_FMC_5V0	epartment: Designer: Rev: IIV Hank Chang 1.1
	5	4		3	2		Date: Wednesday, September 18, 2019 St	heet: 1 of 10

















