

ZeBu[®] zManualPartitioner Application Note

Version V-2024.03-1, July 2024



Copyright and Proprietary Information Notice

© 2024 Synopsys, Inc. This Synopsys software and all associated documentation are proprietary to Synopsys, Inc. and may only be used pursuant to the terms and conditions of a written license agreement with Synopsys, Inc. All other use, reproduction, modification, or distribution of the Synopsys software or the associated documentation is strictly prohibited.

Destination Control Statement

All technical data contained in this publication is subject to the export control laws of the United States of America. Disclosure to nationals of other countries contrary to United States law is prohibited. It is the reader's responsibility to determine the applicable regulations and to comply with them.

Disclaimer

SYNOPSYS, INC., AND ITS LICENSORS MAKE NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARD TO THIS MATERIAL, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE.

Trademarks

Synopsys and certain Synopsys product names are trademarks of Synopsys, as set forth at <https://www.synopsys.com/company/legal/trademarks-brands.html>. All other product or company names may be trademarks of their respective owners.

Free and Open-Source Licensing Notices

If applicable, Free and Open-Source Software (FOSS) licensing notices are available in the product installation.

Third-Party Links

Any links to third-party websites included in this document are for your convenience only. Synopsys does not endorse and is not responsible for such websites and their practices, including privacy practices, availability, and content.

www.synopsys.com

Contents

About This Book	5
Audience	5
Contents of This Book	5
Related Documentation	6
Typographical Convention	7
Synopsys Statement on Inclusivity and Diversity	7
<hr/>	
1. Design Partitioning With zManualPartitioner	9
Launching zManualPartitioner	9
Loading a Netlist File	10
Loading an EDIF File	10
Loading an Automatic Clustering Interconnection File	11
Main Window	12
Netlist Tree Pane	14
Using an EDIF File	14
Using an Automatic Clustering Interconnection File	15
Partition Pane	17
Adding or Removing Partitions From the Partition Pane	17
Hiding or Displaying Partitions	18
Partition Interconnection Matrix Pane	18
Using an EDIF File	18
Using an Automatic Clustering Interconnection File	19
Instance Interconnection Matrix Pane	19
Using an EDIF File	20
Using an Automatic Clustering Interconnection File	20
Timing Analysis Pane	21
Configuration Window	23
Loading a Sample Configuration File	23
Modifying Filling Rates	24
Properties Window	25
Loading and Generating Mapping Files	26

Contents

Generating a Mapping File	26
Loading a Mapping File	26
<hr/>	
2. Managing Black-boxes	27
Applying a Black Box Attribute	27
Removing Black Boxing Attribute	27
<hr/>	
3. Moving Instances Between Partitions	29
Using an EDIF File	29
Using an Automatic Clustering Interconnection File	31
Performing Timing Analysis	32
<hr/>	
4. Limitations of zManualPartitioner	36

Preface

This section contains the following topics:

- [About This Book](#)
- [Audience](#)
- [Contents of This Book](#)
- [Related Documentation](#)
- [Typographical Convention](#)
- [Synopsys Statement on Inclusivity and Diversity](#)

About This Book

The *ZeBu® zManualPartitioner Application Note* describes the *zManualPartitioner* feature for ZeBu. It is a graphical interface that allows you to manually partition a design after a compilation with ZeBu.

Audience

This guide is written for experienced ZeBu users who are familiar with design partitioning.

Contents of This Book

The *ZeBu® zManualPartitioner Application Note* has the following chapters:

Chapter	Describes...
Design Partitioning With zManualPartitioner	<i>zManualPartitioner</i> GUI options
Managing Black-boxes	about managing black-boxes for design partitioning
Moving Instances Between Partitions	about shifting instances from one partition to another
Limitations of zManualPartitioner	the limitations of <i>zManualPartitioner</i>

Related Documentation

Document Name	Description
<i>ZeBu User Guide</i>	Provides detailed information on using ZeBu.
<i>ZeBu Debug Guide</i>	Provides information on tools you can use for debugging.
<i>ZeBu Debug Methodology Guide</i>	Provides debug methodologies that you can use for debugging.
<i>ZeBu Unified Command-Line User Guide</i>	Provides the usage of Unified Command-Line Interface (UCLI) for debugging your design.
<i>ZeBu UTF Reference Guide</i>	Describes Unified Tcl Format (UTF) commands used with ZeBu.
<i>ZeBu Power Aware Verification User Guide</i>	Describes how to use Power Aware verification in ZeBu environment, from the source files to runtime.
<i>ZeBu Functional Coverage User Guide</i>	Describes collecting functional coverage in emulation.
<i>Simulation Acceleration User Guide</i>	Provides information on how to use Simulation Acceleration to enable cosimulating SystemVerilog testbenches with the DUT
<i>ZeBu Verdi Integration Guide</i>	Provides Verdi features that you can use with ZeBu. This document is available in the Verdi documentation set.
<i>ZeBu Runtime Performance Analysis With zTune User Guide</i>	Provides information about runtime emulation performance analysis with zTune.
<i>ZeBu Custom DPI Based Transactors User Guide</i>	Describes ZEMI-3 that enables writing transactors for functional testing of a design.
<i>ZeBu LCA Features Guide</i>	Provides a list of Limited Customer Availability (LCA) features available with ZeBu.
<i>ZeBu Synthesis Verification User Guide</i>	Provides a description of zFmCheck.
<i>ZeBu Transactors Compilation Application Note</i>	Provides detailed steps to instantiate and compile a ZeBu transactor.
<i>ZeBu zManualPartitioner Application Note</i>	Describes the zManualPartitioner feature for ZeBu. It is a graphical interface to manually partition a design.
<i>ZeBu Hybrid Emulation Application Note</i>	Provides an overview of the hybrid emulation solution and its components.

Typographical Convention

This document uses the following typographical conventions:

To indicate	Convention Used
Program code	<code>OUT <= IN;</code>
Object names	<code>OUT</code>
Variables representing objects names	<code><sig-name></code>
Message	Active low signal name ' <code><sig-name></code> ' must end with <code>_X</code> .
Message location	<code>OUT <= IN;</code>
Reworked example with message removed	<code>OUT_X <= IN;</code>
Important Information	NOTE: This rule...

The following table describes the syntax used in this document:

Syntax	Description
<code>[]</code> (Square brackets)	An optional entry
<code>{ }</code> (Curly braces)	An entry that can be specified once or multiple times
<code> </code> (Vertical bar)	A list of choices out of which you can choose one
<code>...</code> (Horizontal ellipsis)	Other options that you can specify

Synopsys Statement on Inclusivity and Diversity

Synopsys is committed to creating an inclusive environment where every employee, customer, and partner feels welcomed. We are reviewing and removing exclusionary language from our products and supporting customer-facing collateral. Our effort also includes internal initiatives to remove biased language from our engineering and working environment, including terms that are embedded in our software and IPs. At the same time, we are working to ensure that our web content and software applications are usable to people of varying abilities. You may still find examples of non-inclusive language in our

software or documentation as our IPs implement industry-standard specifications that are currently under review to remove exclusionary language.

1

Design Partitioning With zManualPartitioner

ZeBu provides a graphical user interface, `zManualPartitioner` that allows manual partitioning of specific parts of a design. The interface allows drag and drop to manipulate design instances after compilation.

The manual partitioning also exists in `zNetgen`, a Tcl-based ZeBu tool.

This section explains how to use the `zManualPartitioner` GUI. This section consists of the following subsections:

- [Launching zManualPartitioner](#)
- [Loading a Netlist File](#)
- [Main Window](#)
- [Partition Pane](#)
- [Partition Interconnection Matrix Pane](#)
- [Instance Interconnection Matrix Pane](#)
- [Timing Analysis Pane](#)
- [Configuration Window](#)
- [Properties Window](#)
- [Loading and Generating Mapping Files](#)

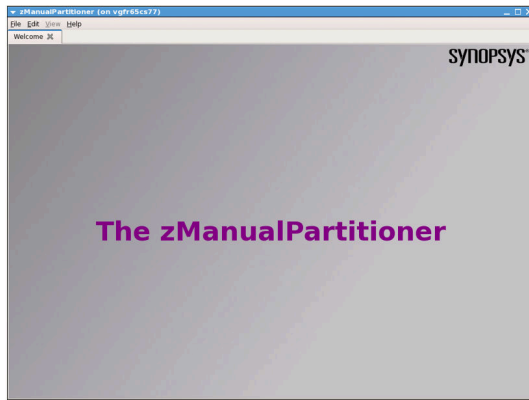
Launching zManualPartitioner

To launch the `zManualPartitioner` GUI, use the following command:

```
$ zManualPartitioner
```

This command opens the following welcome screen:

Figure 1 *ManualPartitioner Welcome Screen*



Loading a Netlist File

You can load two types of netlist files into the `zManualPartitioner` GUI:

- EDIF
- Automatic Clustering Interconnection

This section consists of the following sub-sections:

- [Loading an EDIF File](#)
- [Loading an Automatic Clustering Interconnection File](#)

Loading an EDIF File

To load an EDIF file, perform the following steps:

1. Select **File** > **Open EDIF** file.
2. Browse to your EDIF file. Extensions accepted are `.edf`, `.edf.gz`, and `.edif`.
3. Click **Open**.

Note:

You can load only one EDIF file at a time in a `zManualPartitioner` session.

Loading an Automatic Clustering Interconnection File

You can load an interconnection file in `zManualPartitioner` if you have compiled your design at `zCore-Level` with Automatic Clustering enabled.

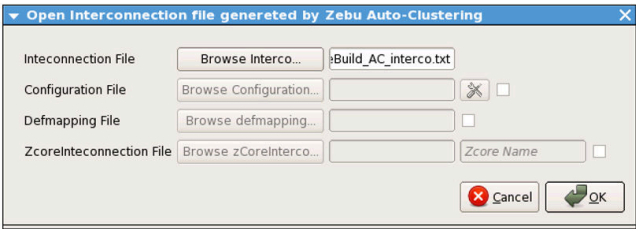
Note:

You can load multiple Automatic Clustering interconnection files in parallel in a `zManualPartitioner` session.

To load an Automatic Clustering Interconnection file, perform the following steps:

1. Click **File > Open AC file** to open the following dialog box:

Figure 2 Loading an Automatic Clustering Interconnection File



Note:

To retrieve an Interconnection file for a design, generated at **zCoreBuild** level, you must activate automatic clustering at the **zTopBuild** level.


2. Click **Browse Interco...** to select your Automatic Clustering Interconnection file.

In addition to the Automatic Clustering Interconnection file, you can also load a configuration file, a defmapping file, and a `ZcoreInterconnection` file to reproduce and modify partitioning results as needed. By default, these fields are inactive in the window. To activate these fields, click the check box next to them (see [Figure 2](#)). For more details on these files, see the following table:

Table 1 Additional Files to Modify Partitioning Results

Files	Description	Mandatory/Optional
Integration Files	This is the <code>zCoreBuild_AC_interco.txt</code> file that is generated by automatic clustering after compilation at <code>zCore-level</code> (<code>zCoreBuild</code>). It can also be generated by the system-level compiler (<code>zTopBuild</code>). This file does not require the EDIF file to perform partitioning. It is available in your <code>zCore-level</code> working directory, that is, <code>work.<core_name></code> .	Mandatory

Table 1 Additional Files to Modify Partitioning Results (Continued)

Files	Description	Mandatory/Optional
Configuration Files	This is a target configuration file that selects the ZeBu configuration in the <code>\$ZEBU_ROOT/etc/configurations</code> directory. To load a configuration file, select the corresponding check box and click the Browse Configuration button. Note: If you click the  button, the Configuration window appears, which allows you to customize the configuration. For more information, see Configuration Window .	Optional
Defmapping File	This is the <code>zCoreBuild_AC_defmapping.tcl</code> file associated with the <code>zCoreBuild_AC_interco.txt</code> file. It contains the results of a previous partitioning. This file is available in your zCore-level working directory, that is, <code>work.<core_name></code> .	Optional
<i>zCoreInterconnection</i> File	This is the <code>zTopBuild_AC_TOPNL_interco.txt</code> file that describes the interconnection information between all zCores. This file is generated by the system-level compiler (zTopBuild) with the cluster enable <code>-map_inter_core_io</code> command.	Optional

Main Window

The **zManualPartitioner** main window changes depending on the file you load, that is, an EDIF file or an Automatic Clustering Interconnection file.

Figure 3 EDIF File loaded in zManualPartitioner

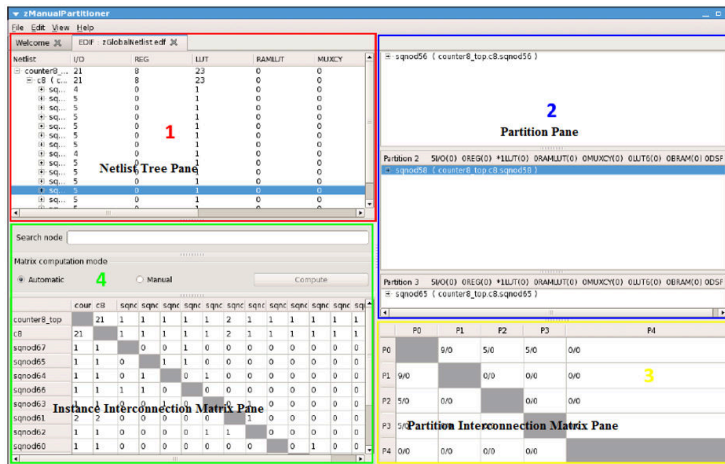
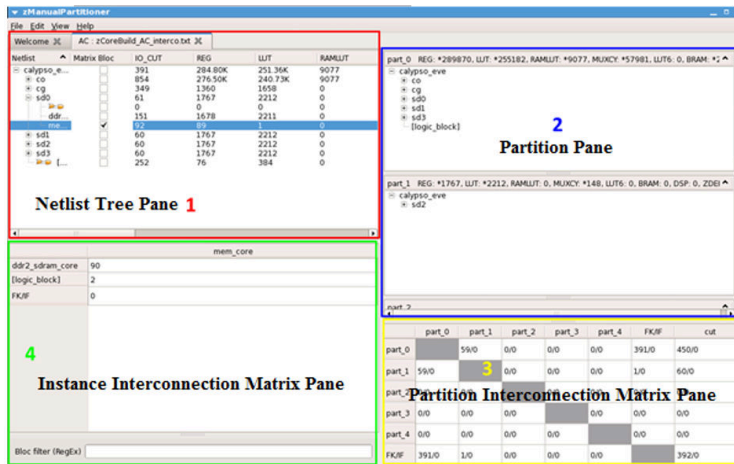


Figure 4 Automatic Clustering Interconnection File loaded in zManualPartitioner



The main window can be divided into the following four panes, (see [Figure 3](#) and [Figure 4](#)).

- [Netlist Tree Pane](#)
- [Partition Pane](#)
- [Partition Interconnection Matrix Pane](#)
- [Instance Interconnection Matrix Pane](#)

The **Instance Interconnection Matrix** pane is replaced by the **Timing Analysis** pane when you launch the **Timing Analysis** feature. For more details, see [Timing Analysis Pane](#).

Netlist Tree Pane

The **Netlist Tree** pane allows you to view all the available instances in a netlist file. The pane changes depending on the file you load into zManualPartitioner.

Using an EDIF File

The following figure displays the **Netlist Tree** pane, when you load an EDIF file in zManualPartitioner.

Figure 5 Netlist Tree Pane (EDIF File)

Netlist	I/O	REG	LUT	RAMLUT	MUXCY	LUT6	BRAM	DSP	Partitio	Matrix Selector
counter8_top	21	8	23	0	0	0	0	0	0	<input checked="" type="checkbox"/>
c8 (counter8)	21	8	23	0	0	0	0	0	0	<input checked="" type="checkbox"/>
sqnod67 ...	4	0	1	0	0	0	0	0	0	<input checked="" type="checkbox"/>
sqnod66 ...	5	0	1	0	0	0	0	0	0	<input checked="" type="checkbox"/>
sqnod65 ...	5	0	1	0	0	0	0	0	3	<input checked="" type="checkbox"/>
sqnod64 ...	5	0	1	0	0	0	0	0	0	<input checked="" type="checkbox"/>
sqnod63 ...	5	0	1	0	0	0	0	0	1	<input checked="" type="checkbox"/>
sqnod62 ...	5	0	1	0	0	0	0	0	0	<input checked="" type="checkbox"/>
sqnod61 ...	5	0	1	0	0	0	0	0	0	<input checked="" type="checkbox"/>
sqnod60 ...	4	0	1	0	0	0	0	0	0	<input checked="" type="checkbox"/>
sqnod59 ...	5	0	1	0	0	0	0	0	0	<input checked="" type="checkbox"/>
sqnod58 ...	5	0	1	0	0	0	0	0	2	<input checked="" type="checkbox"/>
sqnod57 ...	5	0	1	0	0	0	0	0	0	<input checked="" type="checkbox"/>
sqnod56 ...	5	0	1	0	0	0	0	0	1	<input checked="" type="checkbox"/>
sqnod55 ...	5	0	1	0	0	0	0	0	0	<input type="checkbox"/>
sqnod54 ...	5	0	1	0	0	0	0	0	0	<input type="checkbox"/>

The following table describes the columns available in the **Netlist Tree** pane.

Table 2 Column Description in the Netlist Tree Pane (EDIF File)




Column	Description
Netlist	Displays the list of the following available items in the netlist in a tree view: The top of a design. Common instances followed by their module name. Logical gates. Black box in the original design or in zManualPartitioner. (For more information on black-box management in zManualPartitioner, see Managing Black-boxes). Primitives containing logical gates and black-boxes at the same level.
I/O	Displays the number of inputs/outputs.
REG	Displays the number of registers.
LUT	Displays the number of lookup tables.
RAMLUT	Displays the number of RAMLUTs.
MUXCY	Displays the number of multiplexers for Carry Logic.

Table 2 Column Description in the Netlist Tree Pane (EDIF File) (Continued)

Column	Description
LUT6	Displays the number of LUT6s.
BRAM	Displays the number of BRAMs.
DSP	Displays the number of DSPs.
Partition	Partition number where an instance is currently located. It is dynamically updated when you move an instance from one partition to another.
Matrix Selection	A Check box that allows you to select as many items as necessary to display in the Instance Interconnection Matrix pane.

The following table displays the icons available in the **Netlist Tree** pane.

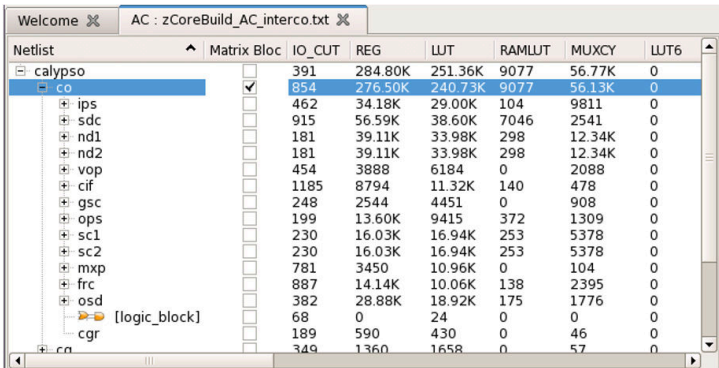
Table 3 Available Icons in the Netlist Tree Pane (EDIF File)

Column	Description
	Black box is already set in the netlist. For more information on black-box management in zManualPartitioner, see Managing Black-boxes.
	Black box set in zManualPartitioner. For more information on black-box management in zManualPartitioner, see Managing Black-boxes.
	Primitives.

Using an Automatic Clustering Interconnection File

The following figure displays the Netlist Tree pane when you load an Automatic Clustering Interconnection file in zManualPartitioner.

Figure 6 Netlist Tree Pane (Automatic Clustering Interconnection File)



The following table describes the columns available in the Netlist Pane when you load Automatic Clustering Interconnection file in zManualPartitioner.

Table 4 Netlist Tree Pane Column Description (Automatic Clustering Interconnection File)

Column	Description
Netlist	Displays the list of the following available items in the netlist organized in a tree view:Top of a designLogic blocksCommon instances followed by their module name
Matrix Block	A check box that allows you to select an item to display in the Instance Interconnection Matrix pane. Only one item can be selected at a time.
IO_CUT	Displays the number of inputs/outputs.
REG	Displays the number of registers.
LUT	Displays the number of LUTs.
RAMLUT	Displays the number of RAMLUTs.
MUXCY	Displays the number of MUXCYs.
LUT6	Displays the number of LUT6s.
BRAM	Displays the number of BRAMs.
DSP	Displays the number of DSPs.
ZDELAY	Displays the number of zDelays.
ZRM	Displays the number of ZRMs.

Partition Pane

When you load a netlist file in zManualPartitioner, both the **Netlist Tree** pane and the **Partition** pane display the file.

The **Partition** pane allows you to view the available partitions and the instance placement. The information in the header displayed in this pane is the same as the information in the columns of the **Netlist Tree** pane (see [Figure 7](#) and [Figure 8](#)).

Figure 7 Partition Pane (An EDIF File)

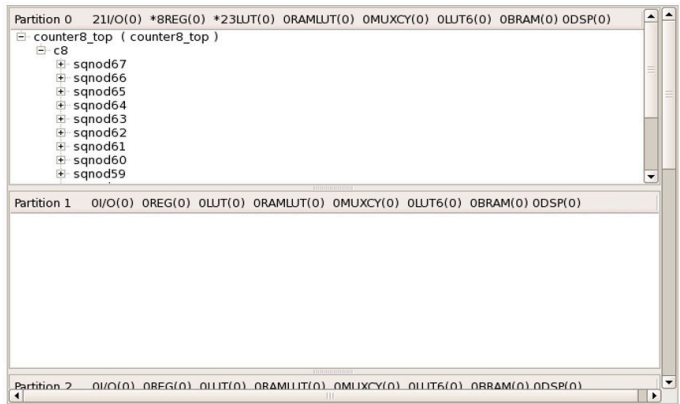


Figure 8 Partition Pane (An Automatic Clustering Interconnection File)

	part_0	part_1	part_2	part_3	part_4	FK/IF	cut
part_0		58/0	57/0	57/0	57/0	391/0	620/0
part_1	58/0		2/0	2/0	2/0	1/0	65/0
part_2	57/0	2/0		0/0	0/0	1/0	60/0
part_3	57/0	2/0	0/0		0/0	1/0	60/0
part_4	57/0	2/0	0/0	0/0		1/0	60/0
FK/IF	391/0	1/0	1/0	1/0	1/0		395/0

A netlist is loaded in Partition 0 by default. However, you may drag and drop instances from one partition to another in the **Partition** pane.

For more information about moving instances between partitions, see [Moving Instances Between Partitions](#).

Adding or Removing Partitions From the Partition Pane

You can add or remove as many partitions as necessary in the **Partition Pane** from the **Properties** dialog box.

To add or remove partitions in the **Partition** Pane, perform the following steps:

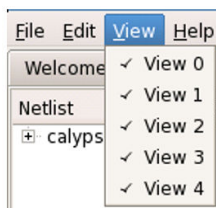
1. From the menu bar, click **Edit > Preferences mapping** to open the **Properties** dialog box.
2. Add or remove partitions using the **Number of partitions** field on the **Properties** dialog box as required.

Hiding or Displaying Partitions

You can hide or display as many partitions as necessary in the **Partition** pane by selecting **View > View N**.

A view is a partition in the **Partition** pane and a check symbol next to a view means its corresponding partition is displayed, see the following figure:

Figure 9 View Menu



Partition Interconnection Matrix Pane

The **Partition Interconnection Matrix** pane displays information about the number of cuts between partitions in a table. The table (Figure 10) displays one column and one row for each partition.

Using an EDIF File

The following figure displays the **Partition Interconnection Matrix** pane when you load an EDIF file.

Figure 10 Partition Interconnection Matrix Pane (EDIF File)

	part_0	part_1	part_2	part_3	part_4	FK/IF	cut
part_0		58/0	57/0	57/0	57/0	391/0	620/0
part_1	58/0		2/0	2/0	2/0	1/0	65/0
part_2	57/0	2/0		0/0	0/0	1/0	60/0
part_3	57/0	2/0	0/0		0/0	1/0	60/0
part_4	57/0	2/0	0/0	0/0		1/0	60/0
FK/IF	391/0	1/0	1/0	1/0	1/0		395/0

Each cell displays information such as, for instance, 4/0 or 9/0. These figures have the following meaning:

- The first figure is the number of cuts between partitions.
- The second figure is the maximum number of cuts allowed by the target configuration between partitions.

Using an Automatic Clustering Interconnection File

The following figure displays the **Partition Interconnection Matrix** pane when you load an *Automatic Clustering Interconnection* file.

Figure 11 Partition Interconnection Matrix Pane (Automatic Clustering Interconnection File)

	part_0	part_1	part_2	part_3	part_4	FK/IF	cut
part_0		58/0	57/0	57/0	57/0	391/0	620/0
part_1	58/0		2/0	2/0	2/0	1/0	65/0
part_2	57/0	2/0		0/0	0/0	1/0	60/0
part_3	57/0	2/0	0/0		0/0	1/0	60/0
part_4	57/0	2/0	0/0	0/0		1/0	60/0
FK/IF	391/0	1/0	1/0	1/0	1/0		395/0

Each cell displays information such as, for instance, 57/0 or 58/0. These figures have the following meaning:

- The first figure is the number of cuts between partitions.
- The second figure is the maximum number of cuts allowed by the target configuration between partitions.

The **Cut** column displays the cut between the current partition and the others.

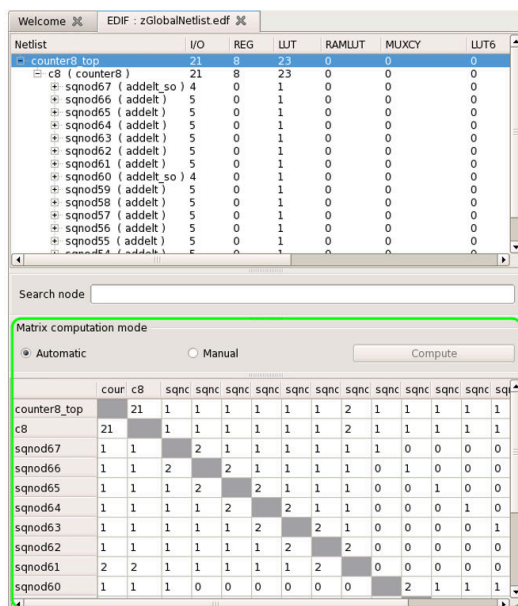
Depending on the results of the automatic clustering, the column FK/IF may be displayed to give information on the interconnection between the current partition and the external interface IF.

Instance Interconnection Matrix Pane

The **Instance Interconnection Matrix** pane computes the interconnection between adjacent instances.

The matrix consists of one column and one row for each instance selected, using the check box in the **Matrix Selection** column in the **Netlist Tree** pane (see [Figure 12](#)). You can select as many instances, as needed.

Figure 12 Instance Interconnection Matrix Pane (An EDIF File)



The **Matrix Computation Mode** allows you to control how the instance interconnection is calculated:

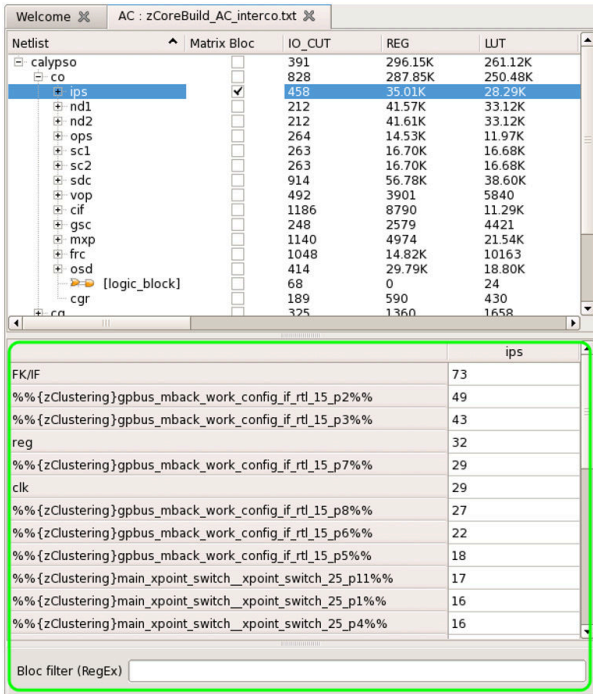
- **Automatic:** It is automatically calculated every time you must select an instance in the **Netlist Tree** pane.
- **Manual:** You first select all instances from the **Netlist Tree** pane and then click **Compute** for manual calculation.

Using an Automatic Clustering Interconnection File

When you load an *Automatic Clustering Interconnection* file, only the interconnection of the selected instance (through the **Matrix Block** check box) and its neighbors can be displayed in the **Instance Interconnection Matrix** pane.

To display the interconnection of an instance, go to the **Netlist Tree** pane and select the check box in the **Matrix Block** column (see [Figure 13](#)).

Figure 13 Instance Interconnection Matrix Pane (Automatic Clustering Interconnection File)



When an instance has too many adjacent instances, you can filter them using the **Bloc Filter (Regex)** field.

Timing Analysis Pane

The **Timing Analysis** pane allows you to view the structure of combinational paths, that is, the set of successive instances in the combinational path for each partition.

Note:

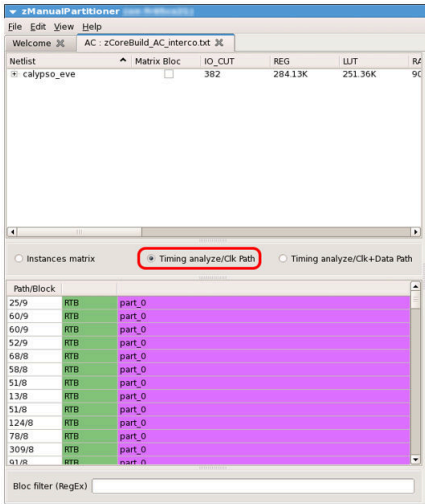
The timing analysis feature is only available when you use an Automatic Clustering Interconnection file in zManualPartitioner.

With a clear view of these paths, you can enhance performance by selecting long combinational paths across a minimum number of partitions.

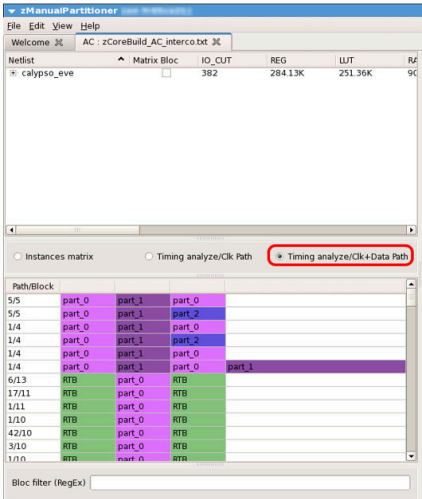
To display the **Timing Analysis** pane, perform the following steps:

1. Open an Automatic Clustering Interconnection file in zManualPartitioner.
2. Click **Edit > Timing Analyze** to replace the **Instance Interconnection Matrix** pane with the **Timing Analysis** pane. By default, it is set on the **Timing Analyze/Clk Path** view (see [Figure 14](#)):

Figure 14 Timing Analysis/Clock Path View



3. The **Timing Analyze/Clk+Data Path** view is another view available that displays combinational paths for the data and clock, see the following figure:



The following table displays options available in the **Timing Analysis** pane.

Table 5 Available Options in the Timing Analysis Pane

Option	Description
Instances Matrix	Reverts to the Instance Interconnection Matrix pane.
Clock Path View	Displays the combinational path for the clock.
Clock + Data Path View	Displays the combinational path for the data and clock.
Path/Block	The Path is the number of paths represented by the set of path elements. The Block is the number of blocks in the path.

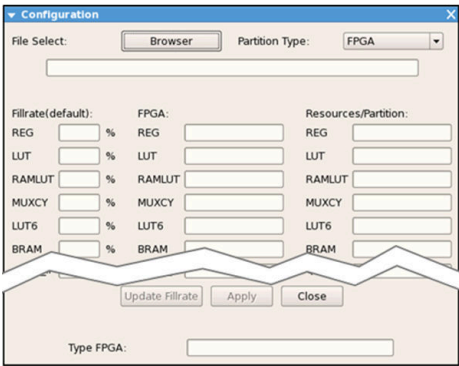
For more information about optimizing timing, see [Performing Timing Analysis](#).

Configuration Window

The `zManualPartitioner` GUI also allows you to select a sample configuration file as a target to map a design with your customized parameters.

From the menu bar, click **Edit > Configuration** to open the **Configuration** window as follows:

Figure 15 Configuration Window

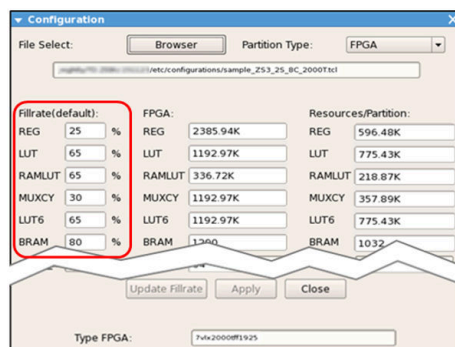


Loading a Sample Configuration File

From the **Configuration** window, you can select a specific configuration file from the `$ZEBU_ROOT/etc/configurations` directory.

To select a target, click **Browser** and select your configuration file. The following figure displays the **Configuration** window loaded for an example configuration file.

Figure 16 Example of Sample Configuration File Loaded



Once a sample configuration file is loaded, you can perform the following actions:

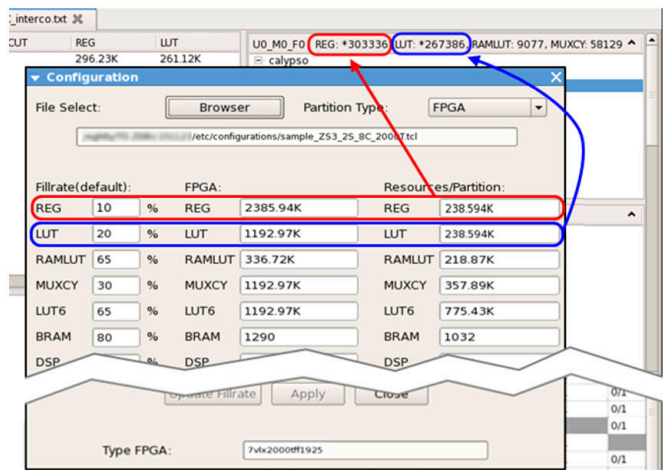
- View various parameters at once.
- Modify filling rates for each resource type.
- See the immediate impact on FPGAs and partitions.

Modifying Filling Rates

To modify filling rates in the Configuration window (see [Figure 17](#)), perform the following steps:

1. Modify the resource percentage as required in the **Fillrate (default)** column.
2. Click **Update Fillrate** to modify the values in FPGA and **Resources/Partition** columns.
3. Click **Apply** to validate your parameters.

Figure 17 *Modifying Filling Rates in Configuration Window*



Note:

If your set of parameters are not sufficient to map your design, an asterisk is displayed next to the impacted resource, as highlighted in [Figure 17](#).

Properties Window

The **Properties** window allows you to set various parameters of your zManualPartitioner environment.

To open the **Properties** window, click **Edit > Preferences mapping**.

The following table displays available options in the **Properties** window.

Table 6 *Available Options in the Properties Window*

Option	Description
Number of partitions	Specifies the number of partitions to display in the Partition pane.
Generate log file	Defines the path for the log file containing all user actions.
Information	Defines the columns to display/hide in the Netlist Tree pane.

Loading and Generating Mapping Files

This section consists of the following sub-sections:

- [Generating a Mapping File](#)
- [Loading a Mapping File](#)

Generating a Mapping File

To retrieve partitioning results into a mapping file, perform the following steps:

1. Click **Edit > Generate mapping**.
2. Type a name for the mapping file and click **Save**.

The generation of the mapping file can also be performed with a Tcl command.

Loading a Mapping File

You can load a previously saved mapping file into `zManualPartitioner`. To do so, perform the following steps:

1. Click **Edit > Load mapping**.
2. Browse to your mapping file and click **Open**.



Note:

You can load a mapping file only with an Automatic Clustering Interconnection file.

2

Managing Black-boxes

In zManualPartitioner, you can manage black boxes in the following ways:

- View black boxes originally defined in the netlist (viewed as )
- Add or remove the black box attribute to any item in the netlist (viewed as )

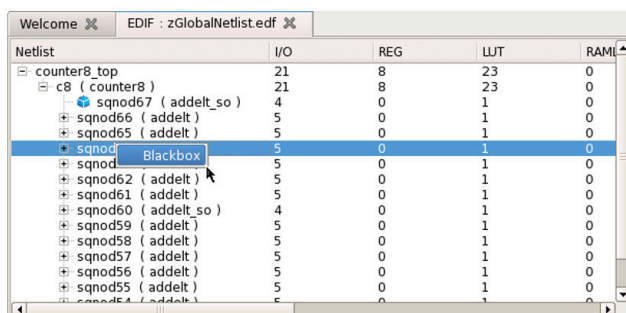
Note:

You can manage black boxes only with EDIF files.

Applying a Black Box Attribute

To apply a black box attribute on an item in the netlist, right-click on it and select **Blackbox**.

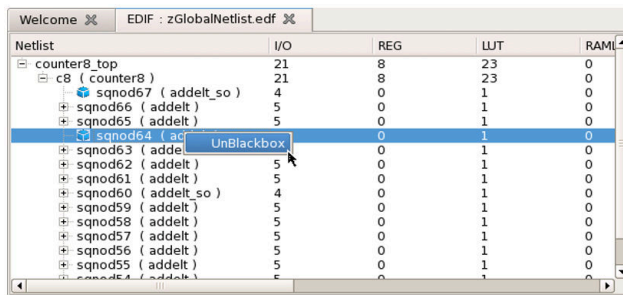
Figure 18 Applying a Black Box Condition



Removing Black Boxing Attribute

To remove a black box attribute and revert an item to its original state, right-click the black box attribute and click **UnBlackbox**.

Figure 19 Removing a Black Box Condition



The screenshot shows the EDIF editor window with the file 'zGlobalNetlist.edf'. The Netlist tree on the left shows a hierarchy starting with 'counter8_top', followed by 'c8', and then a list of 'sqnod' nodes. The 'sqnod64' node is selected, and a context menu is open over it, showing the 'UnBlackbox' option. The main table displays the details for the selected node and its siblings.

Netlist	I/O	REG	LUT	RAM
counter8_top	21	8	23	0
c8 (counter8)	21	8	23	0
sqnod67 (addelt_so)	4	0	1	0
sqnod66 (addelt)	5	0	1	0
sqnod65 (addelt)	5	0	1	0
sqnod64 (addelt)	5	0	1	0
sqnod63 (addelt)	5	0	1	0
sqnod62 (addelt)	5	0	1	0
sqnod61 (addelt)	5	0	1	0
sqnod60 (addelt_so)	4	0	1	0
sqnod59 (addelt)	5	0	1	0
sqnod58 (addelt)	5	0	1	0
sqnod57 (addelt)	5	0	1	0
sqnod56 (addelt)	5	0	1	0
sqnod55 (addelt)	5	0	1	0
sqnod54 (addelt)	5	0	1	0

3

Moving Instances Between Partitions

When you load a netlist file in `zManualPartitioner`, both the **Netlist Tree** pane and the **Partition** pane display the file. By default, the netlist is loaded in Partition 0. However, you can drag and drop instances from one partition to another in the **Partition** pane.

This section consists of the following sub-sections:

- [Using an EDIF File](#)
- [Using an Automatic Clustering Interconnection File](#)
- [Performing Timing Analysis](#)

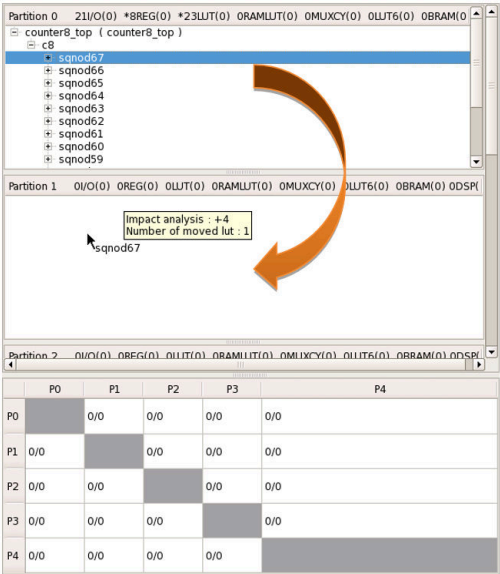
Using an EDIF File

To move an instance to another partition when you load an EDIF file, perform the following steps:

1. In the **Partition** pane, browse to your netlist and select the instance you want to move.
2. Drag this instance into another partition (Partition 1, see [Figure 20](#)).

The information about **Impact Analysis** and **Number of Moved LUTs** is dynamically displayed in a tool tip.

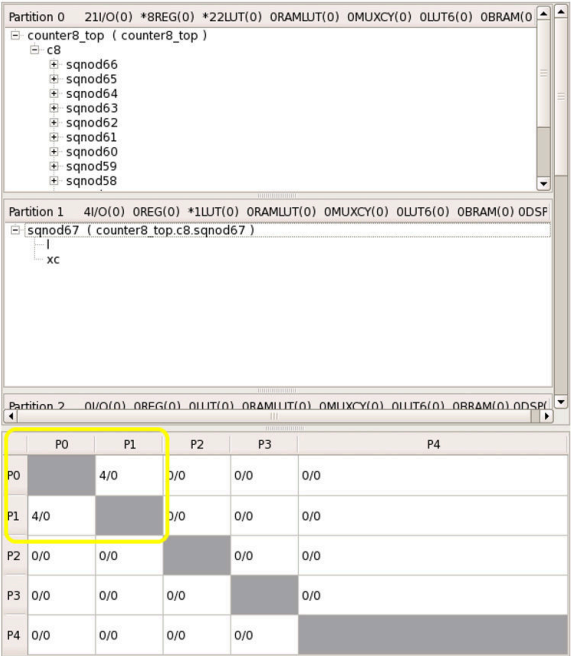
Figure 20 Moving Instances in the Partition Pane Using an EDIF File



1. Drop the instance into the new partition (Partition 1).

The result of moving the instance is displayed in the **Partition Interconnection Matrix** window (see [Figure 23](#)).

Figure 21 Result of Moving Instances in the Partition Pane Using an EDIF File

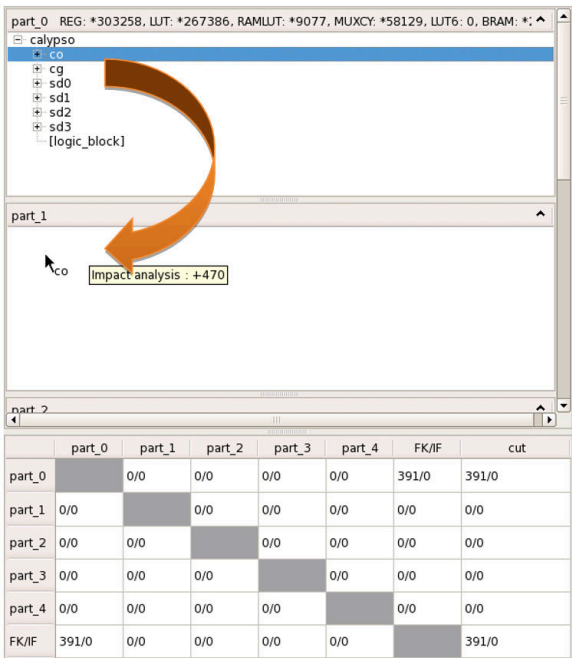


For more information on the **Partition Interconnection Matrix**, see [Partition Interconnection Matrix Pane](#).

Using an Automatic Clustering Interconnection File

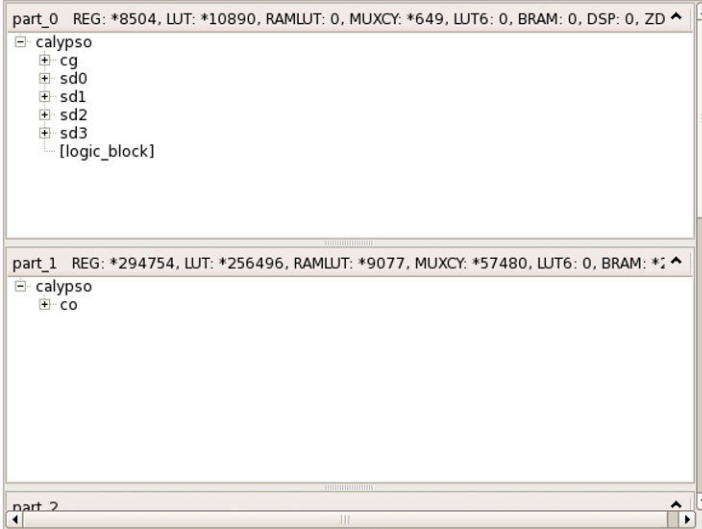
The process for moving an instance to another partition using an `Automatic Clustering Interconnection` file is same as that for an EDIF file, see [Using an EDIF File](#).

Figure 22 Moving Instances in the Partition Pane Using an Automatic Clustering Interconnection File



The following figure displays result of moving instances in the **Partition** pane using an *Automatic Clustering Interconnection* file.

Figure 23 Result of Moving Instances in the Partition Pane Using an Automatic Clustering Interconnection File



The screenshot shows the zManualPartitioner interface with three partitions visible:

- part_0**: REG: *8504, LUT: *10890, RAMLUT: 0, MUXCY: *649, LUT6: 0, BRAM: 0, DSP: 0, ZD: ^. Contains instances: calypso, cg, sd0, sd1, sd2, sd3, [logic_block].
- part_1**: REG: *294754, LUT: *256496, RAMLUT: *9077, MUXCY: *57480, LUT6: 0, BRAM: *. Contains instance: calypso, co.
- part_2**: (Empty)

Below the partitions is the **Instance Interconnection Matrix** table, which is highlighted with a yellow border in the original image.

	part_0	part_1	part_2	part_3	part_4	FK/IF	cut
part_0		465/0	0/0	0/0	0/0	33/0	498/0
part_1	465/0		0/0	0/0	0/0	363/0	828/0
part_2	0/0	0/0		0/0	0/0	0/0	0/0
part_3	0/0	0/0	0/0		0/0	0/0	0/0
part_4	0/0	0/0	0/0	0/0		0/0	0/0
FK/IF	33/0	363/0	0/0	0/0	0/0		396/0

Performing Timing Analysis

After moving instances across partitions (see [Using an Automatic Clustering Interconnection File](#)), you can modify elements of the combinational paths (that is, a set of successive instances that are in the same partition) and their timing to enhance performance.

Note:

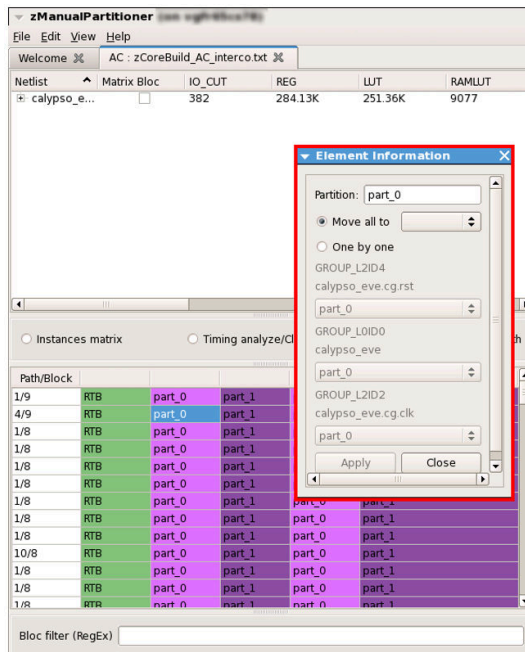
The timing analysis feature is only available when you use an Automatic Clustering Interconnection file in zManualPartitioner.

To modify the content of a combinational path, perform the following steps:

1. Click **Edit > Timing Analyze** to replace the **Instance Interconnection Matrix** pane with the **Timing Analysis** pane.
2. Select your view by selecting **Timing analyze/Clk Path** or **Timing analyze/Clk + Data Path**.

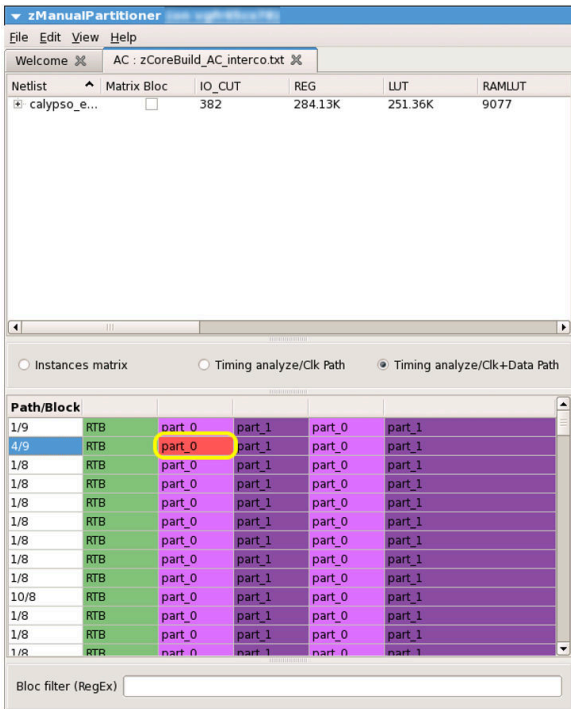
3. Double-click on the path element you want to move to display the **Element Information** window (see [Figure 24](#)).

Figure 24 *Element Information Window*



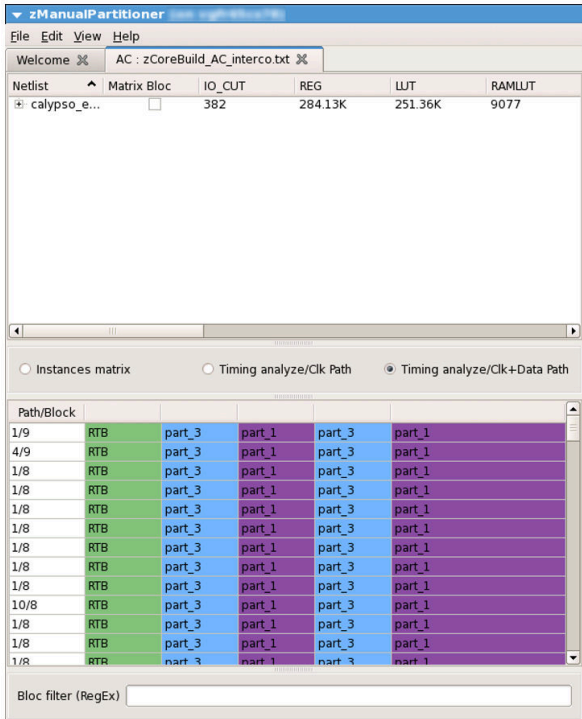
4. In the **Element Information** window, select the method to move by selecting one the following options:
 - **Move all to:** Moves the entire combinational path content of a partition to another partition. Selects the destination partition from the drop-down next to it.
 - **One by one:** Moves an individual element of a partition to another partition. Selects the destination partition from the drop-down below it.
5. Click **Apply** and then **Close**. The modified element is now in red. It means that it is required to launch timing analysis again (see [Figure 25](#)).

Figure 25 *Element Modified in the Timing Analysis Pane*



6. Select **Edit > Timing Analyze** to refresh the **Instance Interconnection Matrix** pane according to your modifications (see [Figure 26](#)).

Figure 26 Timing Analysis Pane Refreshed



4

Limitations of zManualPartitioner

The limitation of `zManualPartitioner` is as follows:

- `zManualPartitioner` is not supported by other partitions.
- It may be challenging to partition large EDIF files using `zManualPartitioner`.