

# **ZeBu<sup>®</sup> Unified Command-Line User Guide**

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# Preface

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This chapter has the following sections:

- [About This Book](#)
- [Intended Audience](#)
- [Contents of This Book](#)
- [Related Documentation](#)
- [Typographical Conventions](#)
- [Synopsys Statement on Inclusivity and Diversity](#)

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## About This Book

The *ZeBu® Unified Command-Line User Guide* describes the usage of Unified Command-Line Interface (UCLI) for debugging your design. This guide describes UCLI commands used for various tasks, such as controlling transactors, debugging, and command-line editing.

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## Intended Audience

This manual is written for engineers to assist them to debug designs targeted for emulation on the ZeBu system.

These engineers should have knowledge of the following Synopsys tools:

- ZeBu system (VCS, zCui, zRci, DPI, waveform reconstruction, and so on)
- Verdi

## Contents of This Book

The *ZeBu® Unified Command-Line User Guide* has the following chapters:

Chapter	Description
<a href="#">ZeBu Runtime Control Interface</a>	Shows how to control ZeBu runtime using zRci. The topics described are as follows: - Running <i>zRci</i> - Passing Parameters to a UCLI ScriptIn addition, ZEMI3 support With <i>zRci</i> is covered.
<a href="#">zRci UCLI Commands</a>	Shows how to use UCLI commands. The UCLI commands discussed include the following: - Controlling Transactors- Debug UCLI Commands- Command-Line Editing- ZEMI3 Commands

## Related Documentation

Document Name	Description
<i>ZeBu User Guide</i>	Provides detailed information on using ZeBu.
<i>ZeBu Debug Guide</i>	Provides information on tools you can use for debugging.
<i>ZeBu Debug Methodology Guide</i>	Provides debug methodologies that you can use for debugging.
<i>ZeBu Unified Command-Line User Guide</i>	Provides the usage of Unified Command-Line Interface (UCLI) for debugging your design.
<i>ZeBu UTF Reference Guide</i>	Describes Unified Tcl Format (UTF) commands used with ZeBu.
<i>ZeBu Power Aware Verification User Guide</i>	Describes how to use Power Aware verification in ZeBu environment, from the source files to runtime.
<i>ZeBu Functional Coverage User Guide</i>	Describes collecting functional coverage in emulation.
<i>Simulation Acceleration User Guide</i>	Provides information on how to use Simulation Acceleration to enable cosimulating SystemVerilog testbenches with the DUT
<i>ZeBu Verdi Integration Guide</i>	Provides Verdi features that you can use with ZeBu. This document is available in the Verdi documentation set.
<i>ZeBu Runtime Performance Analysis With zTune User Guide</i>	Provides information about runtime emulation performance analysis with zTune.
<i>ZeBu Custom DPI Based Transactors User Guide</i>	Describes ZEMI-3 that enables writing transactors for functional testing of a design.

Document Name	Description
<i>ZeBu LCA Features Guide</i>	Provides a list of Limited Customer Availability (LCA) features available with ZeBu.
<i>ZeBu Synthesis Verification User Guide</i>	Provides a description of zFmCheck.
<i>ZeBu Transactors Compilation Application Note</i>	Provides detailed steps to instantiate and compile a ZeBu transactor.
<i>ZeBu zManualPartitioner Application Note</i>	Describes the <code>zManualPartitioner</code> feature for ZeBu. It is a graphical interface to manually partition a design.
<i>ZeBu Hybrid Emulation Application Note</i>	Provides an overview of the hybrid emulation solution and its components.

## Typographical Conventions

This document uses the following typographical conventions:

To indicate	Convention Used
Program code	<code>OUT &lt;= IN;</code>
Object names	<code>OUT</code>
Variables representing objects names	<code>&lt;sig-name&gt;</code>
Message	Active low signal name ' <code>&lt;sig-name&gt;</code> ' must end with <code>_X</code> .
Message location	<code>OUT &lt;= IN;</code>
Reworked example with message removed	<code>OUT_X &lt;= IN;</code>
Important Information	<b>NOTE:</b> This rule...

The following table describes the syntax used in this document:

Syntax	Description
<code>[ ]</code> (Square brackets)	An optional entry

Syntax	Description
{ } (Curly braces)	An entry that can be specified once or multiple times
(Vertical bar)	A list of choices out of which you can choose one
... (Horizontal ellipsis)	Other options that you can specify

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## Synopsys Statement on Inclusivity and Diversity

Synopsys is committed to creating an inclusive environment where every employee, customer, and partner feels welcomed. We are reviewing and removing exclusionary language from our products and supporting customer-facing collateral. Our effort also includes internal initiatives to remove biased language from our engineering and working environment, including terms that are embedded in our software and IPs. At the same time, we are working to ensure that our web content and software applications are usable to people of varying abilities. You may still find examples of non-inclusive language in our software or documentation as our IPs implement industry-standard specifications that are currently under review to remove exclusionary language.



# 1

## ZeBu Runtime Control Interface

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The UCLI provides a common set of commands for certain Synopsys verification products.

The ZeBu Runtime Control Interface (`zRci`) provides a subset of commands for ZeBu and it is compatible with Tcl 8.6. You can use any Tcl command with `zRci`. To leverage Tcl 8.6 64-bit integer support in `zRci`, you need the 64-bit version of Tcl.

For more information, see the following topics:

- [Prerequisites](#)
- [Running zRci in Batch Mode and GUI Mode](#)
- [Passing Parameters to a UCLI Script](#)
- [ZEMI3 Support With zRci](#)

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### Prerequisites

- If **Clock Delay** feature (`clock_delay`) is enabled at compile time and is used to control emulation with time instead of clock cycles, **RunManager** must be instantiated at compile time. By default, **RunManager** is enabled for ZeBu.
- Do not use any variable or namespace, which name starts with "`__ucli`".

---

### Running zRci in Batch Mode and GUI Mode

You can use UCLI to run your test in one of the following modes:

- In [Batch Mode](#) (non-graphical), the UCLI prompt is available with `zRci`.
- In interactive (graphical) [GUI Mode](#), the UCLI prompt is available with Verdi: `verdi -emulation`.

For details, see *ZeBu Verdi Integration Guide*.

You can control the ZeBu runtime with `zRci` using these modes.

## Batch Mode

The syntax is as follows:

```
zRci [<UCLI script>] \
    --zebu-work          <zebu.work> \
    --default-clock      <hw_top.clock_name> \
    --default-clock-edge <both|posedge|negedge>
zRci \
[-h|--help] \
[<setup.ucli>|--do <setup.ucli>] \
[--zebu-work <zebu.work>] \
[--default-clock <clock>] \
[--default-clock-edge <phase>] \
[--so-testbench <lib>] \
[--c-call-library <lib>] \
[--attach <pid>|--testbench <cmd>] \
[-- tcl_args ...]
zRci [<UCLI script>] \
    --testbench "<executable testbench command>"
```

Where,

- `-h [ --help ]`: Prints the help message
- `--do`: Runs the Tcl script without passing positional arguments
- `--zebu-work arg`: Selects `zebu.work` to be used by `zRci`
- `--default-clock arg`: Selects the clock to be set during session initialization
- `--default-clock-edge arg`: Selects the clock edge to be set during session initialization
- `--command arg`: Sets the remote command to be used if remote processes are launched
- `--so-testbench arg`: C++ shared library to be loaded (option can be used more than once)
- `--c-call-library arg`: C Call shared library to be loaded (option can be used more than once)
- `--attach arg`: Attaches to the running testbench process
- `--testbench arg`: Initializes `zRci` as master of a given testbench

### Note:

Runtime and debug features must not be used at the same time in an executable testbench or in the UCLI script.

---

## GUI Mode

The syntax is as follows:

```
verdi -workMode hardwareDebug \
      -emulation \
      --zebu-work <zebu.work> \
      --timescale <ex: 2ns> \
      --default-clock <both|posedge|negedge> \
      [--emulation-options <UCLI script>]
```

---

## Passing Parameters to a UCLI Script

To pass parameters to `zRci Tcl`, you must add the parameters after `--` in the UCLI script.

Example

This example shows the impact of passing parameters to a UCLI script. In this example, the `my_script.ucli` file contains the following:

```
puts "$argc parameters"

foreach argValue $argv {
    puts ">>> parameter: $argValue"
}
```

The following parameters are passed:

```
zRci my_script.ucli --zebu-work zcui.work/zebu.work --default-clock
hw_top.clk -- PARAM_TEST1 PARAM_TEST2 PARAM_TEST3 PARAM_TEST4
```

The output parameters are as follows:

```
>>> parameter: zRci ../SRC/TESTBENCH/ucli_setup.ucli --zebu-work
zcui.work/zebu.work --default-clock hw_top.clk

>>> parameter: PARAM_TEST1

>>> parameter: PARAM_TEST2

>>> parameter: PARAM_TEST3

>>> parameter: PARAM_TEST4
```

---

## ZEMI3 Support With zRci

zRci supports emulation automatically in a ZEMI3 Environment. All `zemi3` commands must be declared before `start_zebu`. For details on the ZEMI3 support, use `help zemi3`. The ZEMI3 commands are available in the ZEMI3 Commands section.

The following is an example of the ZEMI3 support.

```
set finished 0

proc finished_main {} {
    global finished
    puts "##### FINISHED MAIN FUNCTION!"
    set finished 1
}

zemi3 -lib lib.so
zemi3 -main testbench_main -lib another_lib.so -action finished_main

zemi3 -enable

start_zebu db

#free the clocks, so the main function can advance on its own
run

while {!$finished} {
    after 10
}
finish
```

# 2

## zRci UCLI Commands

---

This section provides information about UCLI commands used with emulation.

For more information, see the following topics:

- [Controlling Transactors](#)
- [UCLI Commands](#)
- [Debug UCLI Commands](#)
- [Command-Line Editing](#)
- [ZEMI3 Commands](#)

---

### Controlling Transactors

When using a C/C++ testbench, you have `Board::open()`, `init()` and `close()` calls. Between these functions calls, transactors can be instantiated, initialized, and controlled.

When using `zRci`, the C++ testbench must be compiled with the `-pthread` option given to `g++`.

`zRci` automatically takes control of these `Board` functions calls. It also provides a list of callback functions to maintain the same level of transactor control while within the UCLI environment.

`clang` is also supported as a compiler for C++ testbenches. It is important to be aware when you need to use the `clang` compiler. The `clang` compiler must be configured to use a compatible `libstdc++`. In some systems, by default `clang` uses an older version. In this case, you can use the latest `g++` installation and specify the compatible `libstdc++` using `--gcc-toolchain=` option.

#### Note:

The user-defined procedures (Tcl `procs`) must have a return statement even if it returns void.

For more information, see the following topics:

- [zRci Callback Functions](#)
- [TbOpts Structure](#)
- [Example: Using zRci Callback Functions](#)

## zRci Callback Functions

The following table describes the `zRci` callback functions:

**Table 1** *zRci Callback Functions*

Callback Functions	Description
<code>zRci_pre_board_open(const ZRCI::TbOpts&amp; param);</code>	C++ objects instantiations
<code>zRci_post_board_open(const ZRCI::TbOpts&amp; param);</code>	Obtains access to <code>Board</code> object collected from <code>zRci TbOpts.board</code>
<code>zRci_pre_board_init(const ZRCI::TbOpts&amp; param);</code>	C++ Transactors objects instantiations Transactors initialization using <code>Board</code> object set within <code>zRci_post_board_open()</code> call
<code>zRci_post_board_init(const ZRCI::TbOpts&amp; param);</code>	Transactor initialization Transactor threads start
<code>zRci_pre_save(const ZRCI::TbOpts&amp; param);</code>	Actions before state save (checkpoint or sniffer frame creation)
<code>zRci_post_save(const ZRCI::TbOpts&amp; param);</code>	Actions after state save (checkpoint or sniffer frame creation)
<code>zRci_cleanup(const ZRCI::TbOpts&amp; param);</code>	Transactor threads closing after C++ object deletion
<code>zRci_UCLI_callback(const std::string&amp; expression);</code>	Enqueue UCLI commands to be executed from C++ functions (They are executed as soon as possible.)
<code>zRci_param(const std::string&amp; key, const std::string&amp; value);</code>	Function to be defined in C++ that may be used from UCLI to set up <code>params</code> .  <b>Note:</b> The execution is blocked until completion of the external function.

**Table 1**      *zRci Callback Functions (Continued)*

Callback Functions	Description
<code>zRci_command(const std::string&amp; key, const std::string&amp; value);</code>	Function to be defined in C++ that could be used from UCLI to run certain routines and return a value to UCLI.  <b>Note:</b> The execution is blocked until completion of the external function.

## TbOpts Structure

The structure definition of `TbOpts` is follows:

```
typedef struct {
    ZEBU::Board*   board;
    TbMode         mode;
    const char*     name;
    const char*     db_path;
    const char*     checkpoint_path;
} TbOpts;
```

For details, see the header file `zRci.hh`.

All other features pertaining to controlling transactors are used directly with the UCLI commands.

You can generate the `<testbench.so>` shared object by using the given functions.

Callbacks are called before and after specific actions described in functions signatures. For example, `zRci_pre_boad_init` is called right before board initialization and `zRci_post_board_init` is called right after.

In case multiple shared libraries are loaded, the functions with the same signature is called for all loaded shared libraries.

## Example: Using zRci Callback Functions

This example shows how to use `zRci` callback functions to generate a shared object.

```
#include "zRci.hh"
Board *board = NULL;
void xtors (unsigned int) {...}

// Instantiate C++ Objects
void* zRci_pre_board_open(const ZRCI::TbOpts&) {
    // Code that needs to be executed before Board::open
}
```

```
void* zRci_post_board_open(const ZRCI::TbOpts& arg) {
    // Code that needs to be executed after Board::open
}
// Instantiate C++ Transactors objects and Transactors initialization //
// using Board object set within the zRci_post_board_open() call
void* zRci_pre_board_init() {
    // Code that needs to be executed after Board::open but before
    // Board::init
    display = new (DSI);
    receiver0 = new UartTerm;
    receiver1 = new UartTerm;
    // Using TbOpts parameters in a function
    receiver0->init(arg.board,"hw_top.uart_driver_0");
    // Storing TbOpts parameter to local variable and use it in a
    // function
    board = arg.board;
    receiver1->init(board,"hw_top.uart_driver_1");
    display->init(board, "hw_top.u_dsi_driver");
}
// Initialize Transactor and start Transactor threads
void* zRci_post_board_init(const ZRCI::TbOpts&) {
    // code that needs to be executed after Board::init
    receiver0->...();
    receiver0->config();
    receiver1->...();
    receiver1->init(board,"hw_top.uart_driver_1");
    display->...();
    display->connectExternalDisplay(5000,5000);
    if (pthread_create(&p_dsi, NULL, (void *(*)(void *))xtors, (void
        *)board)) {
        perror("Could not start DSI thread\n");
    }
}
// Close Transactor threads after deleting C++ object
void* zRci_cleanup(const ZRCI::TbOpts&) {
    //Stop all the threads, delete objects
}
```

---

## UCLI Commands

The main UCLI commands are grouped into the following categories:

- [Setting Up Runtime](#)
- [Connecting and Disconnecting the Emulator](#)
- [Runtime Commands](#)
- [Legacy Power Estimation Status](#)



- [Power Management Commands](#)
- [Randomization Commands](#)

For information on UCLI commands used for debug activities, such as waveform capture, record/Replay stimuli and hardware (HW) states, and runtime trigger control, see [Debug UCLI Commands](#).

## Setting Up Runtime

Use the *config* command to complete the runtime setup. The following table lists the arguments used with the *config* command.

**Table 2**      *Config Command Usage*

Syntax	Description
<code>config zebu_work &lt;zebu.work&gt;</code>	Specifies the path of the <code>zebu.work</code> directory. The path must be provided to the <code>config</code> command or the <code>--zebu-work</code> option to <b>zRci</b> . If both are specified, the priority is given to the <code>--zebu-work</code> option.
<code>config default_clock [posedge both &lt;Clock's Name&gt;]</code>	Specifies the default DUT primary clock hierarchical name and active edge. <b>zRci</b> uses this information to count clock cycles. This clock is named as "default clock".
<code>config db_path [&lt;database&gt;]</code>	Specifies the design to be loaded when no connection to the emulator is planned or required. This is useful to process data or waveforms from a previous <b>zRci</b> run.
<code>config async_readback [on off]</code>	Controls the asynchronous readback. Default is <i>off</i> .
<code>config [parallel_io &lt;expressions&gt;]</code>	Controls parallelization of file I/O. Default is <code>'compression=ZSTDDEFAULT,buffer=500000'</code> . To disable compression, use <code>compression=NONE</code> . This command does not have any impact if there is any active dumping.
<code>config plusargs [on off 0 1]</code>	Automatically parses test/value <code>plusargs</code> from " <code>zRci -- tcl_args</code> " and passes them to emulation when SystemVerilog <code>plusargs</code> was enabled at compile time.
<code>config powermgt [on off 0 1]</code>	Enables or disables power management for upcoming emulation session.  <b>Note:</b> It is enabled by default. Disabling this parameter can improve emulation start up time, but no related functionality would be available.

Table 2 Config Command Usage (Continued)

Syntax	Description
<code>config sva_reset_counters [on off 0 1]</code>	Activates SVA failure counters to be reset before enabling every SVA action.

To configure and use transactors (see [Controlling Transactors](#)), use the `testbench` UCLI command:

```
testbench -load <Shared Object name>
```

For more `testbench` command options, see the [testbench Command](#) section.

After completing the runtime setup, you can connect to the emulator.

## Setting Up Sample Control

Use the `wap` command to set sample control as follows:

```
wap -dump -enable [-policy  
<average|performance|balance|accuracy|precision>] [-sampling_ratio 1|2]
```

This command enables you to set sample control based on the policy and the provided sampling ratio.

Where,

- `-sampling_ratio 1`: 100% (sampling at every tick (DEFAULT))
- `-sampling_ratio 2`: 50% (sampling at every 2 ticks)
- `-policy`: Enables you to set execution policy. If the policy is not specified, no action is taken in zRci, the default policy, `ZEBU_PowerProfile_ExecutionPolicy::NotSet` is retained at runtime.

If the policy is specified, zRci overwrites the policy with the parameter set using `-policy`. After a policy is set, it is used until a different one is provided either via `-config_file` or `-policy`.

### Example

```
wap -dump -enable
//The default ZEBU_PowerProfile_ExecutionPolicy:: Balance policy is
  considered at runtime.

wap -dump -disable
wap -bucket -config_file -import ./run_zebu/config.txt ;# Performance
  mode
wap -dump -enable
```

```
//Then, the ZEBU_PowerProfile_ExecutionPolicy:: Performance policy is
considered.

wap -dump -disable
wap -dump -enable
//The ZEBU_PowerProfile_ExecutionPolicy::Performance policy continues to
be considered.

wap -dump -disable
wap -bucket -config_file -import ./run_zebu/config2.txt ;# Average mode
wap -dump -enable -policy performance
//Next, the ZEBU_PowerProfile_ExecutionPolicy:: Performance policy is
considered.

wap -dump -disable
//Set parameters are disabled.
```

---

## Connecting and Disconnecting the Emulator

To connect to the emulator, zRci provides the `start_zebu` UCLI command. By default, it allows you to start a runtime emulation. In addition, it allows you to restore a saved hardware state and replay the stimuli from a previous run. After connecting to the emulator, you can execute the runtime UCLI commands ([Runtime Commands](#)).

For more information, see the following topics:

- [start\\_zebu Command: Connecting to the Emulator](#)
- [restart\\_zebu Command: Restarting the Emulator](#)
- [close\\_zebu Command: Disconnecting the Emulator](#)

### start\_zebu Command: Connecting to the Emulator

To connect to the emulator, use the following command:

```
start_zebu [<database>]
```

Where, <database> corresponds to the zRci output data location. If no <database> is specified, zRci generates an output data location with the following naming convention: `emulation_data_<yyyy><mm><dd>_<hh><mm><ss>`.

The following table lists the optional arguments:

**Table 3**      *Optional Arguments*

Argument	Description
<code>-restore -sniffer_restore &lt;name&gt; [-frames &lt;n&gt;]</code>	<code>-restore</code> accepts a checkpoint entry and starts a session with its restore. <code>-sniffer_restore</code> accepts a frame name entry to start a replay session. If <code>-frames</code> is used, the replay session is available only for the range between the given frame and number <code>&lt;n&gt;</code> of frames provided. Automatically converts HW states and stimuli and restores <code>&lt;frame&gt;</code> .
<code>-designFeatures &lt;file&gt; [-process &lt;process_name&gt;]</code>	Specifies a customized <code>designFeatures</code> file to be used with the <code>Board::open</code> function by UCLI, and optionally defines a process name for UCLI (included in the <code>&lt;file&gt;</code> ).
<code>-ztune -ztune_csv &lt;directory&gt;</code>	<code>-ztune</code> starts <code>zTune</code> and specifies the output directory, if <code>zTune</code> is enabled in UTF. <code>-ztune_csv</code> starts <code>zTune</code> and specifies the <code>zTune</code> CSV output directory, if <code>zTune</code> is enabled in UTF.

## restart\_zebu Command: Restarting the Emulator

If you need to run several sessions using the same ZeBu database, use the `restart_zebu` UCLI command. This command avoids closing and reopening ZeBu's database allowing faster initialization. It is only available after a previous `start_zebu` command specification.

To restart the emulation session, use the following command :

```
restart_zebu [-designFeatures <file>]
```

Where, `[-designFeatures <file>]` sets a specific `designFeatures` file to be used while restarting the emulation session (the `designFeatures` file can be the same one used to first initialize or a different one).

### Note:

`restart_zebu` is not supported in a multiprocess environment.

### Example

Within the same `zRci` Tcl file, you can run several sessions using the `restart_zebu` UCLI command.

```
...
start_zebu
...
restart_zebu
...
```

```
restart_zebu  
...  
close_zebu
```

**Note:**

The emulation session restarts in the regular mode with the same setup as previously (no changes on setup are allowed because the connection to the emulator is not lost). If required, current session configurations must be reissued.

## close\_zebu Command: Disconnecting the Emulator

To disconnect to the emulator, `zRci` provides the `close_zebu` UCLI command. This command enables you to close the current emulation session and retain the `zRci` prompt so that the other emulation sessions using the `start_zebu` and `close_zebu` commands can run.

**Note:**

The testbench (loadable shared library) is not reloaded and a proper clean-up might be required (`zRci_cleanup` function is called). For more information, see the [Controlling Transactors](#) section.

---

## Runtime Commands

The following commands are used during runtime:

- `configuration_commands`
- `tool_advancing_commands`
- `signal_variable_expression_commands`
- `testbench_command`
- `memory_command`
- `assertion_commands`
- `checkpoint_command`
- `clock_delay_command`
- `dollar_finish_command`
- `socket_service_command`
- `help_routine_command`

## Configuration Commands

The `config` commands are grouped in the following categories:

- [Global Persistent Configurations](#)
- [Session Setup Configurations](#)
- [Current Session Configurations](#)

### Global Persistent Configurations

These commands can be changed at any time and value persists until **zRci** is closed. The following table lists the commands that are used for global persistent configurations.

**Table 4** *config Command Usage for Global Persistent Configurations*

Usage	Description
<code>config invalid_signal error continue ignore</code>	Configures an action (error, continue or ignore) to be taken by the Tcl interpreter if a configured signal is not found during execution.
<code>config compression [0-9]</code>	Customizes the ZTDB gzip compression rate. Default is 1.
<code>config default_clock [posedge both &lt;Clock's Name&gt;]</code>	Specifies the default DUT primary clock hierarchical name and active edge. <b>zRci</b> uses this information to count clock cycles. This clock is named as "default clock".

### Session Setup Configurations

These commands can be set when disconnected from the emulator and they cannot be changed when the emulator restarts. The following table lists the commands that are used for session setup configurations.

**Table 5** *Config Command Usage for Session Setup Configurations*

Syntax	Description
<code>config zebu_work &lt;zebu.work&gt;</code>	Specifies the path of the <code>zebu.work</code> directory. The path must be provided to the <code>config</code> command or the <code>--zebu-work</code> option to <b>zRci</b> . If both are specified, the priority is given to the <code>--zebu-work</code> option.
<code>config db_path [&lt;database&gt;]</code>	Specifies the design to be loaded when no connection to the emulator is planned or required. This is useful to process data or waveforms from a previous <b>zRci</b> run.
<code>config async_readback [on off]</code>	Controls the asynchronous readback. Default is <i>off</i> .

## Current Session Configurations

These commands can only be set when connected to the emulator, and their values are lost while disconnecting or restarting the emulation session. The following table lists the commands that are used for current session configurations.

**Table 6** *config Command Usage for Current Session Configurations*

Syntax	Description
<code>config selection_file [&lt;selection_file&gt;]</code>	<p>Loads the specified selection file. If the selection file is not passed as a parameter, zRci uses the default: &lt;zebu_work&gt;/zrdb/csa_supports.zrdb.</p> <p><b>Note:</b> The selection file is a list of support signals for SW waveform reconstruction. The format of the selection file is .zrdb.</p>
<code>config -parallel_io &lt;string&gt;</code>	<p>Specifies dump file writing parameters.</p> <p><b>Note:</b> This option is available only during emulation. Supports the string forms as follows: "maxMemUsageInMB[,nThreads]{,keyword=value} or "keyword=value{,keyword=value}"</p> <p><b>Note:</b></p> <ul style="list-style-type: none"> <li>keyword/value pairs are case insensitive.</li> <li>- maxMB=N: Equivalent to maxMemUsageInMB and sets the maximum amount of memory to be used for the I/O file.</li> <li>- threads=N: Equivalent to nThreads and sets the number of I/O threads.</li> <li>- lz4=feature: Enables LZ4 compression for the given feature.</li> </ul> <p><b>Note:</b> Currently, only FWC is supported.</p>
<code>config waveform_capture_slicing auto &lt;seconds&gt;[s] &lt;size&gt;MB &lt;samples&gt; &gt;total_samples &lt;slices&gt;slices {&lt;samp les&gt;total_samples,&lt;slices&gt;slices}</code>	<p>Specifies ZTDB slicing and applies to all the up-coming ZTDB waveform captures. For details on ZTDB auto-slicing, see the <i>ZeBu Debug Guide</i>.</p>
<code>config dynamic_probe_slicing auto &lt;seconds&gt;[s] &lt;samples&gt;total_sa mples &lt;slices&gt;slices {&lt;samples&gt;total _samples,&lt;slices&gt;slices}</code>	<p>Specifies ZTDB slicing and applies to all the up-coming ZTDB waveform captures. For details on ZTDB auto-slicing, see the <i>ZeBu Debug Guide</i>.</p>

When you are connected to the emulator, you are recommended to set the parameters listed in the following table.

Table 7 *config Command Usage*

Syntax	Description
<code>config invalid_signal error continue ignore</code>	Configures an action (error, continue or ignore) to be taken by the Tcl interpreter if a configured signal is not found during execution.
<code>config compression [0-9]</code>	Customizes the ZTDB gzip compression rate. Default value is 1.
<code>config selection_file [&lt;selection_file&gt;]</code>	Loads the specified selection file. If the selection file is not passed as a parameter, zRci uses the default: <zebu_work>/zrdb/csa_supports.zrdb.  <b>Note:</b> The selection file is a list of support signals for SW waveform reconstruction. The format of the selection file is .zrdb.
<code>config action_log &lt;file&gt;</code>	Stores the line-by-line execution of zRci commands in <file> that can be referred for future purpose for analyzing or re-execution of a line.
<code>config waveform_capture_slicing auto &lt;seconds&gt;[s] &lt;size&gt;MB &lt;samples&gt; &gt;total_samples &lt;slices&gt;slices {&lt;samp les&gt;total_samples,&lt;slices&gt;slices}</code>	Specifies ZTDB slicing and applies to all the up-coming ZTDB waveform captures. For details on ZTDB auto-slicing, see <i>ZeBu Debug Guide</i> .
<code>config dynamic_probe_slicing auto &lt;seconds&gt;[s] &lt;samples&gt;total_sa mples &lt;slices&gt;slices {&lt;samples&gt;total _samples,&lt;slices&gt;slices}</code>	Specifies ZTDB slicing and applies to all the up-coming ZTDB waveform captures. For details on ZTDB auto-slicing, see <i>ZeBu Debug Guide</i> .

## Tool Advancing Commands

For more information, see the following subsections:

- [run Command](#)
- [exit and finish Commands](#)

### run Command

zRci has two types of runs:

- **Non-blocking commands:** Leave **zRci** session opened so that you can enter other commands while clocks are running.
- **Blocking commands:** Block the Tcl interpreter until the run is finished or you press **Ctrl+C**.



## Non-Blocking Run Commands

The following table lists the non-blocking `run` commands.

**Table 8** *Non-Blocking run Command Usage*

Syntax	Description
<code>run</code>	Releases the default clock. To disable clocks, use: <code>run -disable &lt;clock_name&gt; &lt;clock_group&gt; all</code>
<code>run -clock &lt;Clock's Name Clocks Group Name&gt;</code>	Releases only the specified clock of the group of clocks.
<code>run -disable &lt;Clock's Name Clocks Group Name all&gt;</code>	Stops the single primary clock, the group of primary clocks or all the primary clocks.

## Blocking Run Commands

The following table lists the blocking `run` commands.

**Table 9** *Blocking Run Command Usage*

Syntax	Description
<code>run &lt;n&gt; [-clock &lt;Clock's Name&gt;] [-autocheckpoint &lt;cycles&gt;]</code>	Runs <n> the edges of the default clock if <code>config default_clock &lt;posedge negedge&gt; &lt;Clock's Name&gt;</code> was used. If <code>-clock</code> is used, it runs <n> cycles of the specified clock. If <code>-autocheckpoint</code> is specified, a HW checkpoint is created after given <n> cycles or edges.
<code>run &lt;n&gt;ms us ns ps [-clock &lt;Clock's Name&gt;]</code>	Only available when both Run Managers are instantiated and <code>clock_delay</code> is enabled at compiled time: <code>run_manager -number_of_instances &lt;n&gt;</code> <code>clock_delay -module {&lt;HW Top name&gt;}</code> Advances emulation time <n> time units. If <code>-clock</code> is given, time advanced is applied only on the specified clock.
<code>run -wallclock &lt;seconds&gt;s &lt;minutes&gt;m [-clock &lt;name&gt;]</code>	Runs the default clock for the specified time. If <code>-clock</code> option is used, that specific clock is used for the specified time instead.

## exit and finish Commands

Use this command to exit `zRci` and exit the application with the `<code>` value.

```
finish <code>
exit <code>
```

## Example

In this example `zRci` script, the run is executed twice. The first run is for 10ns, while the second is for 1000us. The script is terminated by the `finish` command.

```
testbench -load ./virtual_solution_lib.so
start_zebu runtime_output
memory -load top.stb.mem -file ../memories/rom.hex
force top.stb.rstn 'b0 -freeze
// First run
run 10ns
force top.stb.rstn 'b1 -freeze
// Second run
run 1000us
puts "Value of counters"
puts "    on input : [get top.stb.count_in -radix dec]"
puts "    on output: [get top.stb.count_out -radix dec]"
memory -store top.stb.mem -file memdump.hex
// Script terminated
finish
```

## Signal/Variable/Expression Commands

For more information, see the following subsections:

- [get Command](#)
- [force and release Commands](#)
- [ccall Command](#)

### get Command

The `get` command returns information, such as signal values and properties. The following table lists the usage of the `get` command.

Table 10 *get Command Usage*

Syntax	Description
<code>get &lt;signal&gt; [-radix hexa bin dec oct]</code>	For registers available through dynamic-probes. Supports signals for waveform reconstruction. Signals on which the following UTF dynamic-probe commands have been applied: <code>probe_signals -type dynamic -rtlname &lt;signalname&gt;set_sw_control_signal -name &lt;name&gt; -hdl_path &lt;signalsname&gt;</code> By default, binary radix is used.
<code>get -width -lsb -msb &lt;signal&gt;</code>	<ul style="list-style-type: none"> <li>• <code>-width</code>: Returns <code>&lt;signal&gt;</code>'s width.</li> <li>• <code>-lsb</code>: Returns the value of the least significant bit of <code>&lt;signal&gt;</code>.</li> <li>• <code>-msb</code>: Returns the value of the most significant bit of <code>&lt;signal&gt;</code>. Otherwise, it returns the value of the entire signal.</li> </ul>

**Table 10** *get Command Usage (Continued)*

Syntax	Description
<code>get -freezable</code>	Prints all the freezable signals
<code>get -freezable &lt;name&gt;</code>	Returns 1 if the provided signal is freezable.
<code>get -depositable &lt;name&gt;</code>	Returns 1 if the provided signal is depositable.
<code>get value_sets</code>	Returns all the Value-Sets.
<code>get -clocks &lt;group_name&gt;</code>	Lists all clocks in the specified clock group.
<code>get clock_groups</code>	Returns the existing clock groups.
<code>get -clock_status [&lt;name&gt;]</code>	Returns the status of the running clocksIf <name> is not passed, this command returns a list the clock and the status of clocks. The following is an example of clock and its status. <code>{clock_1 0} {clock_2 1}</code> If <name> is passed, this command returns 0 if disabled, and returns 1, if enabled.
<code>get zebu_work</code>	Returns the <code>zebu.work</code> directory path.
<code>get db_path</code>	Returns the database path currently in use.
<code>get driver_clk_frequency</code>	Returns the driver clock frequency in KHz.
<code>get -instance [-rtl] [&lt;hier_path&gt;]</code>	Returns a Tcl list containing all available instances from the hierarchical path that was provided. If no path is provided, it returns all top instances available.
	<b>Note:</b> By default, it returns EDIF names. If <code>-rtl</code> is used, it returns RTL names.

Table 10 *get Command Usage (Continued)*

Syntax	Description
<pre>get -signal [-rtl   [-has_coords RB FWC QIWC]]   [&lt;hier_path&gt;]</pre>	<p>Returns a Tcl list containing available signals from the hierarchical path that was provided. If no path is provided, it returns all signals available at the top level. By default, it returns EDIF names. If <code>-rtl</code> is used, it returns RTL names. By default, system signals are not included in this list. If system signals are required, use <code>-system</code> to list them in addition to the design signals. This option is not compatible with <code>-rtl</code>. By default, RTL names of all signals from the given hierarchical path are displayed using the <code>-rtl</code> option. If the <code>-has_coords</code> option is specified with one of the possible values (<code>RB FWC QIWC</code>), the signals with the specified coordinate type are displayed. The <code>-has_coords</code> option can be used multiple times with different coordinate type. For example: If <code>-has_coords RB FWC</code> is used, signals with <code>RB</code> or <code>FWC</code> coordinates are displayed. This option can only be used when <code>-rtl</code> is used. If this option is used, place <code>&lt;hier_path&gt;</code> before the <code>has_coords</code> option. For example: <code>get -signals -rtl top.instance -has_coords RB</code> With the <code>-rtl</code> option, if the signal is detected as vector/array signals, all ranges of the signals are displayed. For example, consider that <code>signal_name[0:1][1:0]</code>. In this case if the signal is detected as union/struct, only the top level of union/struct is displayed. An example of range of a signal can be <code>[4:8]</code>. A vector/array signal can have multiple dimensions, which means multiple ranges. <code>signal_name[0:1][1:0]</code> represents the range of the signal.</p>
<pre>get &lt;signal&gt; -has_coords   RB FWC QIWC</pre>	<p>Returns 1 if the signal has one of the type of coordinates provided in command. Supports only RTL hierarchy path. The <code>-has_coords</code> option can be used multiple times with different coordinate type. For example: If <code>-has_coords RB FWC</code> is used, it returns 1 if the signal has <code>RB</code> or <code>FWC</code> coordinates.</p>
<pre>get [-cycles] default_clock     primary_clocks     secondary_clocks   all</pre>	<ul style="list-style-type: none"> <li>• For <code>default_clock</code>: Returns the default clock name.</li> <li>• For <code>primary_clocks</code>: If a default clock is set, returns all clocks that belong to default clock group.</li> <li>• For <code>secondary_clocks</code>: If a default clock is set, returns all clocks that do not belong to the default clock group.</li> <li>• For <code>all</code>: Returns all clocks. If <code>-cycles</code> is specified, the command also returns the current cycle count for the returned clock.</li> </ul>
<pre>get -cycles all_clocks</pre>	<p>Returns all clock counters, including <code>ClockDelayPorts</code>.</p>
<pre>get -cycles primary_clocks</pre>	<p>Returns all signal counters including <code>ClockDelayPort</code> counters if the default clock is on the timestamp group (including <code>tickClk</code>).</p>

Table 10 *get Command Usage (Continued)*

Syntax	Description
<code>get -cycles secondary_clocks</code>	Returns all signal counters including <code>ClockDelayPort</code> counters if the default clock is not on the timestamp group.

### force and release Commands

You can drive the value of any DUT signal, which was declared using one of the following UTF commands during compilation:

- `zforce`
- `zinject`
- `set_sw_control_signal`

The following table lists the usage of `force` and `release` commands:

Table 11 *Usage of force and release Commands*

Syntax	Description
<code>force &lt;signal&gt; &lt;value&gt; [-radix hexa bin dec oct] -deposit -freeze</code>	<p><code>&lt;signal&gt;</code> is the name of the signal to force. <code>&lt;value&gt;</code> is the value to force.</p> <p>If the value is in Verilog format, the radix is not needed. If <code>&lt;value&gt;</code> is not in the Verilog format and <code>-radix</code> is not provided, zRci interprets <code>&lt;value&gt;</code> as one of the following:</p> <ul style="list-style-type: none"> <li>• Binary if the user-specified value contains only 0 and 1.</li> <li>• Decimal if the user-specified value contains only numbers (0 to 9).</li> <li>• HEXA if any other value is present.</li> </ul> <p><code>-radix</code> is optional. It qualifies the base of the value.</p> <p><code>-deposit</code> can only be used if the <code>set_sw_control_signal</code> or <code>zinject</code> UTF command has been applied to the signal or if the signal is a register that has not been optimized at compile time. <code>-freeze</code> can only be used if the <code>zforce</code> UTF command was applied to the signal.</p> <p><b>Note:</b> It is recommended to use the Verilog literals for <code>&lt;value&gt;</code>.</p>
<code>force -list</code>	Returns the list of forced signals.

Table 11 Usage of force and release Commands (Continued)

Syntax	Description
<code>force -batch &lt;dict&gt;</code>	<p>Forces signal(s) in a single operation.<b>Use Model:</b></p> <pre>force -batch sDict</pre> <p>Where, sDict is as follows:</p> <pre>set sDict [dict create] dict append sDict top.signal1 {deposit 'hdead} dict append sDict top.signal2 {freeze 'hdead} dict append sDict top.signal3 {freeze 'hdead}</pre>
<code>release &lt;signal&gt;</code>	Releases the previously forced signal.
<code>release -batch &lt;dict&gt;</code>	<p>Releases previously forced signal in a single operation as follows:</p> <pre>release -batch top.dict release -batch sList</pre> <p>Where, sList is as follows:</p> <pre>set sList {top.signal1 top.signal2 top.signal3}</pre>

### Example

In this example, the `force` command is used to fix the signal at a specified value. As the `release` command is not present, this signal retains the forced value until the end of emulation.

```
testbench -load ./virtual_solution_lib.so
start_zebu runtime_output
memory -load top.stb.mem -file ../memories/rom.hex
force top.stb.rstn 'b0 -freeze
run 10
force top.stb.rstn 'b1 -freeze
run 1000
puts "Value of counters"
puts "    on input : [get top.stb.count_in -radix dec]"
puts "    on output: [get top.stb.count_out -radix dec]"
memory -store top.stb.mem -file memdump.hex
finish
```

Here, the `-freeze` option is applicable only if the signals had a `zforce` UTF command at compile time, such as the following:

```
zforce -mode dynamic -rtlname {top.stb.rstn}
```

## ccall Command

The `ccall` command provides a single interface to call Verilog tasks or vhdl proc. You can use the `ccall` command in the following modes:

- **Online Mode:** In Online Mode, C/C++ functions are executed during the emulation runtime. The functions are loaded through the shared object passed using the `ccall -load` command. Online mode is enabled by the `ccall -enable` command.
- **Offline Mode:** In Offline Mode, the ZTDB is captured to execute the C/C++ function after the emulation runtime has completed. Offline mode is enabled by `ccall -enable_offline` command.

### Note:

`ccall` must be enabled at runtime when emulating `$display` tasks.

The following table lists the usage of `ccall` command:

**Table 12**      *Usage of the ccall Command*

Syntax	Description
<code>ccall -start</code>	Starts the C function calls. This function must also be called when emulating <code>\$display</code> tasks.
<code>ccall -load &lt;so&gt;</code>	Loads a library containing C functions.
<code>ccall -enable</code>	Enables online mode
<code>ccall -enable_offline</code>	Enables offline mode
<code>ccall [-scope &lt;scope_name&gt; [-name &lt;function_name&gt;   -n &lt;call_number&gt;] ] -enable -disable</code>	Enables/disables <code>zDPI</code> function calls depending on the specified parameters.
<code>ccall -flush</code>	Clears enabled <code>zDPI</code> function calls.
<code>ccall -enable_synchronization -disable_synchronization</code>	Enables or disables synchronization. When it is enabled, it ensures that functions are called in a predetermined order according to the execution time and the call number. When it is disabled, a function call is executed in a chronological order corresponding to its execution in the emulator.
<code>ccall -sampling_clock -expression &lt;clock_expression&gt;</code>	Selects a set of clocks and sensitive edges to apply to <code>zDPI</code> function calls.
<code>ccall -sampling_clock -group &lt;group_name&gt;</code>	Selects a clock group to apply to <code>zDPI</code> function calls.

Table 12 Usage of the *ccall* Command (Continued)

Syntax	Description
<code>ccall -offline_spec &lt;filename list&gt;</code>	Reads the file containing the C functions to be executed in offline mode.
<code>ccall -dump_offline [-dump_all] &lt;filename&gt;</code>	Sets the name of the ZTDB file that contains the data needed to run the C function calls in the offline mode. <code>-dump_all</code> allows to force all the C-calls to be executed offline. They cannot be executed online.

## testbench Command

The *testbench* commands allow you to control transactors by using callback functions. The following table lists the usage of the *testbench* command:

Table 13 Usage of *testbench* Commands

Syntax	Description
<code>testbench -load &lt;path&gt;</code>	<p>Loads the shared object file containing functions to be automatically called by <code>zRci</code> in determined session phases. Returns an identifier for a given shared library.</p> <p><b>Note:</b> This command must be executed before connecting to the emulator.</p>
<code>testbench -param &lt;key&gt; [&lt;value&gt;] [-tb &lt;identifier&gt;]</code>	<p>Set up parameters on the user shared object testbench environment. It uses the <code>zRci_param(...)</code> interface. Sends a parameter callback to the shared object by the identifier given in <code>"-tb"</code>. If <code>-tb</code> is not provided, callback is sent to all loaded shared objects (in no particular order).</p> <p><b>Note:</b> The execution is blocked until completion of an external function.</p>
<code>testbench -command &lt;key&gt; [&lt;value&gt;] [-tb &lt;identifier&gt;]</code>	<p>Runs a command/routine that returns values from the shared object testbench environment. It uses the <code>zRci_command(...)</code> interface. <code>&lt;value&gt;</code> is optional. Sends a command callback to the shared object by the identifier given in <code>"-tb"</code>. The identifier is optional if a single shared object is loaded (it is executed on the loaded shared object).</p> <p><b>Note:</b> The execution is blocked until completion of an external function.</p>



## memory Command

The `memory` command handles the memory associated actions, such as loading, writing data to and reading data from a memory instance. The following table lists the usage of the `memory` command.

### Note:

It is recommended to always use Verilog literals (for example, `b0b123456`) as input for memory addresses and values instead of regular literals. If any regular literal is used, it falls back to Tcl parser. For instance, a hexadecimal literal starting with `0b` is parsed as binary instead. In this case, you should use `h0b`.

The `-radix` option must be applied only to display results (on screen or in the files generated).

For examples, see [Example: memory -store](#), [Example: memory -store](#), and [Example: memory -load and -store](#).

Table 14 Usage of memory Command

Syntax	Description
<code>memory</code>	Lists the memories in the design.
<code>memory -load &lt;memory&gt; -file &lt;filename&gt; [-radix bin hexa] [-start &lt;start_address&gt;] [-end &lt;end_address&gt;]</code>	Loads values from a specified file into memory (in a format compatible with <code>readmemb/readmemh</code> ), depending on whether the <code>radix</code> is specified as binary ( <code>bin</code> ) or hexadecimal ( <code>hexa</code> ). By default, the hexadecimal format is used. <code>[start_address]</code> and <code>[end_address]</code> are in the Verilog format.
<code>memory -load &lt;memory&gt; -buffer &lt;buffer&gt; [-radix bin hexa] [-start &lt;start_address&gt;] [-end &lt;end_address&gt;]</code>	Loads values from a specified buffer into memory (in a format compatible with <code>readmemb/readmemh</code> ), depending on whether the <code>radix</code> is specified as binary ( <code>bin</code> ) or hexadecimal ( <code>hexa</code> ). By default, the hexadecimal format is used. <code>[start_address]</code> and <code>[end_address]</code> are in the Verilog format. <b>Note:</b> The buffer is a variable in the Tcl/UCLI environment.

Table 14 Usage of memory Command (Continued)

Syntax	Description
<code>memory -store &lt;memory&gt; -file &lt;filename&gt; [-radix bin hexa raw] [-start &lt;start_address&gt;] [-end &lt;end_address&gt;]</code>	Stores values from memory into a specified file (in a format compatible with readmemb/readmemh), depending on whether the radix is specified as binary (bin), hexadecimal (hexa), or raw (raw). By default, the hexadecimal format is used. [start_address] and [end_address] are in the Verilog format.
<code>memory -store &lt;memory&gt; -buffer &lt;buffer&gt; [-radix bin hexa] [-start &lt;start_address&gt;] [-end &lt;end_address&gt;]</code>	Stores values from memory into a specified buffer (in a format compatible with readmemb/readmemh), depending on whether the radix is specified as binary (bin) or hexadecimal (hexa). By default, the hexadecimal format is used. [start_address] and [end_address] are in the Verilog format.  <b>Note:</b> The buffer is a variable in the Tcl/UCLI environment.
<code>memory -read &lt;memory&gt; [-radix hexa bin dec oct] &lt;address&gt;</code>	Reads a value from memory, depending on whether the radix is specified as binary (bin), hexadecimal (hexa), decimal (dec) or octal (oct). By default, the decimal format is used.
<code>memory -write &lt;memory&gt; [-radix hexa bin dec oct] &lt;address&gt; &lt;value&gt;</code>	Writes <value> into memory, using the radix specified as binary (bin), hexadecimal (hexa), decimal (dec) or octal (oct). By default, the decimal format is used.
<code>memory -size &lt;memory&gt;</code>	Returns the size of a memory.
<code>memory -erase &lt;memory&gt;</code>	Erases the specified memory.
<code>memory -empty</code>	Returns 1 if the design contains no memories. Otherwise, it returns 0.
<code>memory &lt;memory&gt; -pattern &lt;pattern&gt;</code>	Sets the contents of a given memory to follow a given pattern. Pattern should be given in compliant with Verilog literal string.

Table 14 Usage of memory Command (Continued)

Syntax	Description
<code>memory -load -store &lt;dict&gt;</code>	<p>Allows batch execution of memory operations. An interface is introduced for memory <code>-load/-store</code>. This interface requires a Tcl object named, Dict.</p> <p>Each member of the Dict object should map the hierarchy for a given memory and the operation must be done relative to that memory.</p> <p>Following are the supported operations:</p> <ul style="list-style-type: none"> <li>• <code>file</code>: Loads/stores memory content from/to a file.</li> <li>• <code>buffer</code>: Loads/stores memory content from/to an user readable buffer (Tcl environment variable).</li> <li>• <code>pattern</code>: (for load operation only) Sets memory following a given pattern. The use model for this command is provided in <a href="#">Example 1: memory -load</a> and <a href="#">Example: memory -store</a>.</li> </ul>

### Example 1: memory -load

```
set variable {1024'h0}
set variable2 {1024'hdeadbeef}
set mDict[dict create]
dict append mDict top.mem_a {file /path/to/a.mem}
dict append mDict top.mem_b {pattern 32'hdeadbeef}
dict append mDict top.mem_c {file /path/to/c.mem}
dict append mDict top.mem_d {buffer variable}
dict append mDict top.mem_e {buffer variable2}
memory -load mDict
```

### Example: memory -store

```
set dict [dict create]
dict append mDict top.mem_a {file /path/to/a.mem}
dict append mDict top.mem_c {file /path/to/c.mem}
dict append mDict top.mem_d {buffer variable}
dict append mDict top.mem_e {buffer variable2}
memory -store mDict
```

### Example: memory -load and -store

This example shows how to use the `memory -load` command to load a file's content (`../memories/rom.hex`) to a memory and then store the memory's content to another file (`memdump.hex`).

```
testbench -load ./virtual_solution_lib.so
start_zebu runtime_output
memory -load top.stb.mem -file ../memories/rom.hex
force top.stb.rstn 'b0 -freeze
run 10
force top.stb.rstn 'b1 -freeze
run 1000
puts "Value of counters"
puts "    on input : [get top.stb.count_in -radix dec]"
puts "    on output: [get top.stb.count_out -radix dec]"
memory -store top.stb.mem -file memdump.hex
finish
```

## Assertion Commands

The `sva` command displays statistical information regarding pass, fail, or fail attempts of SystemVerilog Assertions (SVA).

The following table lists the usage of the `sva` command.

**Table 15**      *Usage of sva Command*

Syntax	Description
<code>sva -stop</code>	Stops all SVA.
<code>sva -start [-clock &lt;clock&gt;] [-file &lt;filename&gt;] [-action &lt;proc&gt;]</code>	<p>If SVAs were compiled, all SVAs are initialized and enabled as <code>-report</code>. If <code>-clock</code> is not set, the default clock is used for all SVAs. If a file is passed as a parameter, all failure logs are redirected to the file. Otherwise, they are shown on the default output.</p> <p><code>[-action &lt;proc&gt;]</code> is used when ZTDB is not captured. <code>&lt;proc&gt;</code> is a procedure that has the following parameters:</p> <ul style="list-style-type: none"> <li>• Success: value</li> <li>• Severity: value</li> <li>• Message: SVA message from the assertion</li> <li>• filename: Verilog filename from where the assertion comes from</li> <li>• line: line number in the <code>&lt;filename&gt;</code> where the assertion is present</li> <li>• scope: hierarchy where the assertion is present</li> <li>• start: start of the assertion</li> <li>• end: end of the assertion</li> </ul>
<code>sva -enable -notifier -report [&lt;sva&gt;]</code>	Enables a given SVA if <code>&lt;sva&gt;</code> is provided. Otherwise, it enables all SVAs. Both <code>-report</code> and <code>-notifier</code> output failures report to the stream provided in 'sva -start' (file or default output). However, if <code>-notifier</code> is used, the execution stops when failures occur.

**Table 15**      *Usage of sva Command (Continued)*

Syntax	Description
<code>sva -disable [&lt;sva&gt;]</code>	Disables a given SVA if <sva> is provided. Otherwise, it disables all SVAs.
<code>sva -flush</code>	Flushes the set of enabled assertions.
<code>sva -state &lt;sva&gt;</code>	Returns the state of a given assertion.
<code>sva -list</code>	Lists all assertions with their full paths.
<code>sva -has_assertions</code>	Returns 1 if any SVA was compiled. Otherwise, it returns 0.
<code>sva -report &lt;-fatal -error -warning -info  -display -success&gt;</code>	Selects the types of messages to be reported during any failure. It must be called before 'sva -start'.
<code>sva -failures [-counters]</code>	-failures: Lists failed assertions. -counters: Adds failure counter to the result. This includes non-failed assertions.

## checkpoint Command

The checkpoint command saves and restores the emulator hardware state. The following table lists the usage of `checkpoint` command:

**Table 16**      *checkpoint Command*

Syntax	Description
<code>checkpoint [-save -restore] &lt;state_name&gt;</code>	Saves/Restores hardware state to/from the checkpoint database
<code>checkpoint</code> <code>checkpoint -list</code>	Lists all checkpoints saved in the database
<code>checkpoint -import &lt;saved_checkpoint&gt;</code>	Imports a previously saved checkpoint
<code>checkpoint -fullsave</code>	Executes a DMTCP (full hardware and software state) save. Returns 1 when the command is in run mode and performs a DMTCP save. Returns 0 when the command is in restored session and does NOT perform a DMTCP save.

**Table 16** *checkpoint Command (Continued)*

Syntax	Description
<code>checkpoint -auto_create &lt;n&gt;[s m h] [-prefix &lt;prefix&gt;]</code>	Sets a time period to enable automatic hardware state creation. If the time unit is not provided, default time unit is “minutes”. If a prefix is given, the entries are saved in zRci's database with the prefix. Default naming uses the format "checkpoint_auto_<timestamp>".

### Example: Create a checkpoint

```
config zebu_work zcui.work/zebu.work
config db_path ./db
...
# other configurations
...
start_zebu
run
# after a while
checkpoint -save ckpt1
# after a while
exit
```

### Example: Import and restore a saved checkpoint

```
config zebu_work zcui.work/zebu.work
zcui.work/zebu.work
config db_path db_new
db_new/
...
# other commands ...
...
checkpoint -import db/ckpt1 ckpt1_imp
checkpoint -list
```

The following is displayed:

```
ckpt1_imp
```

Use the following command to restore ckpt1\_imp:

```
checkpoint -restore ckpt1_imp
```

## clock\_delay Command

The `clock_delay` command helps reprogram `ClockDelayPort` in the design. The following table lists the usage of `clock_delay` command.

**Table 17**      *Usage of clock\_delay Command*

Syntax	Description
<code>clock_delay -set_clock_shape</code>	Configures the <code>clockDelayPort</code> duty cycle (high/low periods).
<code>clock_delay -instance_name &lt;instance_name&gt;</code>	Specifies the <code>clockDelayPort</code> instance name.
<code>clock_delay -duty_low &lt;duty_low&gt;</code>	Specifies the duty low phase duration.
<code>clock_delay -duty_high &lt;duty_high&gt;</code>	Specifies the duty high phase duration.
<code>clock_delay -phase &lt;phase&gt;</code>	Specifies the phase duration.
<code>clock_delay -immediate</code>	Applies the reconfiguration immediately or wait until the next edge.
<code>clock_delay -set_reset</code>	Configures a new reset port of the instantiated <code>clockDelayPort</code> .
<code>clock_delay -reset_value &lt;reset_active_value&gt;</code>	Defines the reset active value.
<code>clock_delay -reset_duration &lt;reset_duration&gt;</code>	Defines the duration of the reset.
<code>clock_delay -list</code>	Lists all the clock delay port names.
<code>clock_delay -get_clock_shape -instance_name &lt;instance_name&gt; -text</code>	Displays configuration details for the specified duty periods of clock delay port in a readable form.
<code>clock_delay -get_clock_shape -instance_name &lt;instance_name&gt; -duty_low</code>	Displays configuration details for the specified duty periods of clock delay port in a form suitable for use in a script.
<code>clock_delay -get_clock_shape -instance_name &lt;instance_name&gt; -duty_high</code>	Displays configuration details for the specified duty periods of clock delay port in a form suitable for use in a script.
<code>clock_delay -get_clock_shape -instance_name &lt;instance_name&gt; -phase</code>	Displays configuration details for the specified phase of clock delay port in a form suitable for use in a script.
<code>clock_delay -get_reset -instance_name &lt;instance_name&gt; -text</code>	Displays configuration details for the specified reset of clock delay port in readable form.
<code>clock_delay -get_reset -instance_name &lt;instance_name&gt; -reset_value</code>	Displays configuration details for the specified reset of clock delay port in a form suitable for use in a script.

**Table 17**      *Usage of clock\_delay Command (Continued)*

Syntax	Description
<code>clock_delay -get_reset -instance_name &lt;instance_name&gt; -reset_duration</code>	Displays configuration details for the specified reset of clock delay port in a form suitable for use in a script.
<code>clock_delay -get_tolerance</code>	Displays clock delay tolerance.
<code>clock_delay -get_tolerance [-compiled]</code>	Specifies that compile time clock delay tolerance is required.
<code>clock_delay -set_tolerance &lt;tolerance&gt;</code>	Configures a new reset.
<code>clock_delay -disable -instance_name &lt;instance_name&gt;</code>	Disables the specified clock delay port.
<code>clock_delay -wait_reconfigure</code>	Waits until all clock delay port reconfiguration is complete.
<code>clock_delay -recompute_tolerance</code>	Recomputes and sets tolerance based on latest value read from the clock delay port.
<code>clock_delay -get_counter</code>	Obtains all <code>ClockDelayPort</code> counters of the design.
<code>clock_delay -get_counter -instance_name &lt;name&gt;</code>	Obtains the counter for a specific <code>ClockDelayPort</code> signal.

## dollar\_finish Command

The `dollar_finish` command defines the Tcl procedure to run whenever `$finish` is called at runtime. The command must be enabled before calling the `start_zebu` command.

The feature is available only in a single process run. The `designFeatures` file must contain "`$nbProcess = 0;`". The syntax is as follows:

```
dollar_finish -enable <proc_name>|-disable
```

Where:

- `-enable <proc_name>`: Enables the `$finish` and the Tcl callback procedure name. The procedure must not have any arguments.
- `-disable`: Disables `$finish` callbacks



## socket\_service Command

The `socket_service` command setups a socket server. The command must first be enabled before calling the `start_zebu` command. The syntax is as follows:

```
socket_server -port <port>
```

Where:

- `-port <port>`: Specifies the TCP port to be listen
- `-verbose`: Activates the verbose mode for the commands received from socket
- `-kill`: Closes an opened `sock_service`

The `socket_service` follows a formatting similar to XML. Current valid tags that might be sent from zRci as follows:

```
<cmd></cmd>  
<msg></msg>  
<err></err>  
<res></res>
```

The requested commands must be sent in plain text and in a single line. They are executed as soon as a new line character is sent to zRci.

Following are the formats:

- **Erroneous commands** (TCL\_ERROR):  

```
<cmd>error_cmd error_args</cmd><err>error_msg</err>
```
- **Successful commands** (not TCL\_ERROR):  

```
<cmd>valid_cmd valid_args</cmd><res>return_value</res>
```

### Note:

Messages using the `<msg>` tag are sent by zRci to inform about certain actions, such as to confirm connection. They are used only for informative purposes.

## Help Routine Command

Use this command to return the help on the specified command. Otherwise, it returns the list of commands available. The syntax is as follows:

```
help [<command>]
```

### Note:

In the current version of zRci, the list of commands and their options are different when connected and not connected to the emulator.

## Legacy Power Estimation Status

The following table describes the usage of the power estimation commands:

**Table 18**      *Usage of Power Estimation Commands*

Syntax	Description
<code>poes -active</code>	Returns power estimation status
<code>poes -finish</code>	Finalizes power estimation
<code>poes -init &lt;output_directory&gt; &lt;sampling_clock&gt; &lt;format&gt; &lt;period&gt; &lt;window&gt; &lt;timescale&gt; &lt;quick&gt; &lt;value_sets&gt;</code>	Initializes power estimation
<code>poes -list_formats</code>	Returns the supported formats
<code>poes -set_frequency &lt;frequency&gt;</code>	Defines frequency or power estimation

## Power Management Commands

The following table describes the usage of the power management commands:

**Table 19**      *Usage of powermgt Command*

Syntax	Description
<code>powermgt -enable -disable</code>	Enables or disables power management feature
<code>powermgt -corrupt -enable -disable</code>	Enables or disables the corruption feature
<code>powermgt -domain -enable -disable -release [&lt;domain list&gt;]</code>	Lists the power domains to act upon; if omitted, all domains are affected. <ul style="list-style-type: none"> <li>• <code>-enable</code>: Forces domains to turn on.</li> <li>• <code>-disable</code>: Forces domains to turn off.</li> <li>• <code>-release</code>: Stops domains being forced.</li> <li>• <code>-state</code>: Checks the power status of domains.</li> </ul>
<code>powermgt -domain -list -last_fired</code>	<code>-list</code> : Returns the list of power domains defined. <code>-last_fired</code> : Prints the power domain activated last.
<code>powermgt -domain -name &lt;signal-list&gt;</code>	Finds the domain name of each signal

Table 19 Usage of powermgt Command (Continued)

Syntax	Description
<code>powermgt -domain -state [&lt;domain-list&gt;]</code>	<code>&lt;domain-list&gt;</code> is the list of power domains to act upon; if omitted, all domains are affected. <ul style="list-style-type: none"> <li>• <code>-enable</code>: Forces domains to turn on.</li> <li>• <code>-disable</code>: Forces domains to turn off.</li> <li>• <code>-release</code>: Stops domains being forced.</li> <li>• <code>-state</code>: Checks the power status of domains.</li> </ul>
<code>powermgt -force random regular</code>	Checks if the forced signals must be randomized when the power domain is off
<code>powermgt -isolation -enable -disable [&lt;strategy-list&gt;]</code>	<code>&lt;strategy-list&gt;</code> is the list of strategies to act upon; if omitted, all strategies are affected. <ul style="list-style-type: none"> <li>• <code>-enable</code>: Activates strategies.</li> <li>• <code>-disable</code>: Deactivates strategies.</li> <li>• <code>-state</code>: Checks the activation status of strategies.</li> </ul>
<code>powermgt -isolation -list</code>	Returns the list of defined strategies
<code>powermgt -isolation -state [&lt;strategy-list&gt;]</code>	<code>&lt;strategy-list&gt;</code> is the list of strategies to act upon; if omitted, all strategies are affected. <ul style="list-style-type: none"> <li>• <code>-enable</code>: Activates strategies.</li> <li>• <code>-disable</code>: Deactivates strategies.</li> <li>• <code>-state</code>: Checks the activation status of strategies.</li> </ul>
<code>powermgt -randomizer -init -random -fixed &lt;value&gt;</code>	Initializes the randomizer with the given values. The emulated domain signals are randomized on power off, as specified <code>-fixed &lt;value&gt;</code> must be '0' or '1'. <code>-random &lt;value&gt;</code> is the random number generator seed. Default is <code>-random 0</code> .
<code>powermgt -randomizer -run [&lt;domain-list&gt;]</code>	Runs the randomizer <code>&lt;domain-list&gt;</code> is the list of domain names to be randomized. If omitted, all domains are randomized.
<code>powermgt -retention -enable -disable [&lt;strategy-list&gt;]</code>	<code>&lt;strategy-list&gt;</code> is the list of strategies to act upon; if omitted, all strategies are affected. <ul style="list-style-type: none"> <li>• <code>-enable</code>: Activates strategies.</li> <li>• <code>-disable</code>: Deactivates strategies.</li> <li>• <code>-state</code>: Checks the activation status of strategies.</li> </ul>
<code>powermgt -retention -list</code>	Returns the list of strategies defined in the design

**Table 19**      *Usage of powermgt Command (Continued)*

Syntax	Description
<code>powermgt -retention -state [&lt;strategy-list&gt;]</code>	<code>&lt;strategy-list&gt;</code> is the list of strategies to act upon; if omitted, all strategies are affected. <ul style="list-style-type: none"> <li><code>-enable</code>: Activates strategies.</li> <li><code>-disable</code>: Deactivates strategies.</li> <li><code>-state</code>: Checks the activation status of strategies.</li> </ul>
<code>powermgt -srsn -enable -disable</code>	Enables or disables the <code>set_related_supply_net</code> feature
<code>powermgt -supply -state &lt;pad-list&gt;</code>	<code>&lt;pad-list&gt;</code> is the list of pad names whose supply state is checked
<code>powermgt -supplyOff &lt;pad-list&gt;</code>	<code>&lt;pad-list&gt;</code> is the list of pad names to be disabled
<code>powermgt -supplyOn &lt;voltage&gt; &lt;pad-list&gt;</code>	Voltage value for pads <code>&lt;pad-list&gt;</code> is the list of pad names to be enabled.
<code>powermgt -scramble -enable -disable [-all -registers -memory]</code>	Enables/disables power management scrambling for registers and/or memories. If the option is not provided, it activates/deactivates scrambling for all (registers and memories).

## Randomization Commands

The following table describes the usage of the randomization commands:

**Table 20**      *Usage of randomize Commands*

Syntax	Description
<code>randomize &lt;seed&gt;</code>	Randomizes with the given <code>&lt;seed&gt;</code> .
<code>randomize -signals &lt;seed&gt;</code>	Randomizes signals with the given <code>&lt;seed&gt;</code> .
<code>randomize -memories &lt;seed&gt;</code>	Randomizes memories with the given <code>&lt;seed&gt;</code> .
<code>randomize -select_signals &lt;list_file&gt;</code>	Randomizes the signals in the <code>&lt;list_file&gt;</code> .
<code>randomize -select_memories &lt;list_file&gt;</code>	Randomizes the memories in the <code>&lt;list_file&gt;</code> .

## Example

An example use model for randomization commands is as follows:

```
// load signals and memories to randomize
randomize -select_signals <list_file>
randomize -select_memories <list_file>
// randomize using a Seed
randomize -signals <seed>
randomize -memories <seed>
```

---

## Debug UCLI Commands

At compile time, the following UTF command must be set for debugging:

```
debug -all true
```

This command enables the following:

- Offline debug (stimuli records and replay)
- Waveform capture using dynamic-probes (`QIWC` and `FWC` also require `$dumpvars` in a top-level Verilog module)
- Waveform reconstruction

The following commands are used:

- [sniffer command](#)
- [replay Commands](#)
- [dump Commands](#)
- [stop Commands](#)
- [Example Using Debug UCLI Commands](#)

For more information on debugging, see the *ZeBu Debug Guide* and the *ZeBu Debug Methodology Guide*.

---

### sniffer command

Sniffer is the technology that records the ZeBu state and saves the stimuli.

Sniffer records frames. Each frame includes a window of stimuli and the ZeBu State at the beginning of the stimuli window.

With `zRci`, the Sniffer can create frames every `<number>` of cycles only when Blocking Runs are used.

The number of cycles are aligned with the default clocks used with `zRci`.

For more information, see the following subsections:

- [sniffer Commands to Execute Before Connecting to the Emulator](#)
- [sniffer Commands to Execute After Connecting to the Emulator](#)

## sniffer Commands to Execute Before Connecting to the Emulator

The following table lists the `sniffer` commands, which must be executed **before connecting to the emulator**:

**Table 21** *Sniffer Commands to be Executed Before Connecting to the Emulator*

Syntax	Description
<code>sniffer -auto_create &lt;n&gt; &lt;n&gt;s &lt;n&gt;m &lt;n&gt;h [-prefix &lt;prefix&gt;] [-clock &lt;clock&gt;]</code>	Sets Sniffer auto-create to start after the board is opened and initialized. If the clock is passed, it is used as a reference clock. If the clock is not passed, <code>zRci</code> uses the default clock specified with the <code>config</code> command. Default unit for the time units is minutes.
<code>sniffer -start_frame [&lt;name&gt;]</code>	Starts a frame when <code>start_zebu</code> is called. Sniffer is initialized before board initialization. Therefore, sniffing starts at time 0. If a <code>&lt;name&gt;</code> is passed, the frame is saved in the <code>zRci</code> output database.
<code>sniffer -info [&lt;name&gt;]</code>	Depends on usage of: <code>config db_path &lt;database_path&gt;</code> : Lists the frame information. If a name is given, it lists the information of that frame. Otherwise, it lists all frames.
<code>sniffer -import &lt;path&gt; [&lt;name&gt;]</code>	Imports the sniffer directory when created with a non- <code>zRci</code> runtime environment
<code>sniffer -convert &lt;entry path&gt; [-frames &lt;n&gt;]</code>	Converts stimuli from ZTDB to SMD; this is not applicable with transactional Stimuli Capture and Replay technology.
<code>sniffer -info [&lt;name&gt;] [-clock &lt;clock&gt;] [-text]</code>	Returns information such as start cycle, end cycle, and events which occurred within the frame related to a given frame or all, if no name is passed.
<code>sniffer -info [&lt;name&gt;] [-cycles -time]</code>	If used with <code>-clock</code> , this command returns start and end cycle using given clock. If none is passed, information for start and end cycle is provided using default clock.
<code>sniffer -list</code>	Depends on usage of: <code>config db_path &lt;database_path&gt;</code> . Lists all frames in the current database.

## sniffer Commands to Execute After Connecting to the Emulator

The following `sniffer` commands must be executed **after connecting to the emulator**:

**Table 22** *Sniffer Commands to be Executed After Connecting to the Emulator*

Syntax	Description
<code>sniffer -list [-dbs   &lt;db_name&gt;]</code>	<code>sniffer -list</code> : Lists all frames inside zRci's database. If <code>&lt;db_name&gt;</code> is passed, only frames in given database are listed. To list databases, you use <code>-dbs</code> .
<code>sniffer -list -at &lt;n&gt; &lt;time&gt;&lt;time_unit&gt; [-db &lt;db_name&gt;] [-clock &lt;clock&gt;]</code>	Lists sniffer frames that contain a cycle or a timestamp. Passing the sniffer database is optional and used when several are available.
<code>sniffer -list -event [-before &lt;n&gt; &lt;time&gt;&lt;time_unit&gt;]</code>	
<code>sniffer -info [&lt;name&gt;]</code>	Returns information such as start cycle, end cycle and events, which occurred within the <code>&lt;name&gt;</code> frame or all, if no <code>&lt;name&gt;</code> is passed.
<code>sniffer -config clock &lt;name&gt;</code>	Configures the clock <code>&lt;name&gt;</code> to be used as a reference clock by sniffer. If this command is not called, zRci uses the current default clock as a reference clock for sniffer. If the default clock is not set, sniffer selects the reference clock internally from the primary clocks. To check the reference clock that is in use, call the <code>sniffer -reference_clock</code> command after sniffer is started. This command must be called before calling <code>sniffer -start_frame</code> or <code>-auto_create</code> for the first time.
<code>sniffer -config keep_frames &lt;n&gt;</code>	Configures how many frames must be kept while recording. This command must be called before calling <code>sniffer -start_frame</code> or <code>-auto_create</code> for the first time. The default value of <code>&lt;n&gt;</code> is 20.
<code>sniffer -config output_record</code>	This command must be called before calling <code>sniffer -start_frame</code> or <code>-auto_create</code> .
<code>sniffer -config no_memory_copy</code>	Uses links to memory files loaded while sniffing, instead of copying them. This command must be called before calling <code>sniffer -start_frame</code> or <code>-auto_create</code> for the first time.

Table 22 Sniffer Commands to be Executed After Connecting to the Emulator (Continued)

Syntax	Description
<code>sniffer -start_frame [&lt;name&gt;]</code>	<p>Starts a new frame; where, &lt;n&gt; is the frame ID number. If a name is specified, the given name is used. If you pass a &lt;name&gt;, the name is used for the new entry in the zRci database. If Board is already initialized and the sniffer is not ON, this command starts the sniffer and a new frame. If Board is not initialized, this command sets the sniffer to be initialized before Board initialization and guarantees sniffing to start in time 0. Starting the sniffer causes a sniffer database creation; the sniffer database name is generated based on time/date they were started. The database name pattern follows: YYYYMMDD_SSMMHH.</p> <p><b>Example:</b> 20180213_143511. For listing the databases, use <code>sniffer -list -dbs</code>. The created frames are linked to their sniffer database. By default, the frame names, if user does not specify one, is: YYYYMMDD_SSMMHH.F_&lt;n&gt; name entry of the zRci's database. The frames can be listed using <code>sniffer -list</code>. This command lists all frames in zRci's database or <code>sniffer -list &lt;database_name&gt;</code>, which lists frames within the specific sniffer database. While recording, this command starts a new frame. When the auto-creation is ON and this command is given, the current frame is closed and a new frame is opened, and then the auto-creation time is restarted.</p>
<code>sniffer -stop</code>	Stops the sniffer.
<code>sniffer -auto_create</code> <code>&lt;minutes&gt; &lt;seconds&gt;s &lt;minutes&gt;m &lt;</code> <code>hours&gt;h -cycles &lt;n&gt; -runtime</code> <code>&lt;n&gt;h m s ms us ns ps fs [-prefix</code> <code>&lt;prefix&gt;]</code>	<p>Starts auto-frame creation based on the given parameters. If Sniffer auto-frame creation is already started, it sets the new time for auto-creation to occur. If Sniffer auto-frame creation is not started, it starts the sniffer auto creation. If a prefix is given, the specified prefix is added to name entries in the zRci's output database in the following the format:</p> <p>&lt;prefix&gt;_&lt;YYYYMMDD_SSMMHH&gt;.F_&lt;n&gt;,  where, &lt;YYYYMMDD_SSMMHH&gt; is the database creation time. &lt;n&gt; is the frame number</p>
<code>sniffer -config &lt;name&gt; [&lt;value&gt;]</code>	<p>Supports the following options:</p> <ul style="list-style-type: none"> <li>• clock &lt;name&gt;</li> <li>• keep_frames &lt;n&gt;</li> <li>• output_record</li> <li>• no_memory_copy</li> </ul>
<code>sniffer -state_capture</code> <code>enable disable</code>	Creates a new frame while -auto_create mode is active.



Table 22      *Sniffer Commands to be Executed After Connecting to the Emulator (Continued)*

Syntax	Description
<code>sniffer -reference_clock</code>	Returns the current reference clock in use.
<code>sniffer -restore &lt;name&gt;</code>	Closes the board and restores the specified frame.
<code>sniffer -restore -event [-before &lt;n&gt; &lt;time&gt;&lt;time_unit&gt;]</code>	Restores the emulation to a specified cycle or timestamp
<code>sniffer -restore -at &lt;n&gt; &lt;time&gt;&lt;time_unit&gt; [-db &lt;db_name&gt;] [-clock &lt;clock&gt;]</code>	
<code>sniffer -status</code>	Indicates whether the Sniffer is ON (returns 1) or OFF (returns 0)

**Example**

This example shows the usage of the sniffer command:

```
sniffer -auto_create -cycles 1000000
...
run 1234
...
run 1002020290
...
```

For more example of `sniffer` command usage, see the *Zebu Debug Methodology Guide*.

**replay Commands**

The `replay` command replays the stimuli and can be used only if a frame is restored. If the number of cycles to be replayed is given, this command replays the given number of cycles. If the number of cycles is not provided, it replays the cycles until all the frames are completed.

The following table lists the usage of the replay command.

Table 23 Usage of the replay Command

Syntax	Description
<code>replay [&lt;cycles&gt; &lt;time&gt;]</code>	<p>Replays stimuli for given cycles/time. If no parameter is passed, replays until frames are exhausted. If the provided cycles/time is bigger than remaining stimuli, it also replays until frames are exhausted. The accepted time units for time are as follows:msusnsps</p> <p><b>Note:</b> This command is a blocking command.</p>
<code>replay -end</code>	<p>Replays the remaining cycles from the current frame only.</p> <p><b>Note:</b> This command is a blocking command.</p>
<code>replay -status</code>	Returns 1 if it is in Replay Mode, otherwise returns 0.
<code>replay -current</code>	Returns the current frame from which the stimuli are being replayed.
<code>replay -config &lt;config&gt; &lt;value&gt;</code>	<p>&lt;config&gt; is the configuration setting and &lt;value&gt; holds the input for the given configuration setting. The following options are available only with legacy Stimuli Capture and Replay:</p> <pre>reader   &lt;smd ztdb_threaded_smd_api ztdb_threaded_scanner&gt; max_buffer_depth &lt;n&gt; time_out &lt;n&gt; smd_threaded_reading_buffer_depth &lt;n&gt; safe_mode &lt;on off&gt; tasks &lt;n&gt; injector_process_count &lt;n&gt;</pre> <p>The following options are available with legacy Stimuli Capture and Replay and new technologies:</p> <pre>enable_state_checks &lt;on&gt; enable_io_checks &lt;input output both&gt; progress &lt;on off&gt; multi_host_command &lt;remote_command&gt; [&lt;command&gt;]   (&lt;remote_command&gt; must contain %host)</pre>

Table 23 Usage of the replay Command (Continued)

Syntax	Description
<code>replay -config time_out &lt;n&gt;</code>	For Transactional Stimuli Capture and Replay technology, when the timeout option is not used, no timeout mechanism is applied. Configures the timeout value. If <code>n</code> is not equal to 0, the error message is generated if the <code>driverClk</code> is stopped for more than <code>&lt;n&gt;</code> seconds. If <code>n</code> is equal to 0, no error message is generated but, after 500 seconds the following information message is displayed to inform the user: <code>driverClk</code> is stopped for more than 500 seconds. The default values are as follows: For <code>zRci</code> replay, if <code>n = 0</code> , no error message and the user is indicated only info after 500 seconds. For non- <code>zRci</code> replay, if <code>n = 500</code> , an error message is displayed after 500 seconds.

Replay using time relies on stimuli sampling. The time passed to the `replay` command might not always be reachable as expected. In such a scenario, the following error message is displayed:

```
- ZeBu : zRci: ERROR : ZRCI0168E : The amount of time given was not
  enough to be converted to cycles. Try using a bigger value.
```

You can catch the `replay` command and decide the action to be taken.

## dump Commands

The `dump` command controls waveform capture. The following table explains the usage of `dump` command:

Table 24 Usage of the dump Command

Syntax	Description
<code>dump -file &lt;name&gt; [-sampling_clock &lt;clock_expression&gt;] -dynamic_probe -fwc -qiwc  -file &lt;file&gt;.ztdb -awc</code>	Creates a ZTDB output stream in the disk and returns the file identifier to this stream. ZTDB waveforms are saved in the <code>zRci</code> output database. The cycles to start and end are based on the default clock.
	The <code>&lt;fid&gt;</code> returned by this <code>dump</code> command is required by other <code>dump</code> commands.

Table 24 Usage of the dump Command (Continued)

Syntax	Description
	On ZS 3, for FWC and QiWC, the sampling setup is ignored and sampling is applied on all the edges of all primary clocks. For dynamic-probe, if a sampling clock is provided, the clock expression is used to sample the dynamic-probes support signals. If the sampling clock is not provided, the sampling is applied on all the edges from all primary clocks. Otherwise, the default clock is used as the sampling clock. Several FWC and QiWC waveform files can be captured simultaneously as long as they do not share any Value-Set. Only one dynamic-probe waveform can be captured at a time.
	<code>-file &lt;file&gt;.ztdb -awc:</code> Accepts ValueSets declared as FWC or QiWC on the DUT. However, <code>-fwc</code> rejects QiWC ValueSets, and <code>-qiwc</code> rejects FWC ValueSets.
<code>dump -add_value_set &lt;value_set1&gt; [ &lt;value_set2&gt;...&lt;value_setn&gt;] -fid &lt;fid&gt;</code>	Adds Value-Sets to a given <code>fid</code> . This command is supported by both FWC and QiWC technologies. Where, <code>&lt;value_setn&gt;</code> denotes the number of Value-Sets. <b>Note:</b> You can add one or multiple Value-Sets.
<code>dump -add_signals &lt;list&gt; -fid &lt;fid&gt;</code>	Adds signal(s) to a given <code>fid</code> . This command is supported only by QiWC.
<code>dump -add_instances &lt;list&gt; -fid &lt;fid&gt; [-depth &lt;n&gt;]</code>	Adds instance(s) to a given <code>fid</code> . This command is supported only by QiWC technology.
<code>dump -load_selection &lt;path&gt; -fid &lt;fid&gt;</code>	Loads a Selection File (Support File for SW Waveform Reconstruction) to a given <code>fid</code> . This command is supported only for QiWC and dynamic-probes.
	It is not possible to use the <code>-load_selection</code> and <code>-add_signals</code> or <code>-add_instances</code> . After a dynamic-probe selection file is loaded, the other dynamic-probe cannot be loaded until a new runtime is started using the <code>start_zebu</code> command.
<code>dump -enable [-fid &lt;fid&gt;]</code>	Enables waveform capture. If the waveform capture is not started yet, this command starts the waveform capture. If <code>fid</code> is given, it enables the waveform capture of the corresponding <code>&lt;fid&gt;</code> . If <code>fid</code> is not given, it enables all opened waveform captures.

Table 24 Usage of the dump Command (Continued)

Syntax	Description
<code>dump -disable [-fid &lt;fid&gt;]</code>	Disables waveform capture. If the waveform capture is not stopped yet, this command stops the waveform capture. If <code>fid</code> is given, it disables the waveform capture of the corresponding <code>&lt;fid&gt;</code> . If <code>fid</code> is not given, it disables all opened waveform captures.
<code>dump -close [-fid &lt;waveform_id name&gt;]</code>	Stops waveform capture and closes the corresponding file. If <code>fid</code> is given, it closes the waveform capture of the corresponding <code>&lt;fid&gt;</code> . If <code>fid</code> is not given, it closes all opened waveform captures.
<code>dump -flush [-fid &lt;fid&gt;]</code>	Releases data from the emulator to the Host PC. If <code>fid</code> is given, it releases the data of the corresponding <code>&lt;fid&gt;</code> . If <code>fid</code> is not given, it releases the data all opened waveform captures.
<code>dump -force_full_frame -fid &lt;fid&gt;</code>	Forces ZTDB Slice creation to occur in the next cycle for a waveform capture of the corresponding <code>fid</code> .
<code>dump -interval [-interval auto &lt;seconds&gt;[s] &lt;size&gt;MB &lt;samples&gt;total_samples &lt;slices&gt;slices {&lt;samples&gt;total_samples,&lt;slices&gt;slices}]</code>	<p>Forces full frame creation on a dump stream based on given parameters.</p> <p>Valid values of the <code>&lt;interval&gt;</code> string and the usage are follows:</p> <ul style="list-style-type: none"> <li><code>&lt;seconds&gt;S</code>: <code>dump -interval &lt;seconds&gt;s -fid &lt;File ID&gt;</code></li> <li><code>&lt;size&gt;MB</code>: <code>dump -interval &lt;size&gt;MB -fid &lt;File ID&gt;</code></li> <li><code>-interval auto</code>: Corresponds to specifying none of <code>&lt;samples&gt;total_samples</code> or <code>&lt;slices&gt;slices</code></li> <li><code>&lt;slices&gt;slices</code>: If the value is not specified, <code>&lt;slices&gt;slices</code> is chosen during the runtime to a suitable value for the design.</li> <li><code>&lt;samples&gt;total_samples</code> and <code>&lt;samples&gt;total_samples,&lt;slices&gt;slices</code>: When <code>&lt;samples&gt;total_samples</code> is not specified, the following can be one of the default: <ul style="list-style-type: none"> <li>1 million cycles of sampling clock for FWC/QiWC;</li> <li>200k cycles of sampling clock for readback.</li> <li>Default total number of ZTDB samples: 1 Million</li> <li>Maximum number of ZTDB slices: about 250</li> <li>Maximum number of samples per slice: about 1 Million</li> </ul> </li> </ul>

Table 24 Usage of the dump Command (Continued)

Syntax	Description
<code>dump -auto_flush &lt;on off&gt; -fid &lt;fid&gt;</code>	Forces the emulator to release the data after each blocking command.
<code>dump -simulate &lt;file_name&gt; [-batch &lt;project_file&gt; [-nogui]]</code>	Invokes <code>zCSA</code> to convert an FSDB waveform from a given ZTDB file. <code>&lt;project_file&gt;</code> : Contains <code>zCSA</code> execution configuration. If this command is executed in <code>batch</code> mode, a file called <code>&lt;file_name&gt;.done</code> is generated, which represents <code>zCSA</code> has finished conversion. You can perform this execution with GUI or without GUI using <code>-nogui</code> option.
<code>dump -opened</code>	Returns a list containing information about the currently opened waveform capture files in the following format: {<fid> <type> <name> <status>}
<code>dump -file &lt;file&gt;.ztdb -awc</code>	Creates the ZTDB output in the stream. It also accepts ValueSets declared as FWC or QiWC on the DUT. However, <code>-fwc</code> rejects QiWC ValueSets, and <code>-qiwc</code> rejects FWC ValueSets.
<code>dump -on_slice &lt;procName&gt;</code>	Creates a single <code>proc</code> for all waveform capture. This is applicable for all slices that are being created in future. <code>proc &lt;procName&gt; {fid start end}</code> <code>{&lt;proc body&gt;}</code> Where, <code>fid</code> : Path to the ZTDB name <code>start</code> : Starting point <code>end</code> : Ending point

## stop Commands

Use the `stop` command to manage Runtime Triggers. Depending on the setup, it allows you to execute a Tcl callback procedure. The following table lists the usage of `stop` commands.

Table 25 Usage of the stop Command

Syntax	Description
<code>stop</code>	Returns the names of all the available Runtime Triggers.

Table 25 Usage of the stop Command (Continued)

Syntax	Description
<code>stop -list</code>	<p>Lists all configured stop conditions. In addition, it indicates if the condition was met. It returns a list containing the following information from each available stop condition:</p> <pre>{&lt;id&gt; &lt;status&gt; &lt;type&gt; &lt;name&gt; &lt;extra&gt; &lt;fired&gt;}</pre> <p>where:</p> <ul style="list-style-type: none"> <li>• <b>&lt;id&gt;</b>: Positive integer, stop condition identifier.</li> <li>• <b>&lt;status&gt;</b>: Enabled or disabled – status of the stop condition.</li> <li>• <b>&lt;type&gt;</b>: <ul style="list-style-type: none"> <li>◦ For Runtime Trigger: <code>sw</code></li> <li>◦ For Static/Dynamic Trigger: <code>trigger</code></li> </ul> </li> <li>• <b>&lt;name&gt;</b>: trigger name (module name for Runtime Trigger)</li> <li>• <b>&lt;extra&gt;</b>: <ul style="list-style-type: none"> <li>◦ <b>For Runtime trigger</b>: <code>qiwc fwc</code> – Indicates Waveform Capture technology in use.</li> <li>◦ <b>For Dynamic trigger</b>: <code>&lt;expression&gt;</code> - Indicates loaded dynamic trigger expression. For details on Dynamic Trigger expression, see the <i>ZeBu Debug Guide</i>.</li> <li>◦ <b>For Static trigger</b>: <code>"NOT_DYNAMIC"</code></li> </ul> </li> <li>• <b>&lt;fired&gt;</b>: <code>0 1</code> - Informs if the stop condition is currently met. This is not used for runtime trigger (it would always be reported as 0, user should check/use database or action callbacks).</li> </ul>

Table 25 Usage of the stop Command (Continued)

Syntax	Description
<pre>stop -cel &lt;name&gt; -action &lt;name&gt;] [-repeat &lt;N&gt;] [-once] [-repeat_forever] [-clock &lt;clock&gt;]</pre>	<p>Enables the Runtime Trigger. The <code>-action</code> parameter can be used if you want to execute a Tcl procedure when the stop condition is reached. The Tcl procedure must have a prototype like the following:</p> <pre>proc call {module SampleNumber DUTCycleNumber lastNotify} { [proc body] }</pre> <p>Where:</p> <ul style="list-style-type: none"> <li><code>module</code>: Module name where the notification occurs.</li> <li><code>SampleNumber</code>: Cycle count for the composite clock (sample number) when the notification occurs.</li> <li><code>DUTCycleNumber</code>: Cycle count for the user-defined clock (usually the default clock defined in UCLI initialization) when the notification occurs.</li> <li><code>lastNotify</code>: If Runtime Trigger was enabled in repeat, the value for this parameter is "1" only for the callback for the last notification, "0" otherwise.</li> <li>If <code>-repeat &lt;N&gt;</code> is used, the Complex Event Language (CEL) FSM is restarted whenever the Runtime Trigger is sent for <code>&lt;N&gt;</code> times.</li> <li>If <code>-once</code> is used, the CEL FSM runs once, similar if no parameter is passed.</li> <li>If <code>-repeat_forever</code> is used, the CEL FSM is reset until you disable the Runtime Trigger.</li> <li>If you want to be notified in a different clock other than the default one, you can pass another clock using <code>-clock &lt;clock&gt;</code>. It corresponds to the <code>designClock(cycle)</code> of the Tcl procedure callback.</li> </ul>
<pre>stop -config stop_on_notify [on off]</pre>	<p>Enables the emulation to be stopped when the notification from the Runtime Trigger happens.</p> <p><b>Note:</b> This command is available only in "main run" and not available in replay recorded stimuli.</p>
<pre>stop -config transition_on_same_cycle on/off</pre>	<p>Decreases the transition cycles of runtime triggers by 1. Default is on.</p>
<pre>stop -expression &lt;expr&gt; &lt;name&gt;</pre>	<p>Enables the given <code>&lt;name&gt;</code>. <code>&lt;name&gt;</code> is the instantiation path. <code>&lt;expr&gt;</code> is the equation.</p>



**Table 25**      *Usage of the stop Command (Continued)*

Syntax	Description
<code>stop -enable &lt;name&gt; [-action &lt;proc&gt;]</code>	Enables execution of a <proc> Tcl procedure when the equation is true. This command must be executed after the <code>stop -expression &lt;expr&gt; &lt;name&gt;</code> command.
<code>stop -disable &lt;name&gt;</code>	Disables the Runtime Trigger.
<code>stop -is_dynamic &lt;name&gt;</code>	Returns 1 if the <name> refers to a dynamic stop, otherwise 0 is returned.
<code>stop -state &lt;name&gt;</code>	Checks the status of a given equation and returns if the condition is currently satisfied.
<code>stop -info -events [&lt;entry&gt;] -cycles -time</code>	

## Example Using Debug UCLI Commands

In this example, the `sniffer` and `replay` commands are used. Before using sniffer and replay commands with transactors, make sure you compile with the following commands:

```
debug -offline_debug true
debug -offline_debug_params {INCL_XTORS=true}
```

The example follows:

```
testbench -load ./virtual_solution_lib.so
start_zebu runtime_output
memory -load top.stb.mem -file ../memories/rom.hex
force top.stb.rstn 'b0 -freeze
run 50
sniffer -config clock hw_top.clk
# following command is taken into account only for blocking runs
sniffer -auto_create -cycle 10000
run 50000
sniffer -stop
sniffer -restore -at 3500
dump -file ...
replay 10000
dump -close ...
finish
```

## Command-Line Editing

The following table lists the command-line entires that can be used with **zRci**.

**Table 26** *Command-Line Entires Supported with zRci*

Command-Line Entries	Description
ctrl-C	If in a blocking run, <b>zRci</b> stops the blocking run and continues executing outstanding commands, if any.
ctrl-D	<b>zRci</b> must exit (after control comes back to user).
ctrl-\	<b>zRci</b> stops immediately and exits.

## ZEMI3 Commands

The following table lists the **zemi3** commands supported with **zRci**.

**Table 27** *Usage of zemi3 Commands*

Syntax	Description
<b>zemi3</b> -setup <function> -lib <path>	Adds a setup step call (after board open).
<b>zemi3</b> -init <function> -lib <path>	Adds an init step call (after board init).
<b>zemi3</b> -close <function> -lib <path>	Adds a close step call (just before board close).
<b>zemi3</b> -main <function> -lib <path> [-action <action>]	Specifies a main function.
<b>zemi3</b> -config <file>	Specifies a transactor configuration file.
<b>zemi3</b> -xtor <name> -lib <path>	Specifies transactor to use given lib.
<b>zemi3</b> -lib <path>	Specifies a shared library from where the file is loaded.
<b>zemi3</b> -threading <model> [-param <param>]	Specifies threading model to be used. The valid values are as follows: <ul style="list-style-type: none"> <li>• xtor_with_service</li> <li>• xtor_only</li> <li>• xtor_only_with_wait</li> <li>• service_only</li> </ul> Default is “ <i>xtor_only</i> ”.

**Table 27**      *Usage of zemi3 Commands (Continued)*

Syntax	Description
<code>zemi3 -yield &lt;model&gt;</code>	Specifies yield properties. The valid values are as follows: <ul style="list-style-type: none"> <li>• none</li> <li>• tx</li> <li>• rx</li> <li>• all</li> </ul> Default is “none”.
<code>zemi3 -waitgroup &lt;timeout&gt;</code>	Enables <code>WaitGroup</code> timeout (negative value is disabled).
<code>zemi3 -printdebug &lt;on off 0 1&gt;</code>	Enables/disables the debug mode.
<code>zemi3 -xtor &lt;xtor&gt; -lib &lt;path&gt;</code>	Specifies transactor to use the given <code>lib</code> .
<code>zemi3 -args &lt;args&gt; ...</code>	Specifies arguments for calls. The default is set to the values specified on the <code>zRci</code> command line.
<code>zemi3 -unload [&lt;path&gt;]</code>	Unloads the shared library by deactivating all linked actions. If no <code>&lt;path&gt;</code> is provided, all <code>zemi3</code> shared libraries are unloaded.
<code>zemi3 -enable -disable</code>	Enables/disables <code>Zemi3Manager</code> .