clk													
rst													
Driver::instr_str		NOT RO R3		NOP	SUB R3	R1	RD R2 0	x14			SUB R0 R0		RD R3 0x20
data_out	00	43		00	2d		52	14	a5		20		53
address	00			01	02		03	04	14		05		06
data_in	00		01	00 02	2 00 ff	00 03	00 04	00		05	00	06	00
write													
R0_out	00												
R1_out	00					01							
R2_out	00									a5			
R3_out	00		ff										