clk																				
rst																				
Driver::instr_str	SUB RO RO RD R3 0x20					ADD R1 R1			RDI R2 0x97				ADD R2 R0				NOT RO RO			
data_out	20	53	20	9c		15			92		97		18				40			
address	05	06	07	20		80			09		(0a		0b				Ос		0d	
data_in	00 06	(00 (07	00		08	00	01	09	00	0a	00	0b	00	97	00	Ос	97	0d	68	
write																				
R0_out	00															97		68		
R1_out	01							02												
R2_out	a5											97								
R3_out	ff				9с															