**Chapter 4, Connecting the Testbench and Design homework 1**

The purpose of this homework is to practice using interfaces and functions in interfaces by improving on your solution for Chapter 3 Procedural Statements and Routines homework. Modify this solution to:

1. Use an interface in the I/O declaration for module my\_mem and use the interface in the testbench.
2. Move the write=1 and read=1 checker from the testbench to the interface.
3. Move the function that calculates even parity from module my\_mem to to the interface.

Deliverables:

1. Code for my\_mem, interface, and test-bench.
2. Waveform showing at a minimum the I/O of the memory model and the error counter.
3. Transcript window showing 0 memory errors and at least 1 read=1 & write=1 error.