

## Specify program memory settings

### Processor's reset and exception vectors (read-only)

Reset vector address (hex): 0

Exception vector address (hex): 20

### Memory options

Here you can specify the starting addresses of sections identified by `.text` and `.data` assembler directives. These addresses can be in the same or in different memories (on-chip, SDRAM, ...). They can be used to ensure that the `.text` and `.data` sections do not overlap with other sections, such as `.reset` and `.exceptions`. If `.text` and `.data` are specified to have the same address, the `.data` section will be placed right after the `.text` section by the linker.

#### .text section

Memory device: sdram/s1 (0h - 7ffffffh) ▼

Start offset in device (hex): 400

#### .data section

Memory device: sdram/s1 (0h - 7ffffffh) ▼

Start offset in device (hex): 400

&lt; Back

Next &gt;

Finish

Cancel