

Specify a system

Select a system

DE2 Basic Computer



Documentation

This system, called the DE2 Basic Computer, is intended to be used as a platform for introductory experiments in computer organization and embedded systems. To support these beginning experiments, the system contains only a few components: a processor, memory, and some simple I/O peripherals.

System details

System description (PTF) file:

C:\altera\80sp1\nios2eds\bin\monitor\samples\System\DE2_Basic_Computer\nios_system.ptf

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Quartus II programming (SOF) file (optional):

:\altera\80sp1\nios2eds\bin\monitor\samples\System\DE2_Basic_Computer\DE2_Basic_Computer.sof

Browse...

The SOF file represents the FPGA programming file for the Nios II system. If it is specified here, then the Monitor Program can be used to download this programming file onto the board. Otherwise, the system will need to be downloaded using some other method (for example, by using Quartus II).

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