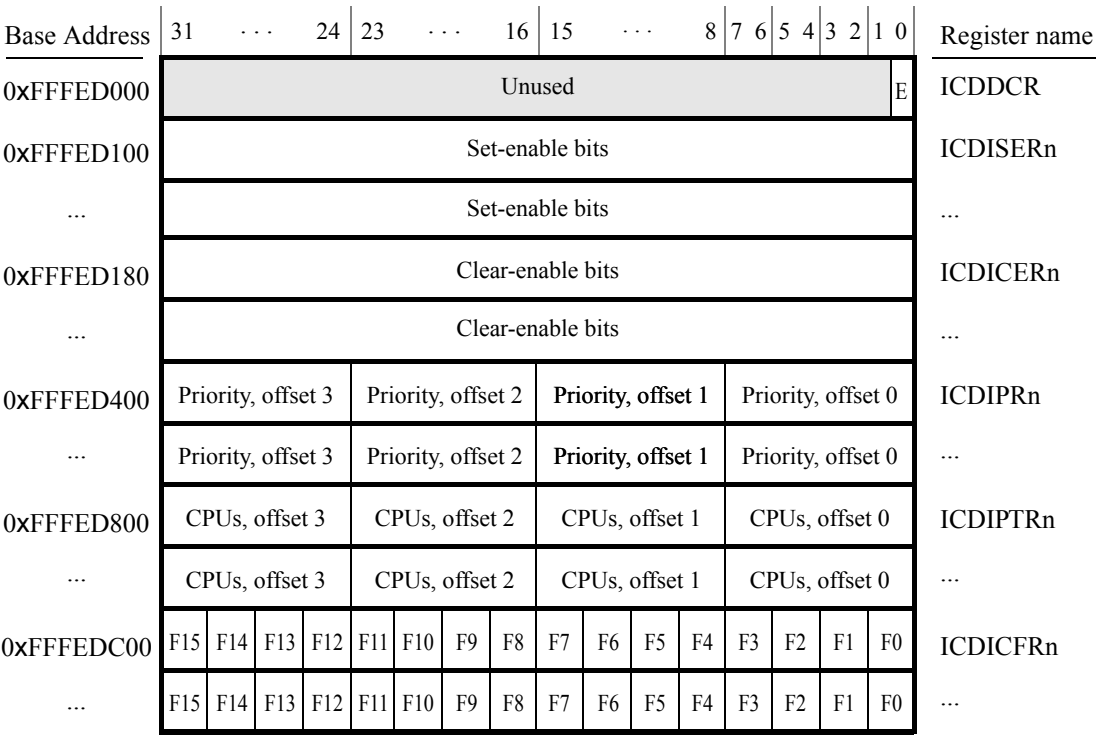
Base Address
31 · · · 24
23 · · · 16
15 · · · 8
7 6 5 4 3 2 1 0
Register name
0xFFFED000
Unused
E
ICDDCR
0xFFFED100
Set-enable bits
ICDISERn
...
Set-enable bits
...
0xFFFED180
Clear-enable bits
ICDICERn
...
Clear-enable bits
...
0xFFFED400
Priority, offset 3
Priority, offset 2
Priority, offset 1
Priority, offset 0
ICDIPRn
...
Priority, offset 3
Priority, offset 2
Priority, offset 1
Priority, offset 0
...
0xFFFED800
CPUs, offset 3
CPUs, offset 2
CPUs, offset 1
CPUs, offset 0
ICDIPTRn
...
CPUs, offset 3
CPUs, offset 2
CPUs, offset 1
CPUs, offset 0
...
0xFFFEDC00
F15 F14 F13 F12 F11 F10 F9 F8 F7 F6 F5 F4 F3 F2 F1 F0
ICDICFRn
...
F15 F14 F13 F12 F11 F10 F9 F8 F7 F6 F5 F4 F3 F2 F1 F0
...