Custom AXI4 LITE IP

DUT 8-bit Counter

count\_out [7:0]

aclk

Count out [7:0]

aclk

aresetn

aresetn

Slv\_reg0

[0]

[1]

Enable

Increment/dec

Slv\_reg1 [7:0]

Start value [7:0]

Slv\_reg3 [31:0]

Sanity check