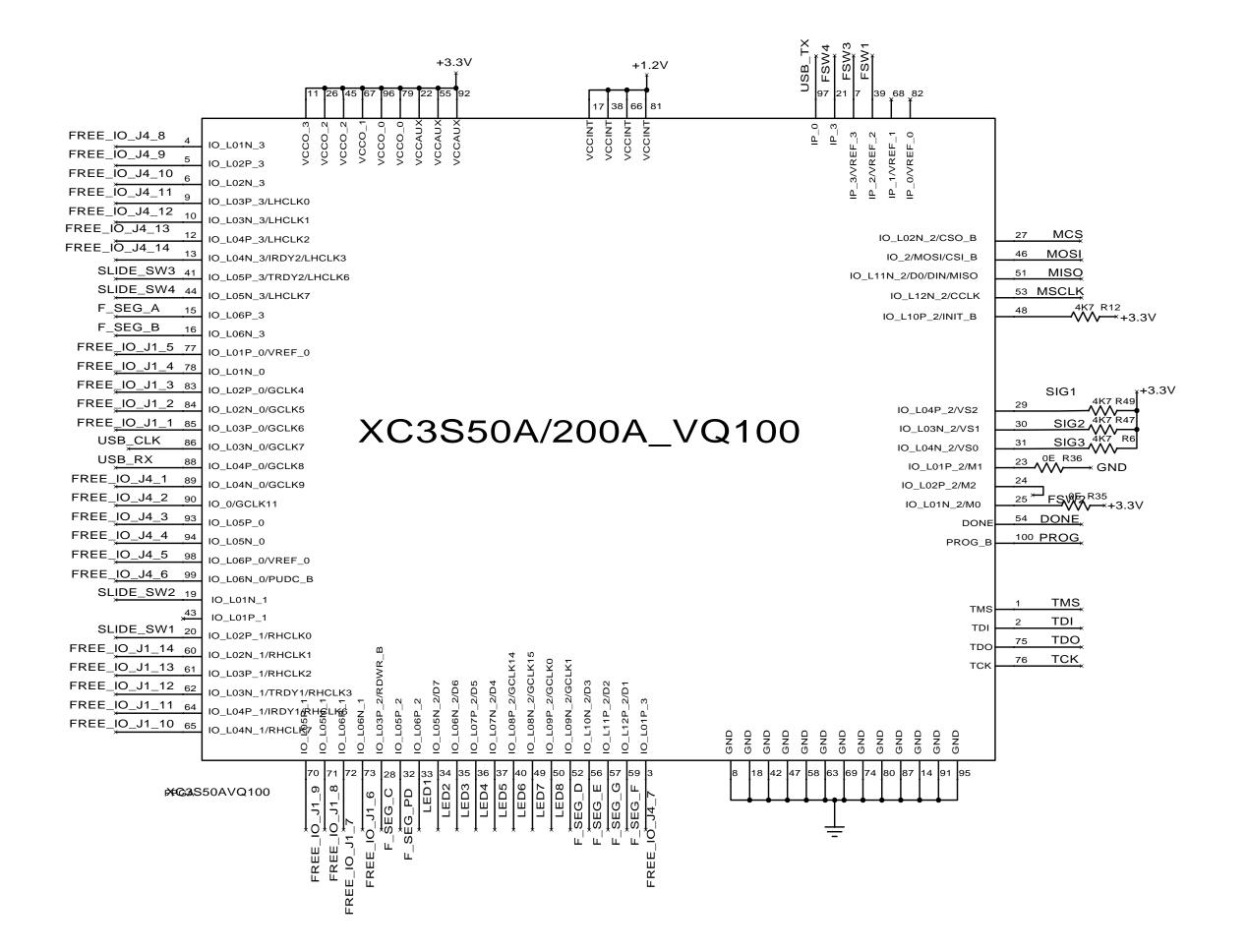
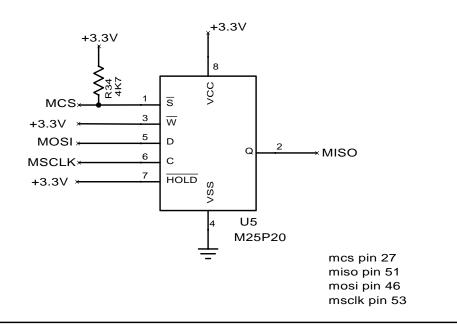
POWER SUPPLY +1.2V +1.2V REG1V2 C20 C19 10uF RELIMATE-2-M-ST POWER CONNECTOR FPGA DE-CAP +3.3V +3.3V REG3V3 USB_PWR J3 MOUNTING BERG-SR-3-M-ST +3.3V LED DWG NAME: COMPANY NAME: **FPGATECHSOLUTION** TITLE: CAMPUS-FPGA DATE PCB NO. REV. DESIGNED SIZE: CHECKED CUSTOMER: SHEET DRAWN

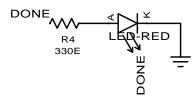
FPGA



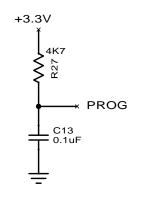
SPI FLASH



DONE

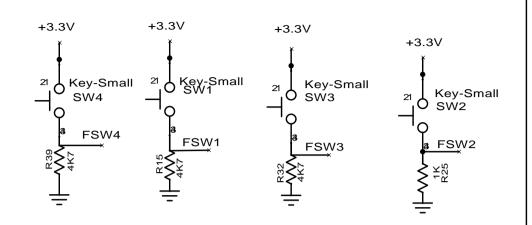


CONFIG RESET



DWG NAME:		COMPANY NAME:	FPGATECHSOLUTION	
DATE		TITLE:	CAMPUS-FPGA	
DESIGNED		PCB NO.		REV.
CHECKED		SIZE:		
DRAWN		CUSTOMER:		SHEET

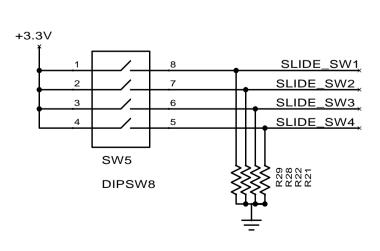
PUSH BUTTON



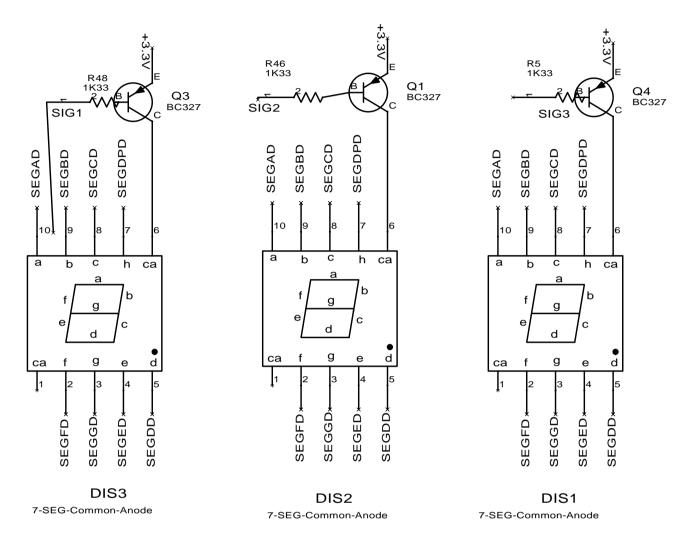
LED

LED-RED LED8 R13560E LED7 R14 560E LED6 R23560E LED5 R24560E LED4 R30560E LED3 R31 560E LED2 R37560E LED1 R38560E LED1 R38560E LED1 R38560E LED1 R38560E LED1 R38560E LED1 R38560E

SLIDE SWITCH

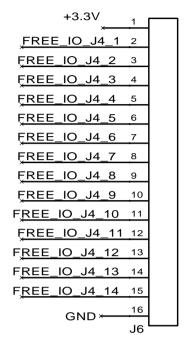


7SEG DISPLAY



F_SEG_A	R43 560E	SEGAD
F_SÊG_B	R42 560EV	SEGBD
F_SEG_C	R41 560E	SEGCD
F_SEG_PD	R40 560E	SEGDPD
F_SEG_F	R19 560E	SEGFD.
F_SEG_G	R18 560E	SEGGD
F_SEG_E	R17 560E	SEGED
F_SEG_D	R16 560E	SEGDD

FREE IO



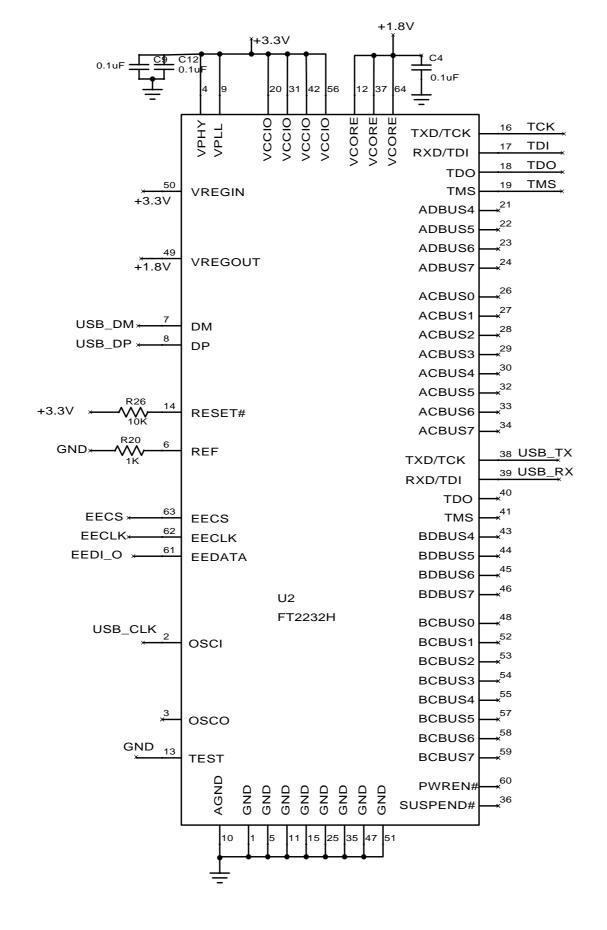
BERG-SR-16-M-ST



BERG-SR-16-M-ST

DWG NAME:		COMPANY NAME:	FPGATECHSOLUTION	
DATE		TITLE:	CAMPUS-FPGA	
DESIGNED		PCB NO.		REV.
CHECKED		SIZE:		
DRAWN		CUSTOMER:		SHEET

FT2232HL



MICROUSB2.0

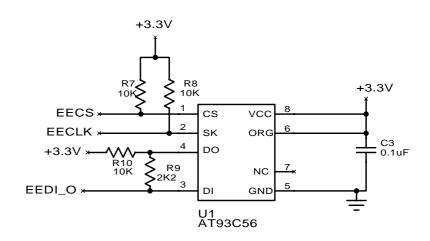
USB

1USB_PWR

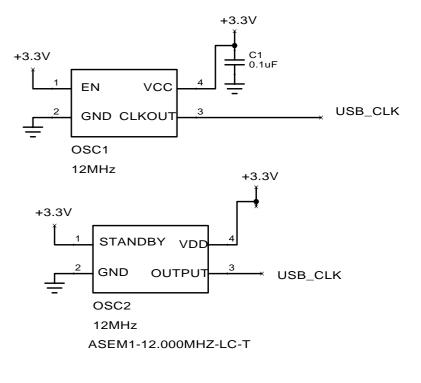
→ USB_DM

→ USB_DP

EEPROM



OSCILLATOR



DWG NAME:		COMPANY NAME:	FPGATECHSOLUTION	
DATE		TITLE:	CAMPUS-FPGA	
DESIGNED		PCB NO.		REV.
CHECKED		SIZE:		
DRAWN		CUSTOMER:		SHEET