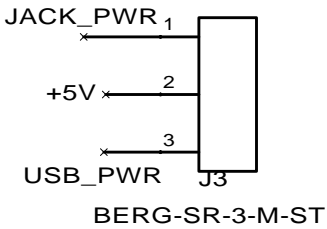
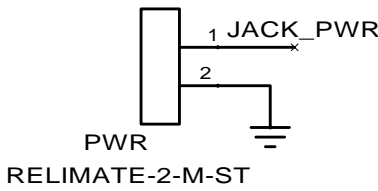
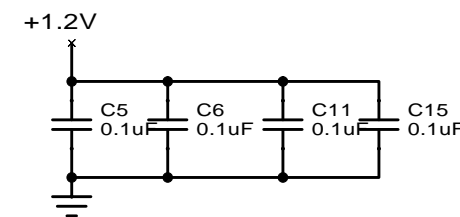
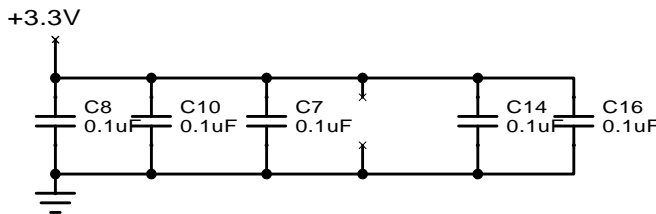
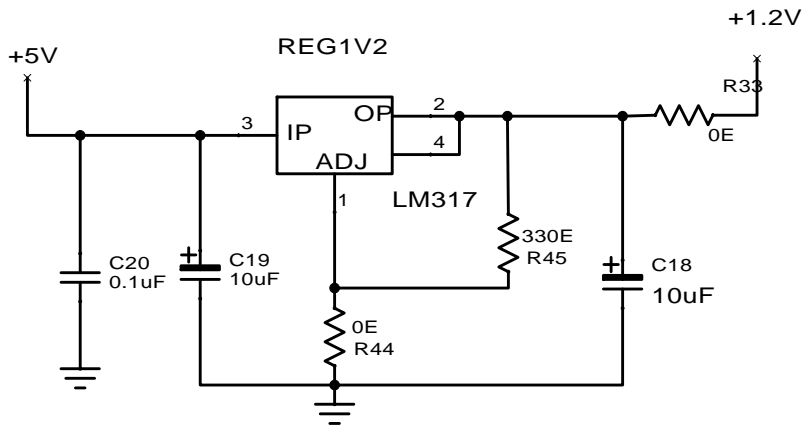


POWER CONNECTOR



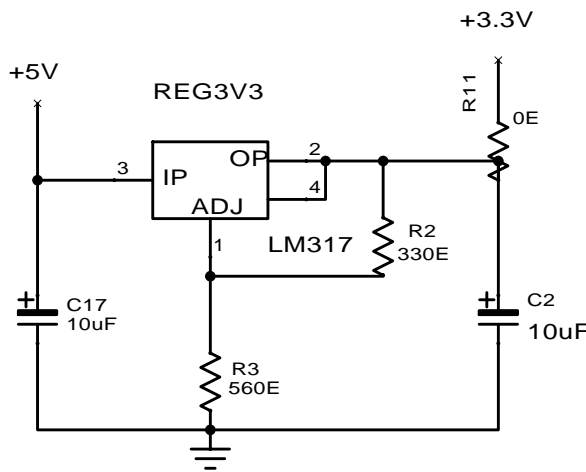
POWER SUPPLY

+1.2V

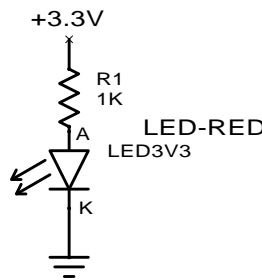


FPGA DE-CAP

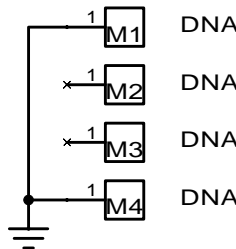
+3.3V



+3.3V LED

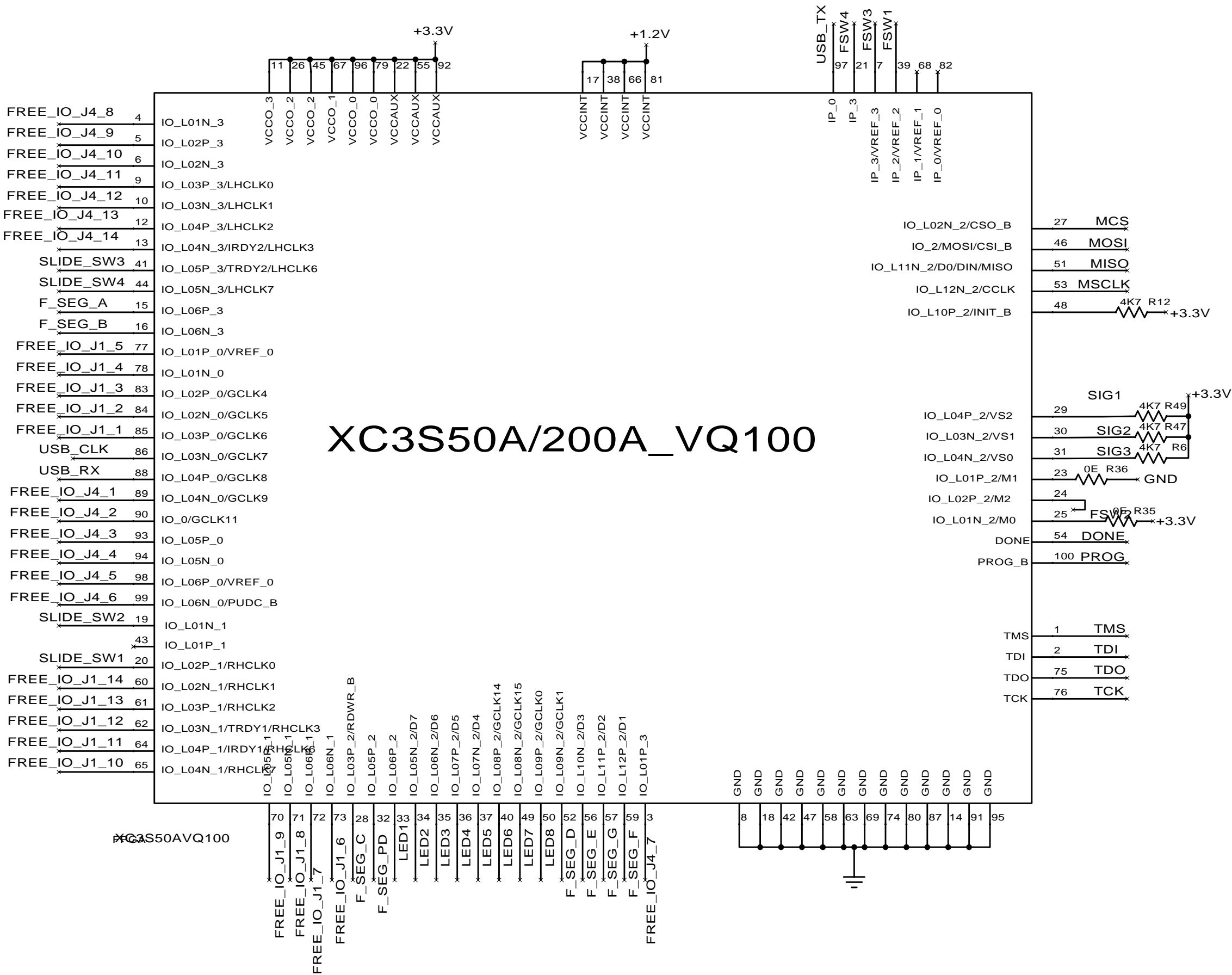


MOUNTING

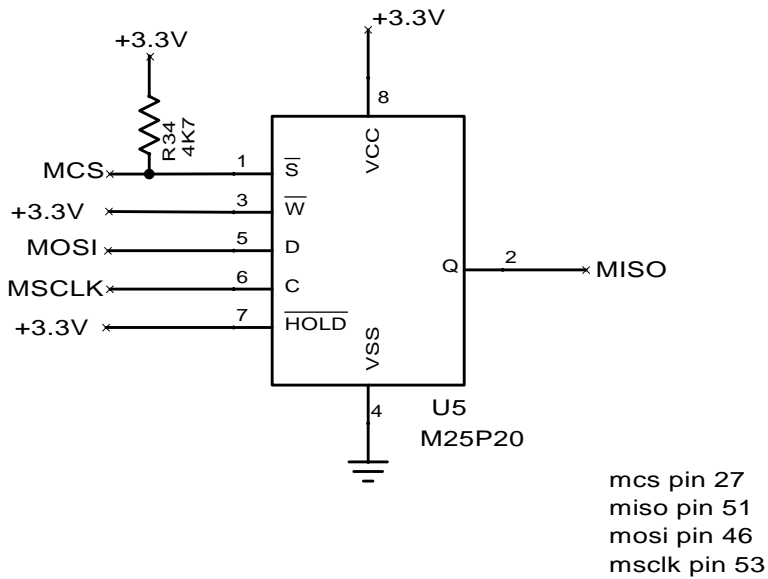


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| DWG NAME: | | COMPANY NAME: FPGATECHSOLUTION | |
| DATE | | TITLE: CAMPUS-FPGA | |
| DESIGNED | | PCB NO. | REV. |
| CHECKED | | SIZE: | |
| DRAWN | | CUSTOMER: | SHEET |

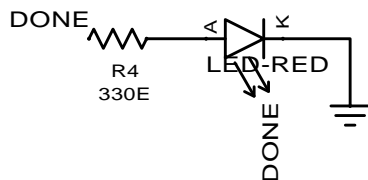
FPGA



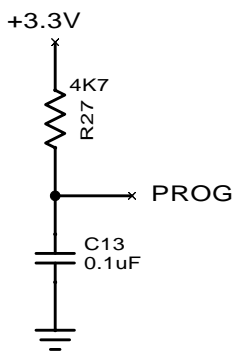
SPI FLASH



DONE

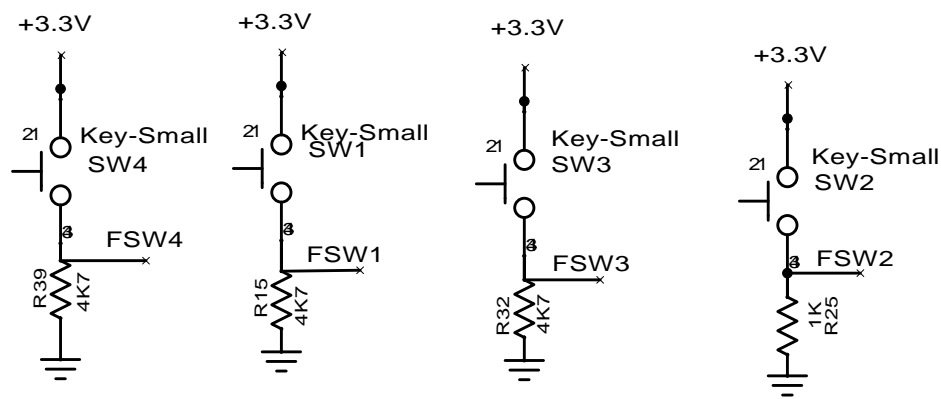


CONFIG RESET

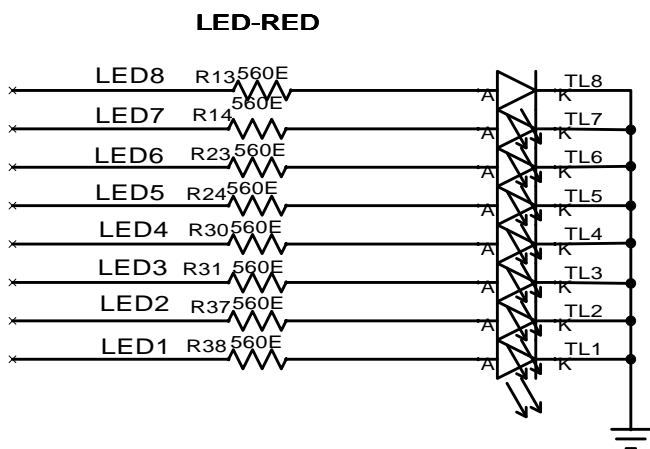


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| DWG NAME: | | COMPANY NAME: FPGATECHSOLUTION | |
| DATE | | TITLE: CAMPUS-FPGA | |
| DESIGNED | | PCB NO. | REV. |
| CHECKED | | SIZE: | |
| DRAWN | | CUSTOMER: | SHEET |

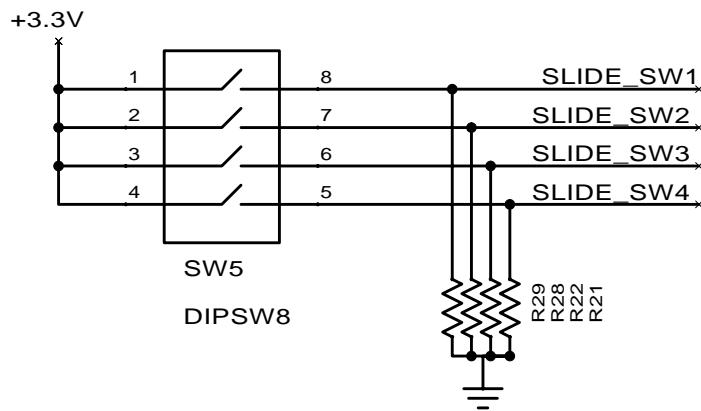
PUSH BUTTON



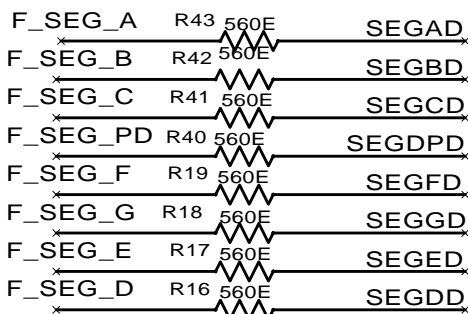
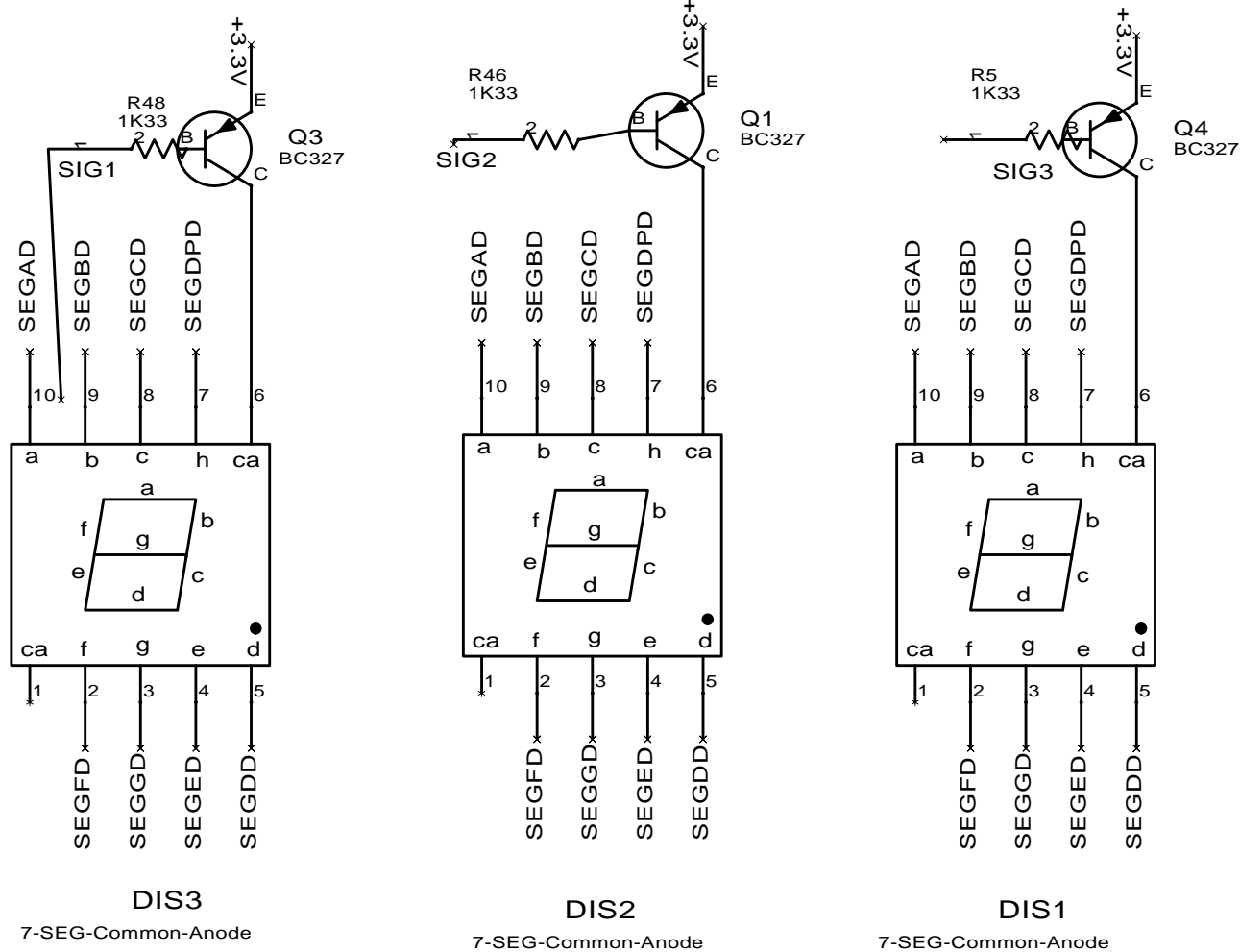
LED



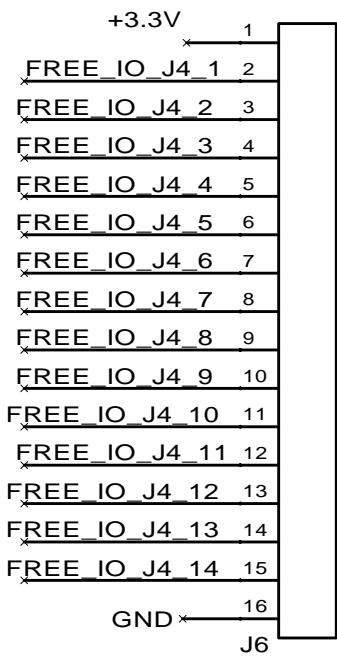
SLIDE SWITCH



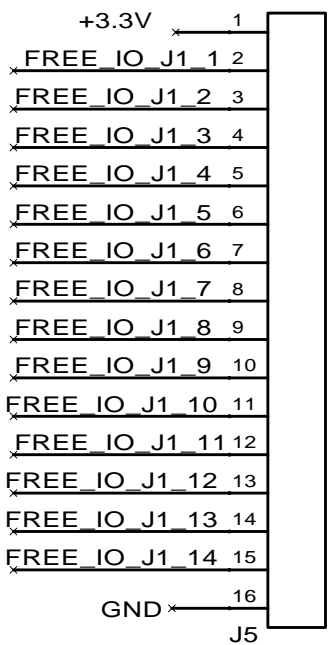
7SEG DISPLAY



FREE IO



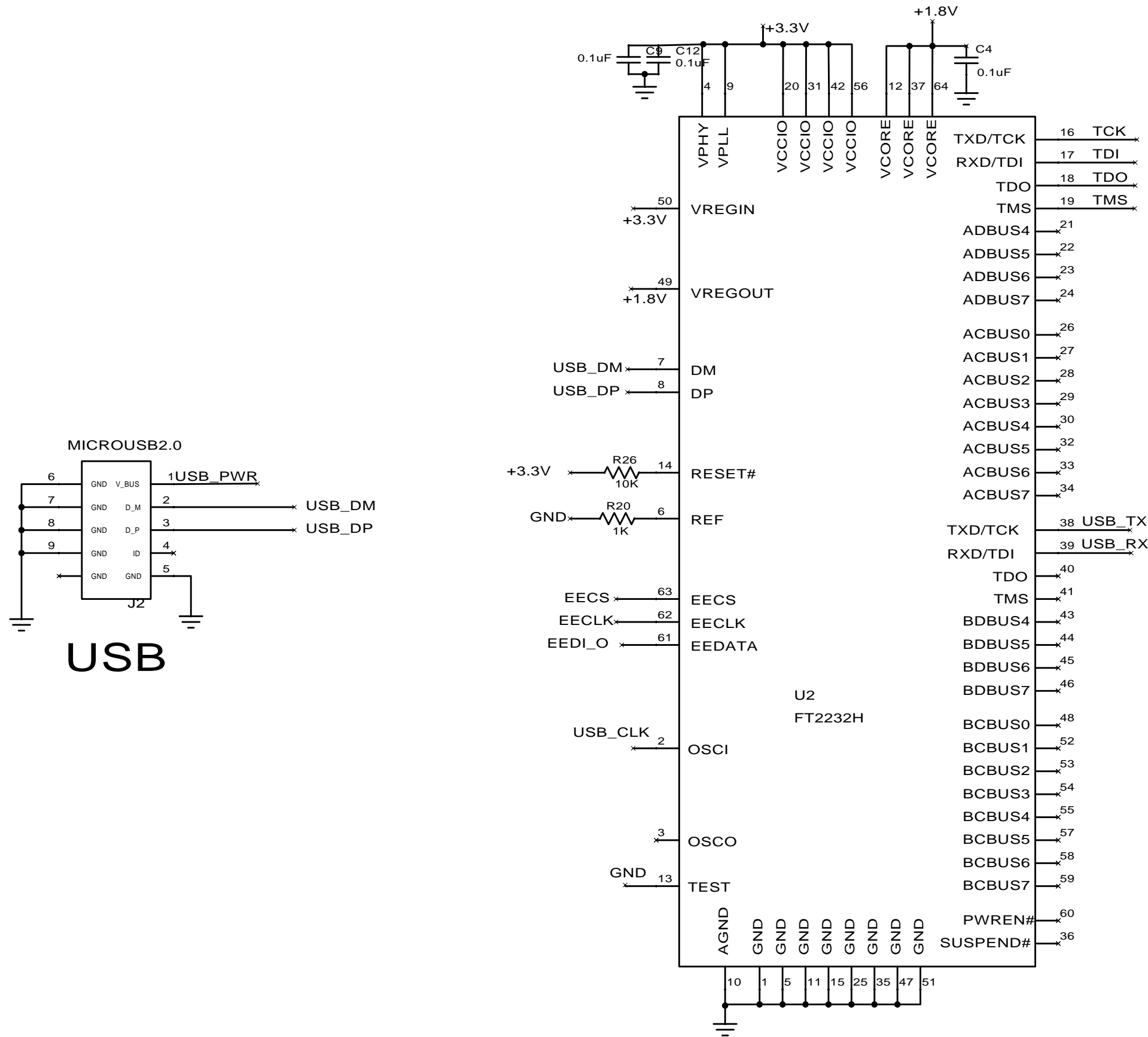
BERG-SR-16-M-ST



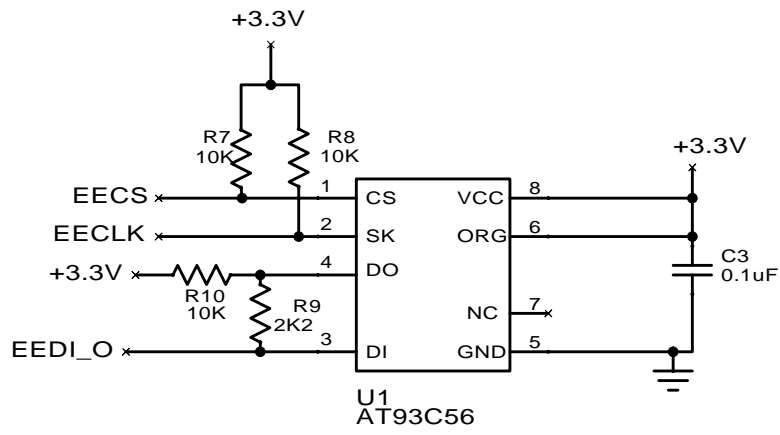
BERG-SR-16-M-ST

| | | | |
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| DWG NAME: | | COMPANY NAME: FPGATECHSOLUTION | |
| DATE | | TITLE: CAMPUS-FPGA | |
| DESIGNED | | PCB NO. | REV. |
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| DRAWN | | CUSTOMER: | SHEET |

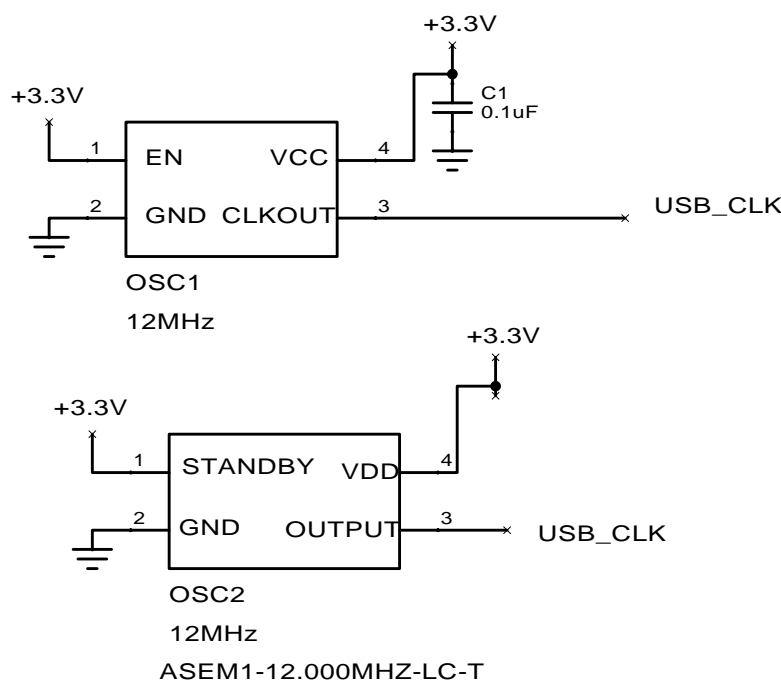
FT2232HL



EEPROM



OSCILLATOR



| | | | |
|-----------|--|--------------------------------|-------|
| DWG NAME: | | COMPANY NAME: FPGATECHSOLUTION | |
| DATE | | TITLE: CAMPUS-FPGA | |
| DESIGNED | | PCB NO. | REV. |
| CHECKED | | SIZE: | |
| DRAWN | | CUSTOMER: | SHEET |