**PINE S7 Board**

**Getting Started with Vivado M**UX4\_1**Testing Using the PINE S7 FPGA Kit**

This tutorial provides a step-by-step procedure for testing an OR gate on the PINE S7 kit. First, create a project in Vivado 2020.2 and run on the PINE S7. The peripheral application demonstrates LED output, where the inputs are switches.







