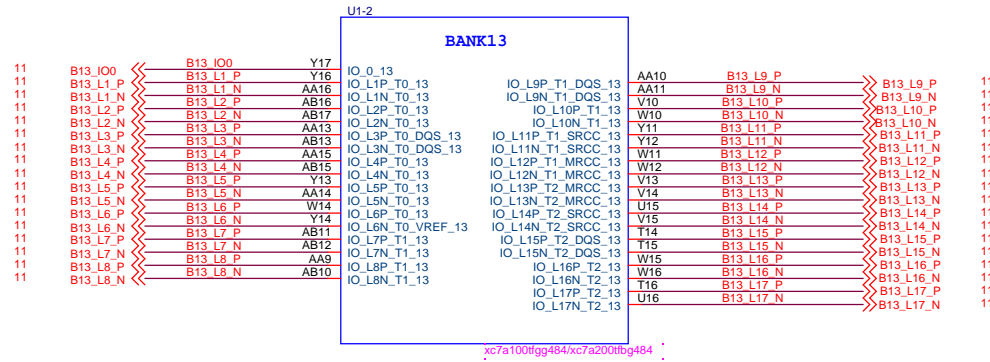
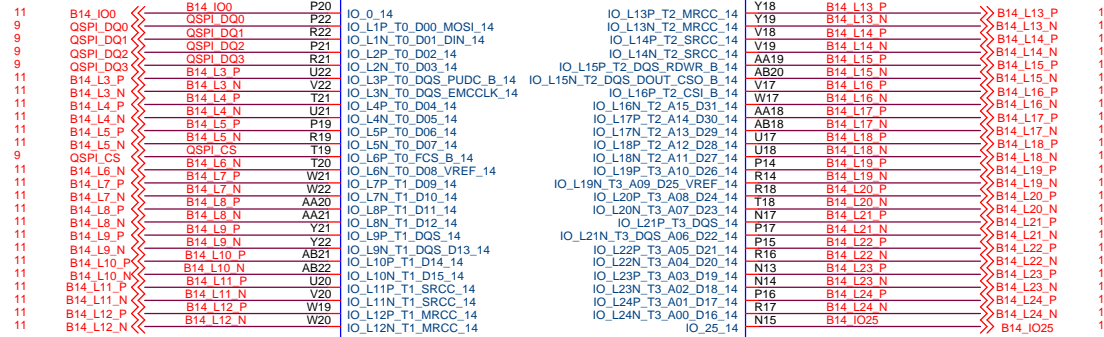
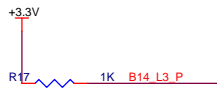


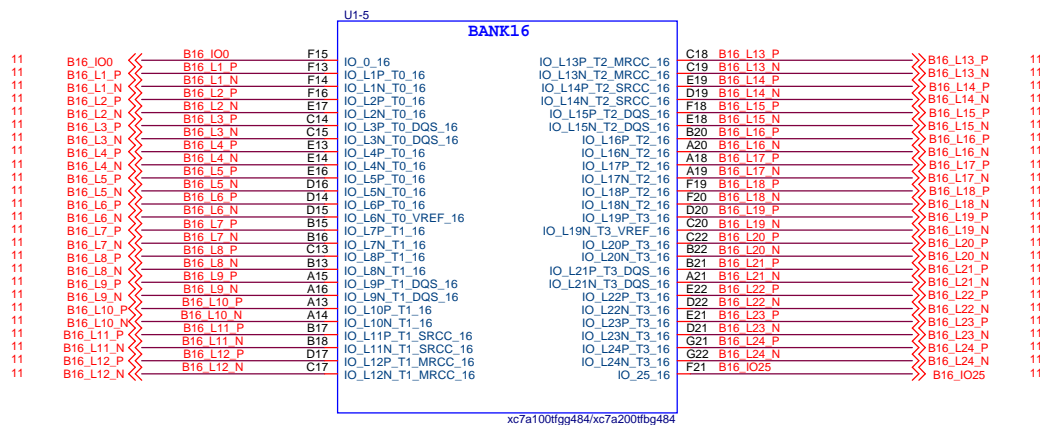
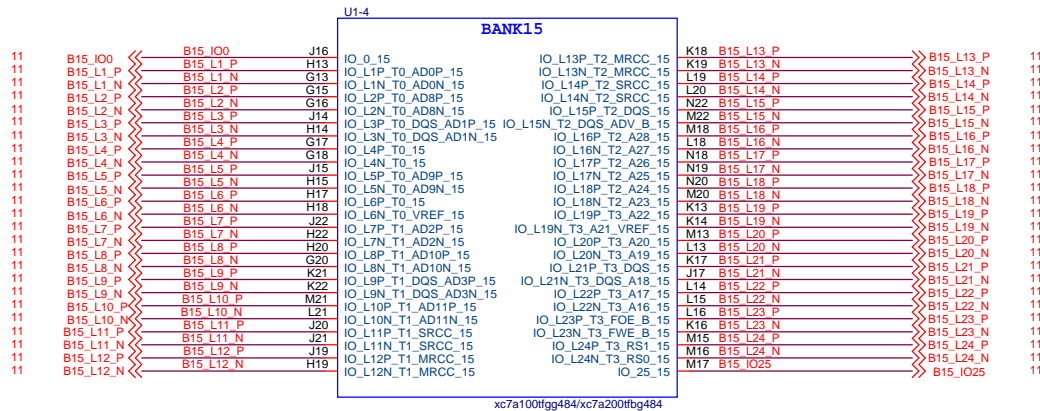


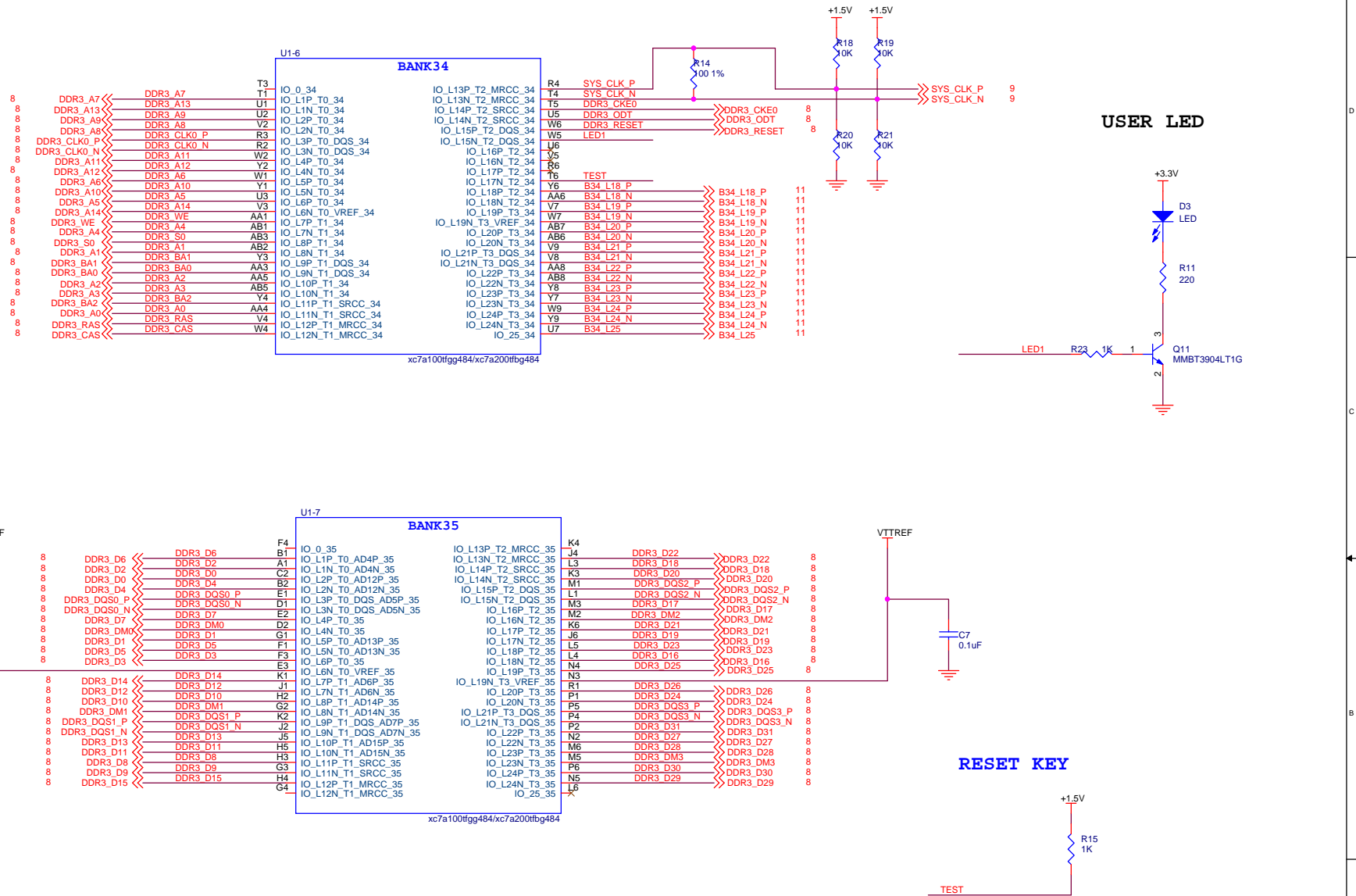
PUDC\_B=1: Deactive internal Pull up Resister



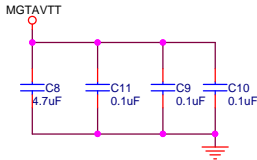
xc7a100tgg484/xc7a200tbg484

xc7a100tgg484/xc7a200tbg484

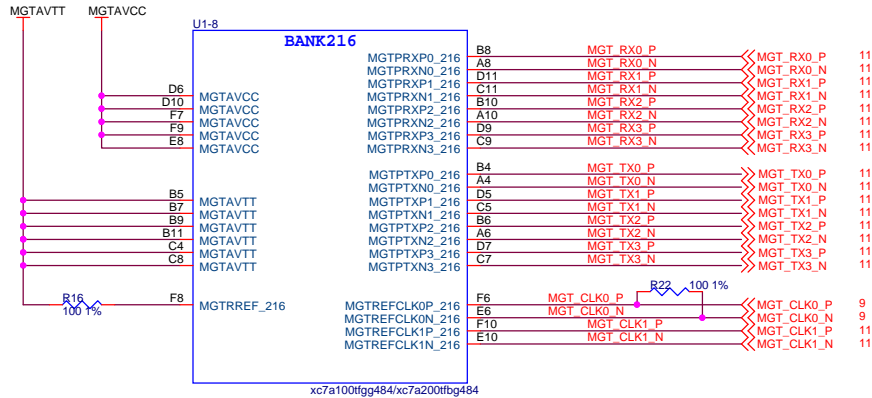
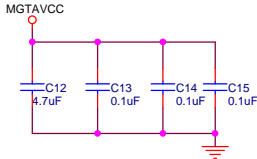


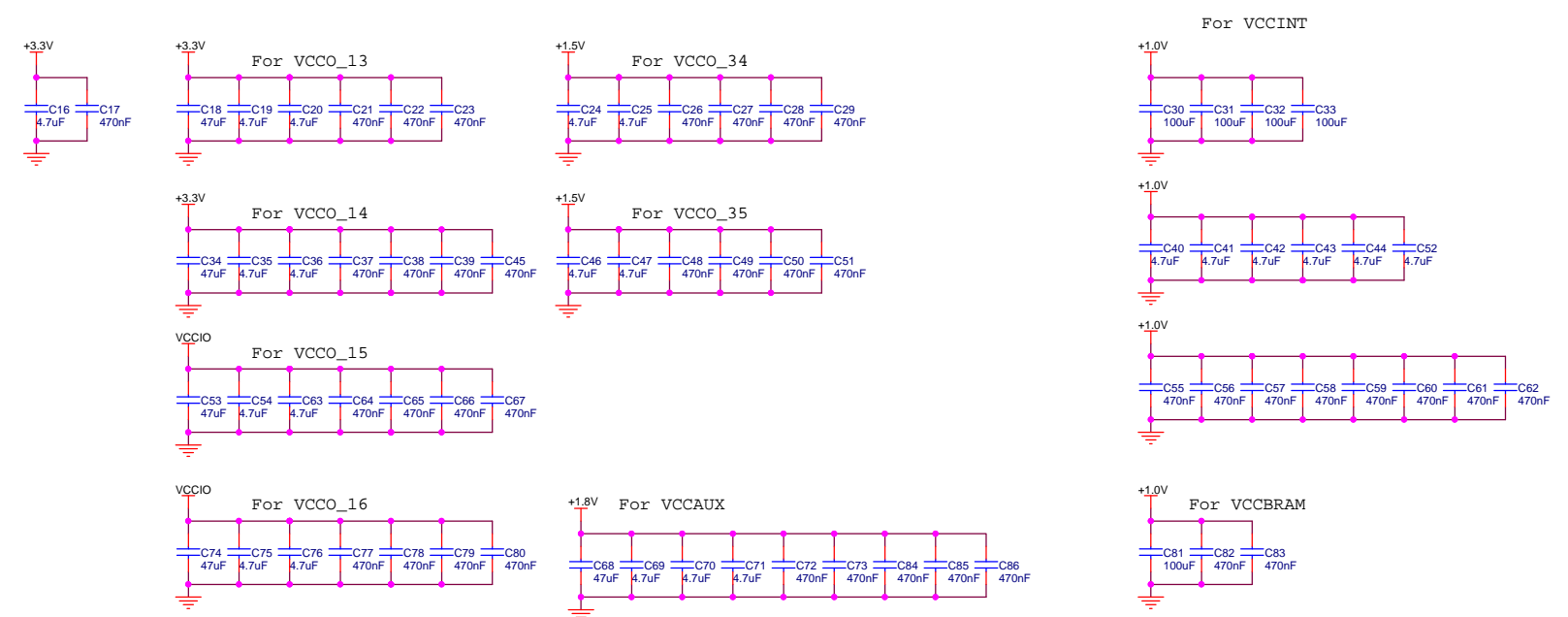
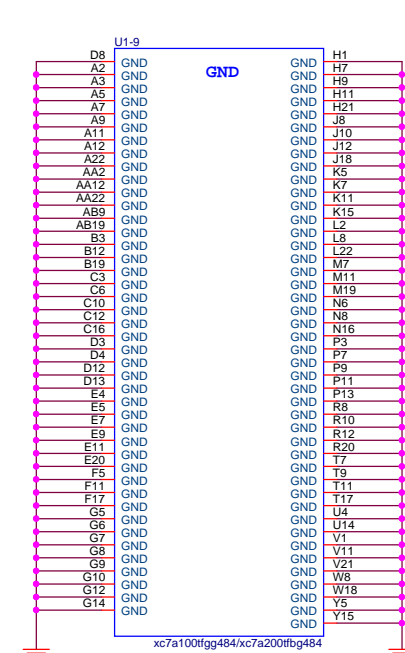
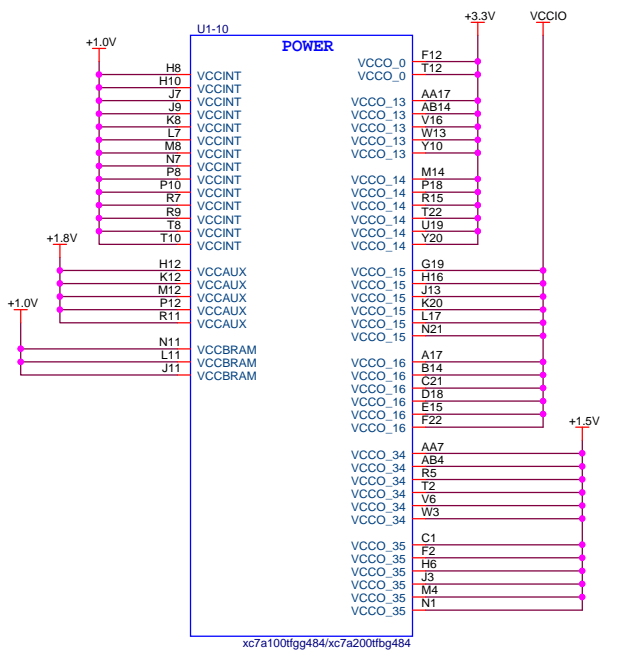


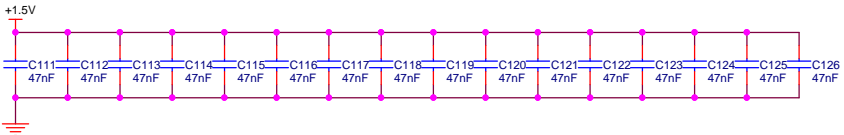
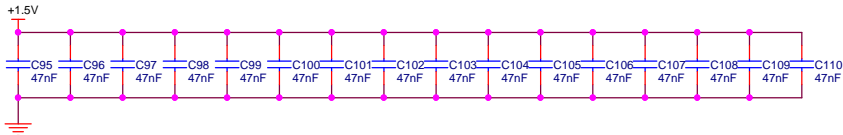
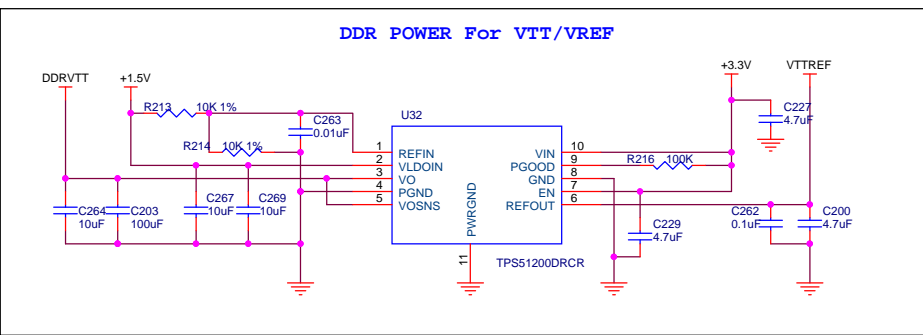
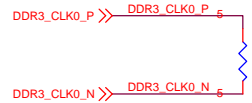
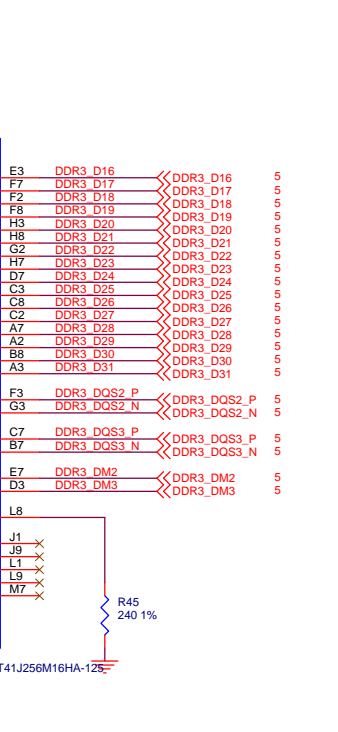
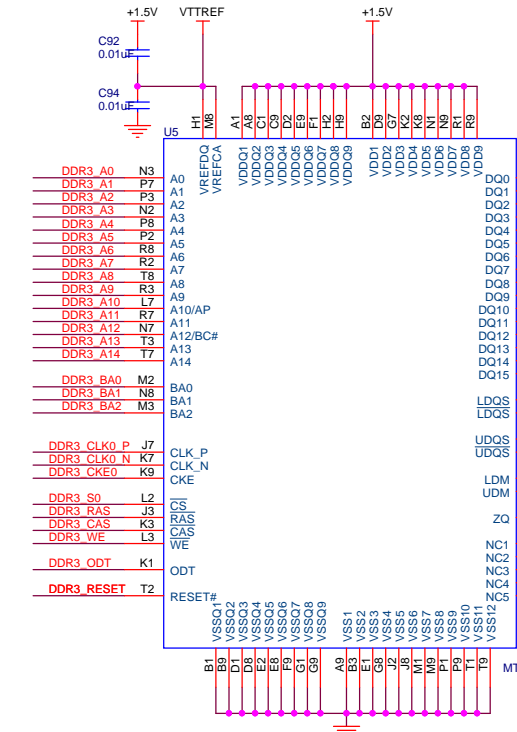
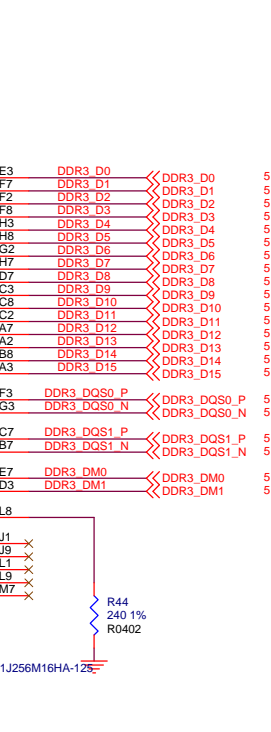
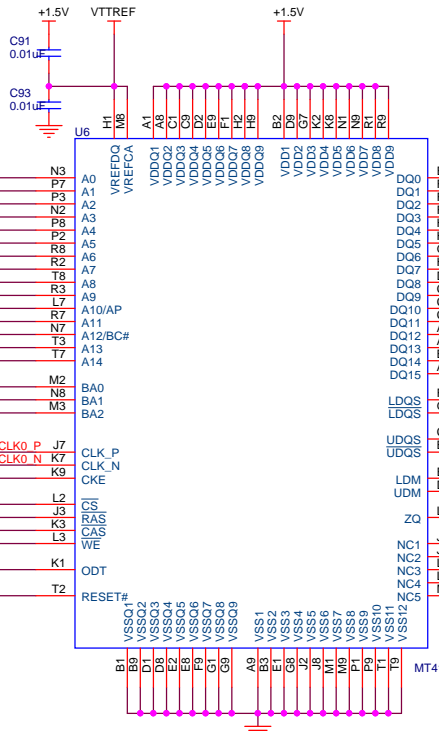
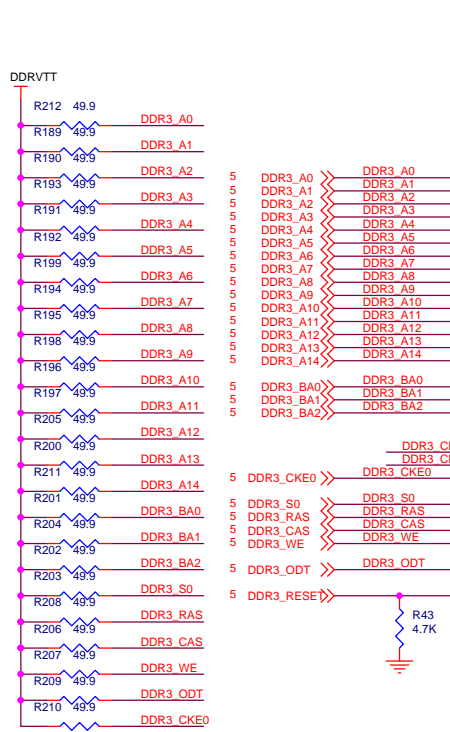
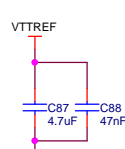
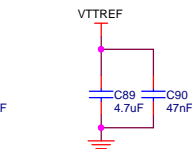
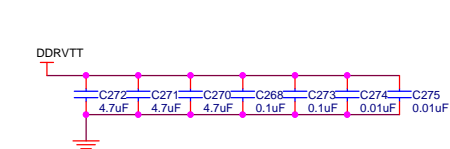
MGTAVTT 4.7uF(1) 0.1uF(2)



MGTAVCC 4.7uF(1) 0.1uF(2)

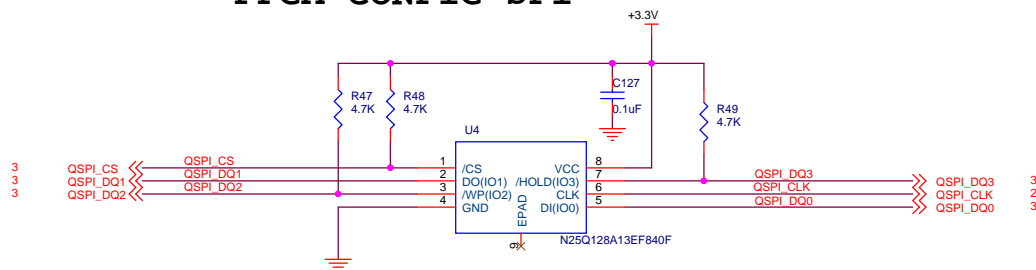






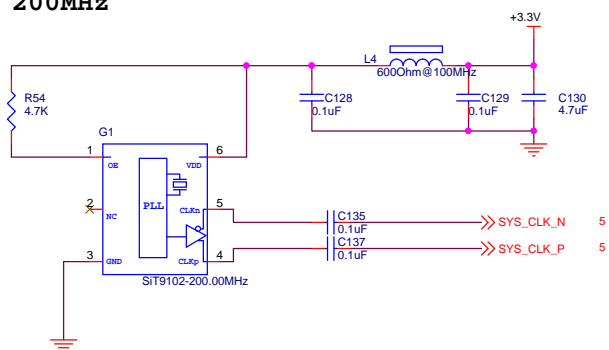


## FPGA CONFIG SPI



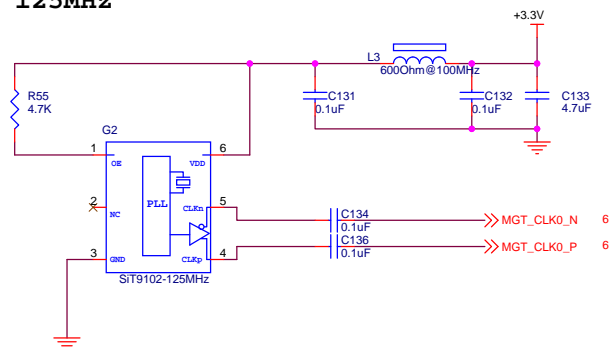
## SYSTEM CLOCK

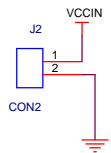
200MHz



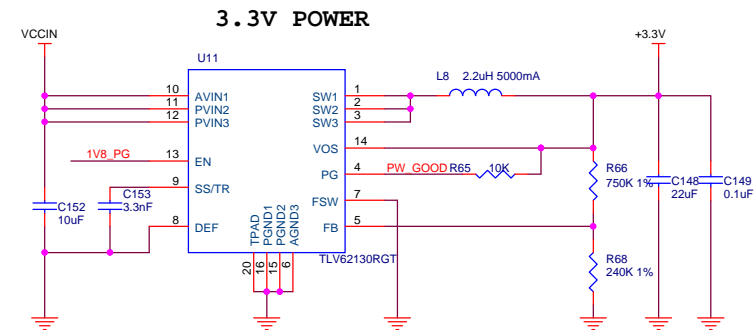
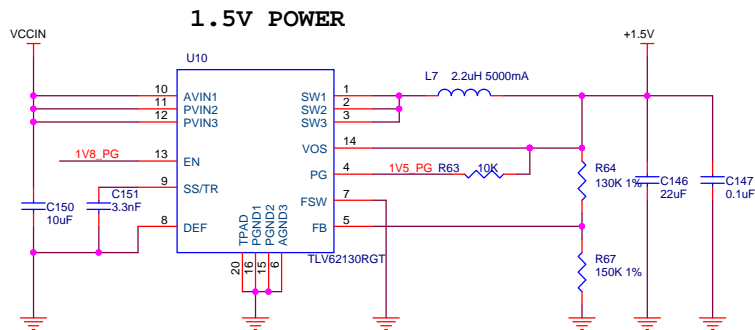
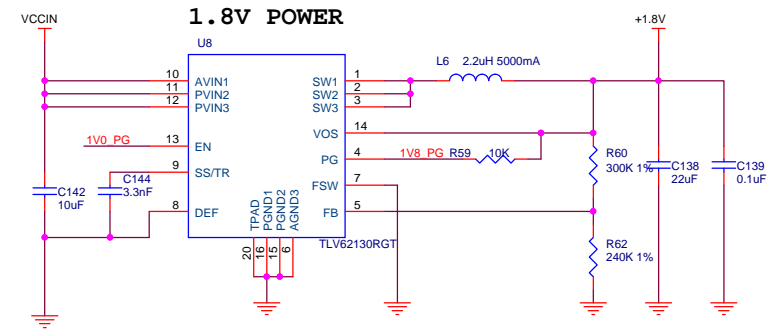
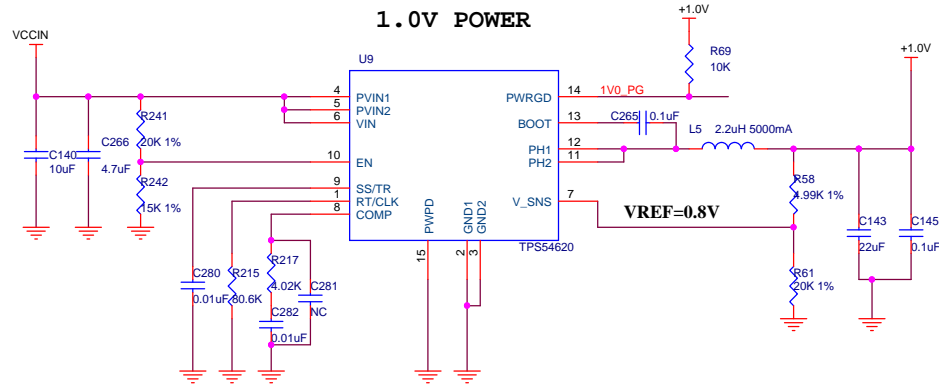
## GTP CLOCK

125MHz

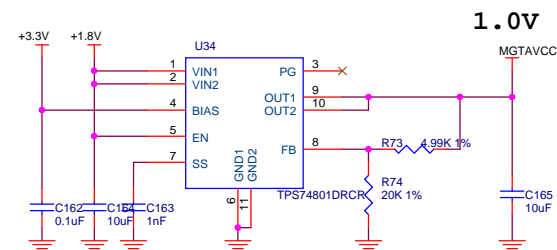
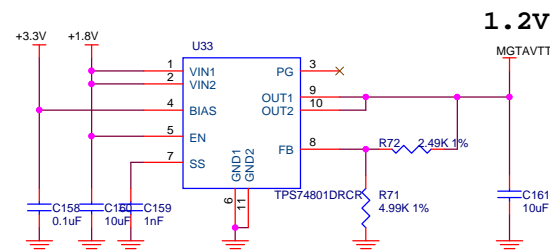
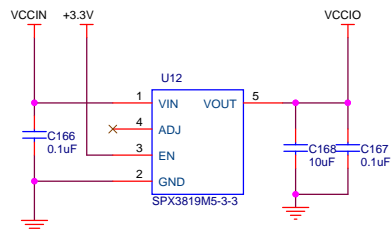




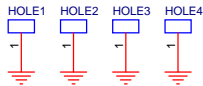
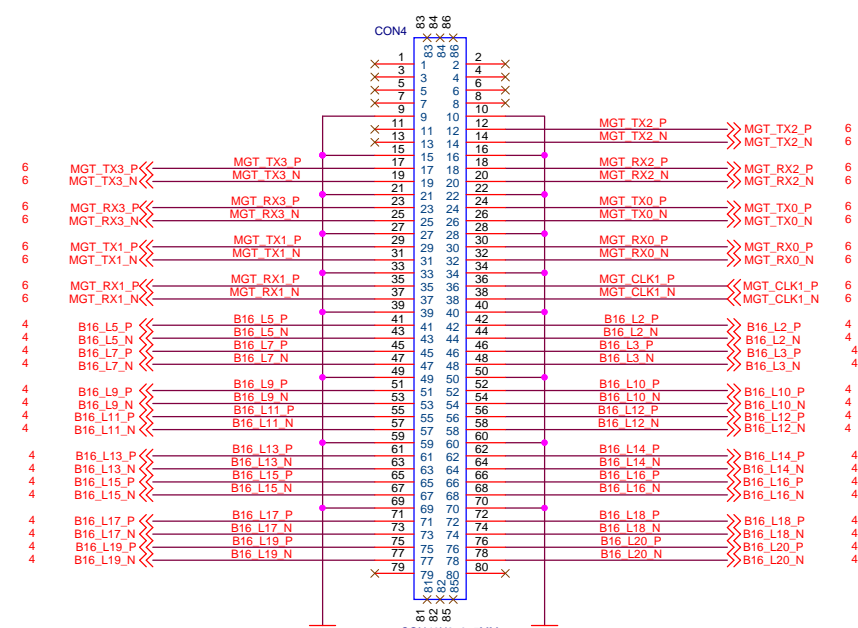
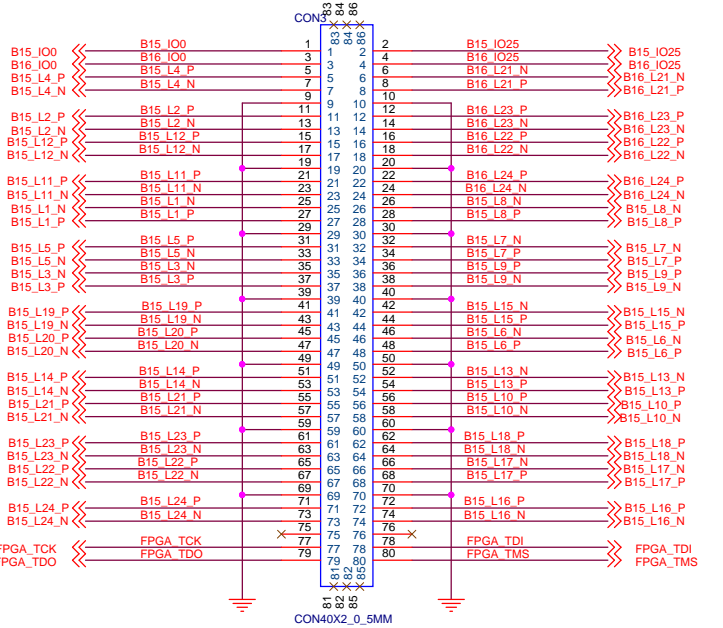
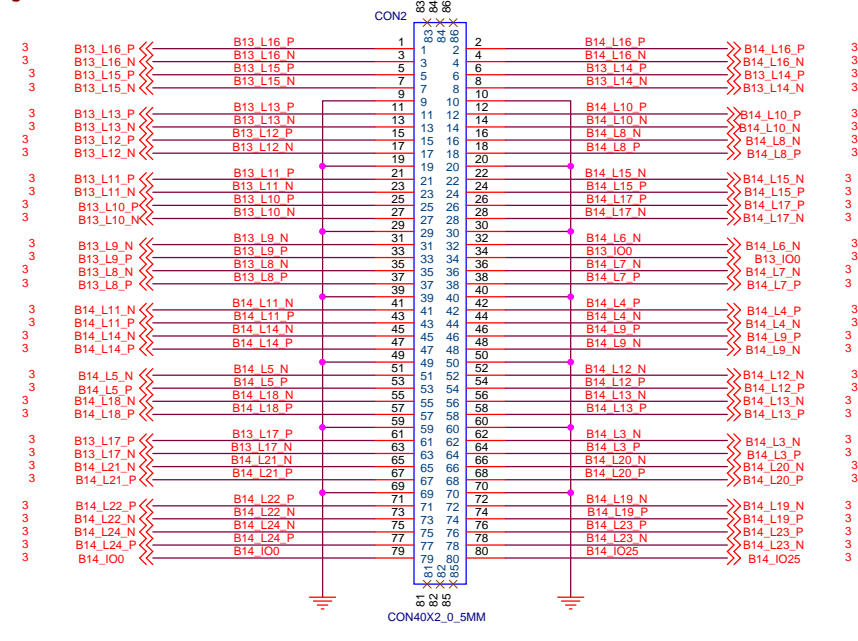
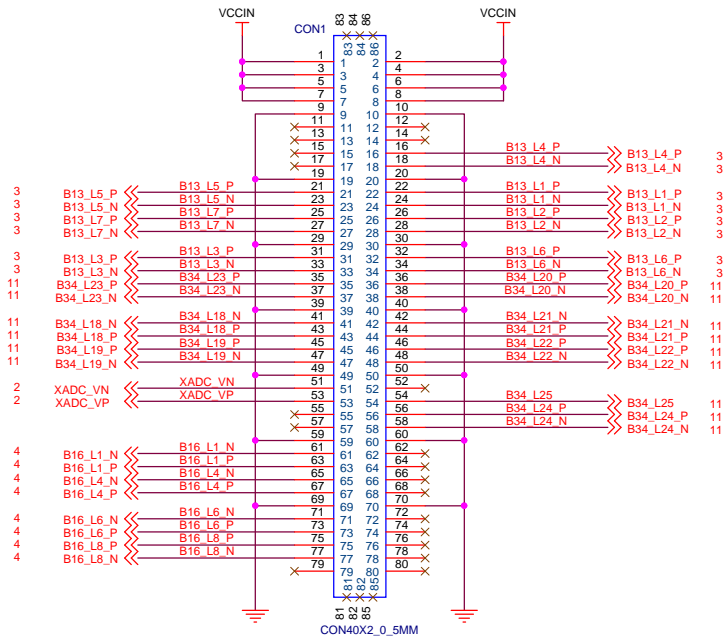
POWER ON: VCCINT(1.0V)->VCCBRAM(1.0V)->VCCAUX-(1.8V)>VCCO(1.5V and 3.3V)



POWER ON: VCCINT(1.0V)->VMGTAVCC(1.0V)->VMGTAVTT(1.2V)



These IOs are on BANK34, Voltage is 1.5V!!!



ALINX Confidential