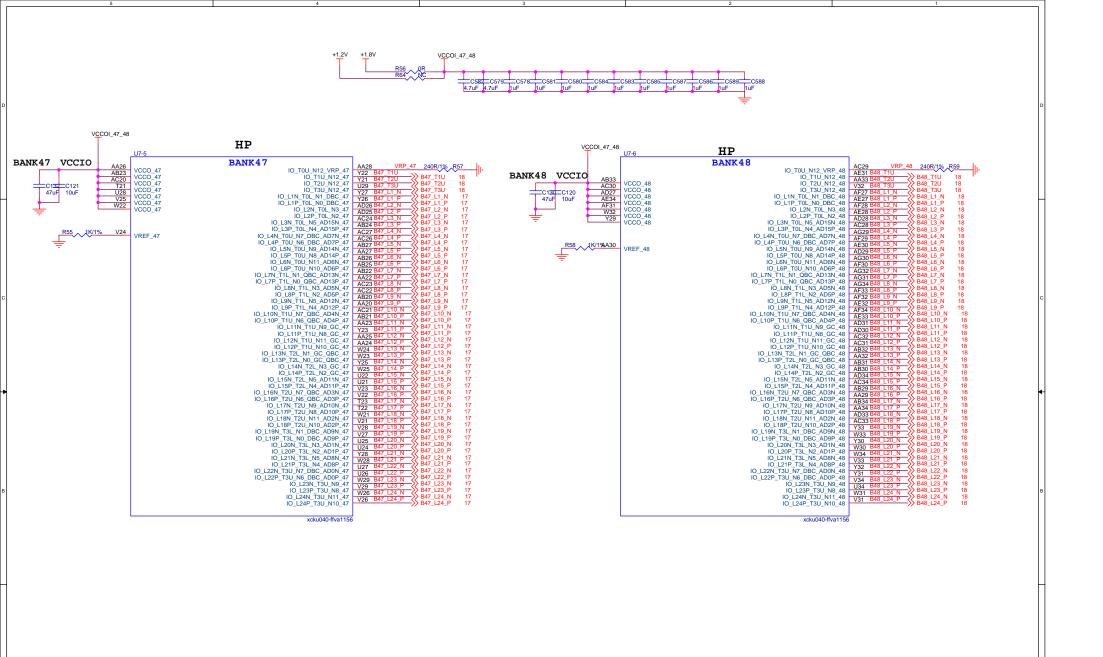


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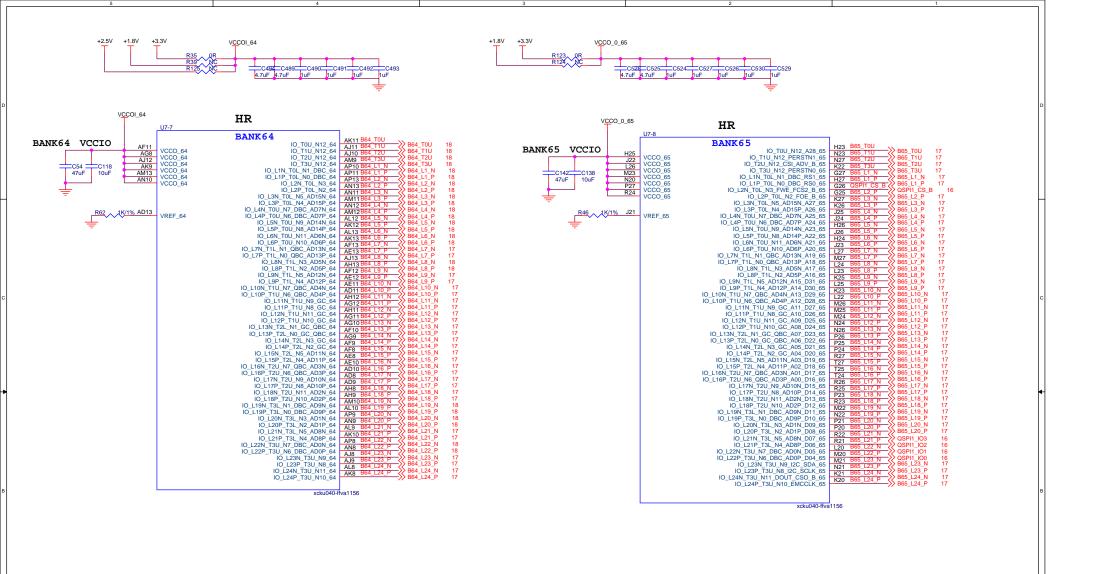
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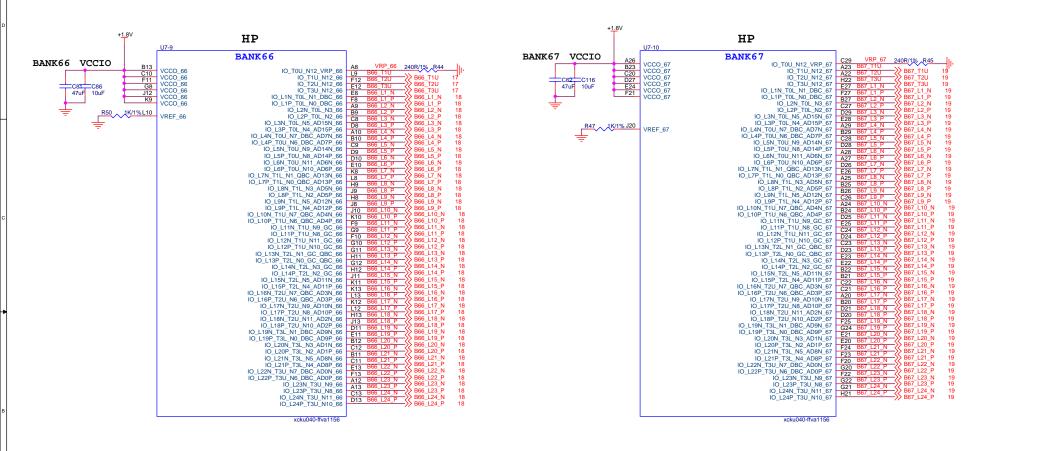
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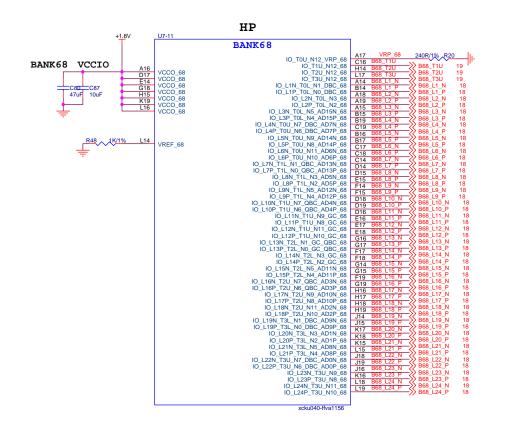










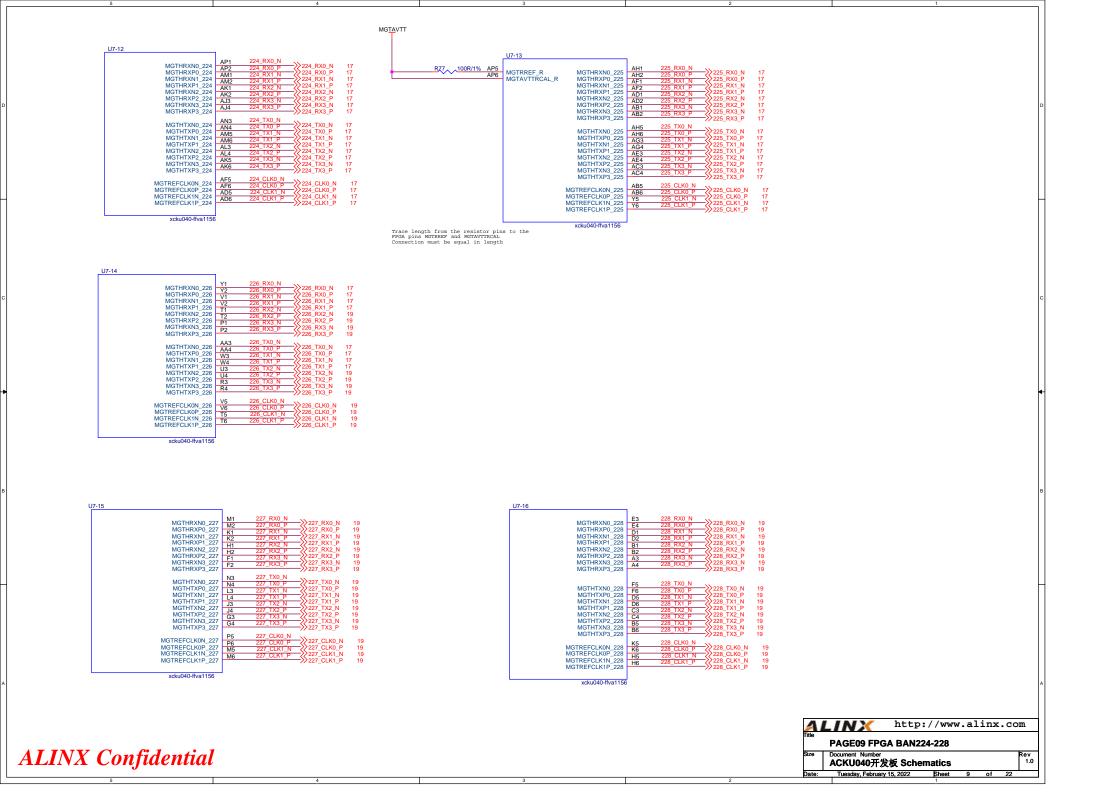


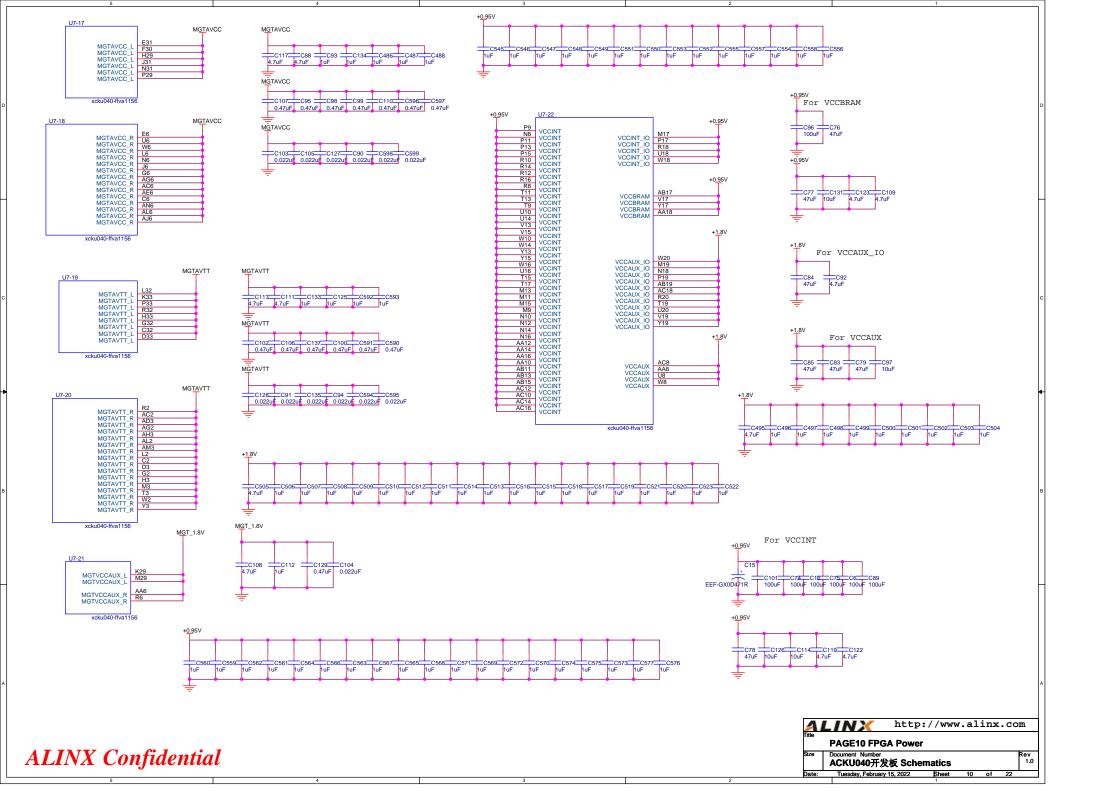
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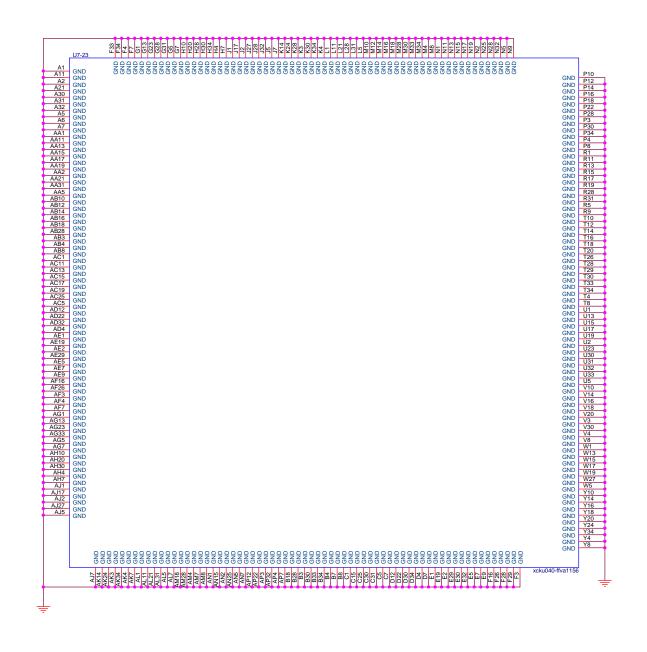
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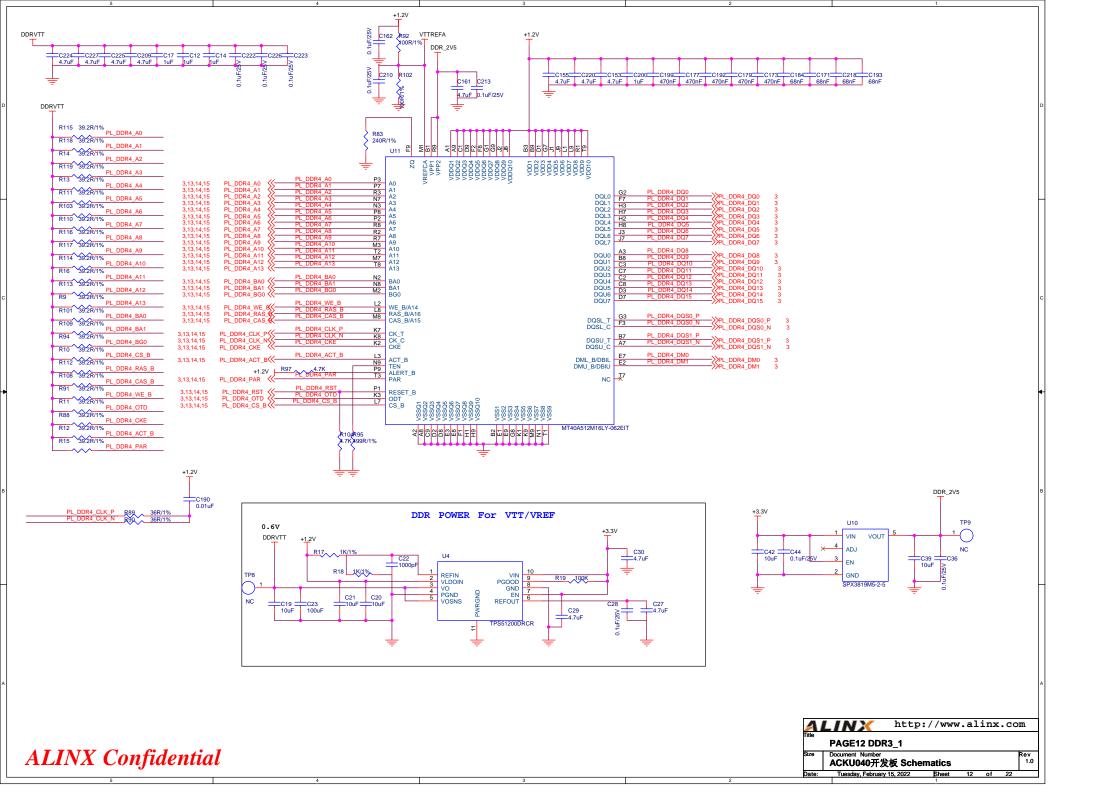
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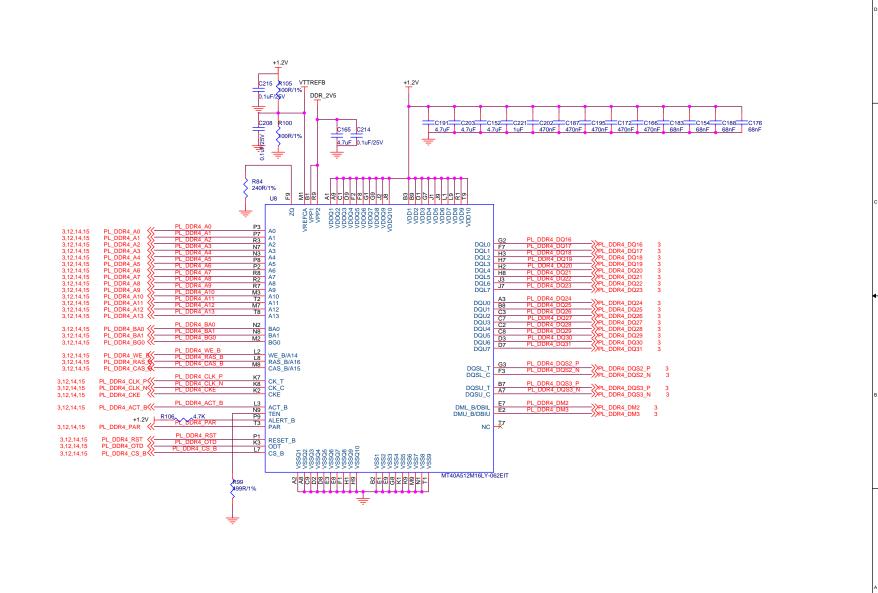






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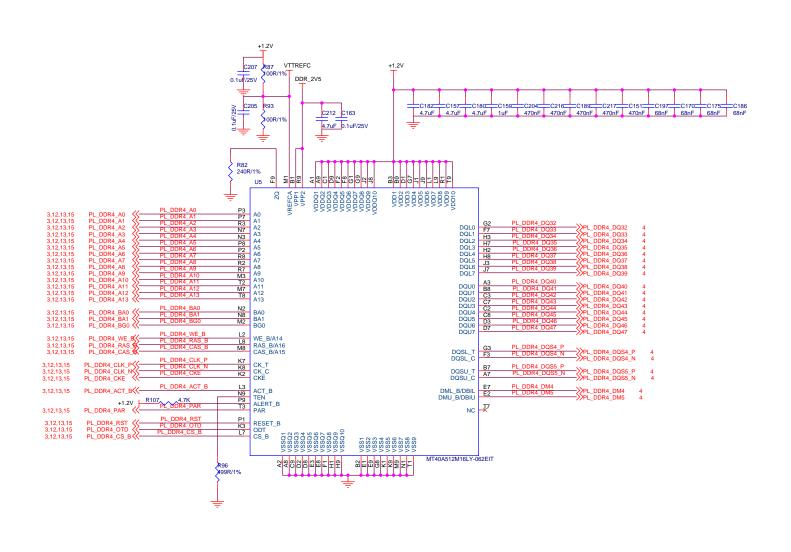
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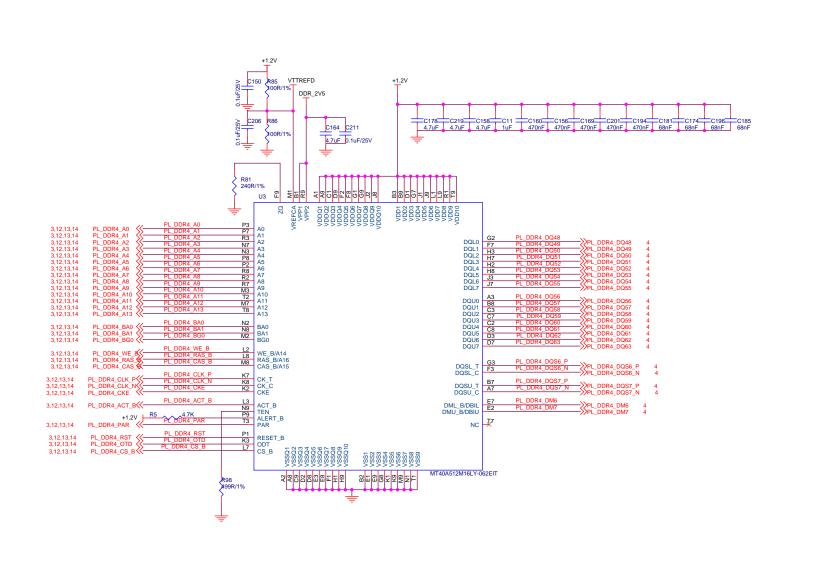
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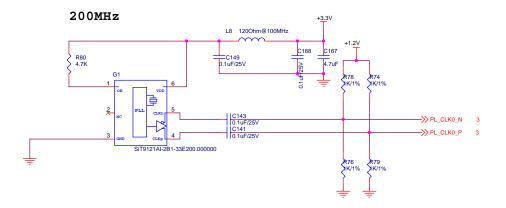
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PAGE15 DDR4_4

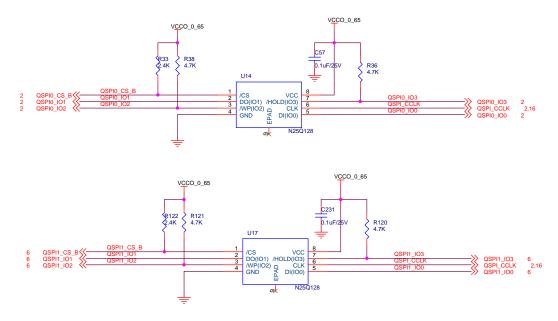
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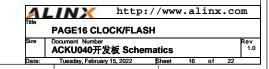
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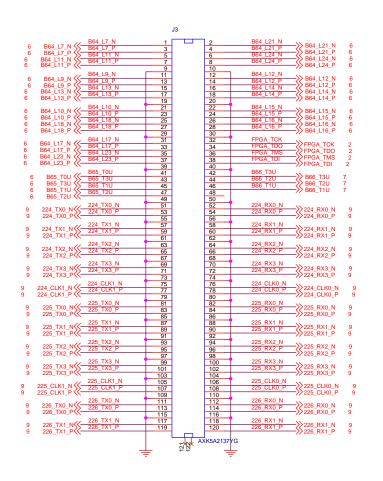
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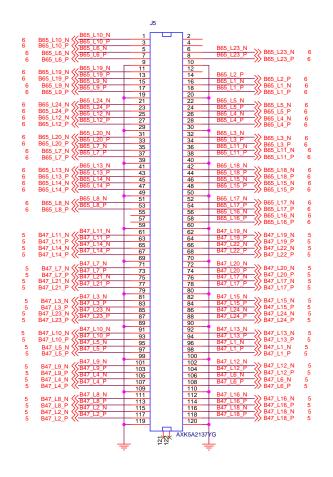


FPGA CONFIG SPI

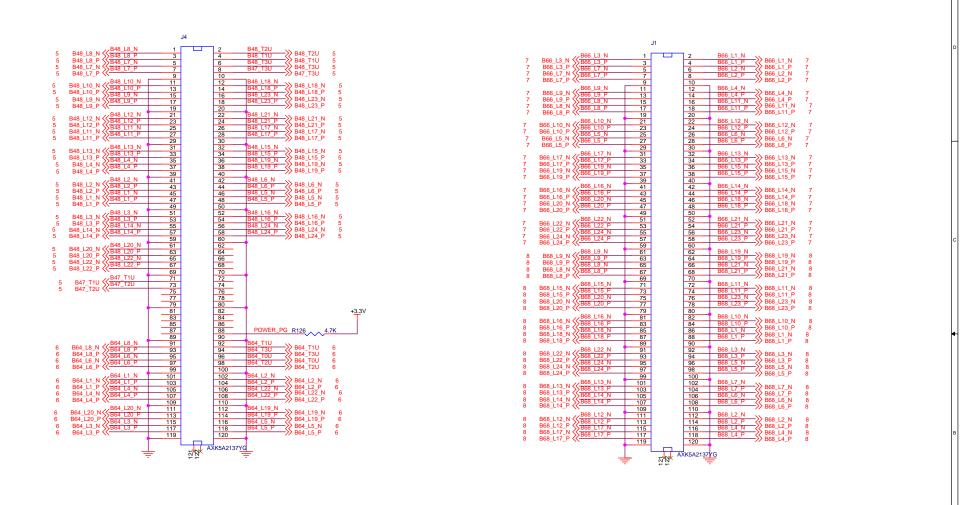




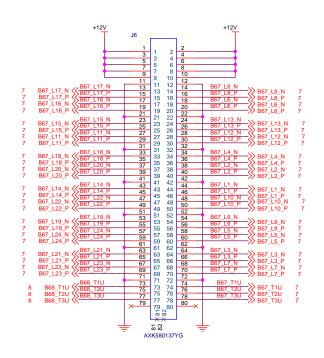


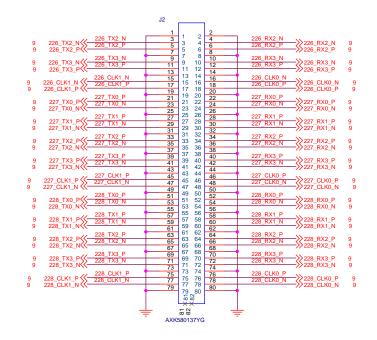








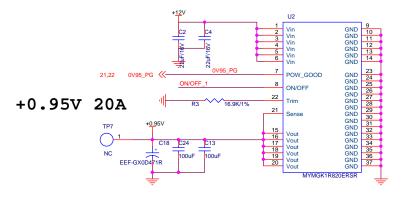




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Title PAGE19 BTB Connector3
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Input: 8V to 15V



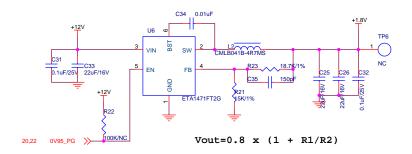
+12V R7 100K/NC ON/OFF_1 R8 100K/NC

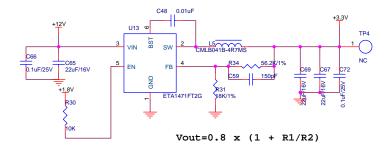
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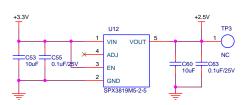
Title PAGE20 POWER +1.0V

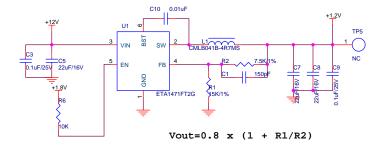
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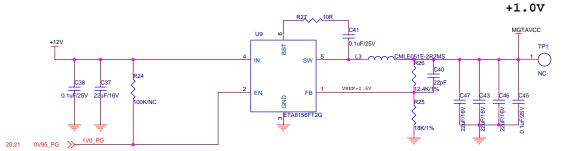




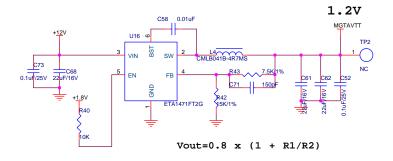


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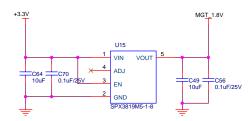
1.0V POWER 6A



 $Vout=0.6 \times (1 + R1/R2)$



 $Vout=0.8 \times (1 + R1/R2)$



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Title P.	AGE22 POWER MGT						
Size	Document Number ACKU040开发板:	Schematics					Rev 1.
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